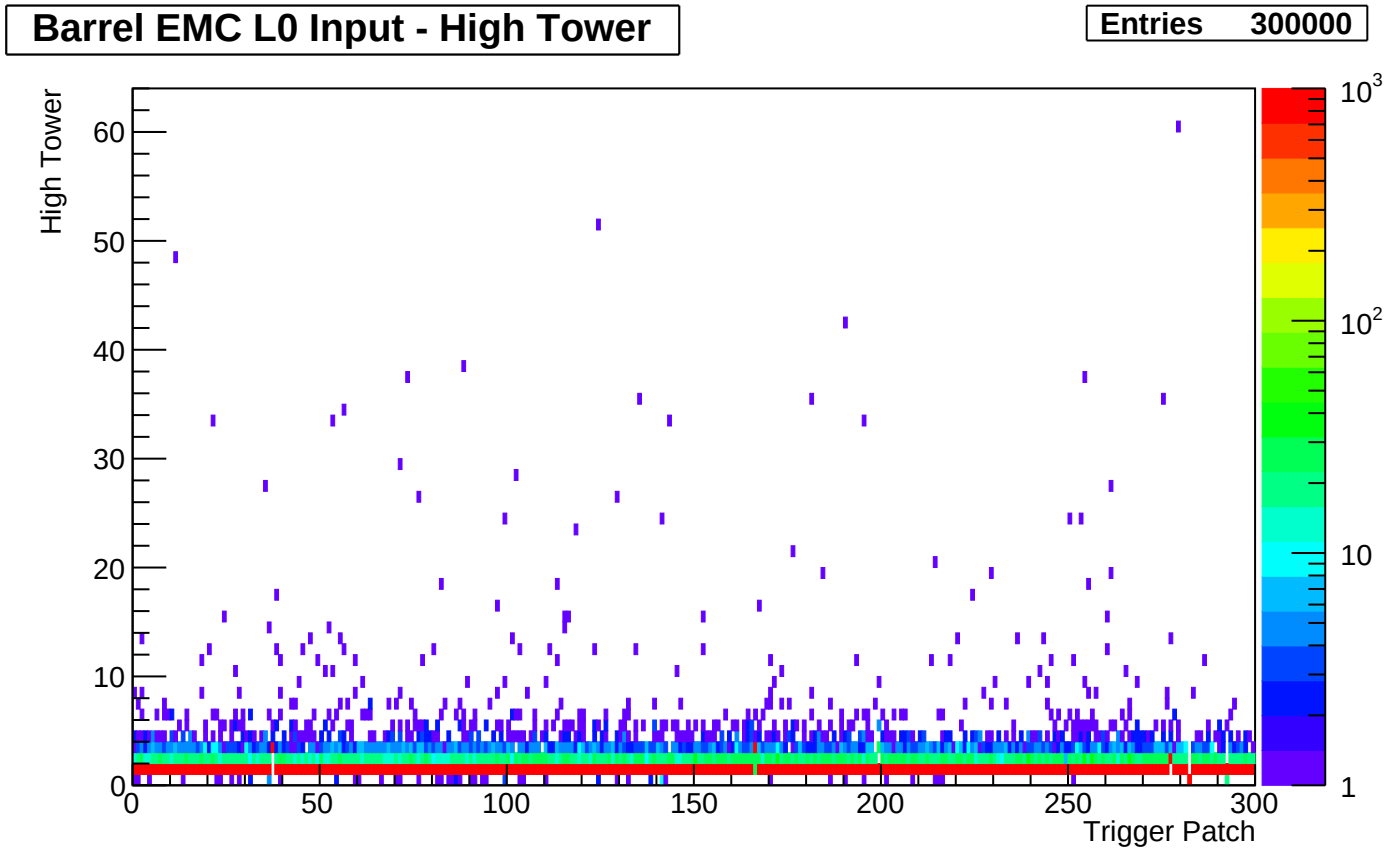
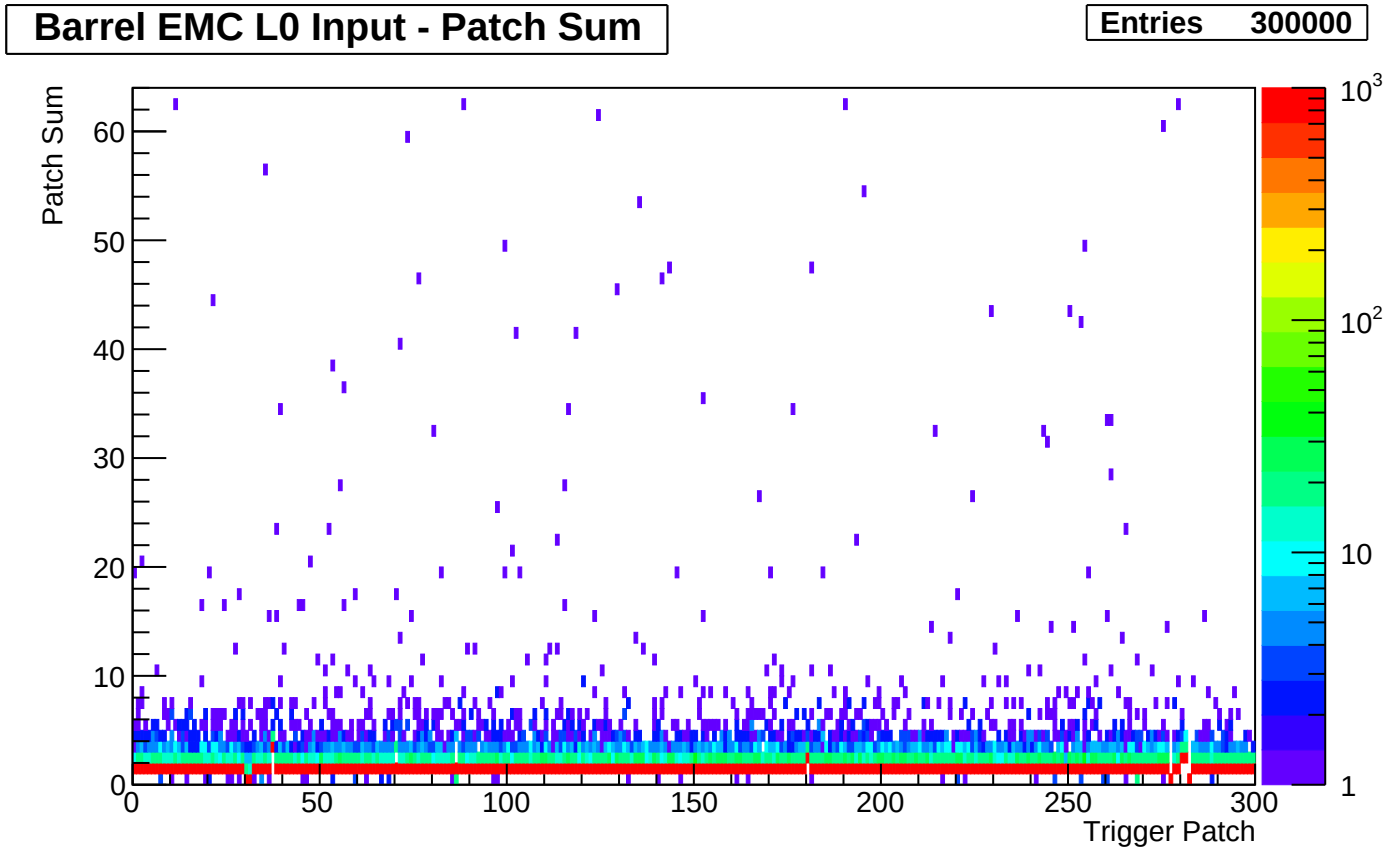
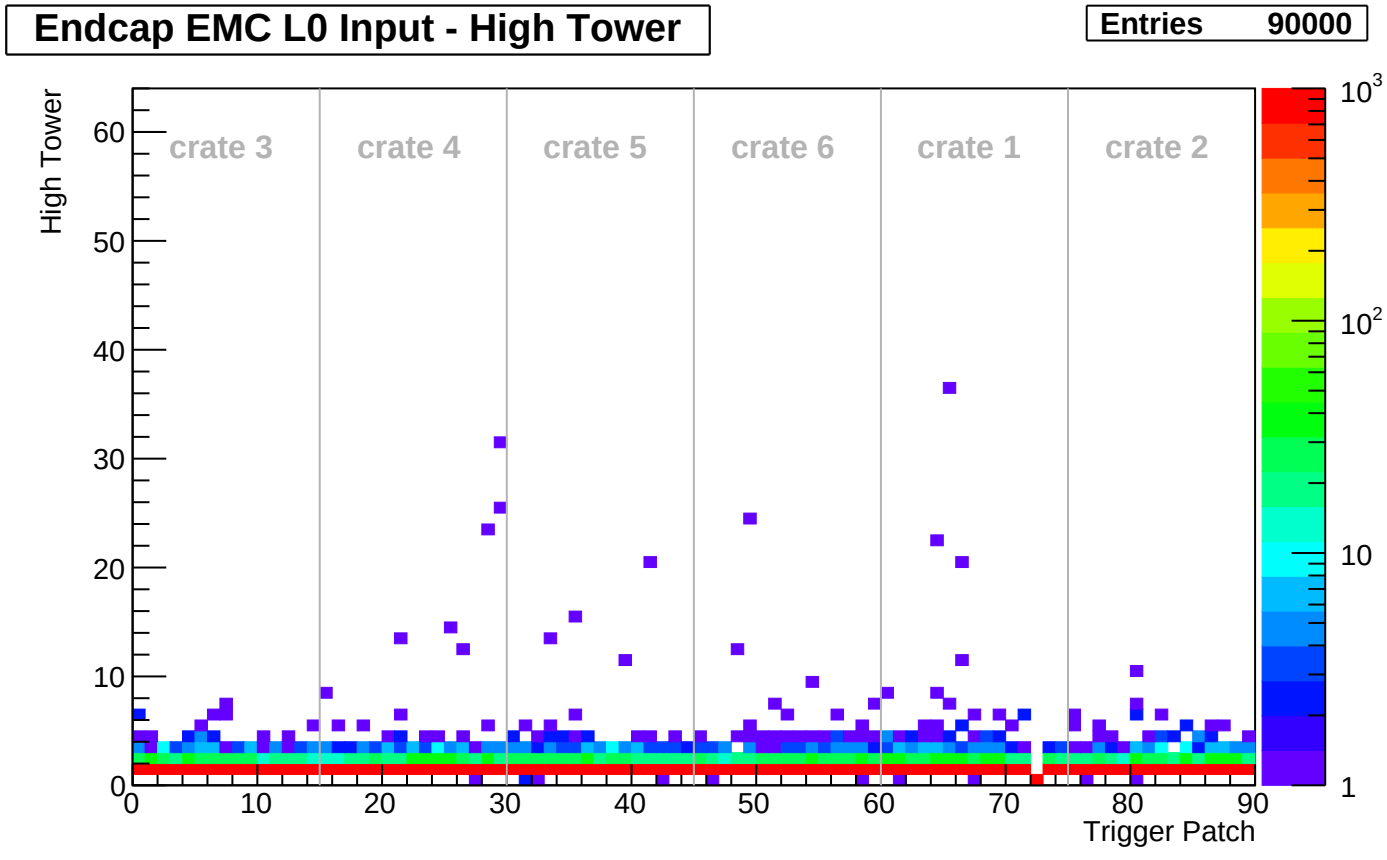
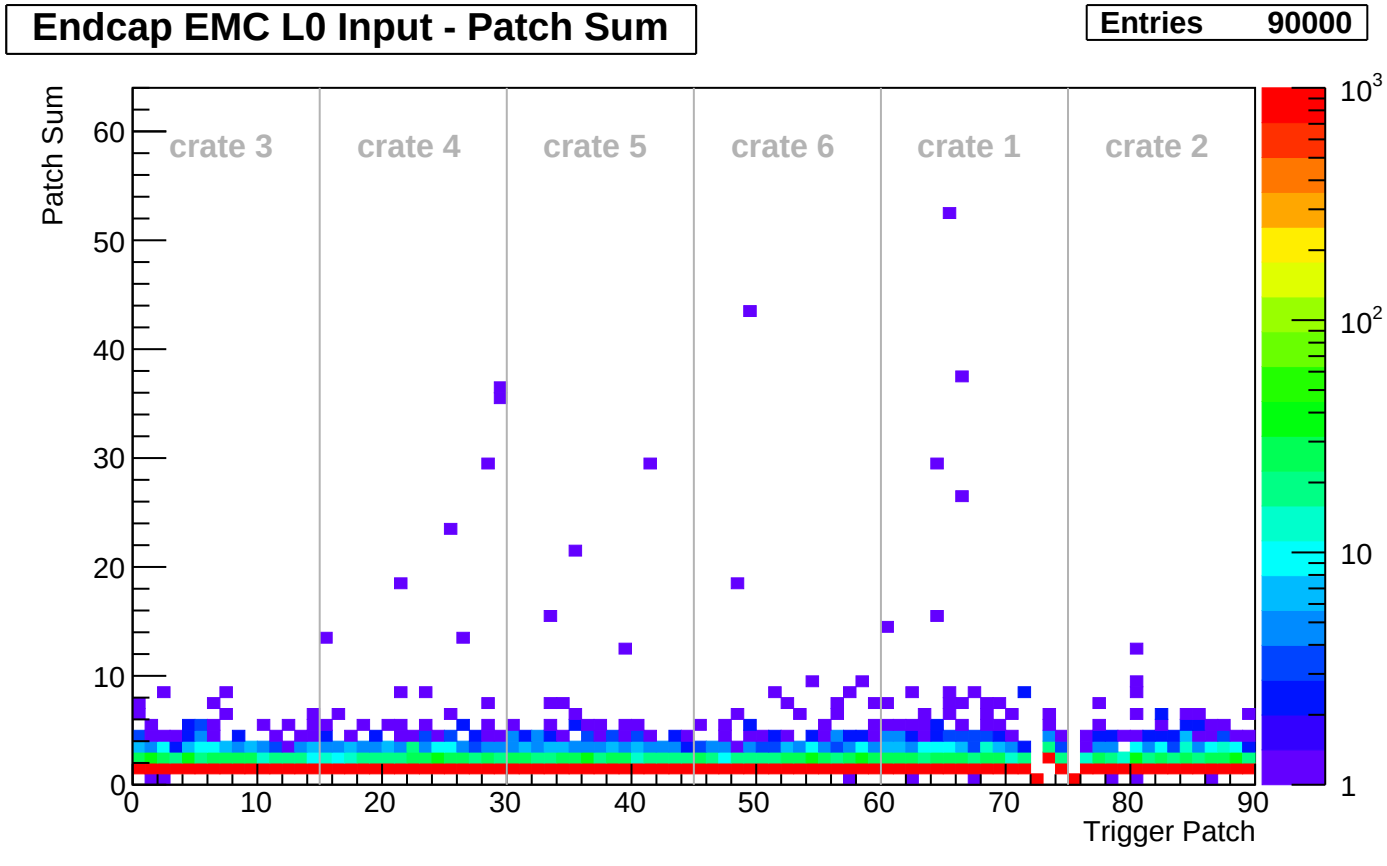
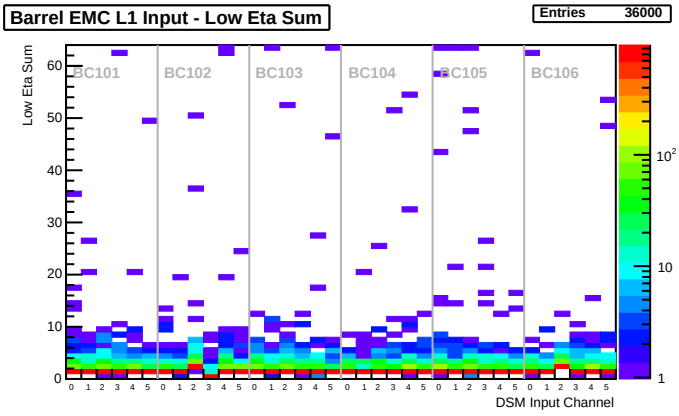
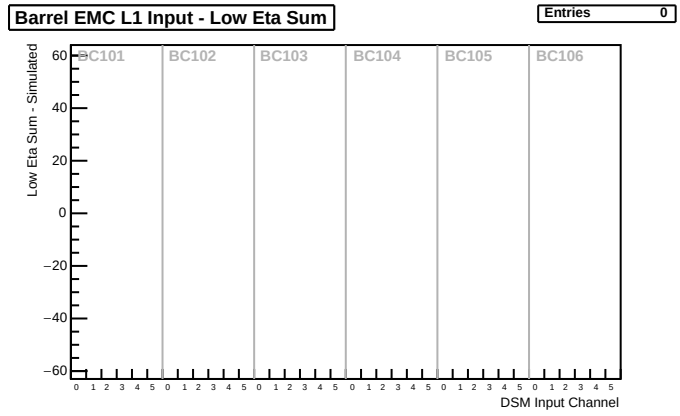

Barrel EMC L0 Input - High Tower
Entries 300000
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

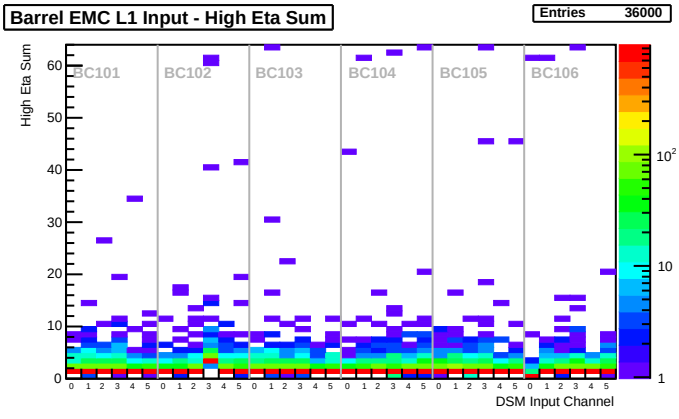

Barrel EMC L0 Input - Patch Sum
Entries 300000
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

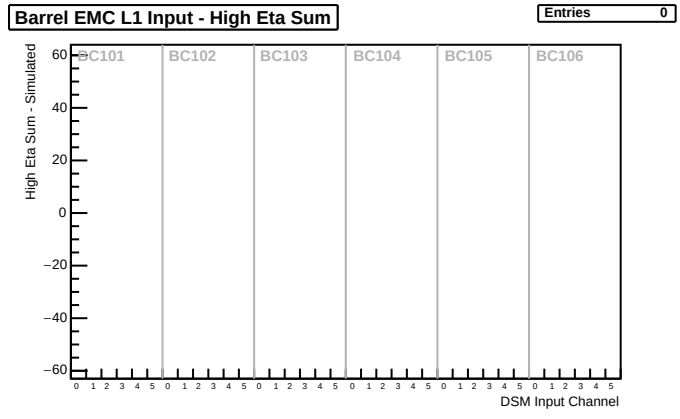

Endcap EMC L0 Input - High Tower
Entries 90000
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3

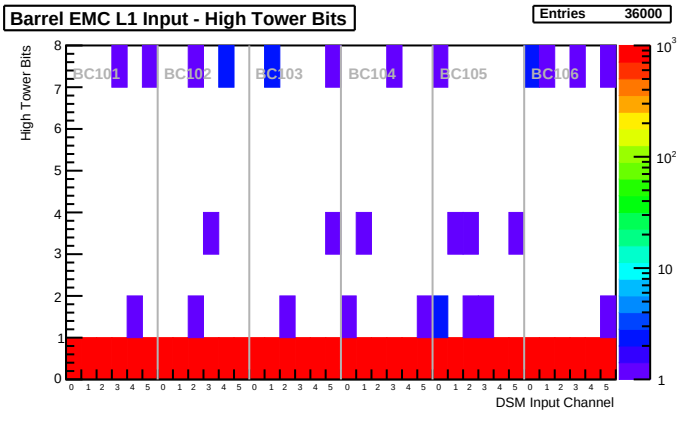

Endcap EMC L0 Input - Patch Sum
Entries 90000
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3

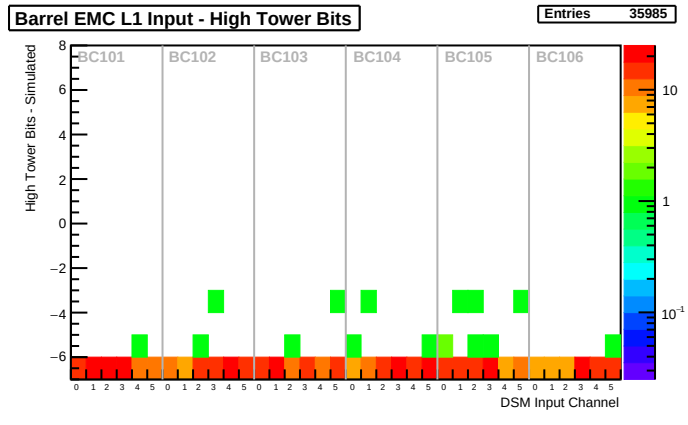

Barrel EMC L1 Input - Low Eta Sum
Entries 36000
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

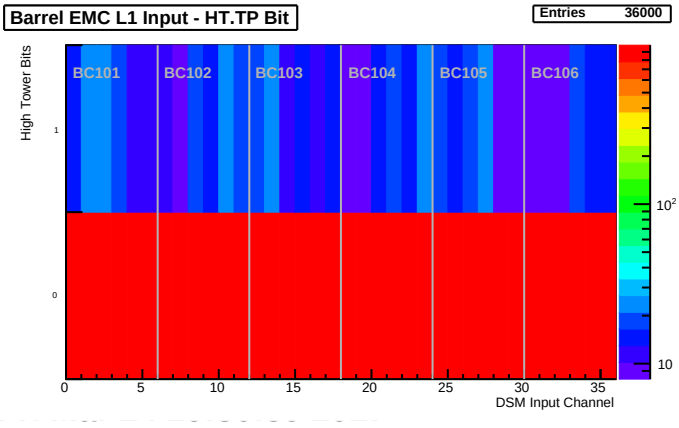

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

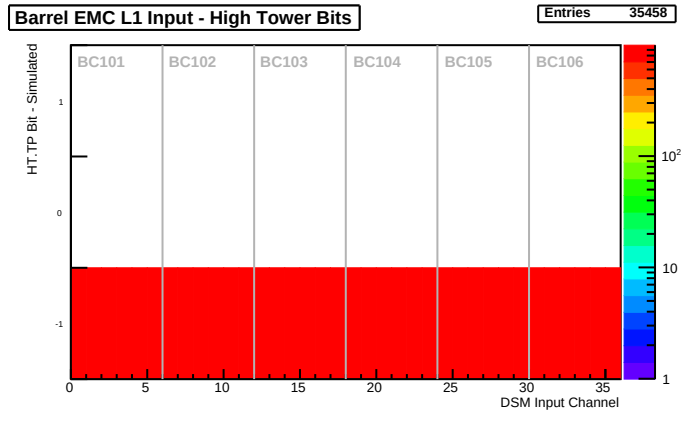

Barrel EMC L1 Input - High Eta Sum
Entries 36000
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

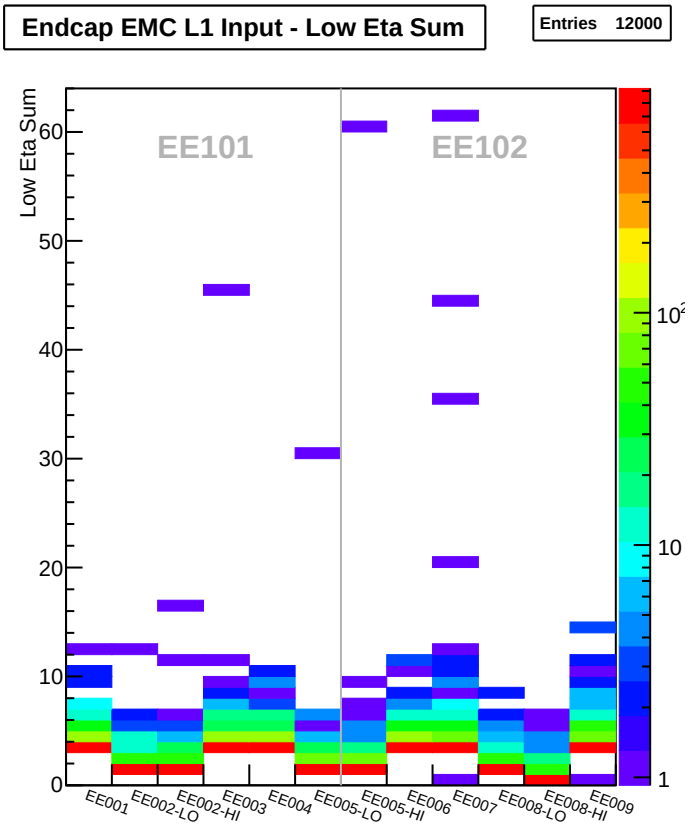

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

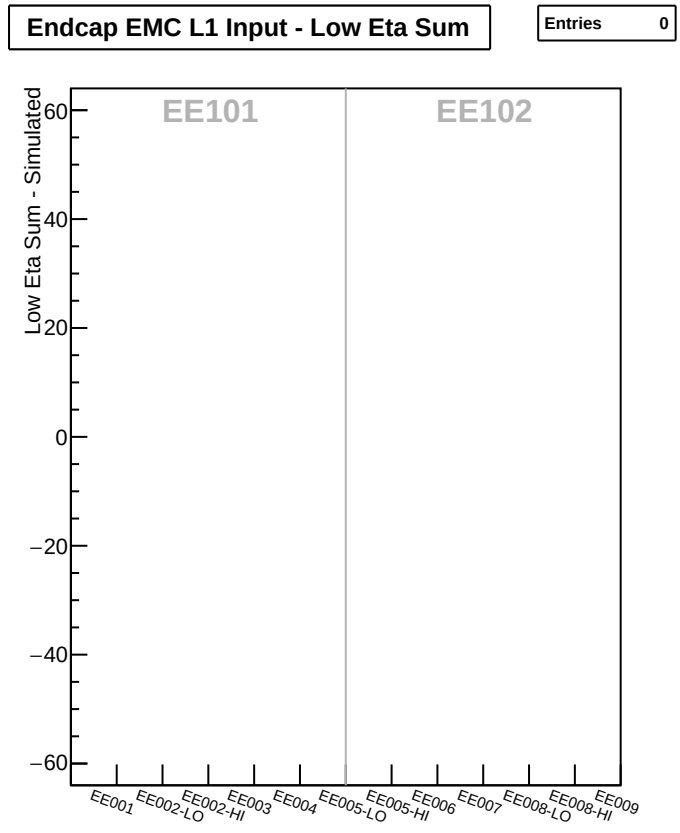

Barrel EMC L1 Input - High Tower Bits
Entries 36000
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

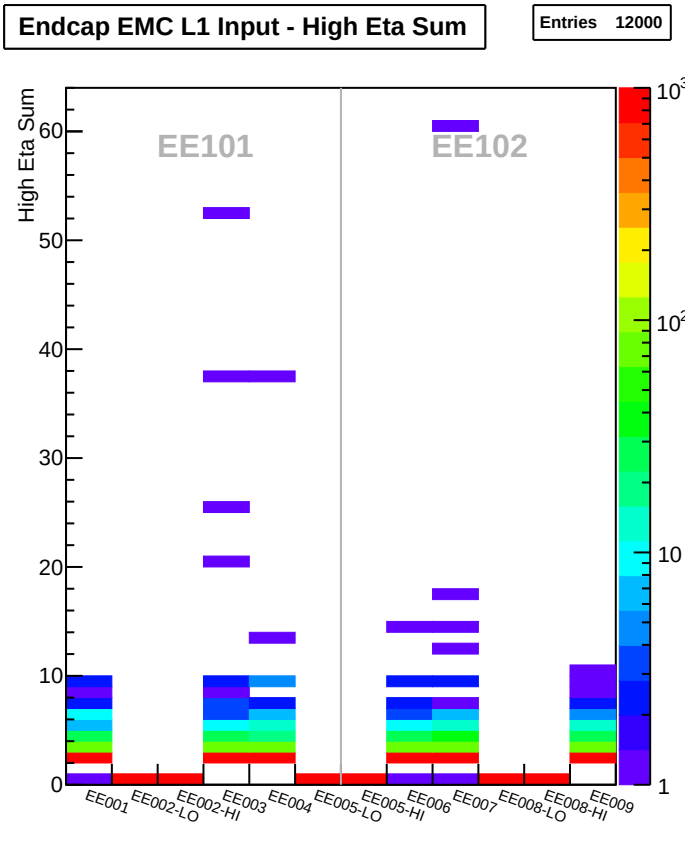

Barrel EMC L1 Input - High Tower Bits
Entries 35985
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

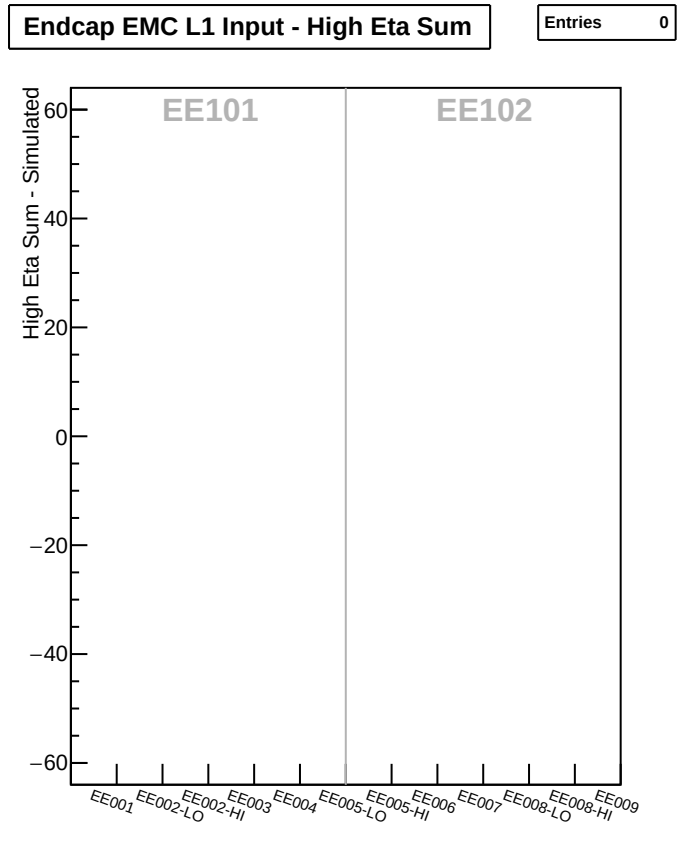

Barrel EMC L1 Input - HT.TP Bit
Entries 36000
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

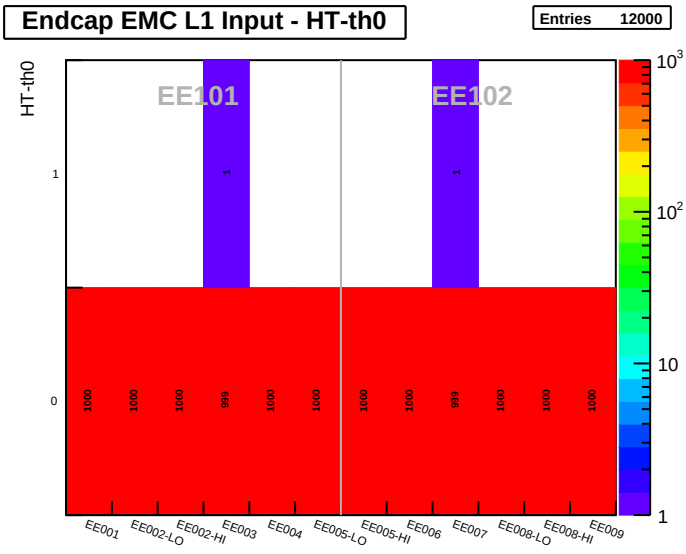

Barrel EMC L1 Input - High Tower Bits
Entries 35458
HT.TP Bit - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

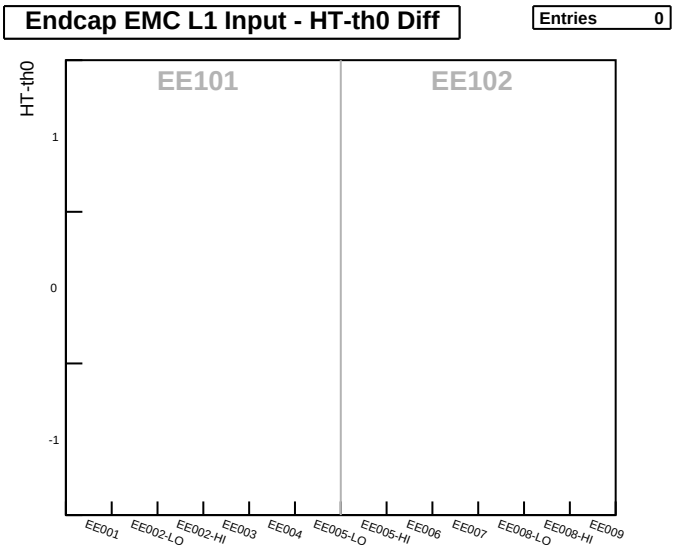

Endcap EMC L1 Input - Low Eta Sum
Entries 12000
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

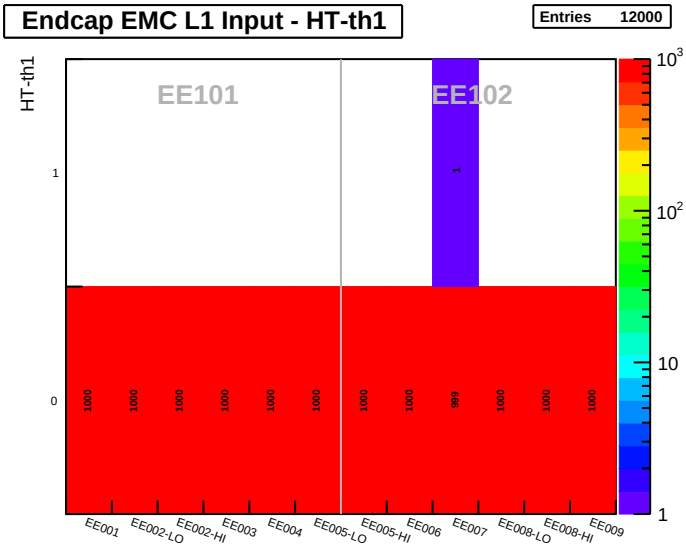

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

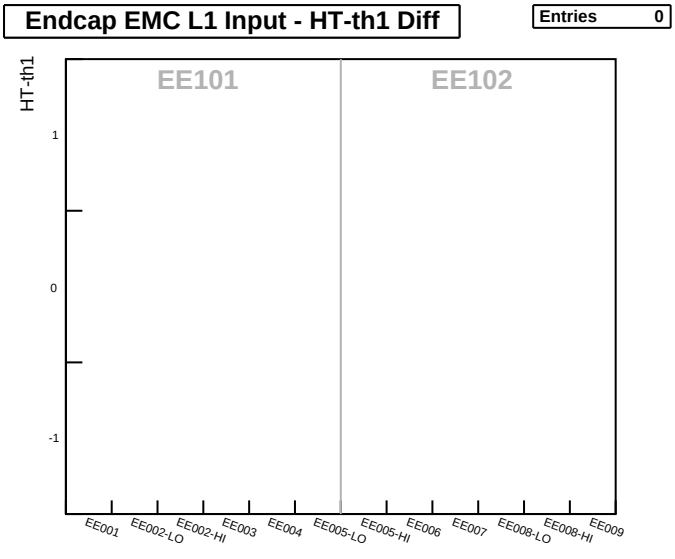

Endcap EMC L1 Input - High Eta Sum
Entries 12000
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

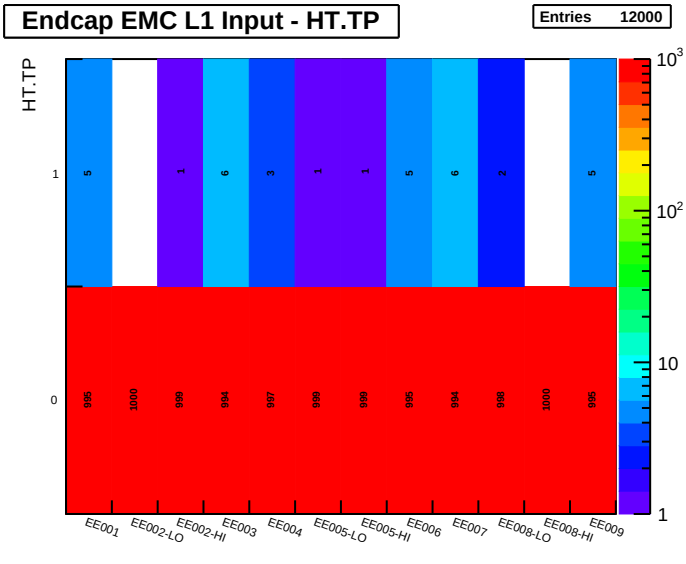

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

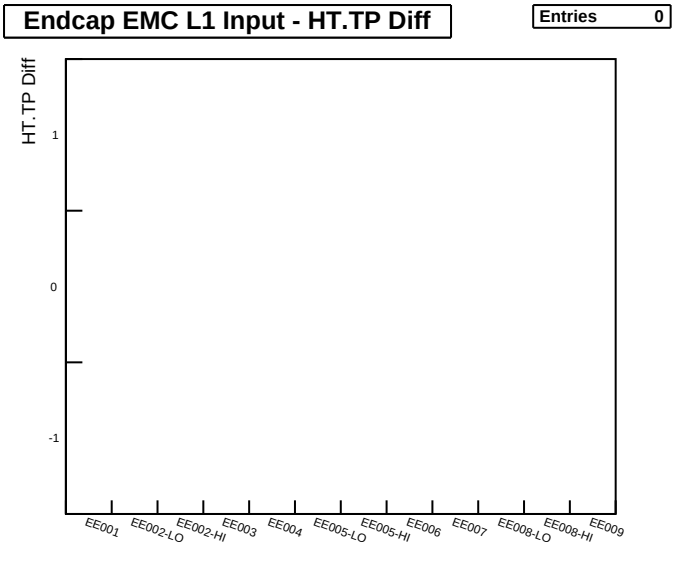

Endcap EMC L1 Input - HT-th0
Entries 12000
HT-th0
EE101
EE102
1
0
1000 1000 1000 999 1000 1000 1000 1000 999 1000 1000 1000
1 1
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009
10^3
10^2
10
1

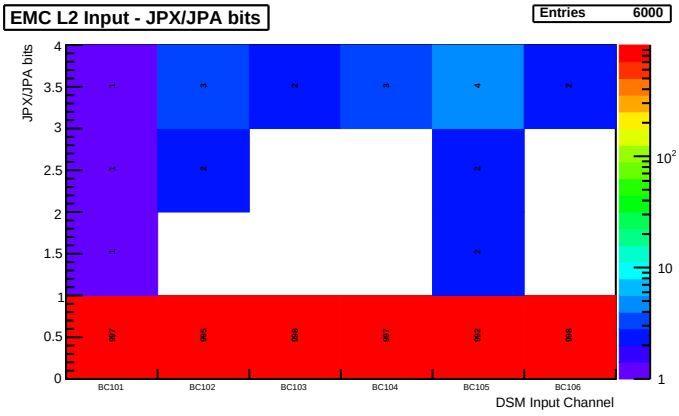

Endcap EMC L1 Input - HT-th0 Diff
Entries 0
HT-th0
EE101
EE102
1
0
-1
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

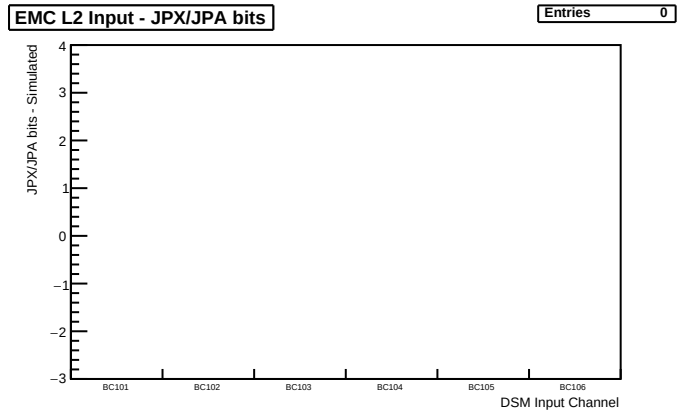

Endcap EMC L1 Input - HT-th1
Entries 12000
HT-th1
EE101
EE102
1
0
1000 1000 1000 1000 1000 1000 1000 1000 999 1000 1000 1000
1
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009
10^3
10^2
10
1

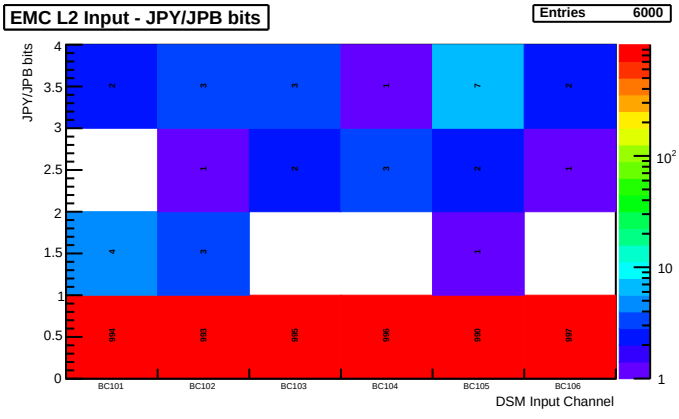

Endcap EMC L1 Input - HT-th1 Diff
Entries 0
HT-th1
EE101
EE102
1
0
-1
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

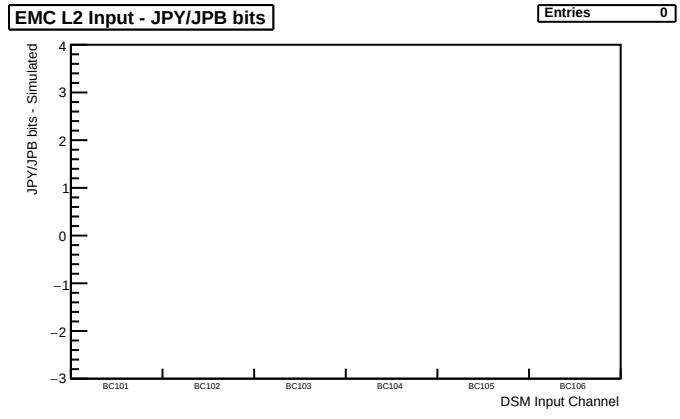

Endcap EMC L1 Input - HT.TP
Entries 12000
HT.TP
1
0
5 1 6 3 1 1 5 6 2 5
995 1000 999 994 997 999 999 995 994 998 1000 995
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009
10^3
10^2
10
1

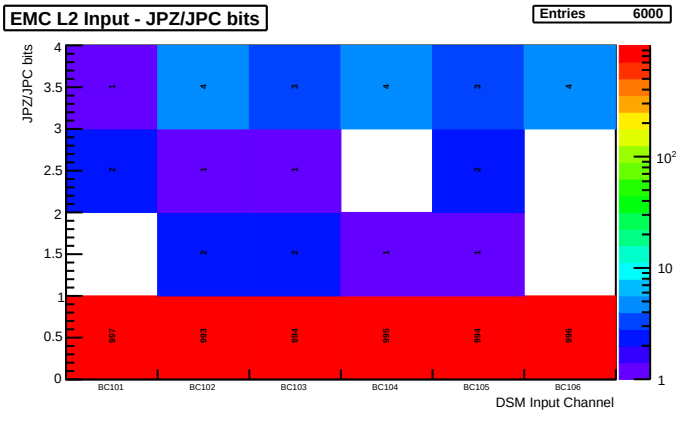

Endcap EMC L1 Input - HT.TP Diff
Entries 0
HT.TP Diff
1
0
-1
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

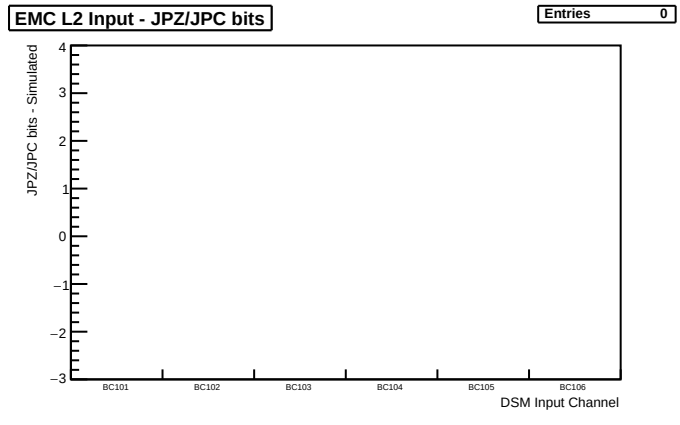

EMC L2 Input - JPX/JPA bits
Entries 6000
JPX/JPA bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

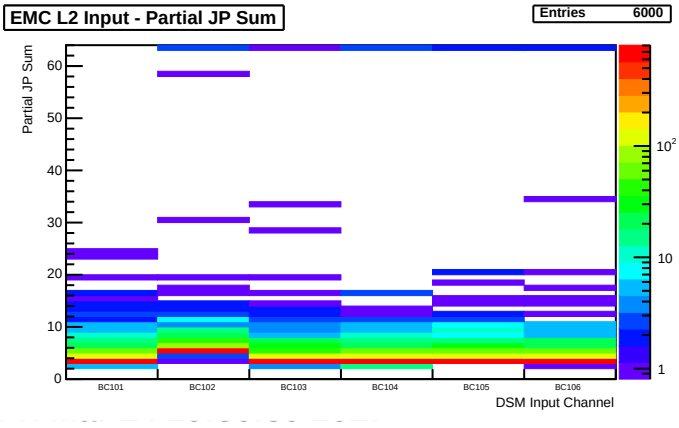

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

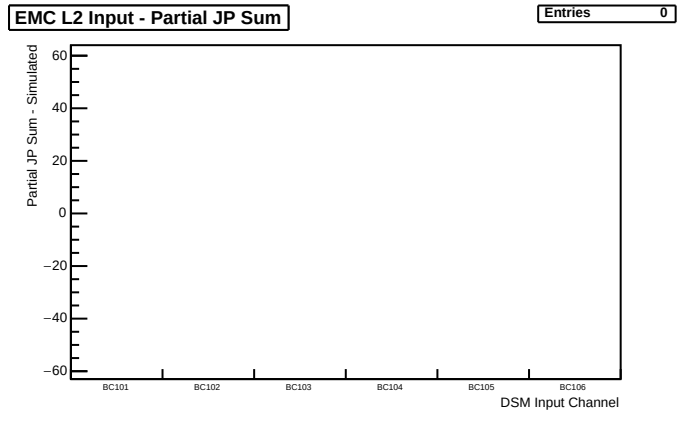

EMC L2 Input - JPY/JPB bits
Entries 6000
JPY/JPB bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

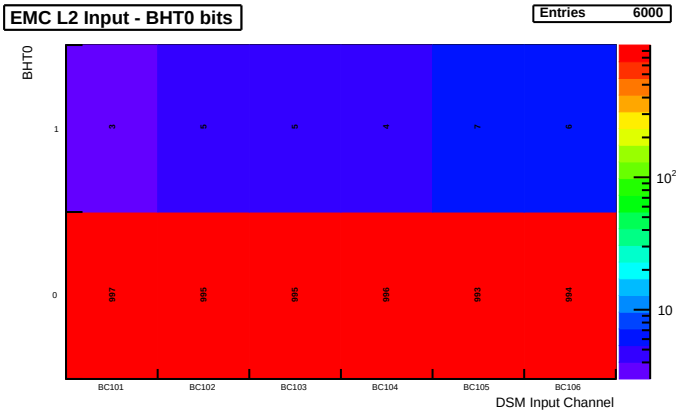

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

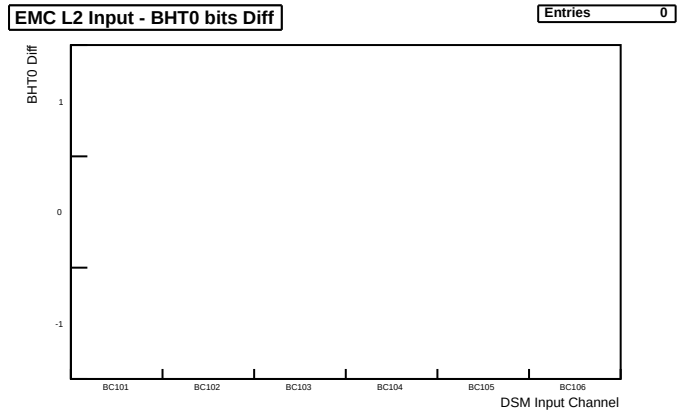

EMC L2 Input - JPZ/JPC bits
Entries 6000
JPZ/JPC bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

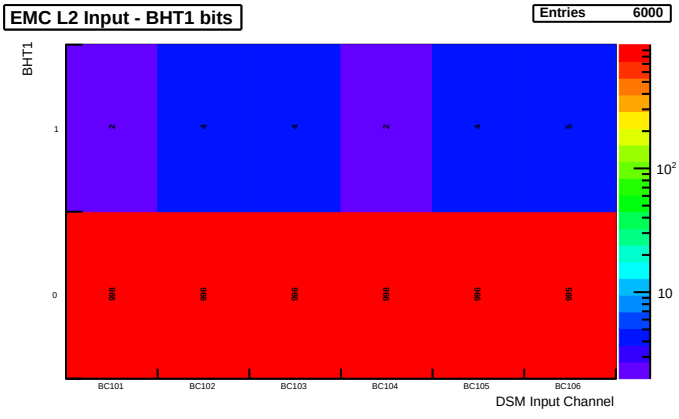

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

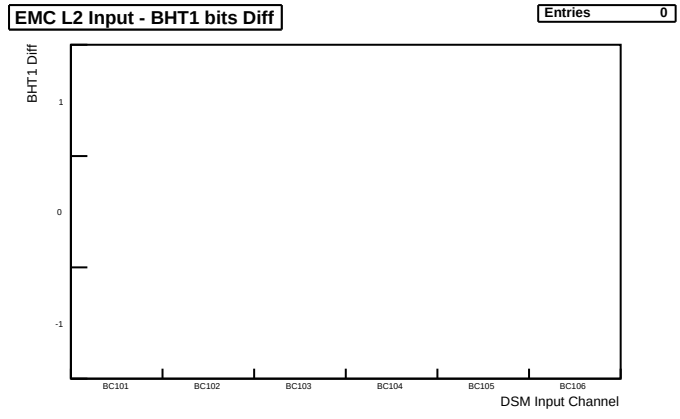

EMC L2 Input - Partial JP Sum
Entries 6000
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

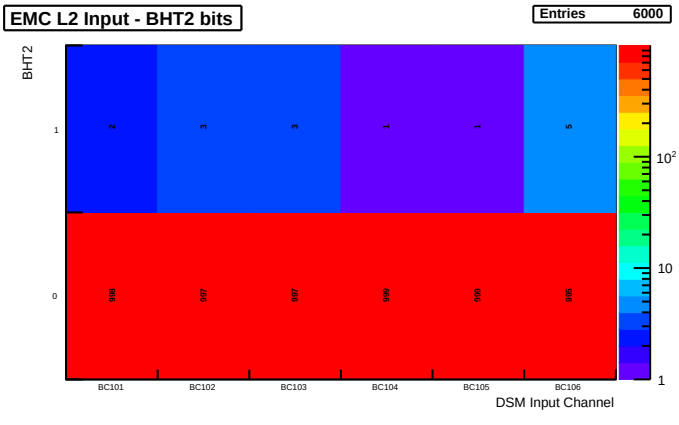

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

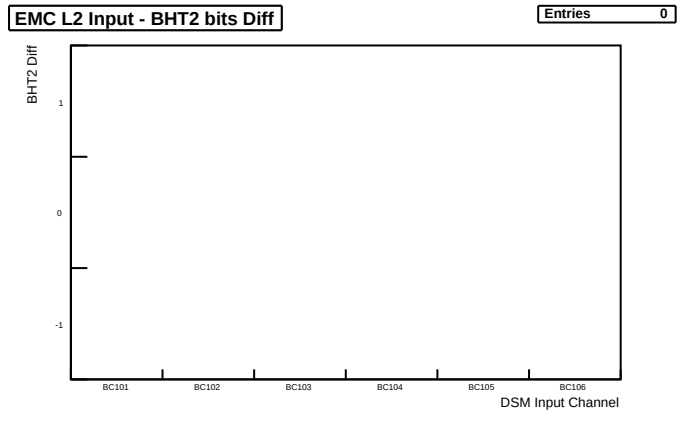

EMC L2 Input - BHT0 bits
Entries 6000
BHT0
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106
3 5 5 4 7 6
997 995 995 996 993 994

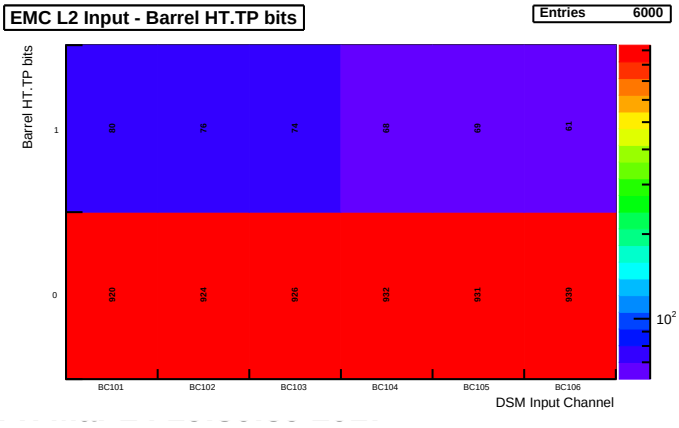

EMC L2 Input - BHT0 bits Diff
Entries 0
BHT0 Diff
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106

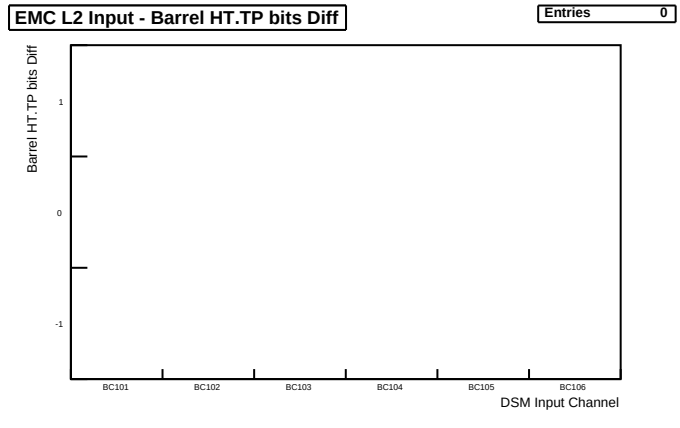

EMC L2 Input - BHT1 bits
Entries 6000
BHT1
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106
2 4 4 2 4 5
998 996 996 998 996 995

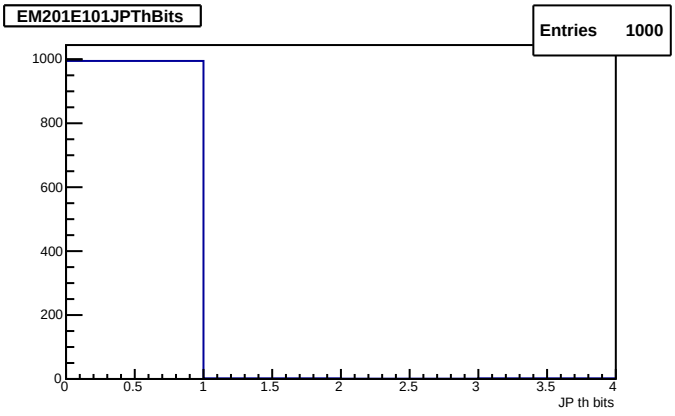

EMC L2 Input - BHT1 bits Diff
Entries 0
BHT1 Diff
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106

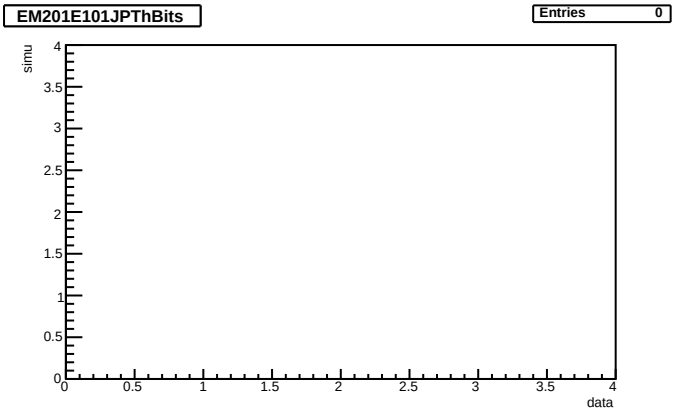

EMC L2 Input - BHT2 bits
Entries 6000
BHT2
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106
2 3 3 1 1 5
998 997 997 999 999 995

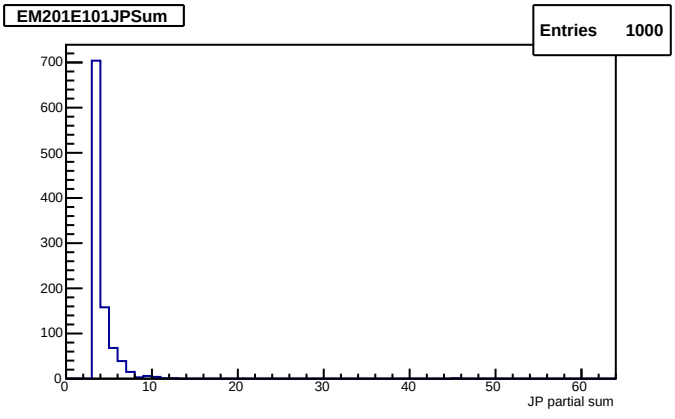

EMC L2 Input - BHT2 bits Diff
Entries 0
BHT2 Diff
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106

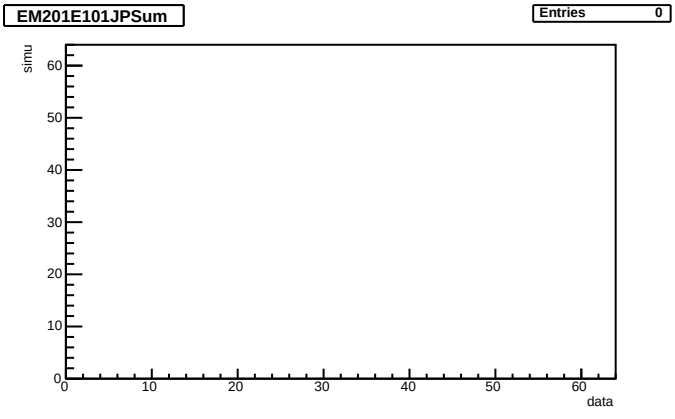

EMC L2 Input - Barrel HT.TP bits
Entries 6000
Barrel HT.TP bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106
80 76 74 68 69 61
920 924 926 932 931 939

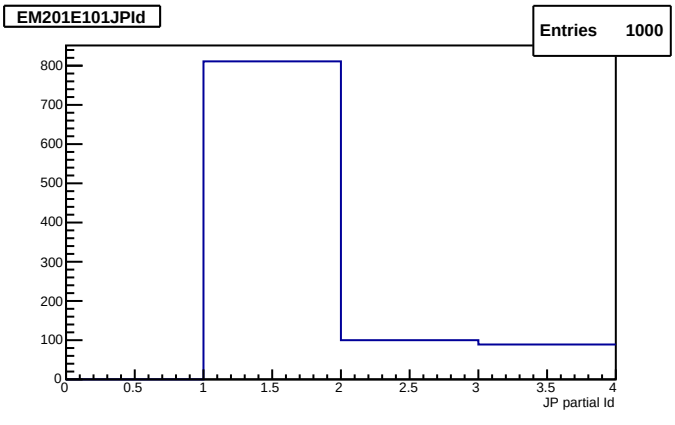

EMC L2 Input - Barrel HT.TP bits Diff
Entries 0
Barrel HT.TP bits Diff
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106

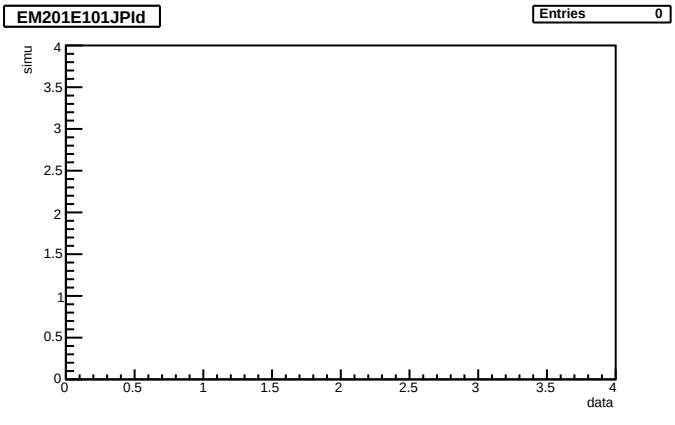

EM201E101JPThBits
Entries 1000
JP th bits

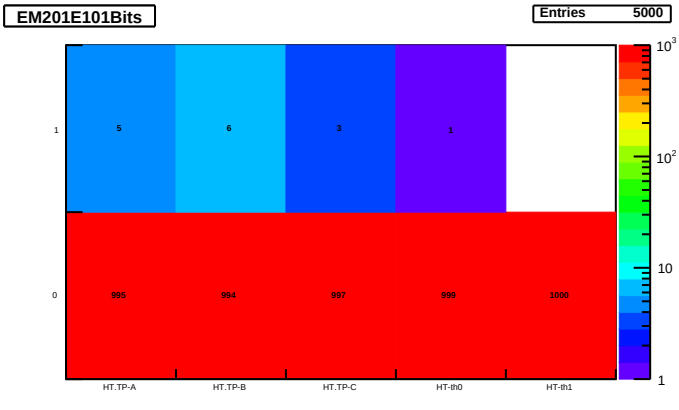

EM201E101JPThBits
Entries 0
simu
data

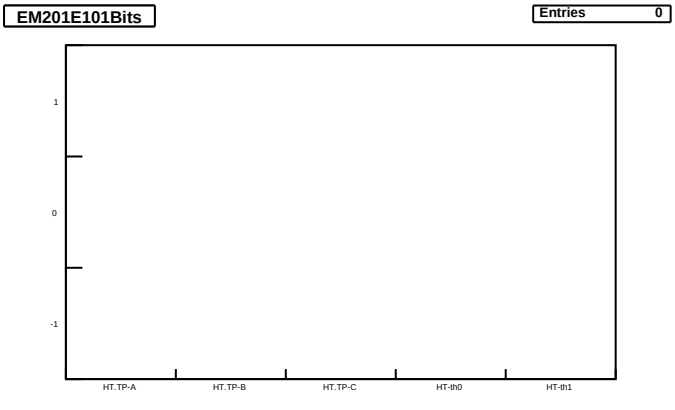

EM201E101JPSum
Entries 1000
JP partial sum

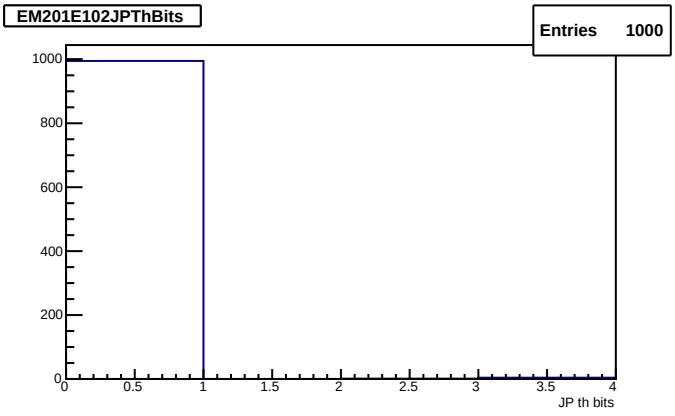

EM201E101JPSum
Entries 0
simu
data

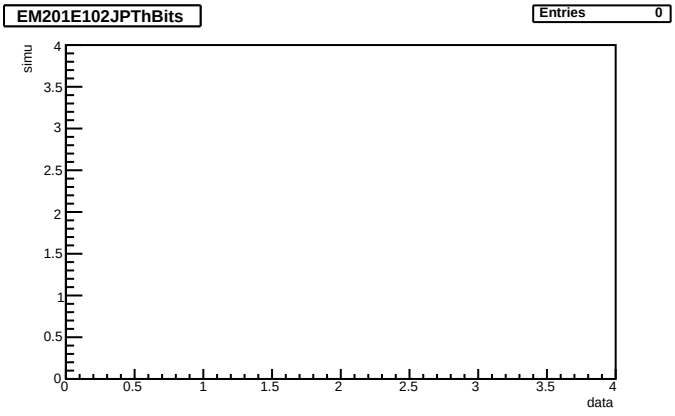

EM201E101JPId
Entries 1000
JP partial Id

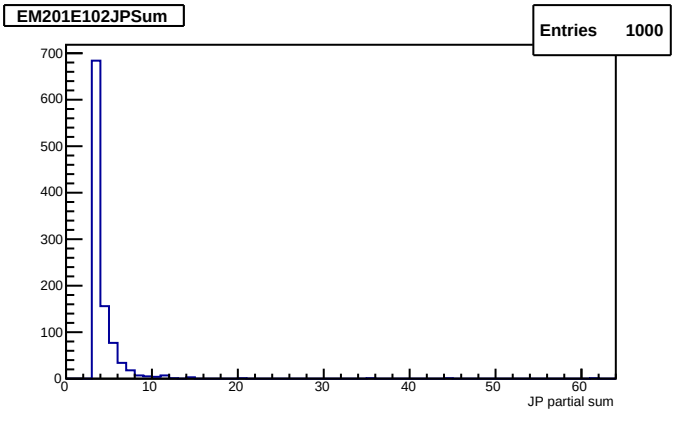

EM201E101JPId
Entries 0
simu
data

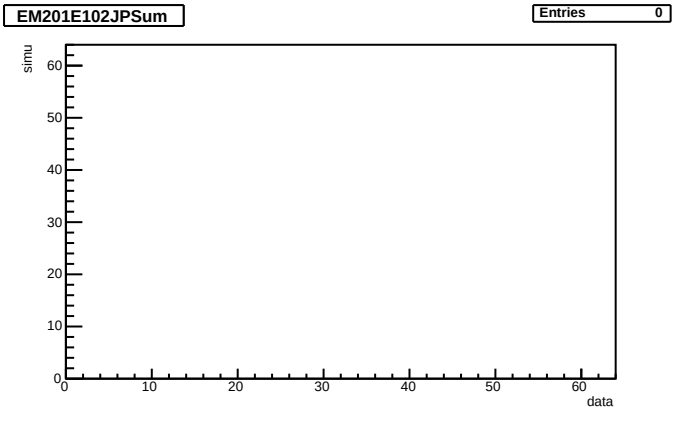

EM201E101Bits
Entries 5000
5
6
3
1
995
994
997
999
1000
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

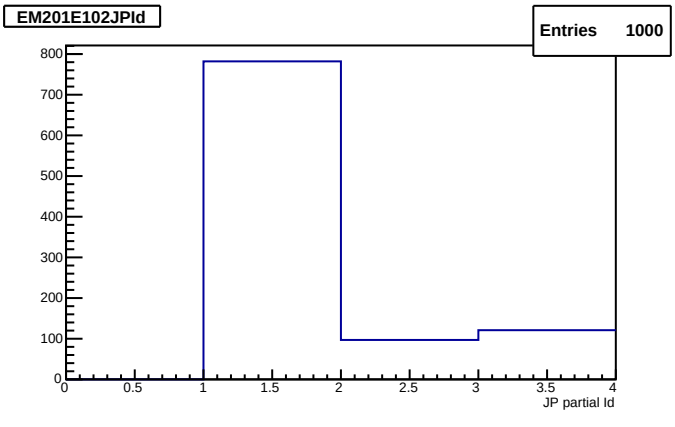

EM201E101Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

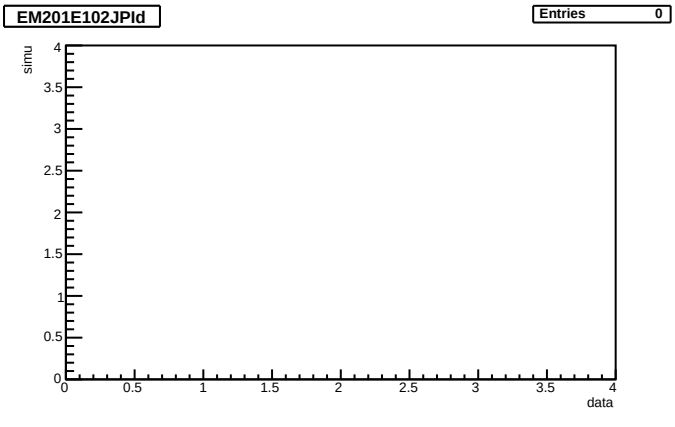

EM201E102JPThBits
Entries 1000
JP th bits

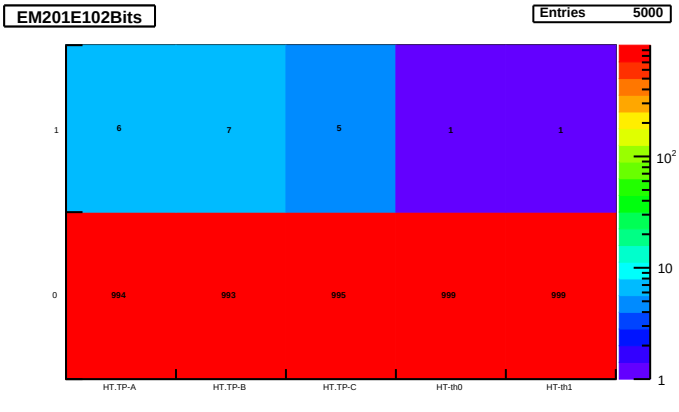

EM201E102JPThBits
Entries 0
simu
data

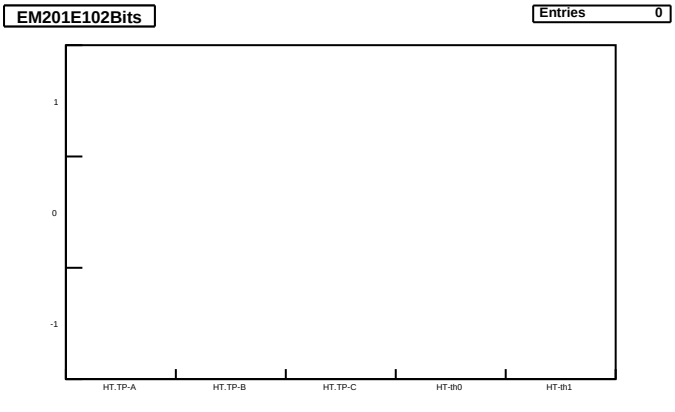

EM201E102JPSum
Entries 1000
JP partial sum

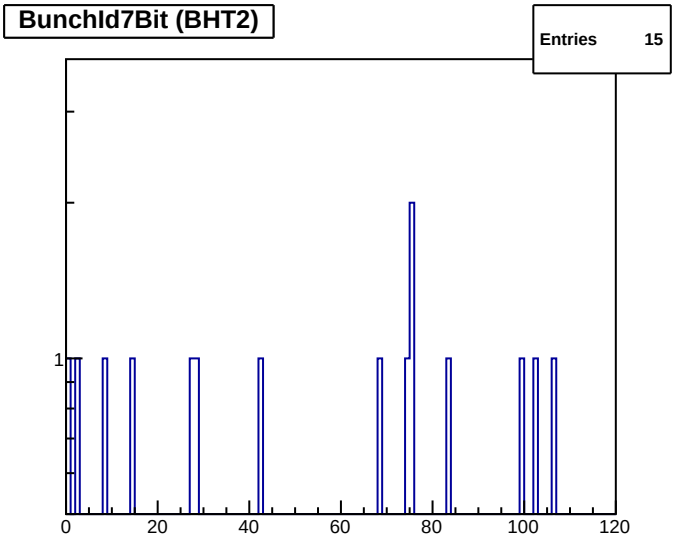

EM201E102JPSum
Entries 0
simu
data

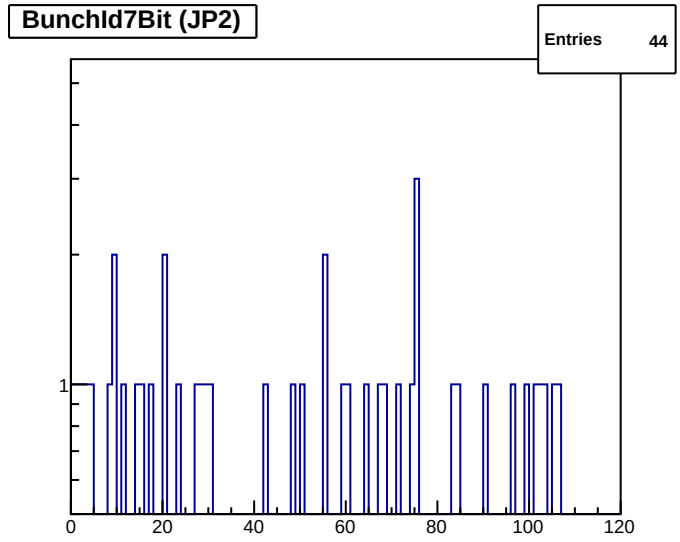

EM201E102JPId
Entries 1000
JP partial Id

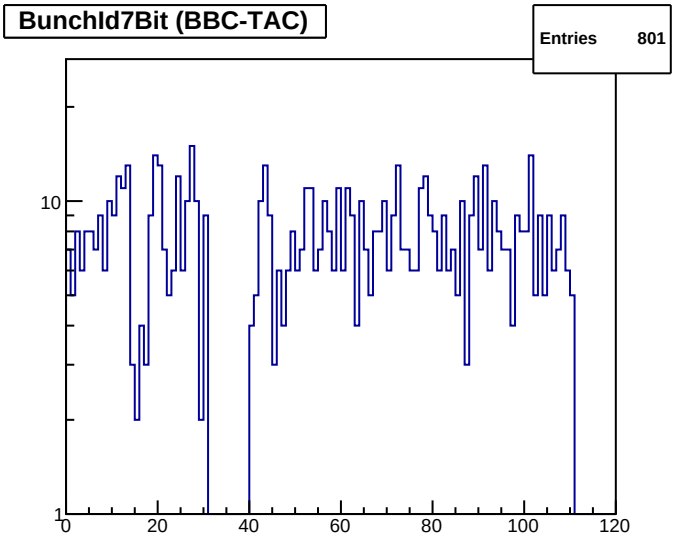

EM201E102JPId
Entries 0
simu
data

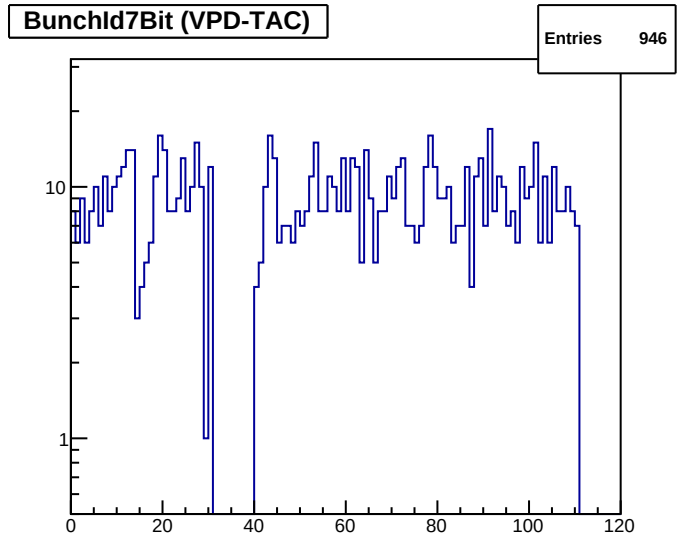

EM201E102Bits
Entries 5000
6
7
5
1
1
994
993
995
999
999
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

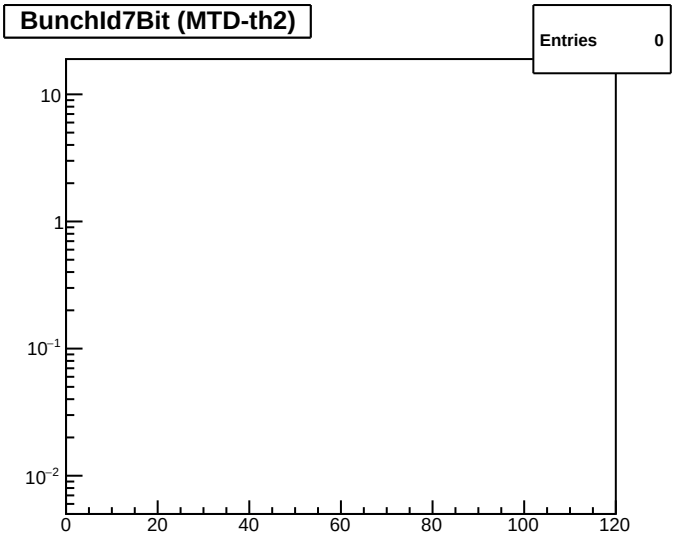

EM201E102Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

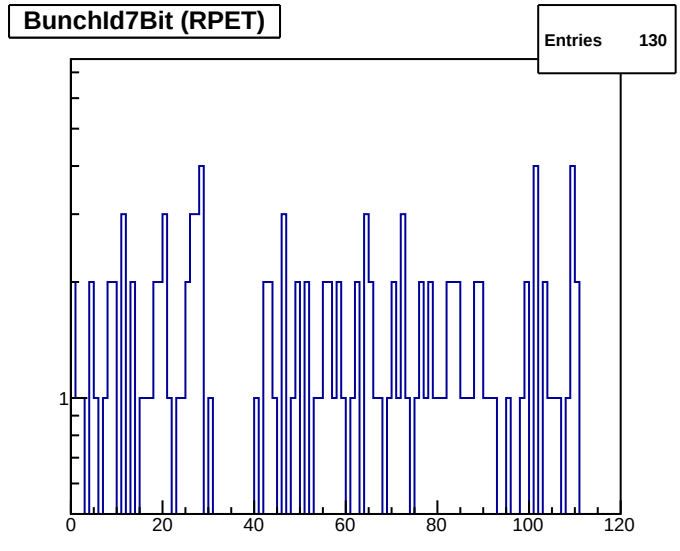

BunchId7Bit (BHT2)
Entries 15
1
0 20 40 60 80 100 120

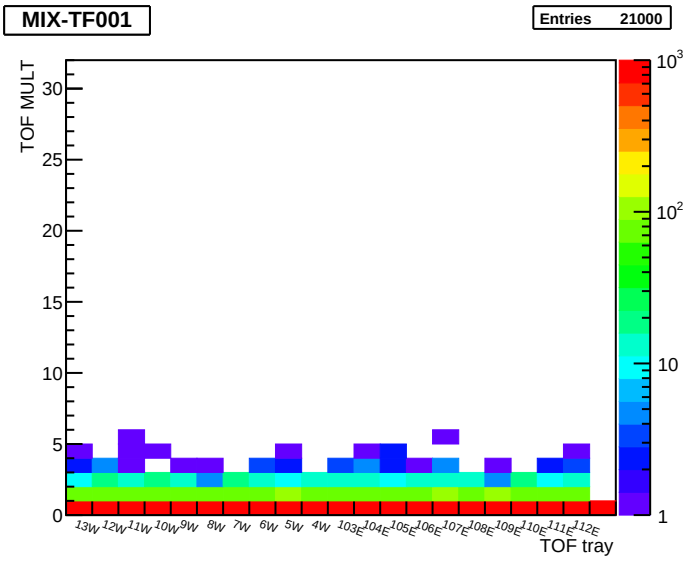

BunchId7Bit (JP2)
Entries 44
1
0 20 40 60 80 100 120

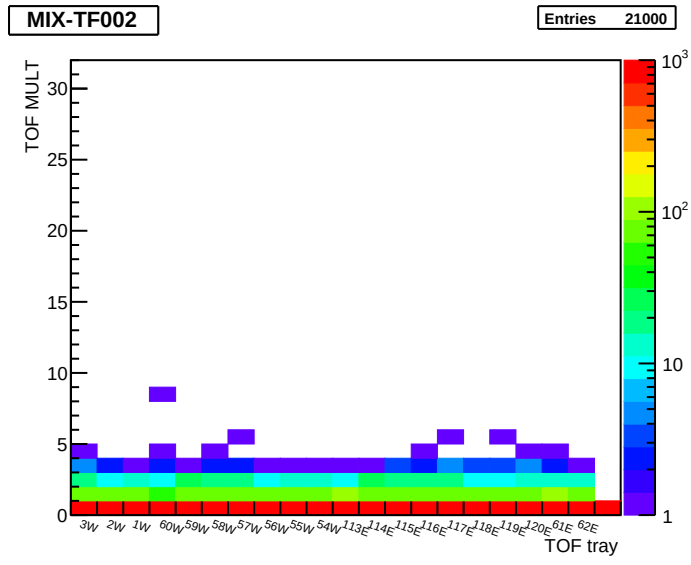

BunchId7Bit (BBC-TAC)
Entries 801
10
1
0 20 40 60 80 100 120

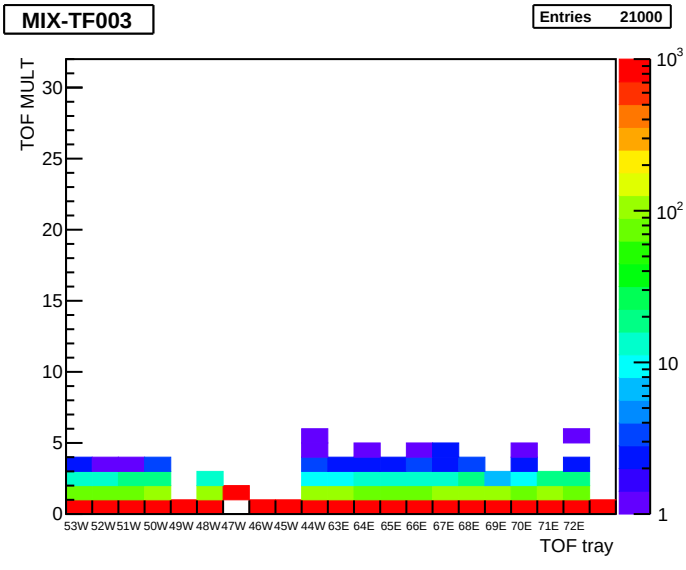

BunchId7Bit (VPD-TAC)
Entries 946
10
1
0 20 40 60 80 100 120

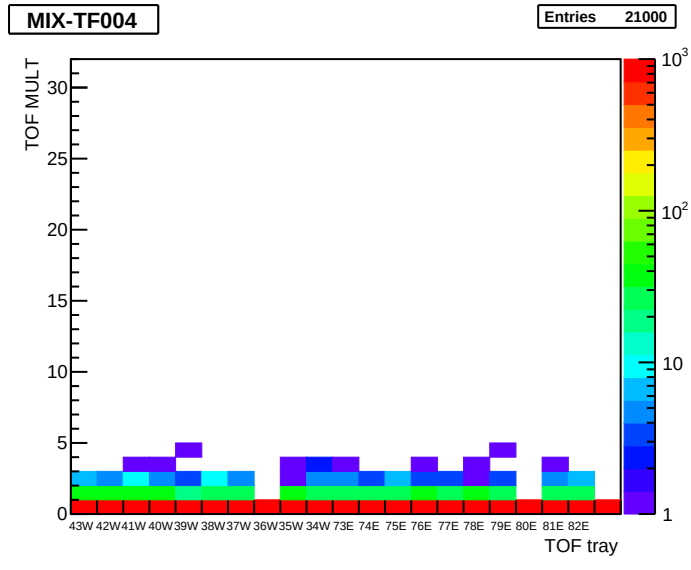

BunchId7Bit (MTD-th2)
Entries 0
10
1
10⁻¹
10⁻²
0 20 40 60 80 100 120

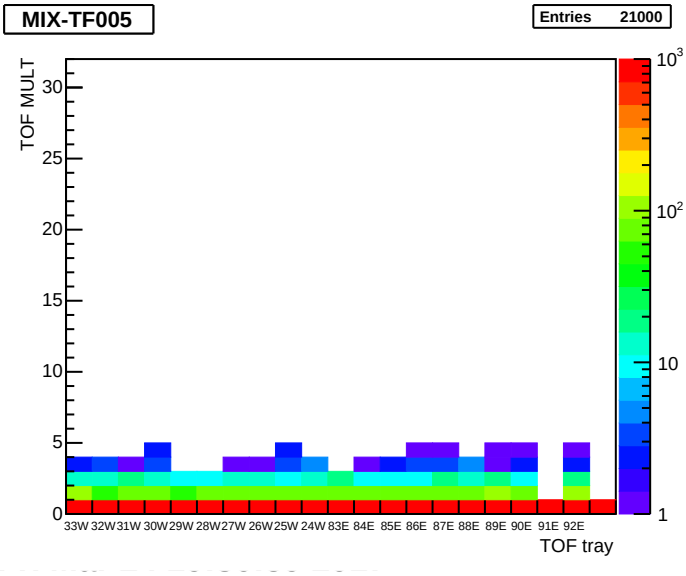

BunchId7Bit (RPET)
Entries 130
1
0 20 40 60 80 100 120

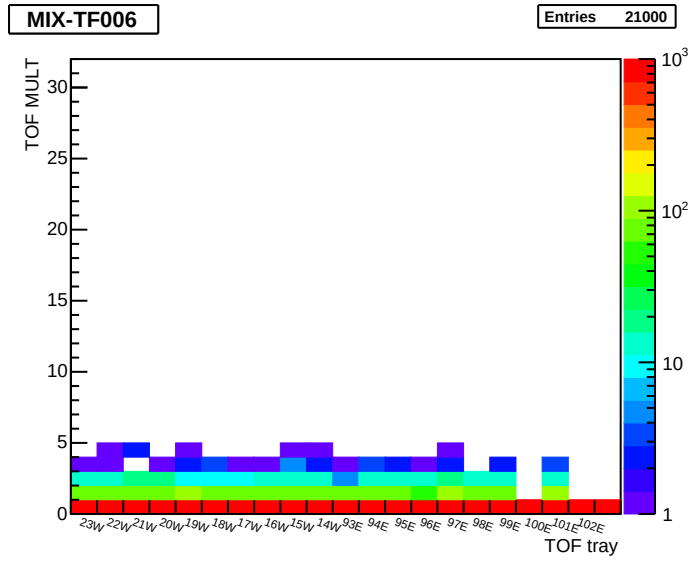

MIX-TF001
Entries 21000
TOF MULT
TOF tray

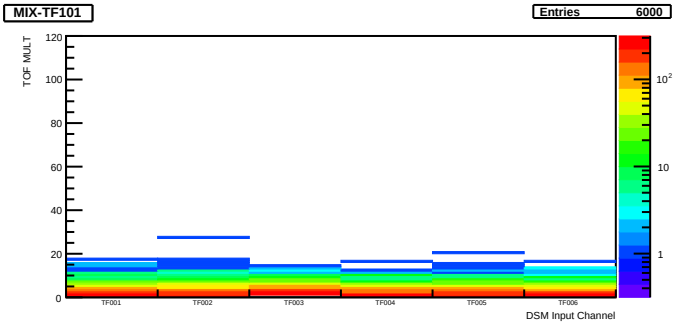

MIX-TF002
Entries 21000
TOF MULT
TOF tray

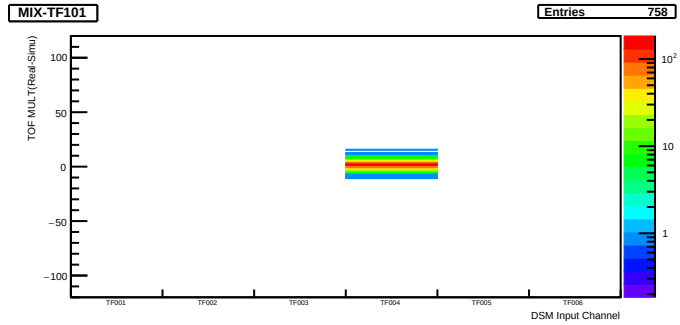

MIX-TF003
Entries 21000
TOF MULT
TOF tray

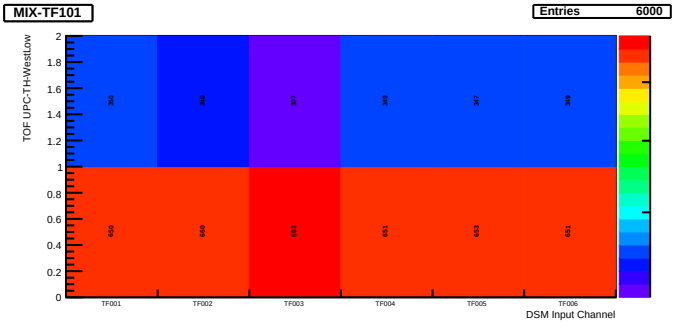

MIX-TF004
Entries 21000
TOF MULT
TOF tray

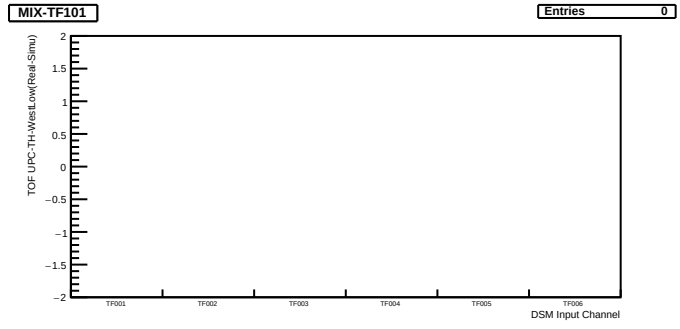

MIX-TF005
Entries 21000
TOF MULT
TOF tray

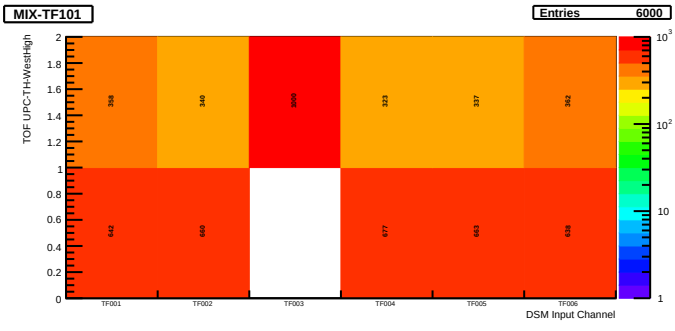

MIX-TF006
Entries 21000
TOF MULT
TOF tray

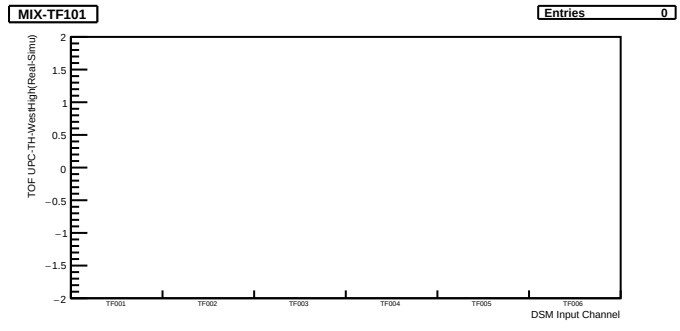

MIX-TF101
Entries 6000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

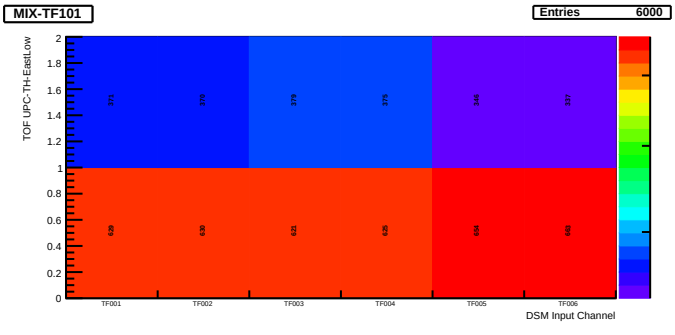

MIX-TF101
Entries 758
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

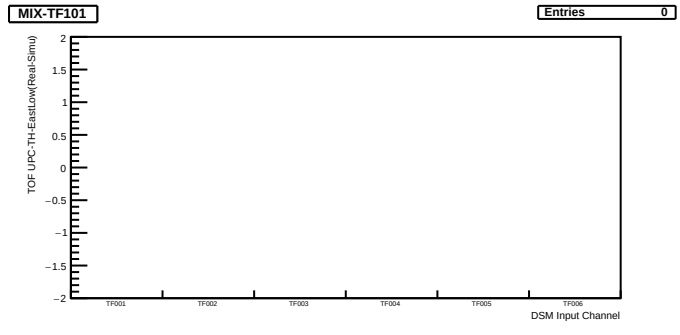

MIX-TF101
Entries 6000
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

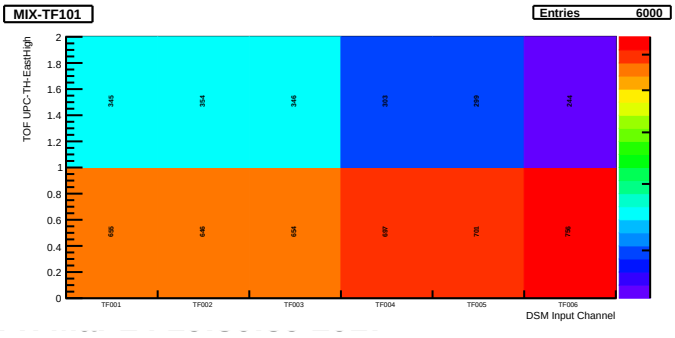

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

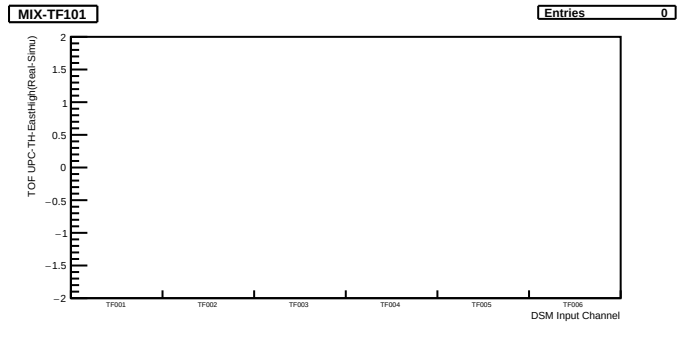

MIX-TF101
Entries 6000
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

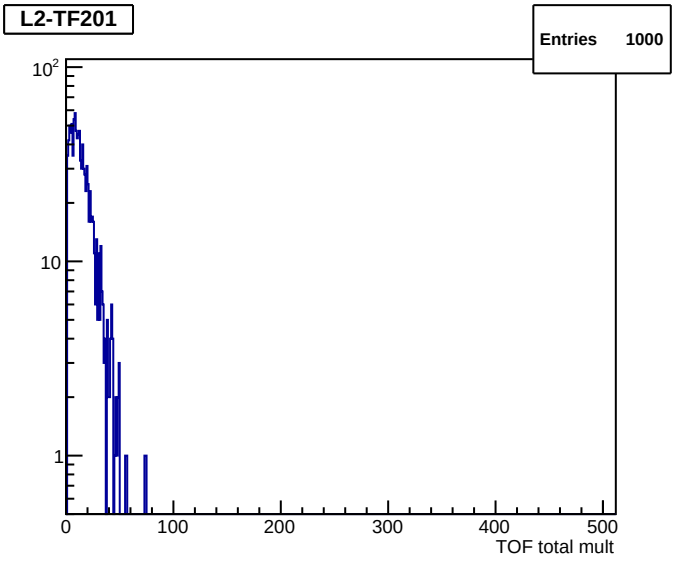

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

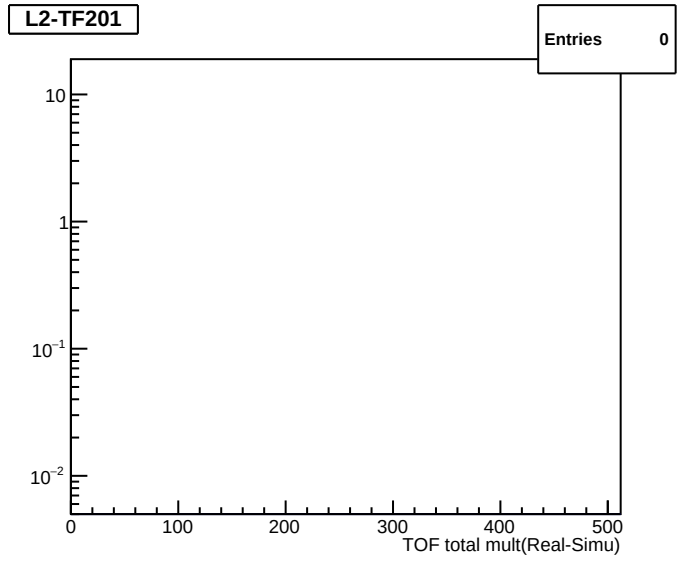

MIX-TF101
Entries 6000
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

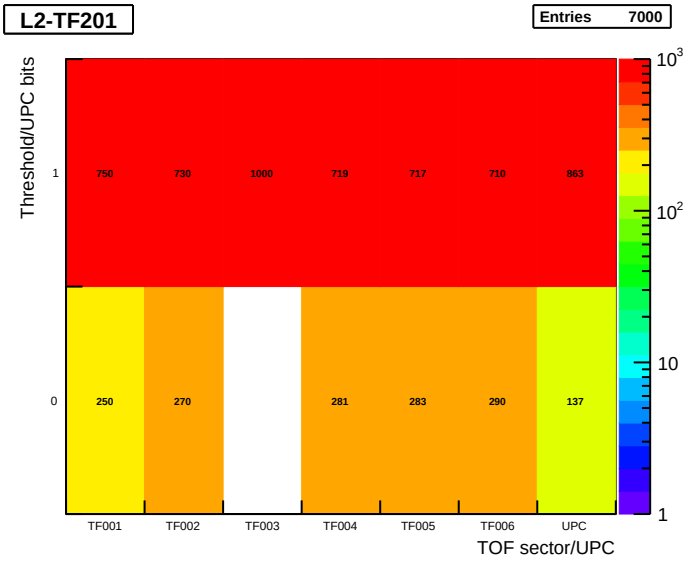

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

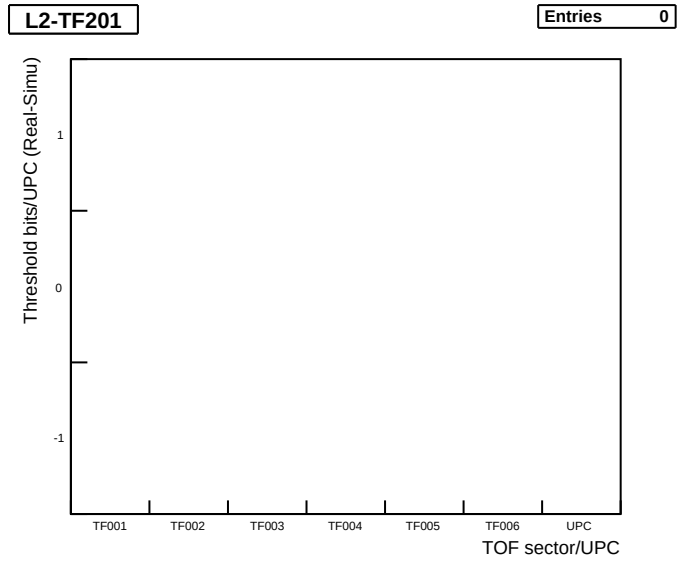

MIX-TF101
Entries 6000
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

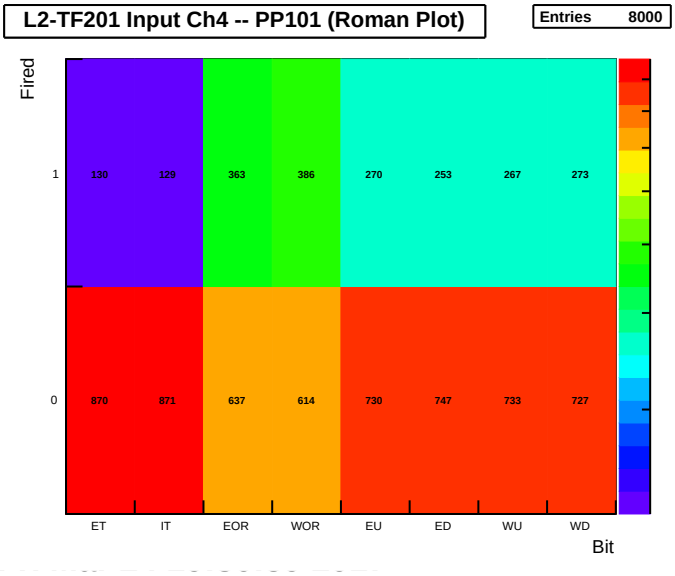

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

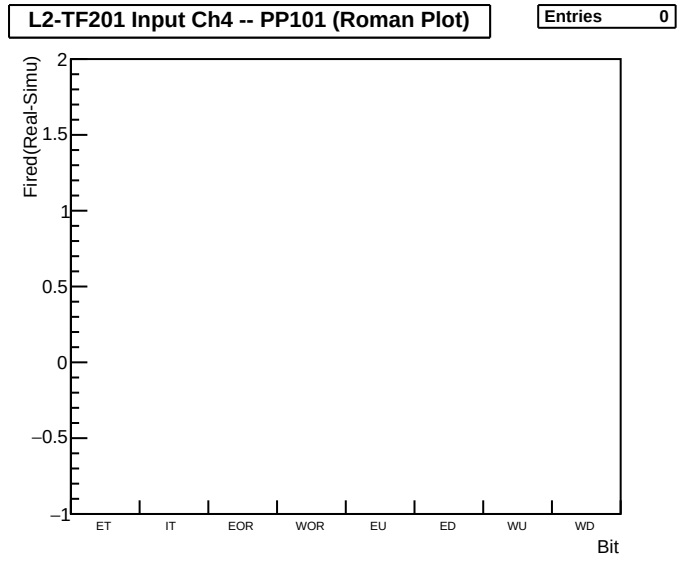

L2-TF201
Entries 1000
TOF total mult

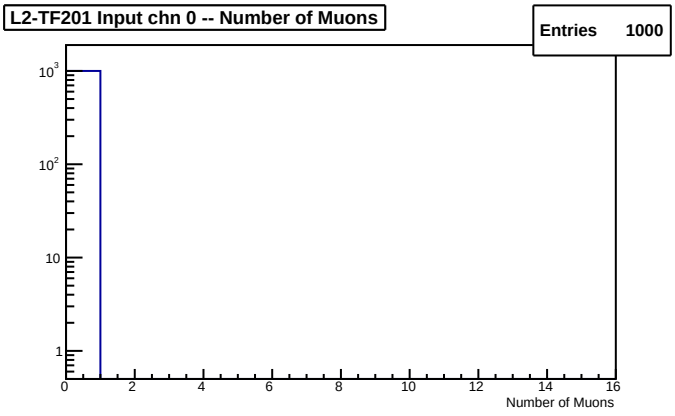

L2-TF201
Entries 0
TOF total mult(Real-Simu)

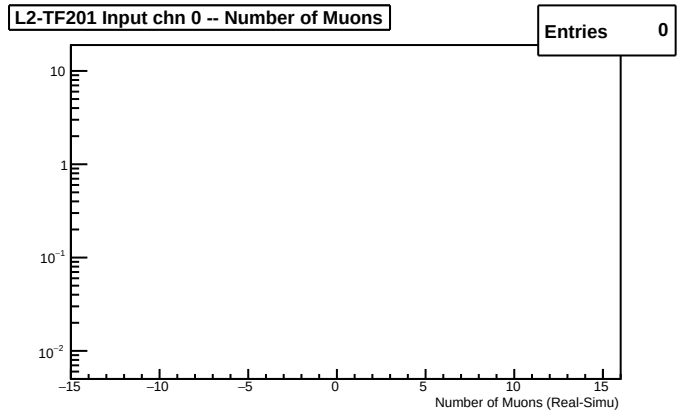

L2-TF201
Entries 7000
Threshold/UPC bits
750 730 1000 719 717 710 863
250 270 281 283 290 137
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

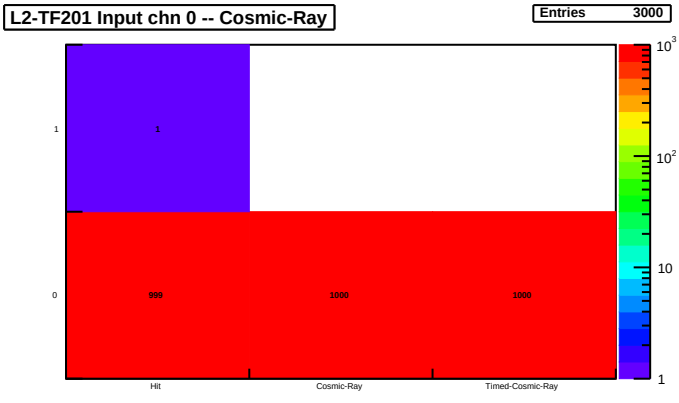

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

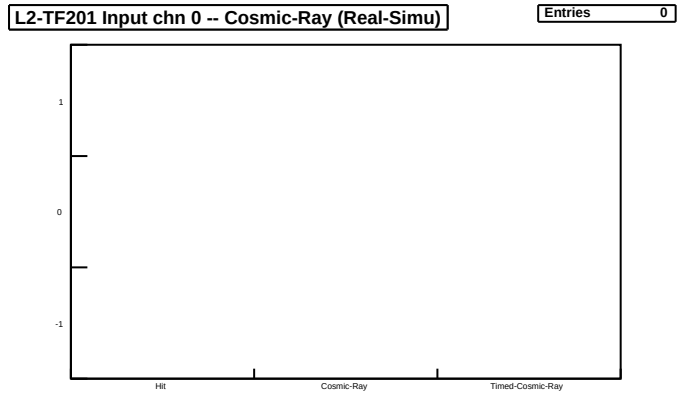

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 8000
Fired
130 129 363 386 270 253 267 273
870 871 637 614 730 747 733 727
ET IT EOR WOR EU ED WU WD
Bit

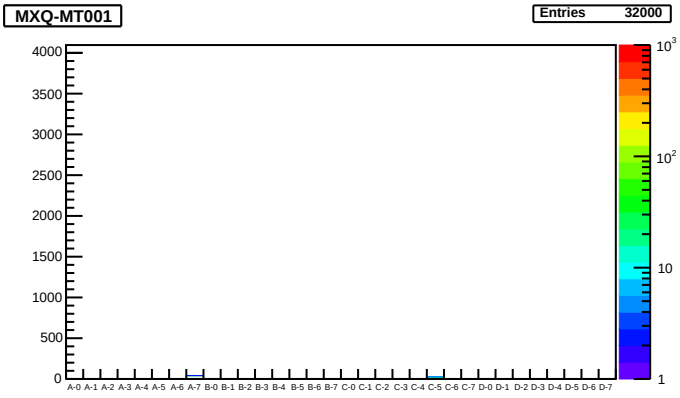

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 0
Fired(Real-Simu)
ET IT EOR WOR EU ED WU WD
Bit

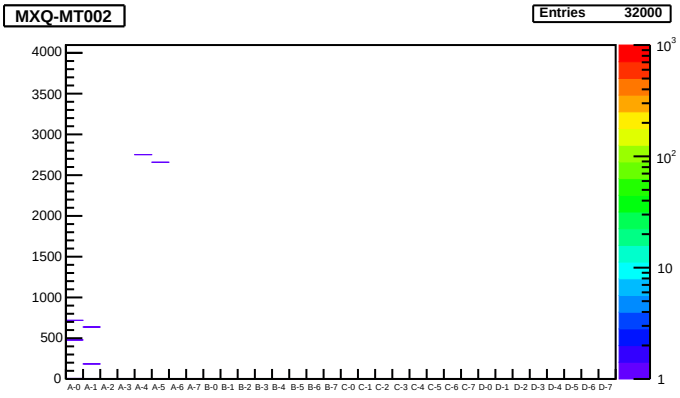

L2-TF201 Input chn 0 -- Number of Muons
Entries 1000
Number of Muons

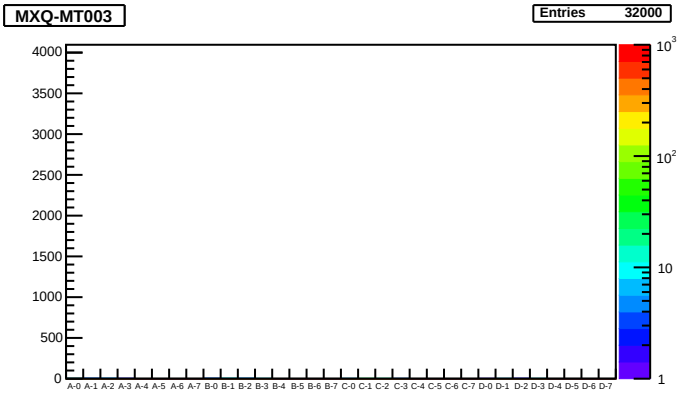

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

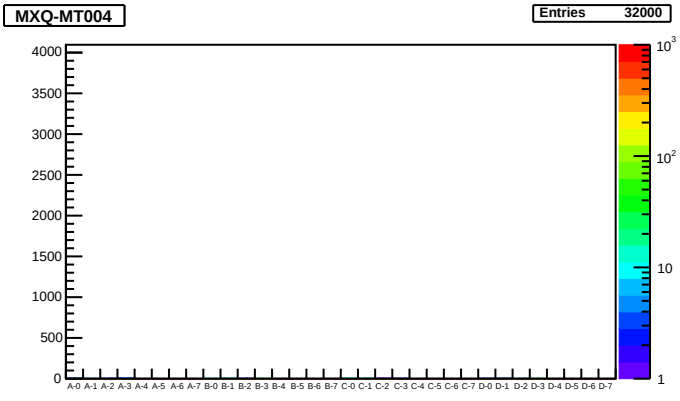

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 3000
1
999
1000
1000
Hit
Cosmic-Ray
Timed-Cosmic-Ray

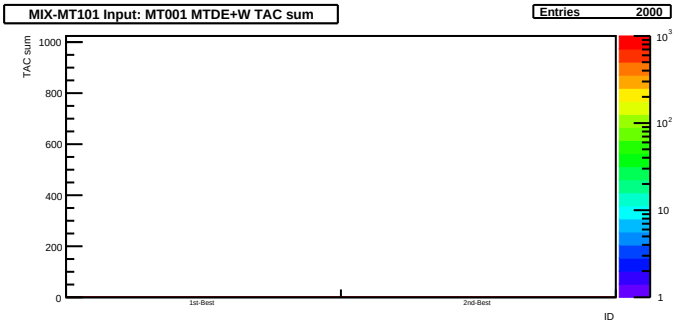

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

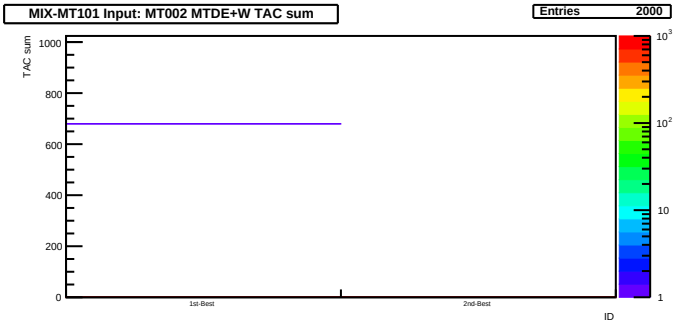

MXQ-MT001
Entries 32000
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^3
10^2
10
1

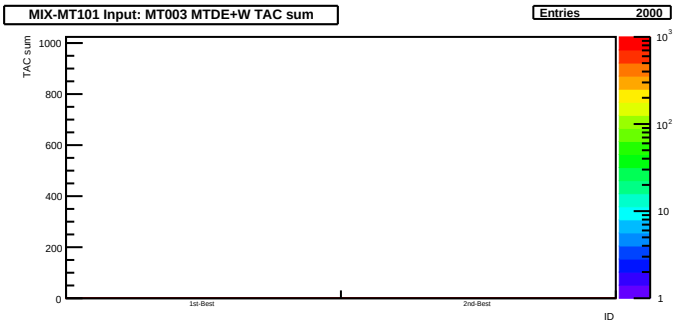

MXQ-MT002
Entries 32000
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^3
10^2
10
1

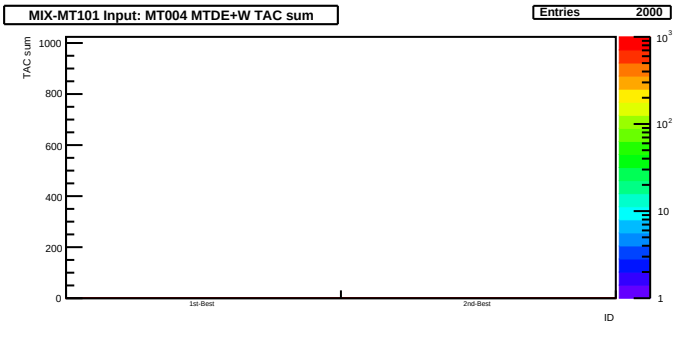

MXQ-MT003
Entries 32000
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^3
10^2
10
1

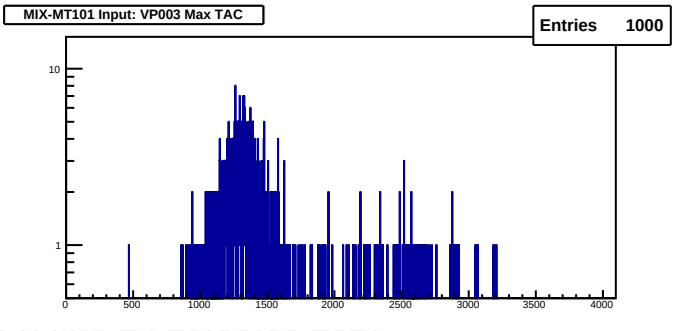

MXQ-MT004
Entries 32000
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^3
10^2
10
1

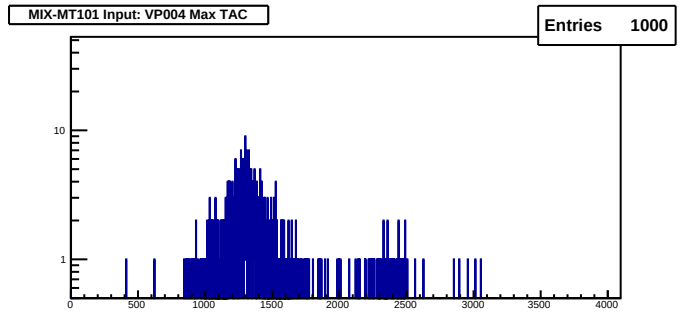

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 2000
TAC sum
1st-Best
2nd-Best
ID

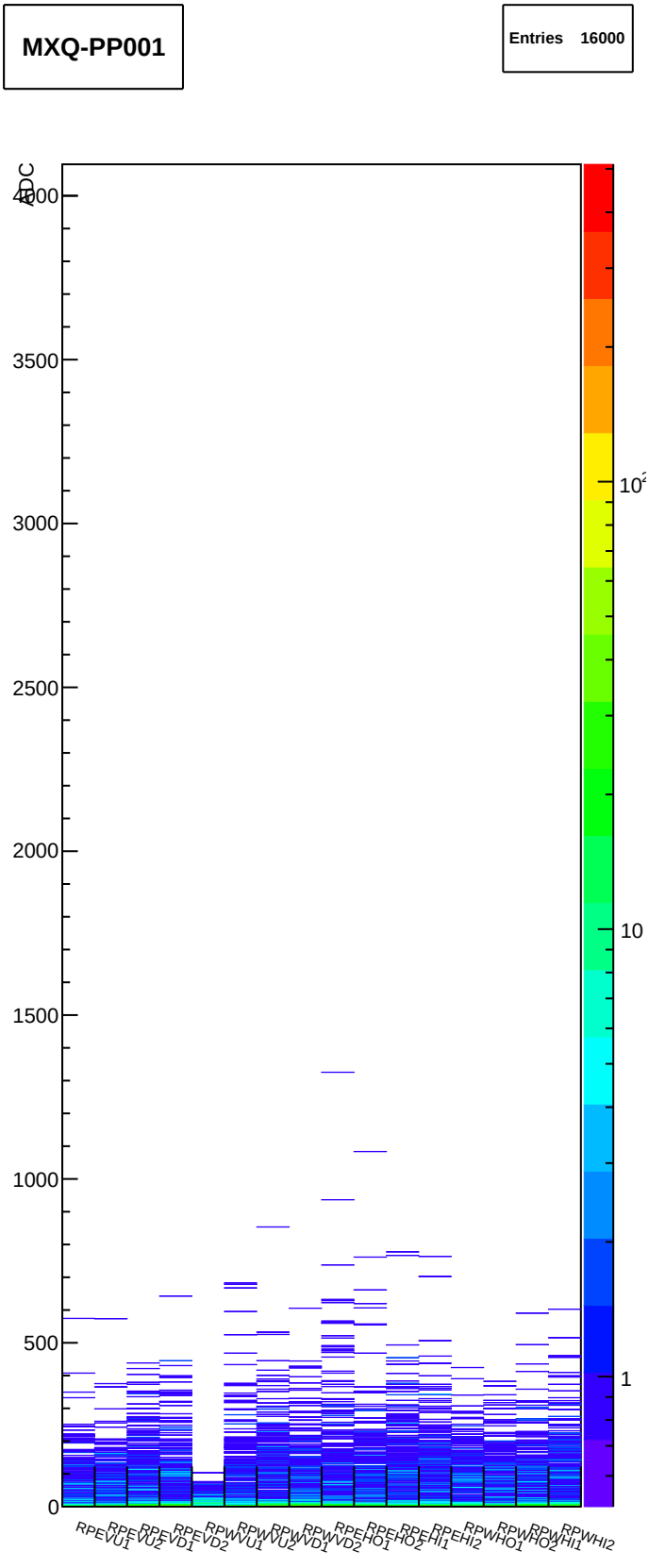

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 2000
TAC sum
1st-Best
2nd-Best
ID

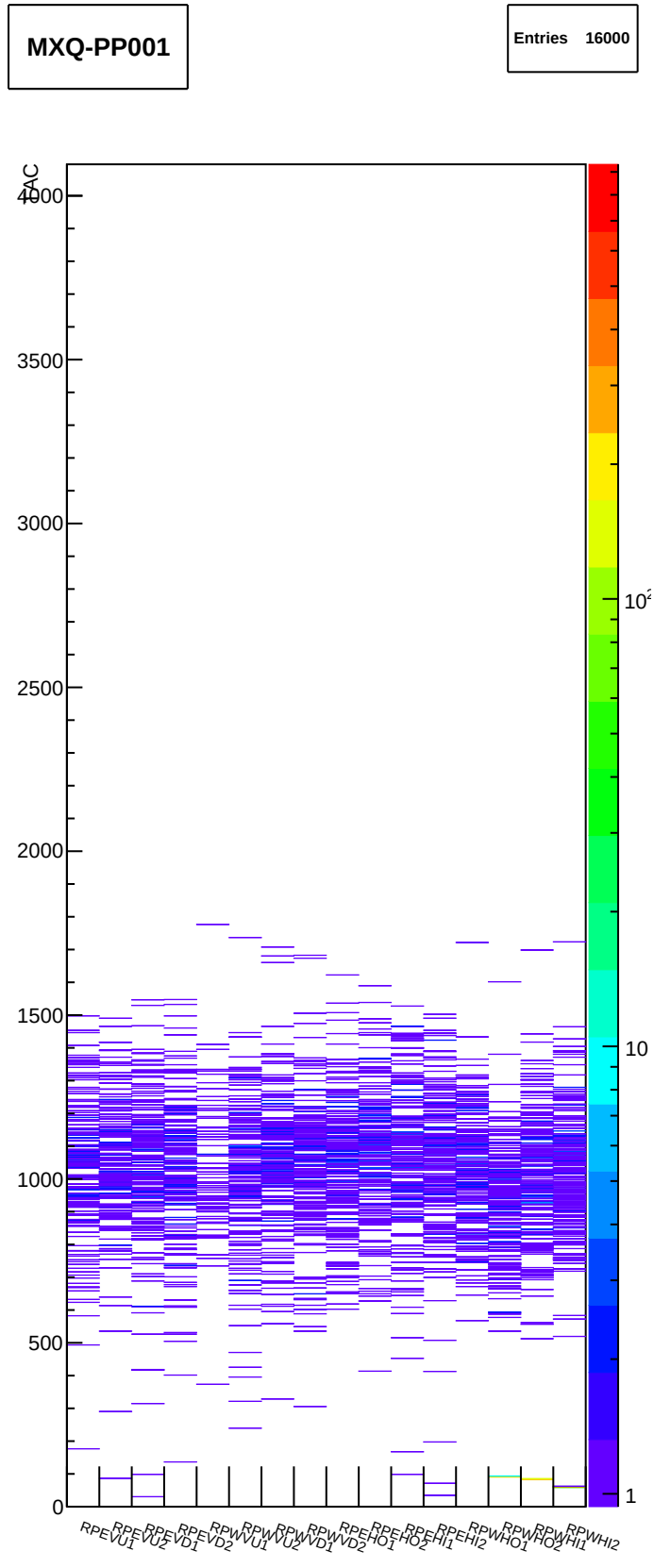

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 2000
TAC sum
1st-Best
2nd-Best
ID

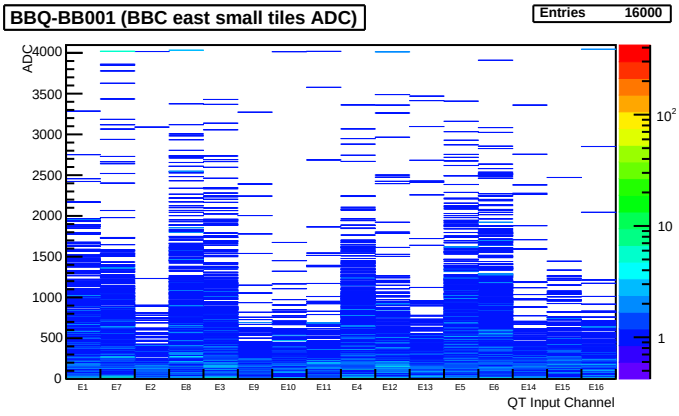

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 2000
TAC sum
1st-Best
2nd-Best
ID

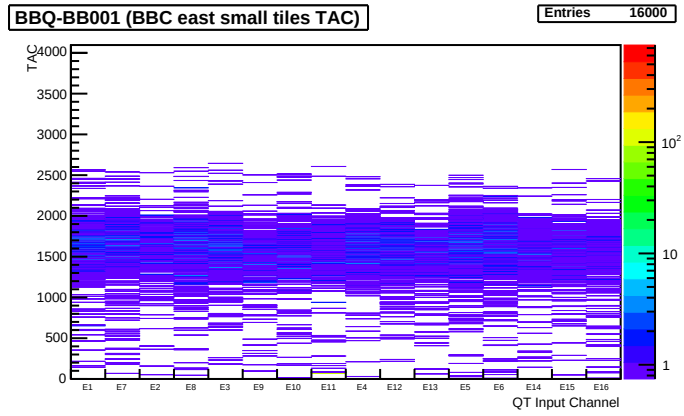

MIX-MT101 Input: VP003 Max TAC
Entries 1000

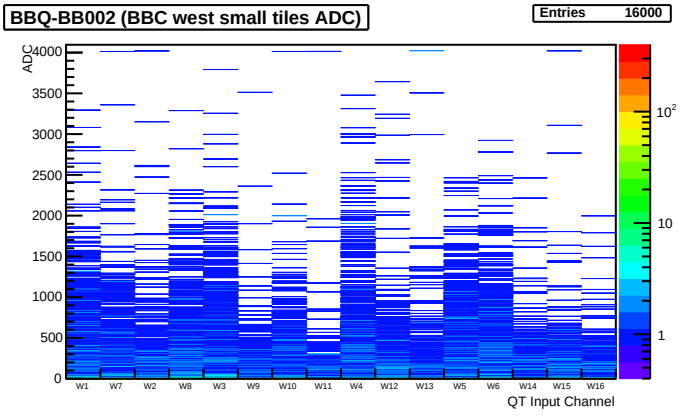

MIX-MT101 Input: VP004 Max TAC
Entries 1000

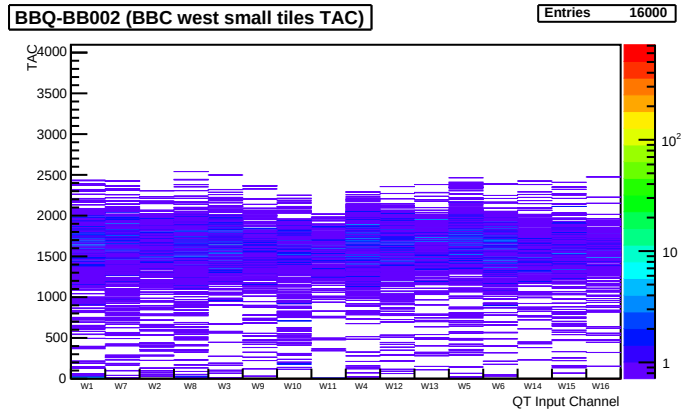

MXQ-PP001
Entries 16000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10²
10
1

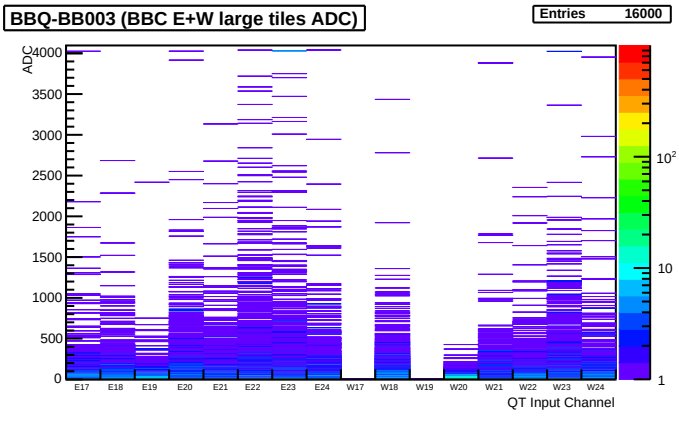

MXQ-PP001
Entries 16000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10²
10
1

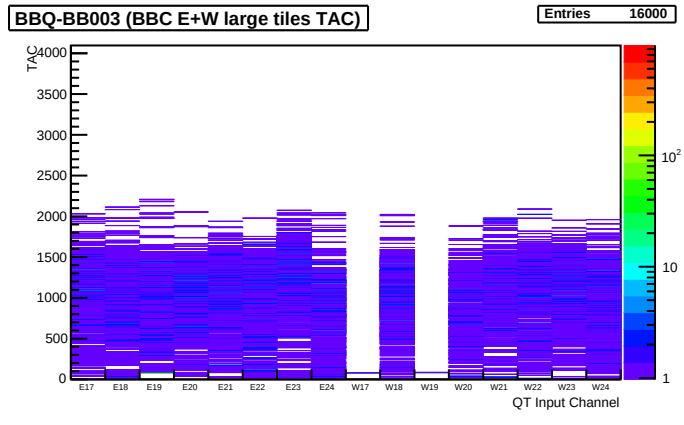

BBQ-BB001 (BBC east small tiles ADC)
Entries 16000
ADC
QT Input Channel

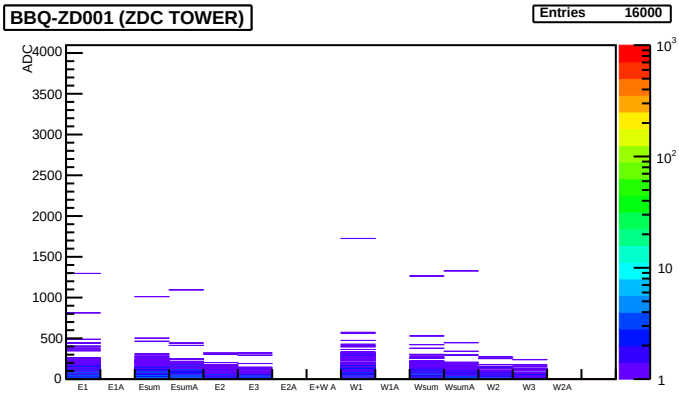

BBQ-BB001 (BBC east small tiles TAC)
Entries 16000
TAC
QT Input Channel

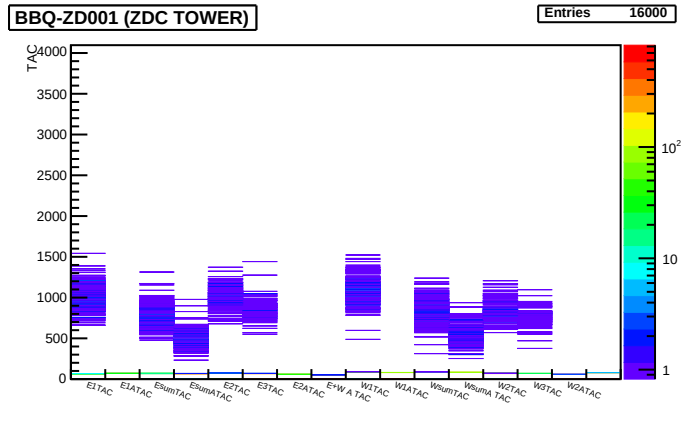

BBQ-BB002 (BBC west small tiles ADC)
Entries 16000
ADC
QT Input Channel

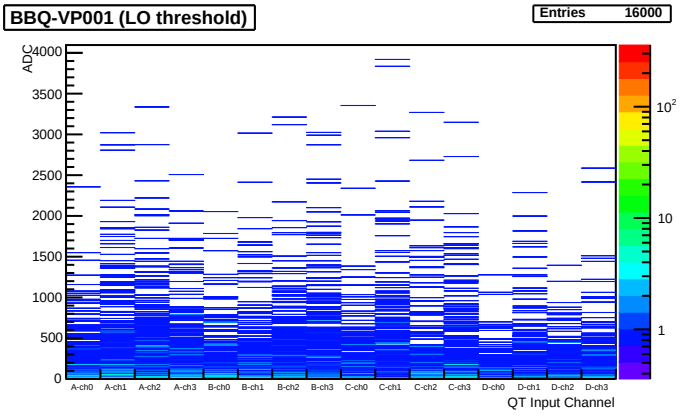

BBQ-BB002 (BBC west small tiles TAC)
Entries 16000
TAC
QT Input Channel

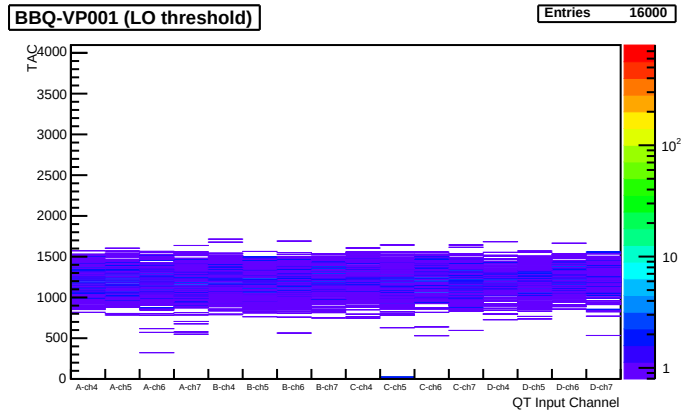

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 16000
ADC
QT Input Channel

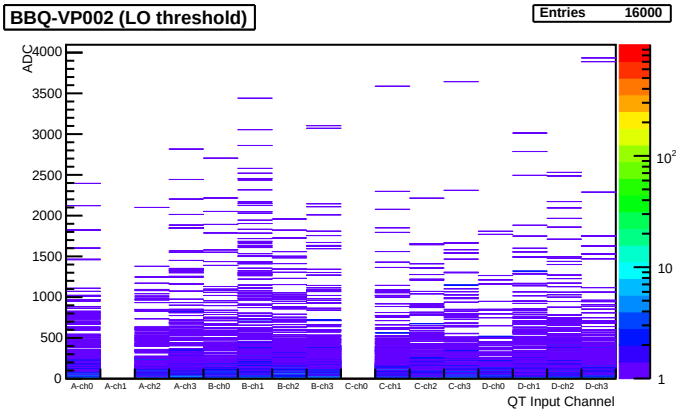

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 16000
TAC
QT Input Channel

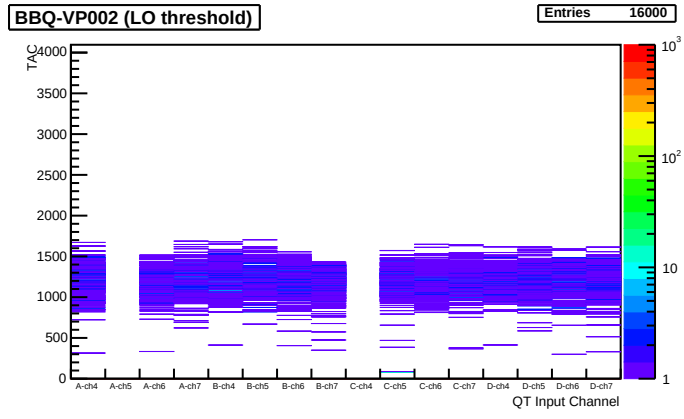

BBQ-ZD001 (ZDC TOWER)
Entries 16000
ADC

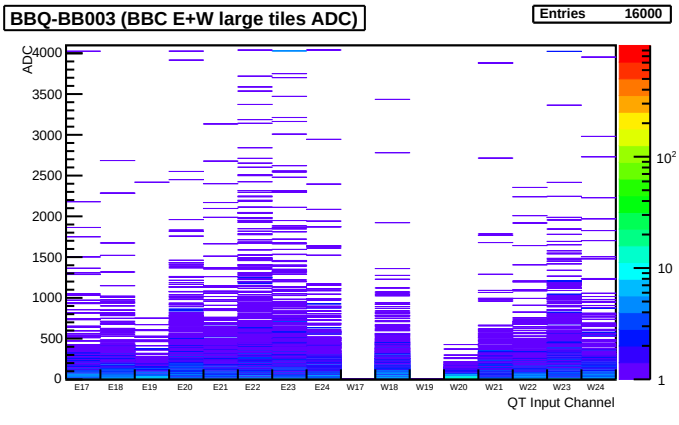

BBQ-ZD001 (ZDC TOWER)
Entries 16000
TAC

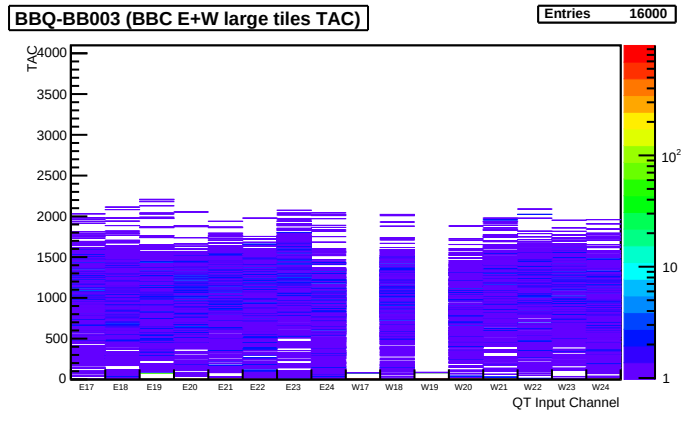

BBQ-VP001 (LO threshold)
Entries 16000
ADC
QT Input Channel

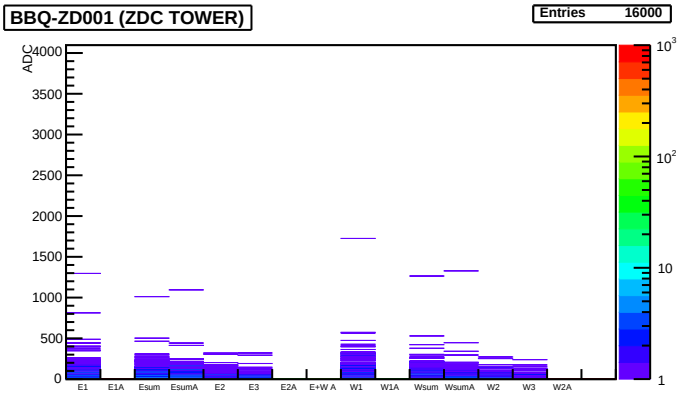

BBQ-VP001 (LO threshold)
Entries 16000
TAC
QT Input Channel

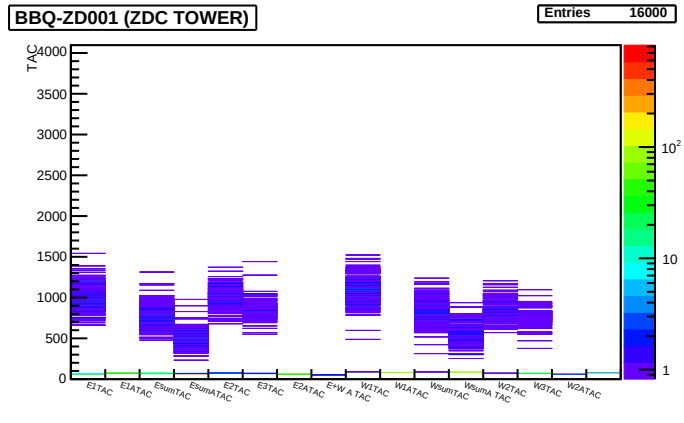

BBQ-VP002 (LO threshold)
Entries 16000
ADC
QT Input Channel

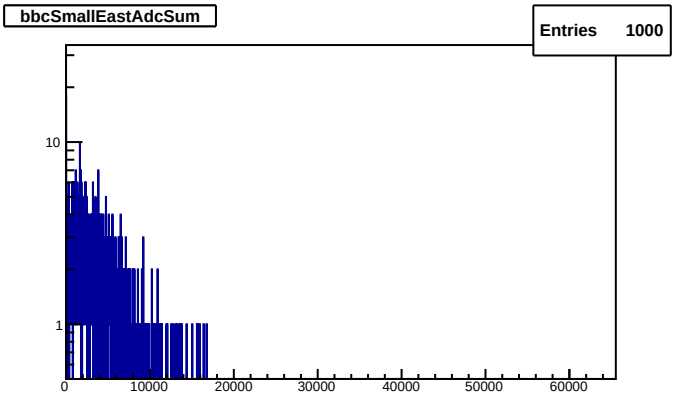

BBQ-VP002 (LO threshold)
Entries 16000
TAC
QT Input Channel

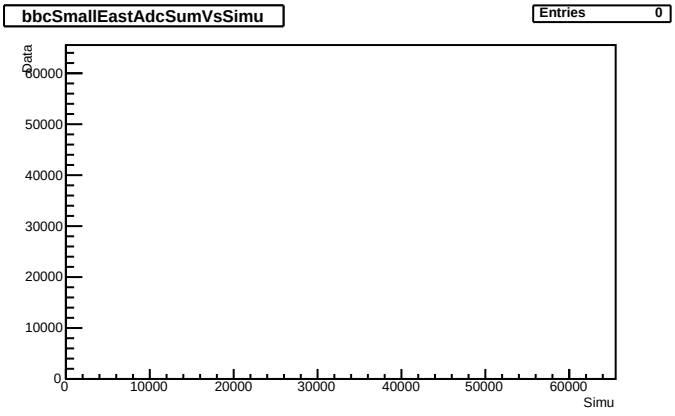

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 16000
ADC
QT Input Channel

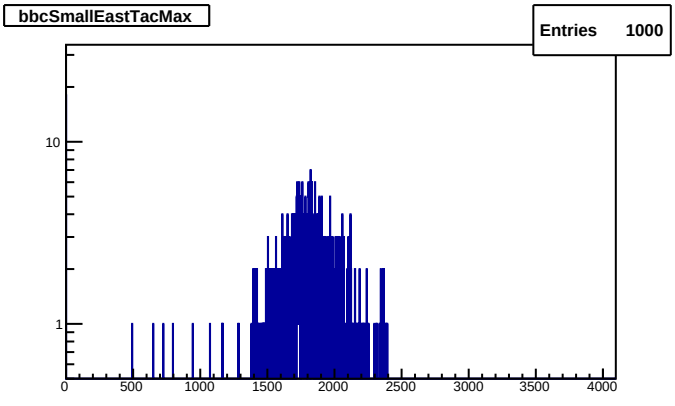

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 16000
TAC
QT Input Channel

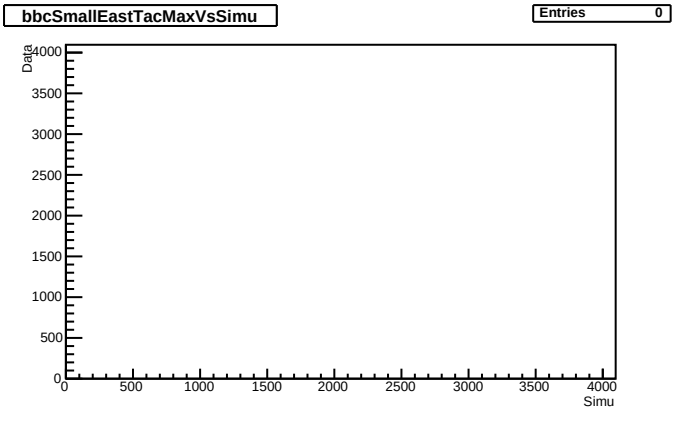

BBQ-ZD001 (ZDC TOWER)
Entries 16000
ADC

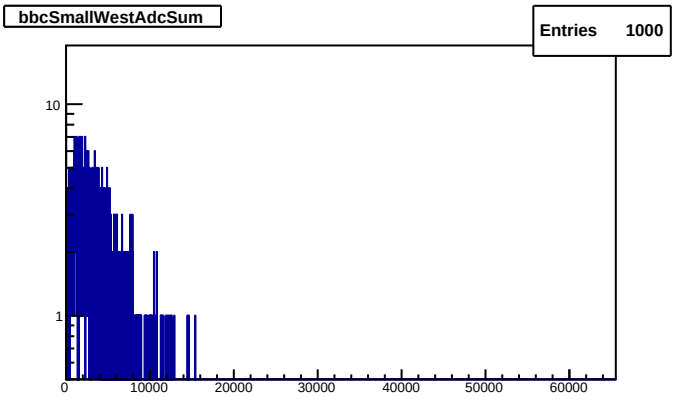

BBQ-ZD001 (ZDC TOWER)
Entries 16000
TAC

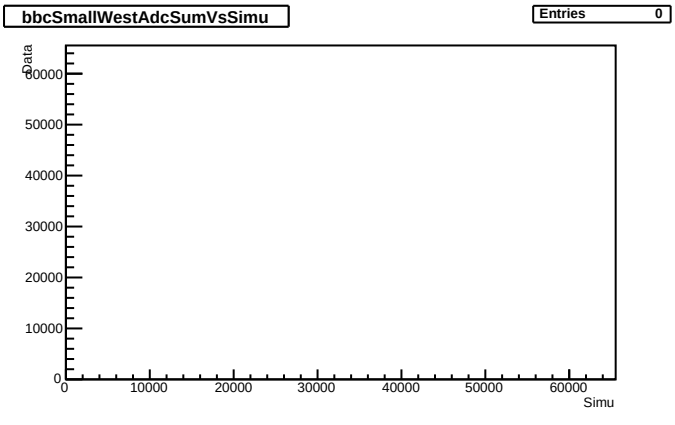

bbcSmallEastAdcSum
Entries 1000
10
1
0
10000
20000
30000
40000
50000
60000

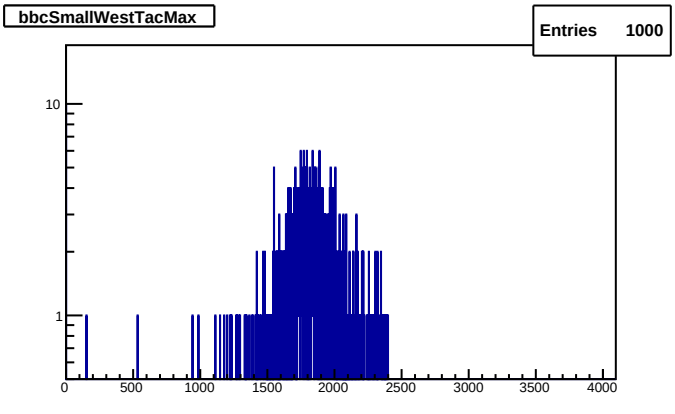

bbcSmallEastAdcSumVsSimu
Entries 0
Data
60000
50000
40000
30000
20000
10000
0
0
10000
20000
30000
40000
50000
60000
Simu

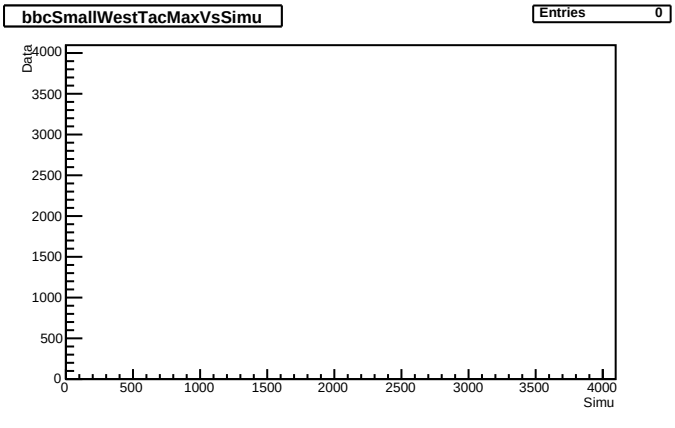

bbcSmallEastTacMax
Entries 1000
10
1
0
500
1000
1500
2000
2500
3000
3500
4000

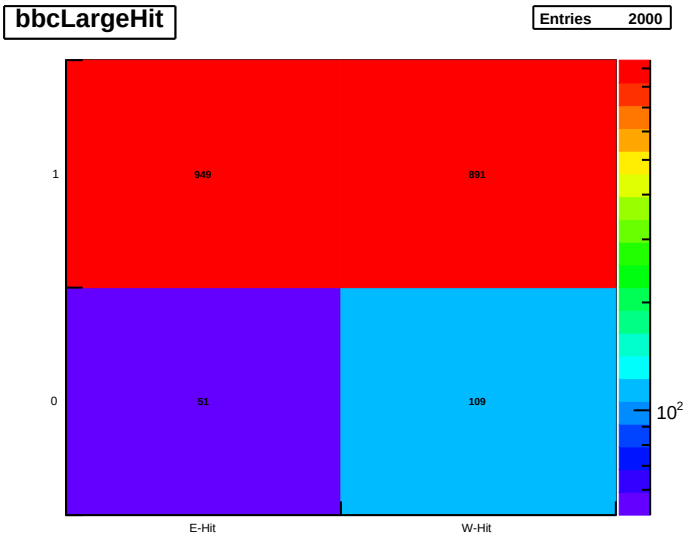

bbcSmallEastTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

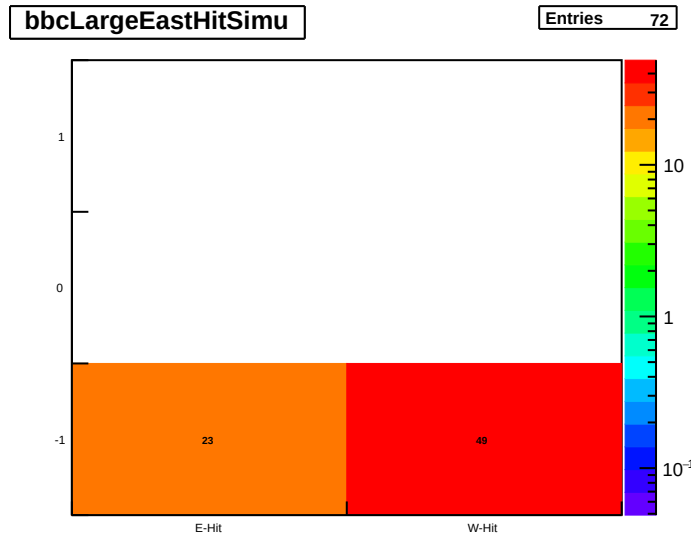

bbcSmallWestAdcSum
Entries 1000
10
1
0
10000
20000
30000
40000
50000
60000

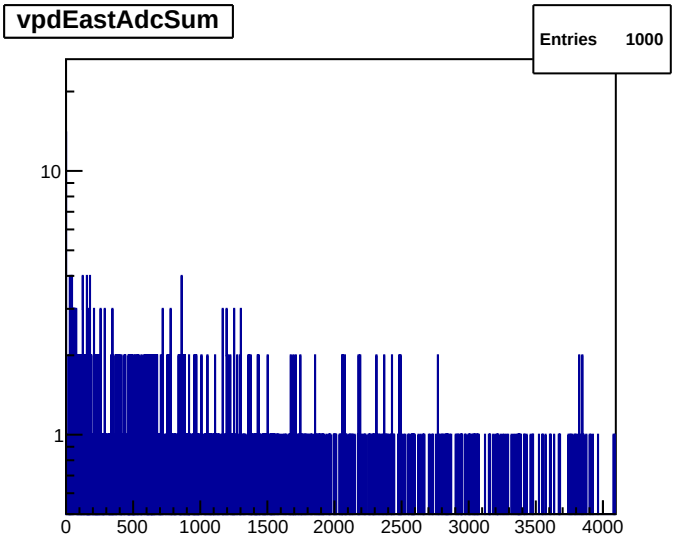

bbcSmallWestAdcSumVsSimu
Entries 0
Data
60000
50000
40000
30000
20000
10000
0
0
10000
20000
30000
40000
50000
60000
Simu

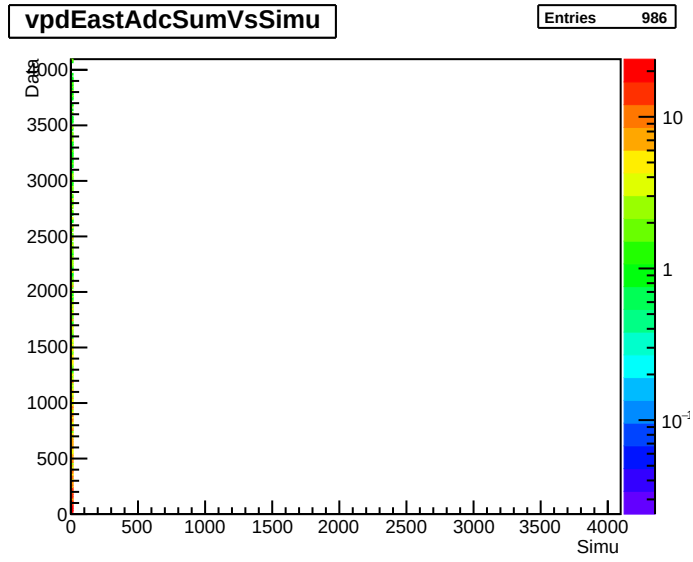

bbcSmallWestTacMax
Entries 1000
10
1
0
500
1000
1500
2000
2500
3000
3500
4000

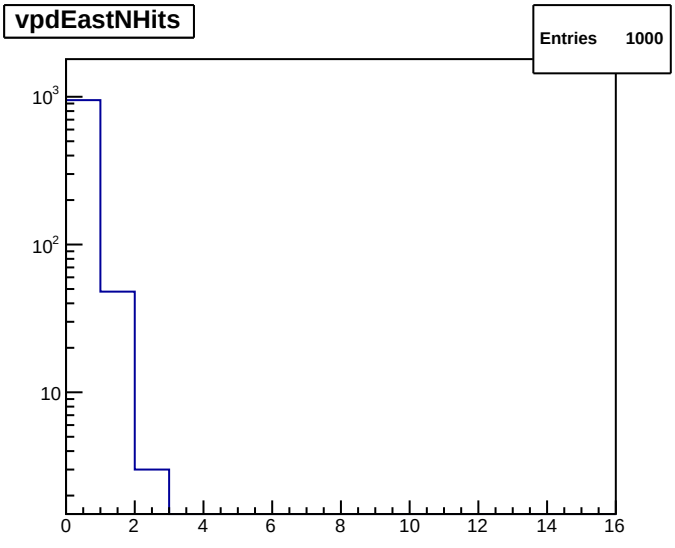

bbcSmallWestTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

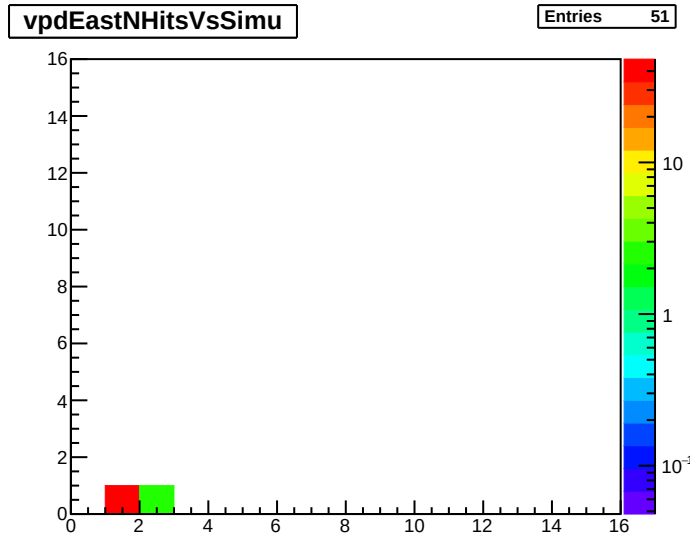

bbcLargeHit
Entries 2000
949
891
51
109
1
0
E-Hit
W-Hit
10^2

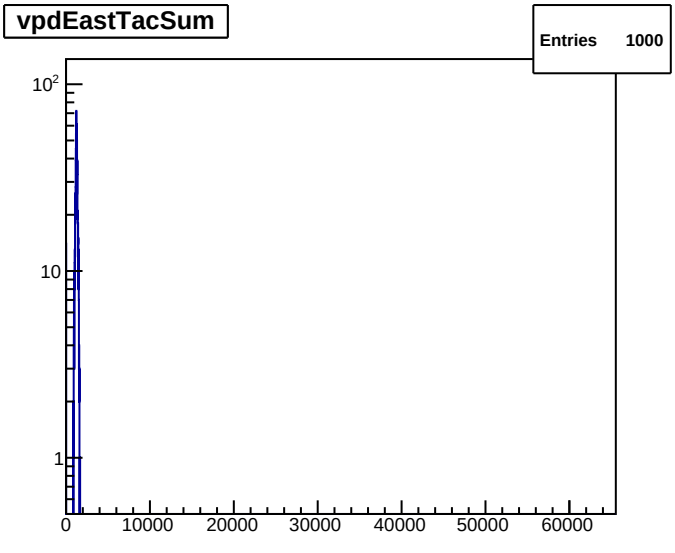

bbcLargeEastHitSimu
Entries 72
23
49
1
0
-1
E-Hit
W-Hit
10
1
10^{-1}

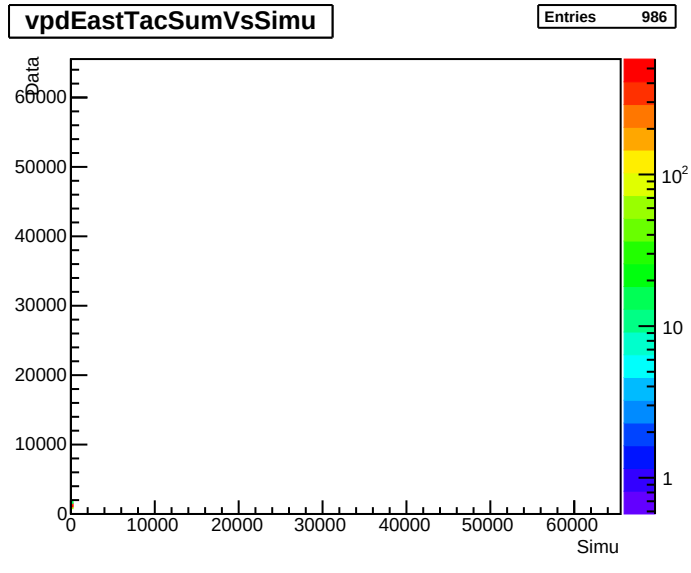

vpdEastAdcSum
Entries 1000

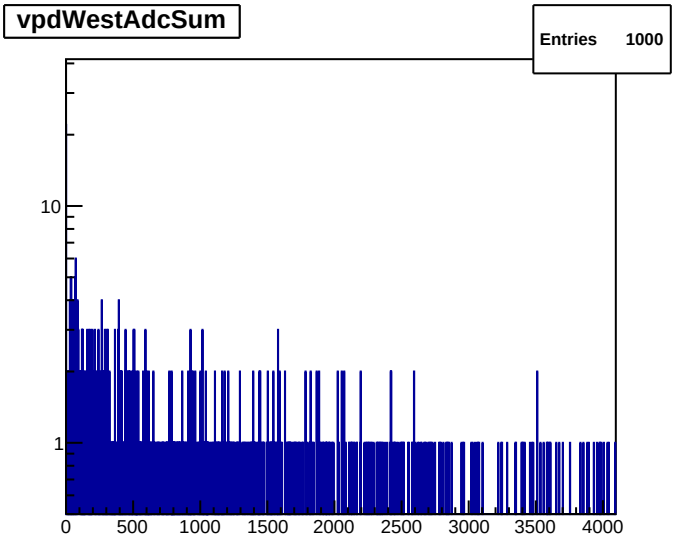

vpdEastAdcSumVsSimu
Entries 986
Data
Simu

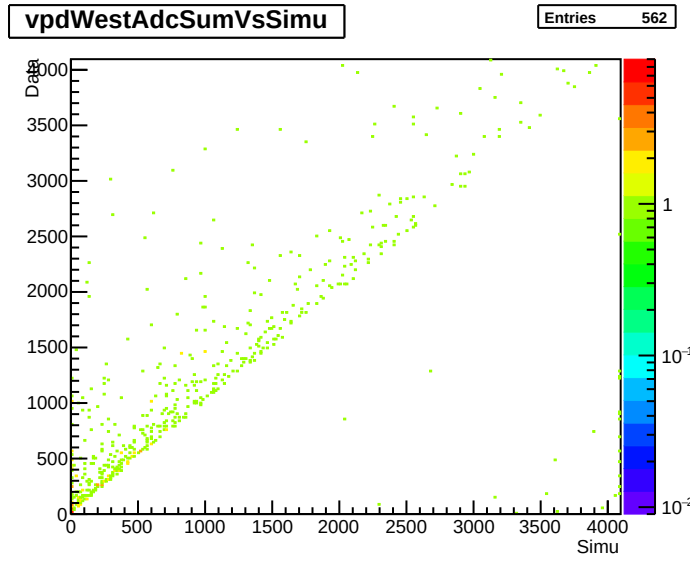

vpdEastNHits
Entries 1000

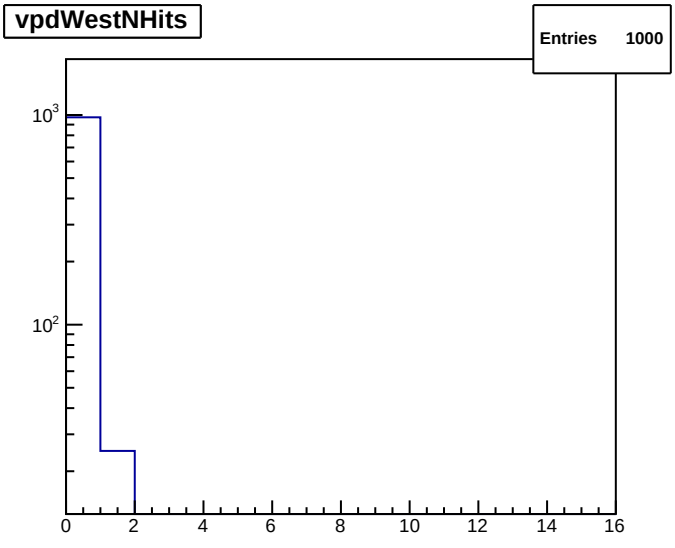

vpdEastNHitsVsSimu
Entries 51

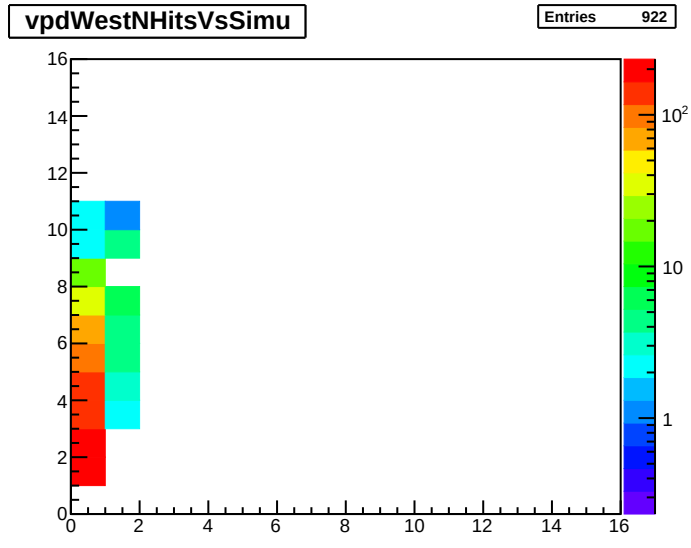

vpdEastTacSum
Entries 1000

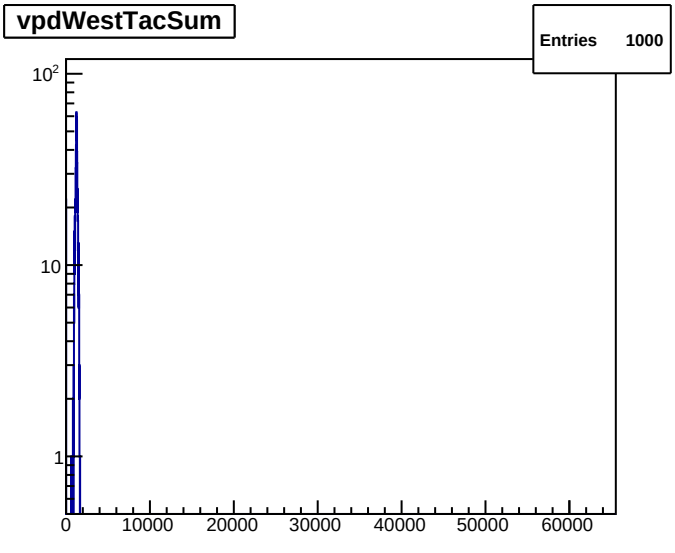

vpdEastTacSumVsSimu
Entries 986
Data
Simu

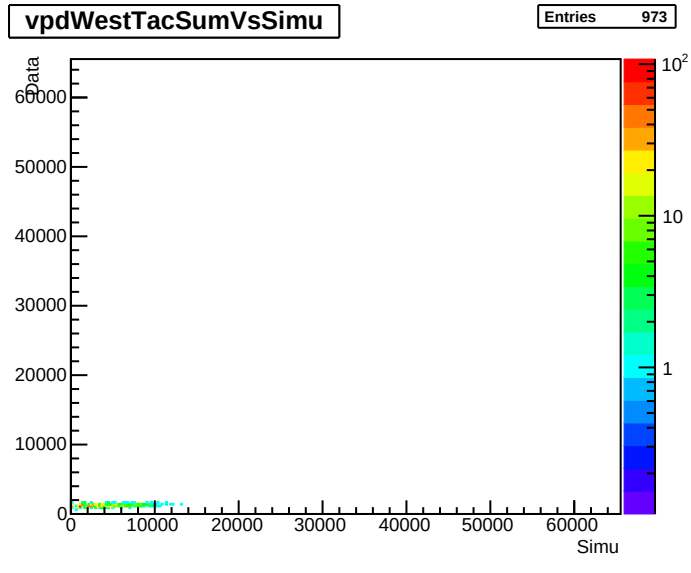

vpdWestAdcSum
Entries 1000

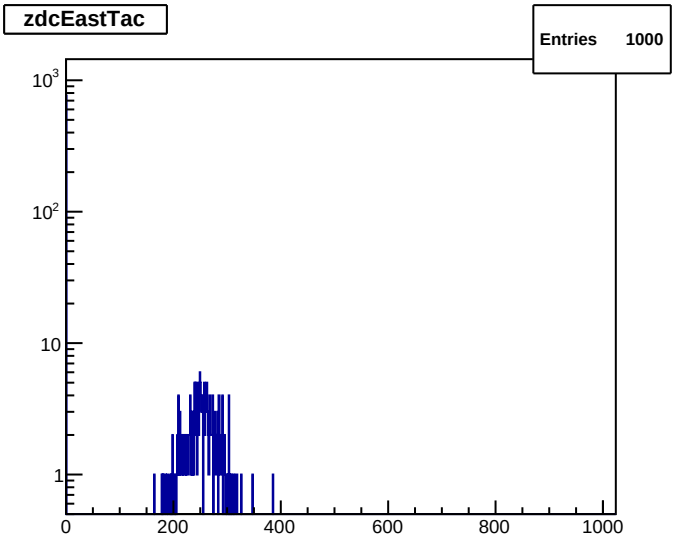

vpdWestAdcSumVsSimu
Entries 562
Data
Simu

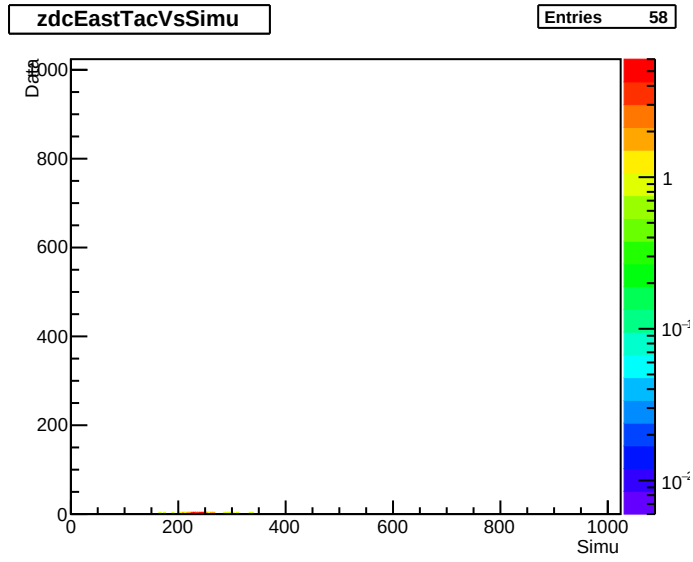

vpdWestNHits
Entries 1000

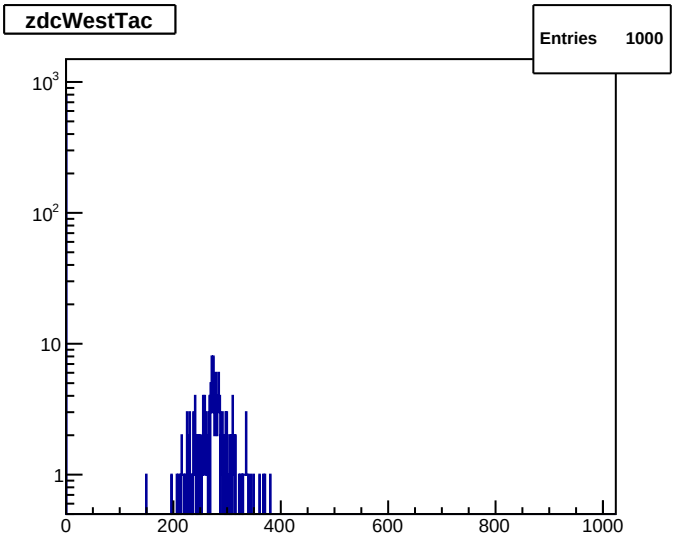

vpdWestNHitsVsSimu
Entries 922

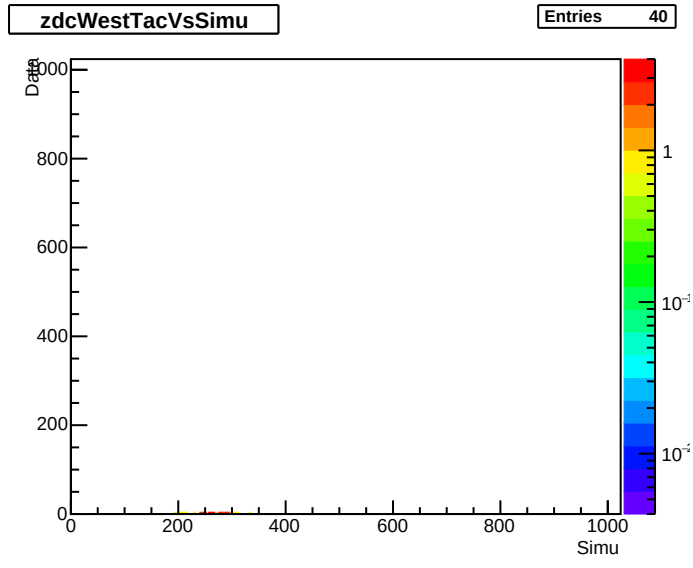

vpdWestTacSum
Entries 1000

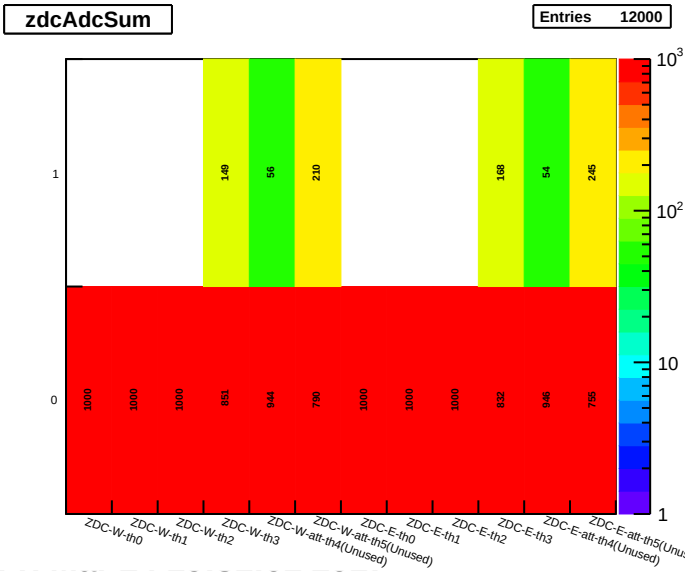

vpdWestTacSumVsSimu
Entries 973
Data
Simu

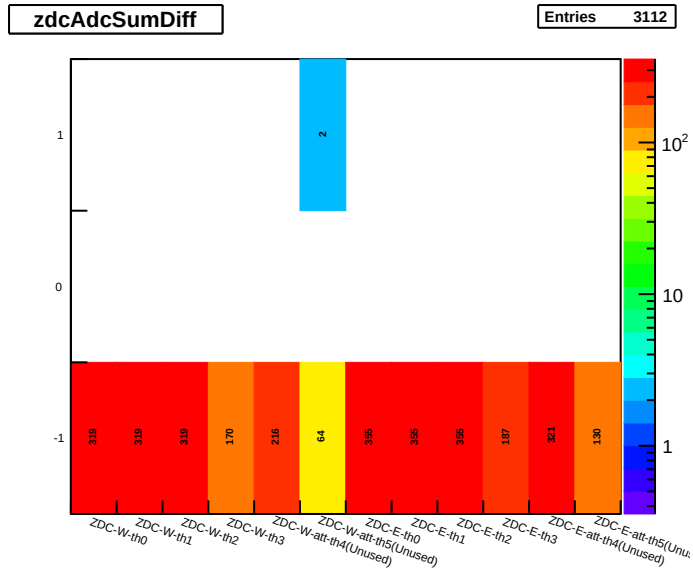

zdcEastTac
Entries 1000

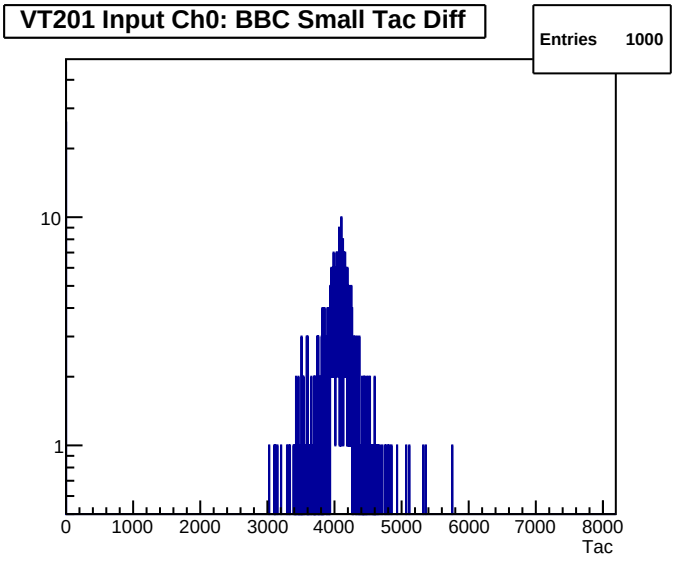

zdcEastTacVsSimu
Entries 58
Data
Simu

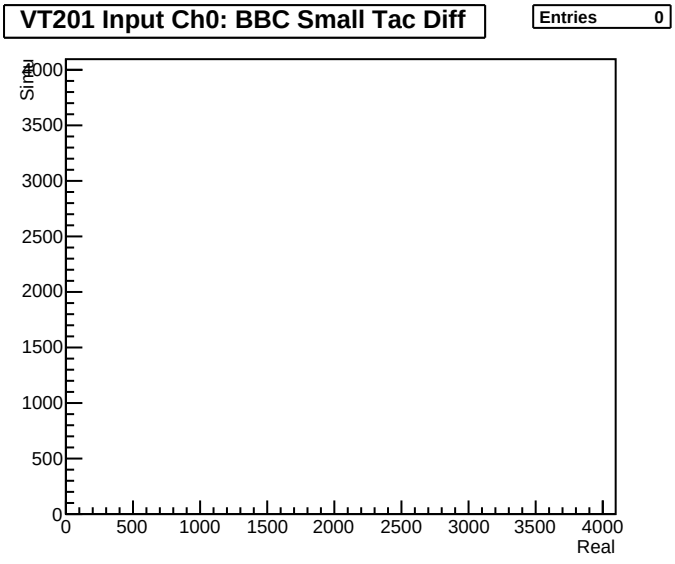

zdcWestTac
Entries 1000

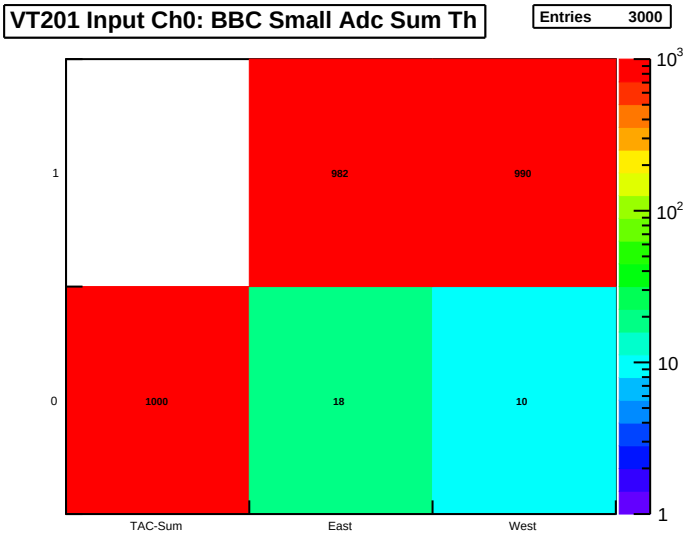

zdcWestTacVsSimu
Entries 40
Data
Simu

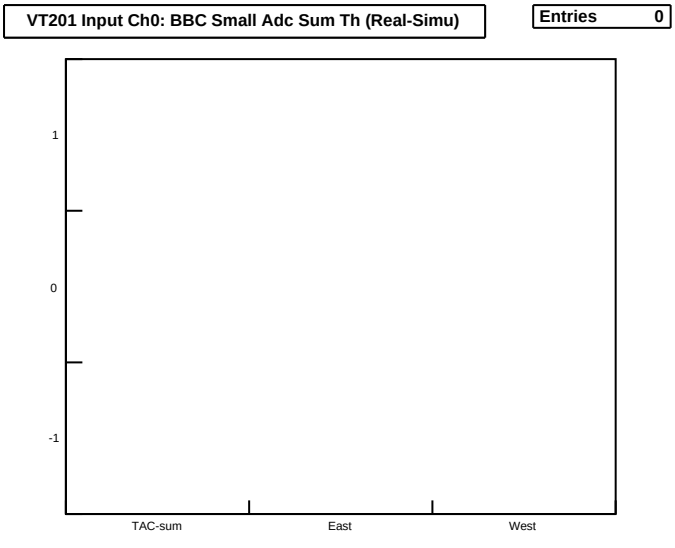

zdcAdcSum
Entries 12000
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unus

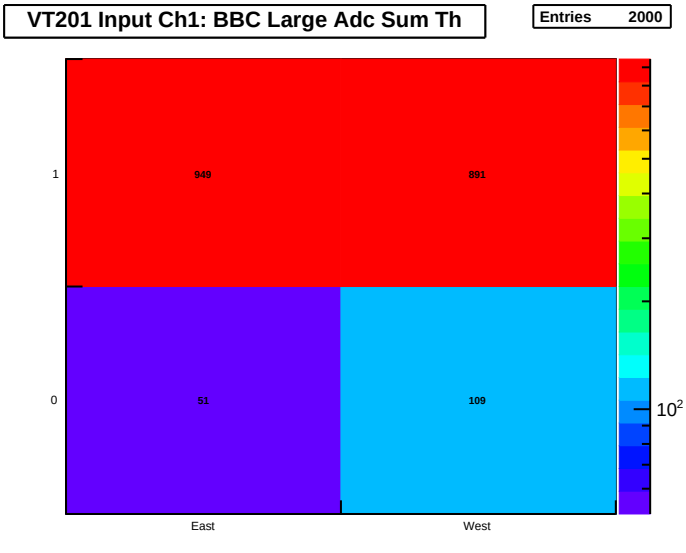

zdcAdcSumDiff
Entries 3112
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unus

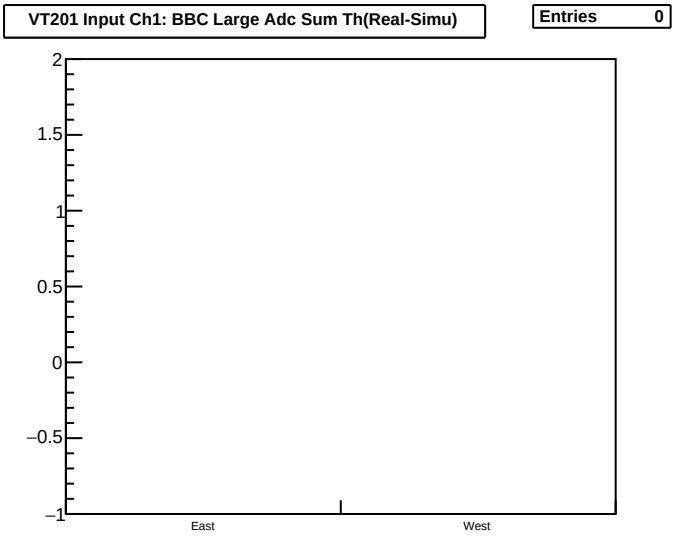

VT201 Input Ch0: BBC Small Tac Diff
Entries 1000
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

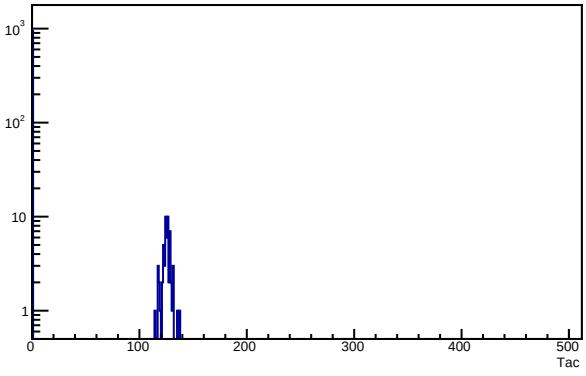

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 3000
982
990
1000
18
10
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

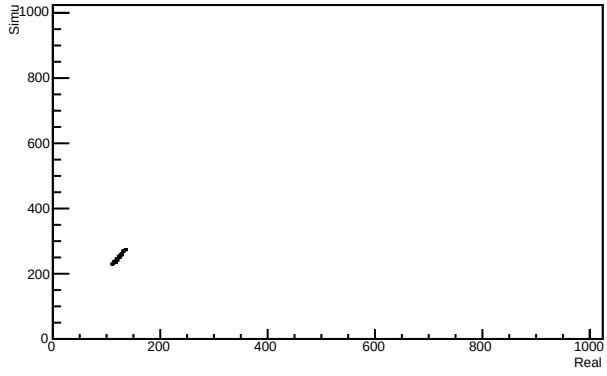

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 2000
949
891
51
109
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

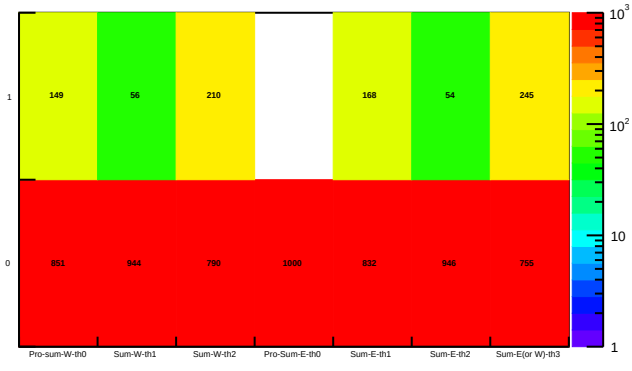
VT201 Input Ch2: ZDC Tac Diff



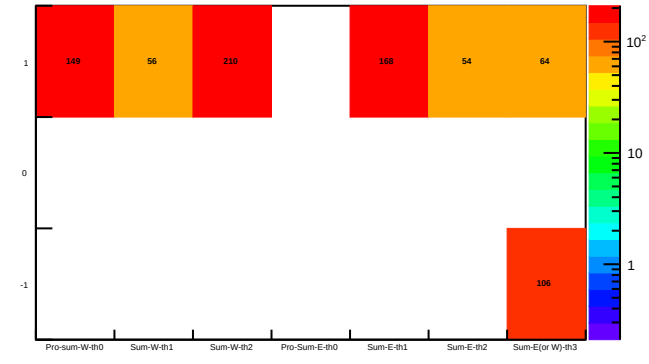
VT201 Input Ch2: ZDC Tac Diff



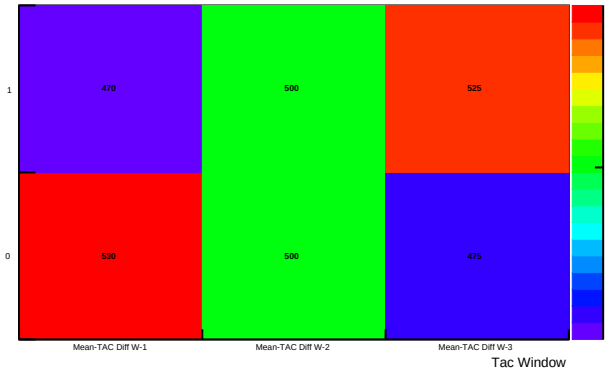
VT201 Input Ch2: ZDC Adc Sum Th



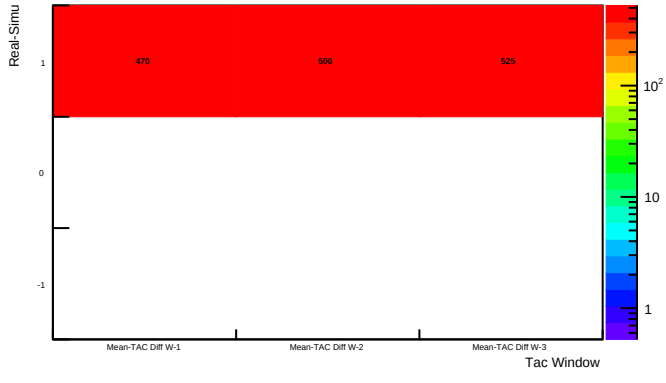
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



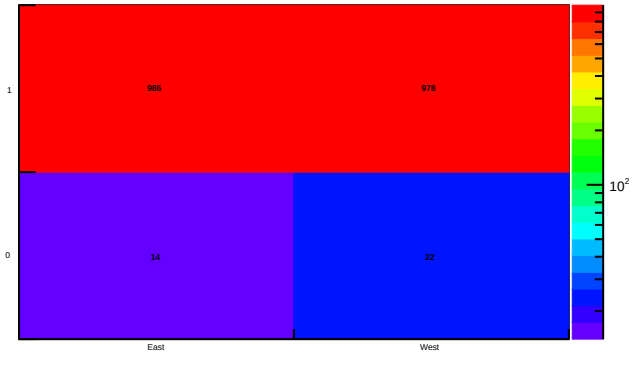
VT201 Input Ch4: VPD Tac Diff



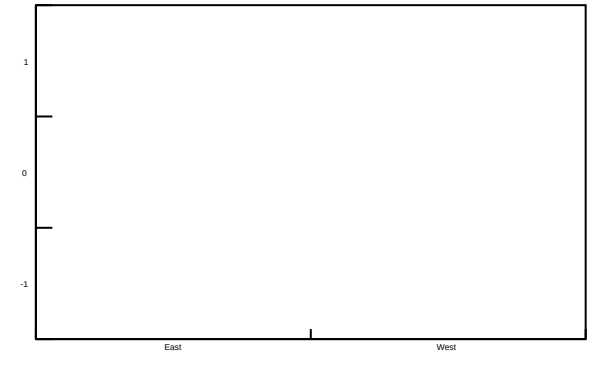
VT201 Input Ch4: VPD Tac Diff

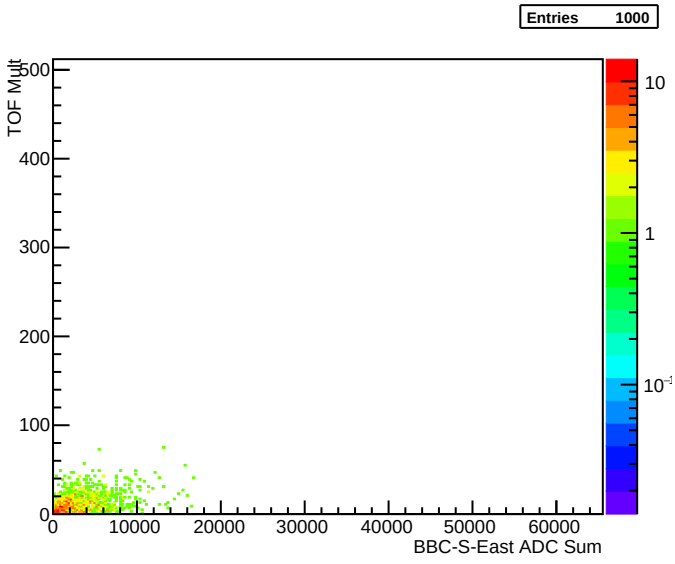


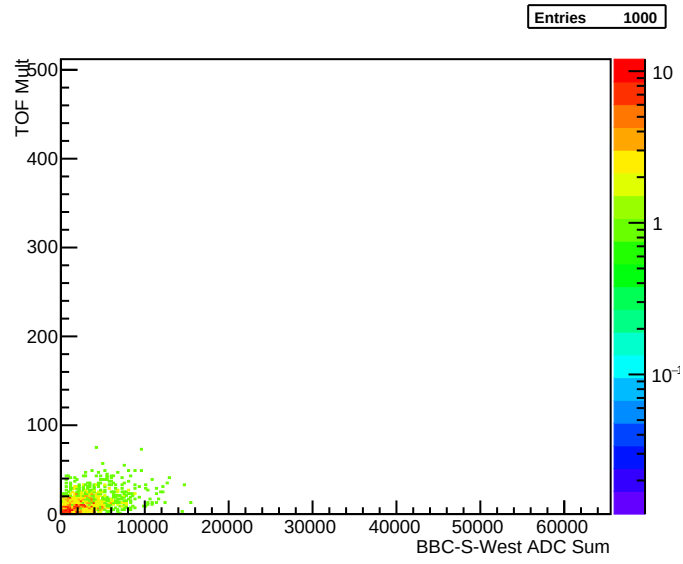
VT201 Input Ch4: VPD Adc Sum Th

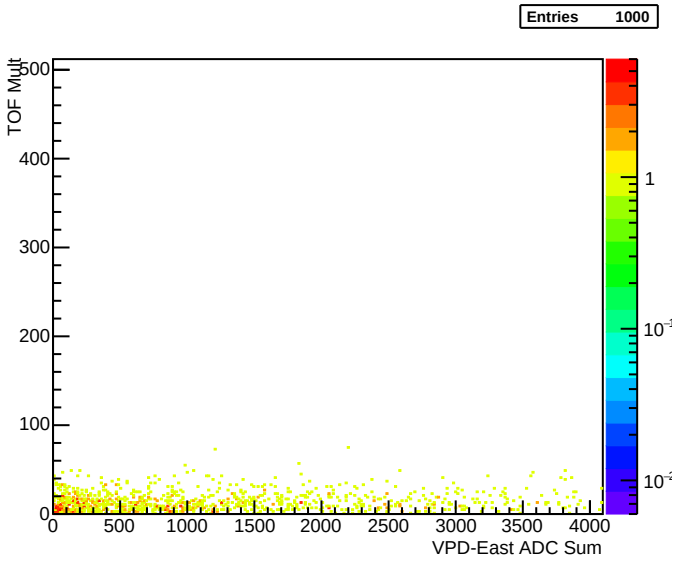


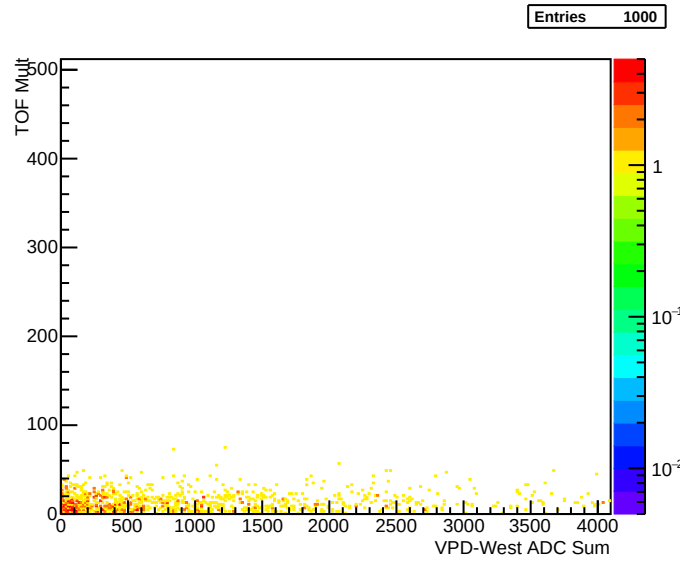
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

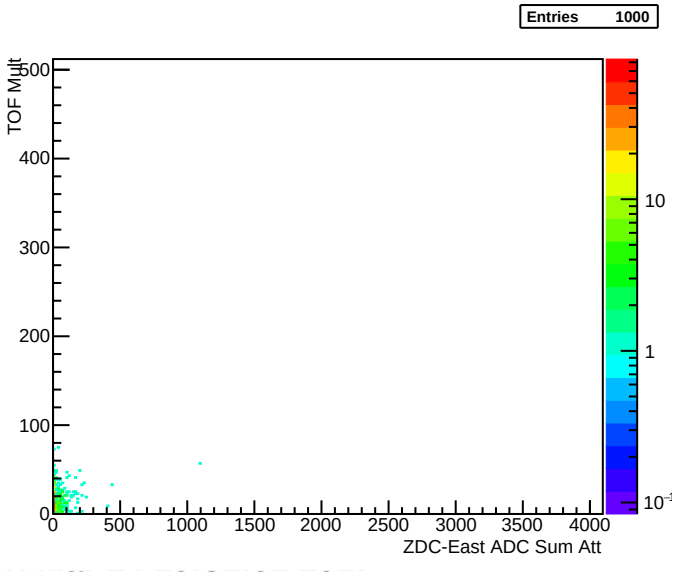


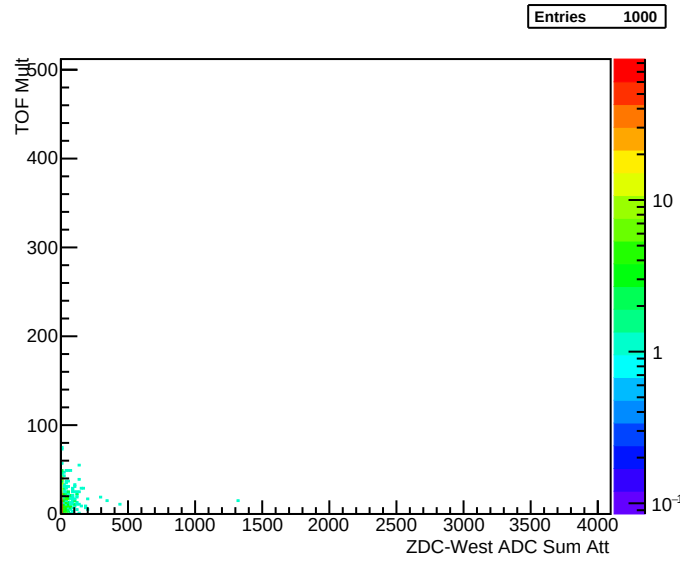

Entries 1000
TOF Mult
BBC-S-East ADC Sum

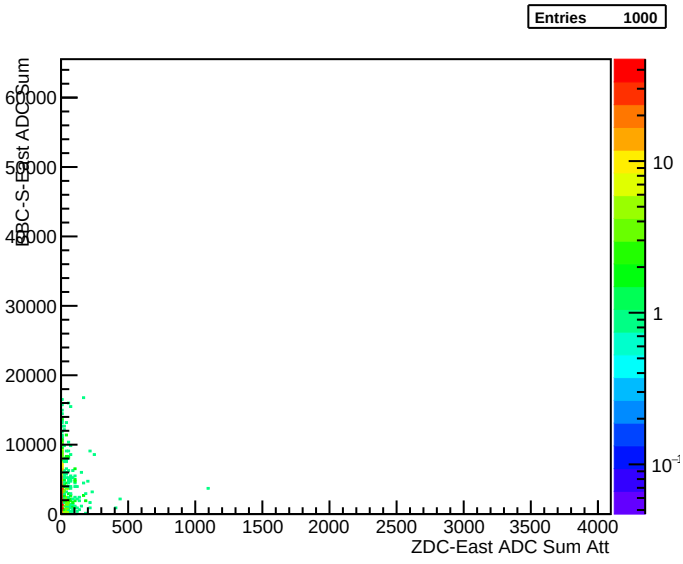

Entries 1000
TOF Mult
BBC-S-West ADC Sum

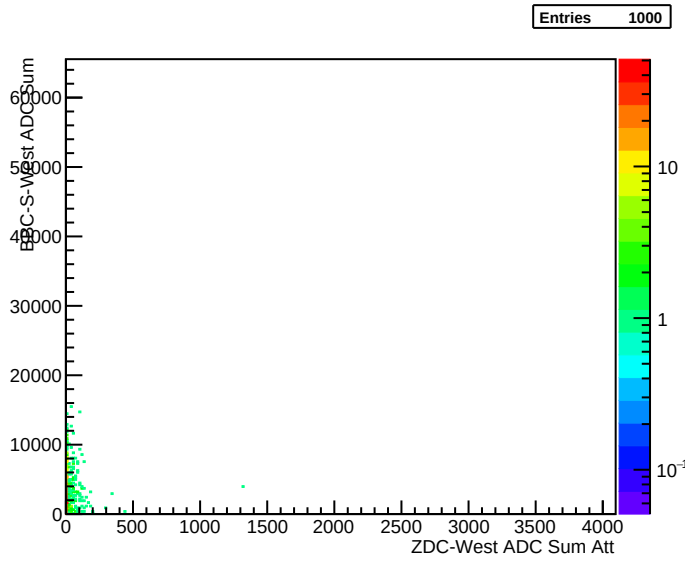

Entries 1000
TOF Mult
VPD-East ADC Sum

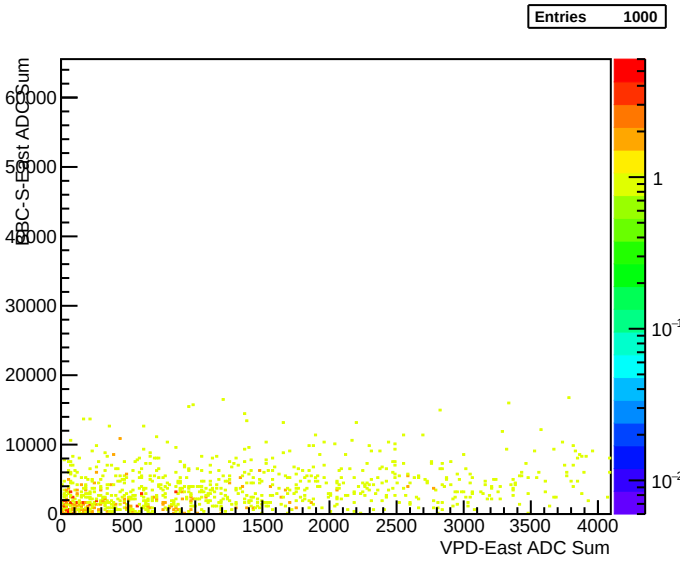

Entries 1000
TOF Mult
VPD-West ADC Sum

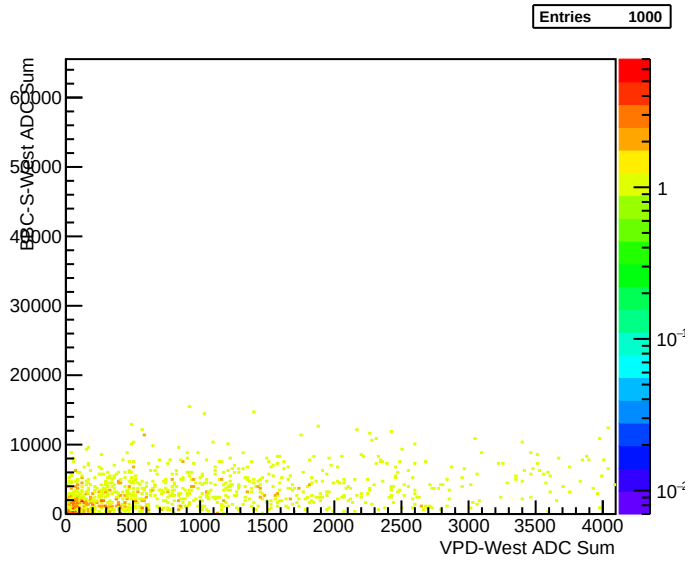

Entries 1000
TOF Mult
ZDC-East ADC Sum Att

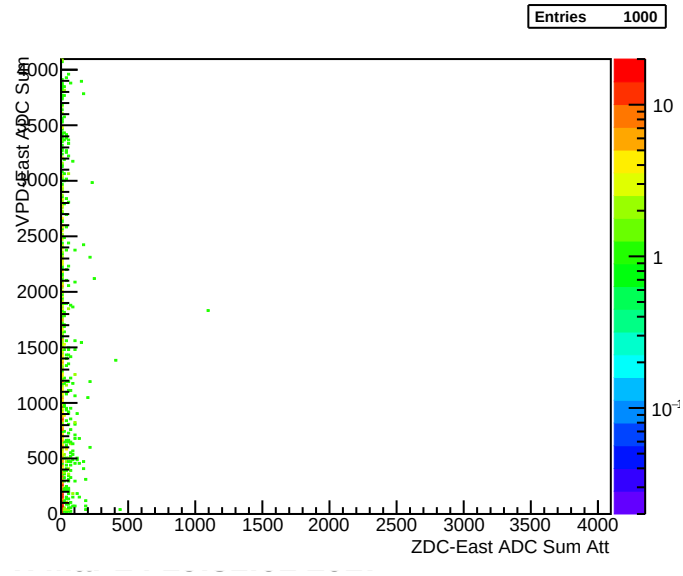

Entries 1000
TOF Mult
ZDC-West ADC Sum Att

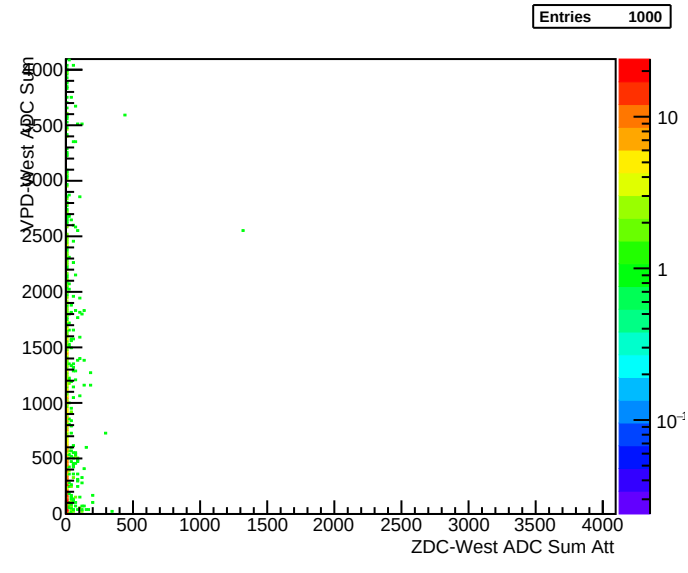

Entries 1000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

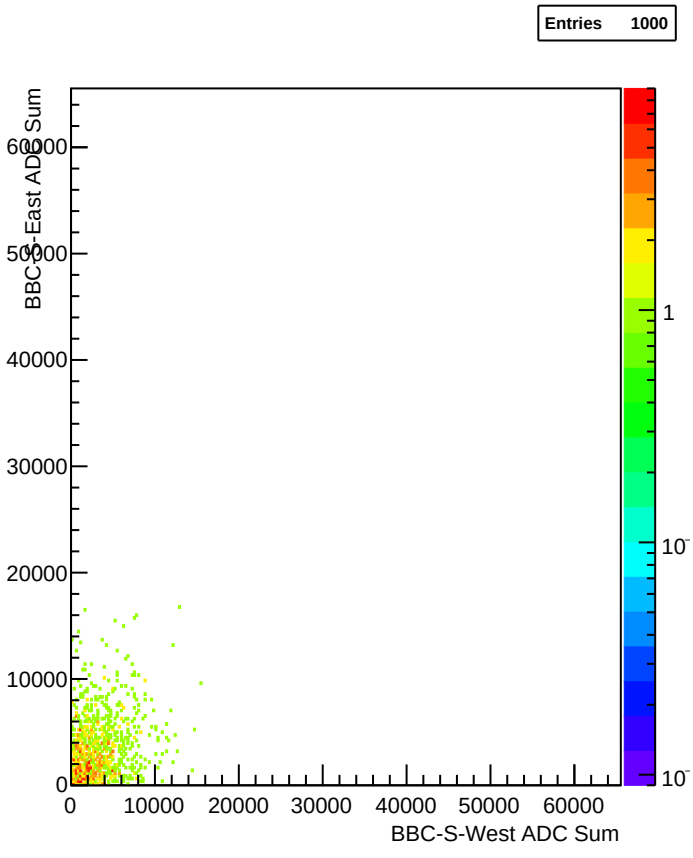

Entries 1000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

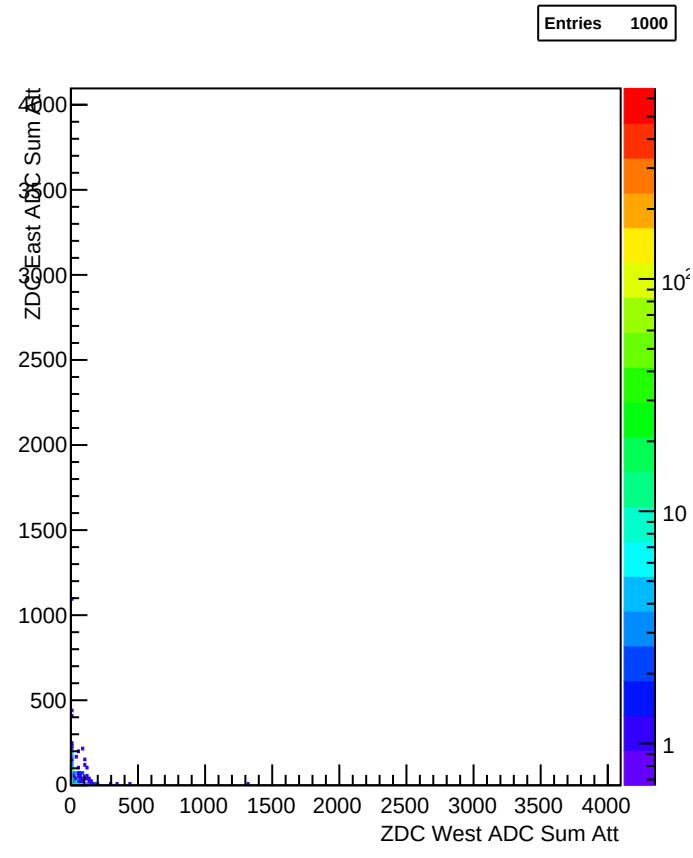

Entries 1000
BBC-S-East ADC Sum
VPD-East ADC Sum

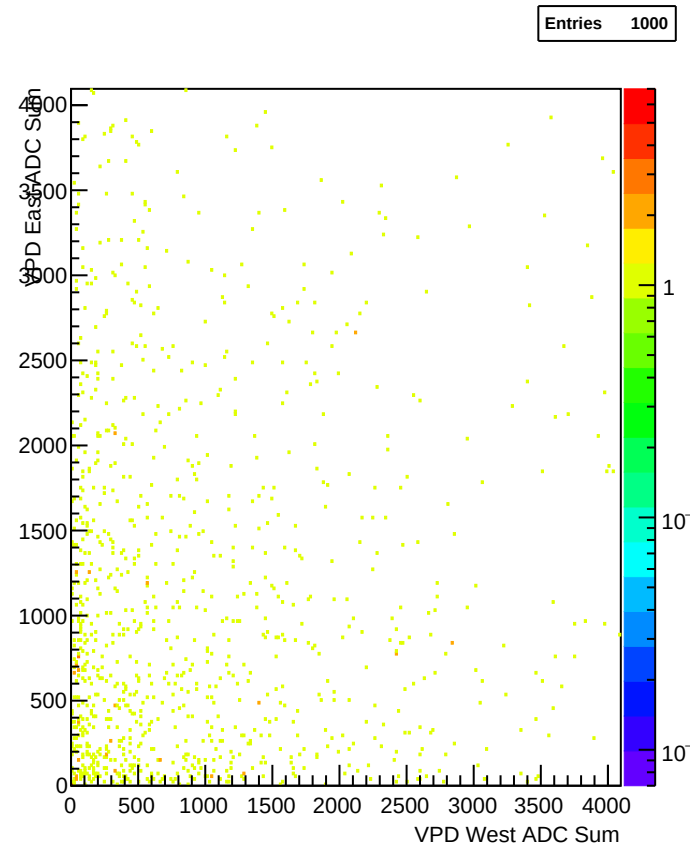

Entries 1000
BBC-S-West ADC Sum
VPD-West ADC Sum

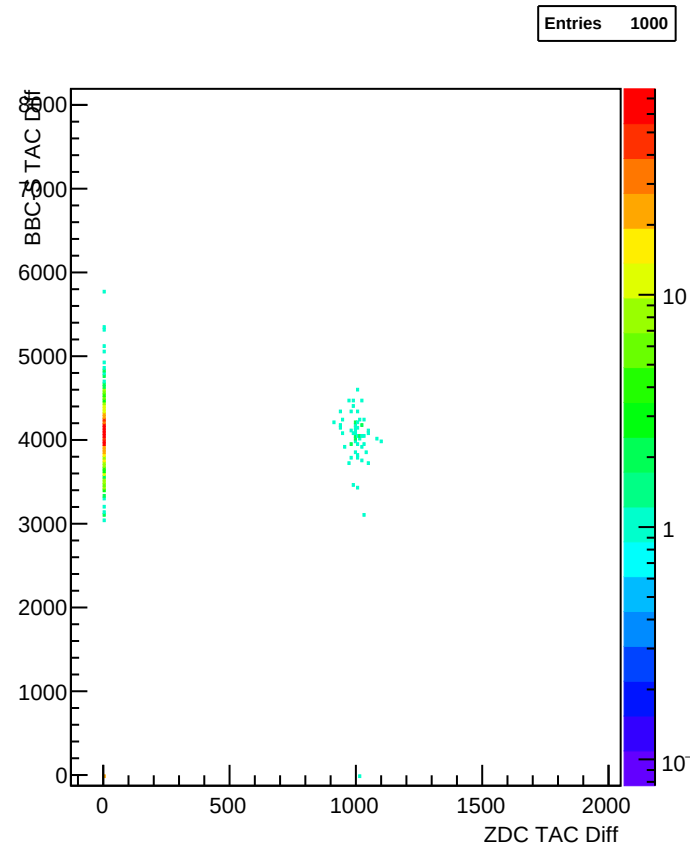

Entries 1000
VPD-East ADC Sum
ZDC-East ADC Sum Att

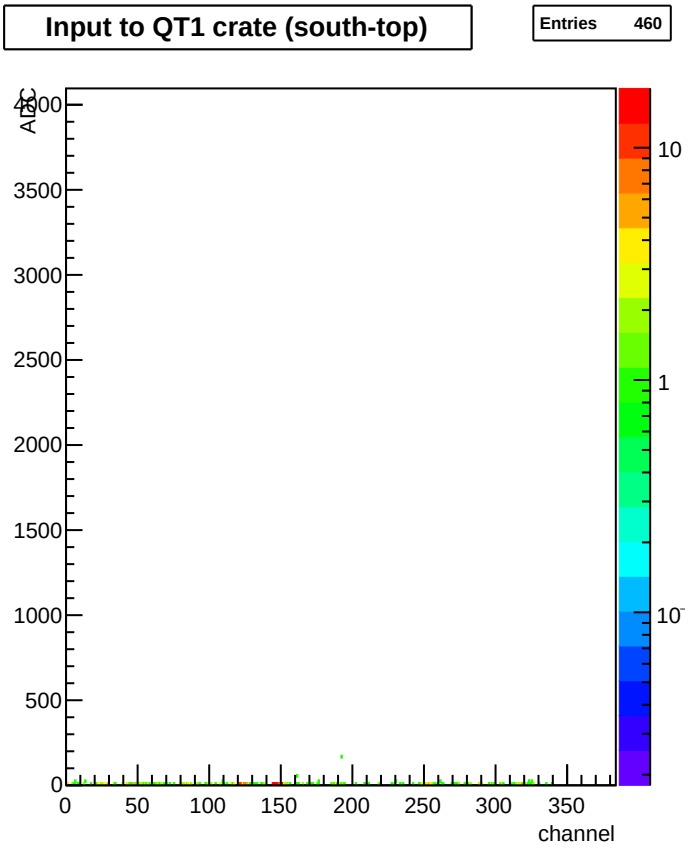

Entries 1000
VPD-West ADC Sum
ZDC-West ADC Sum Att

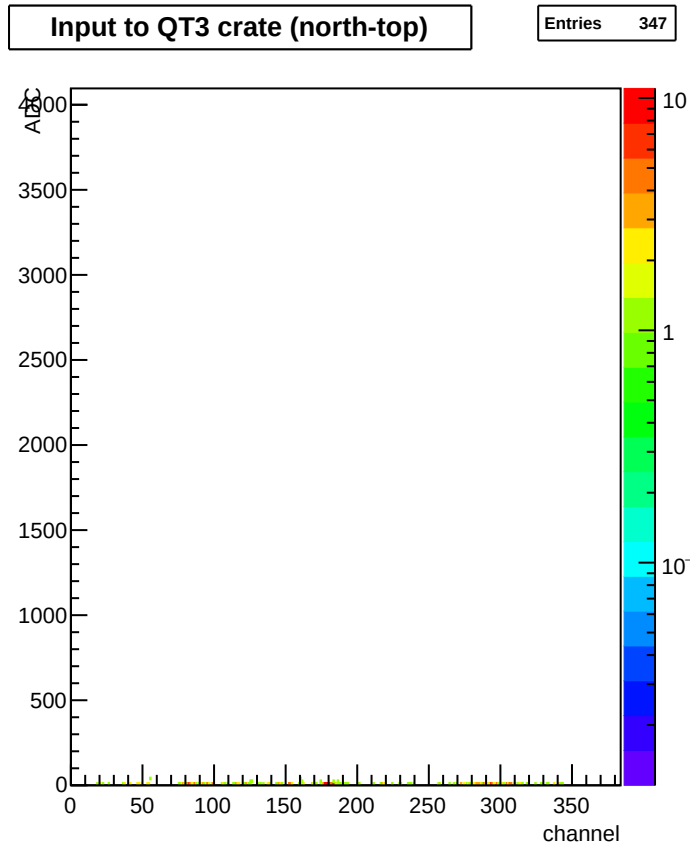

Entries 1000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

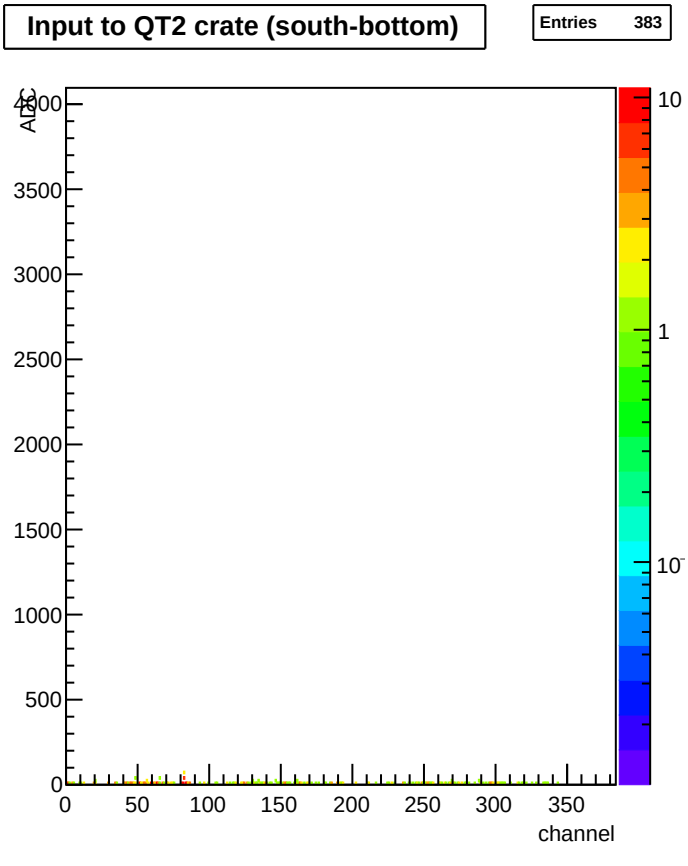

Entries 1000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

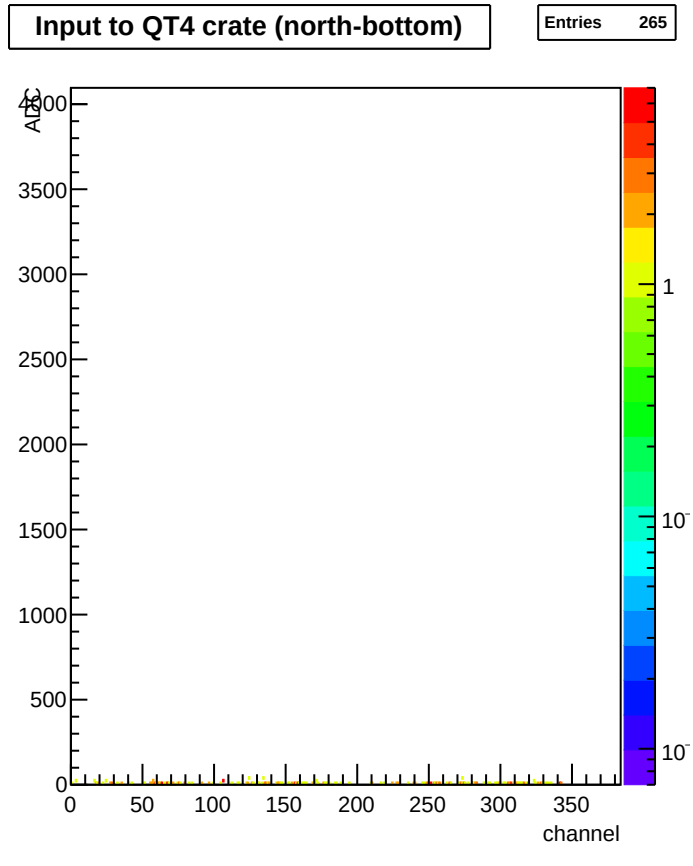

Entries 1000
VPD East ADC Sum
VPD West ADC Sum

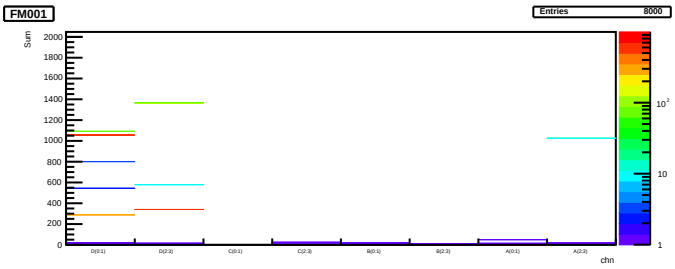

Entries 1000
BBC-S TAC Diff
ZDC TAC Diff

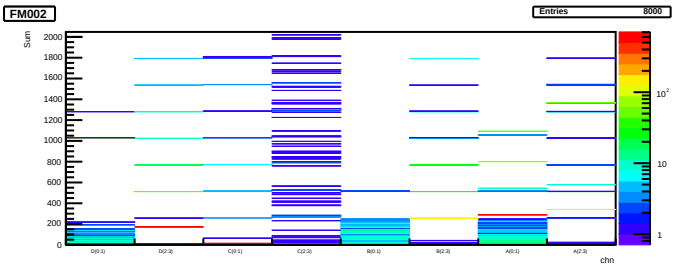

Input to QT1 crate (south-top)
Entries 460
ADC
channel

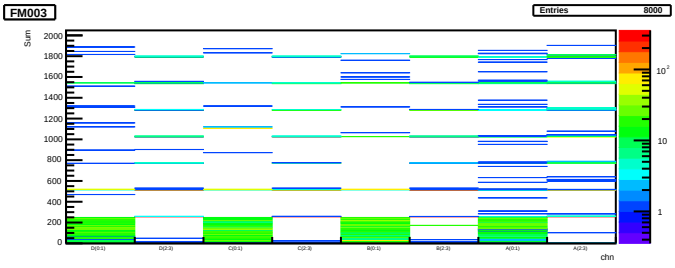

Input to QT3 crate (north-top)
Entries 347
ADC
channel

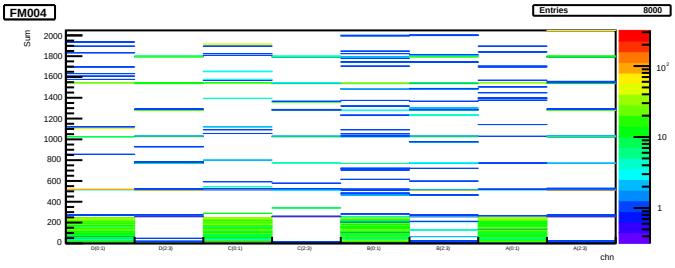

Input to QT2 crate (south-bottom)
Entries 383
ADC
channel

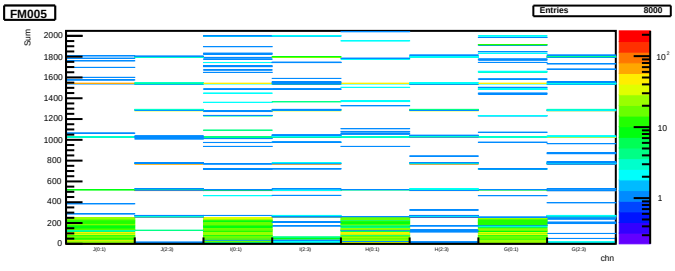

Input to QT4 crate (north-bottom)
Entries 265
ADC
channel

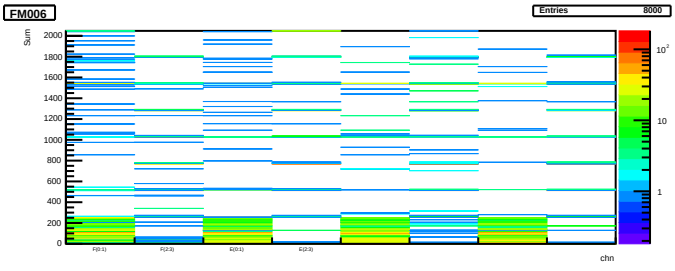

FM001
Entries 8000
Sum
chn

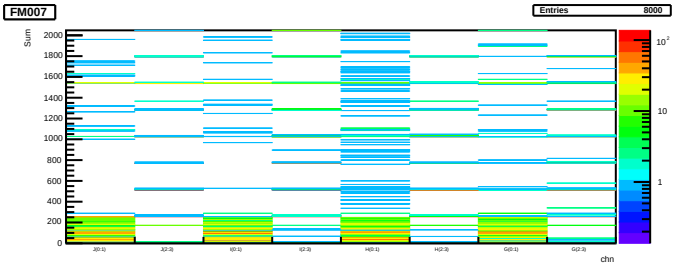

FM002
Entries 8000
Sum
chn

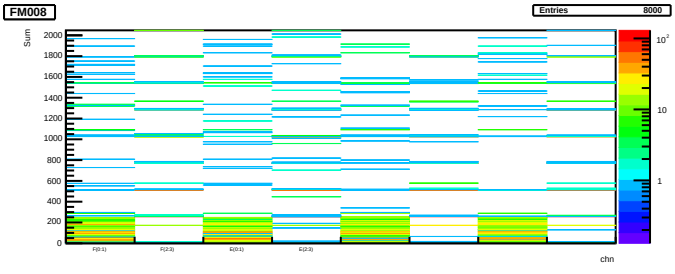

FM003
Entries 8000
Sum
chn

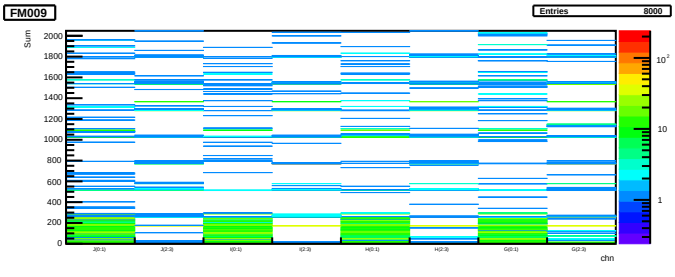

FM004
Entries 8000
Sum
chn

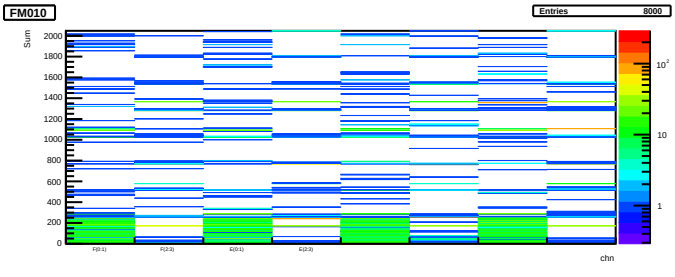

FM005
Entries 8000
Sum
chn

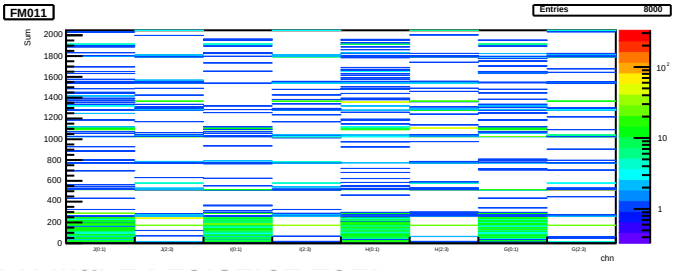

FM006
Entries 8000
Sum
chn

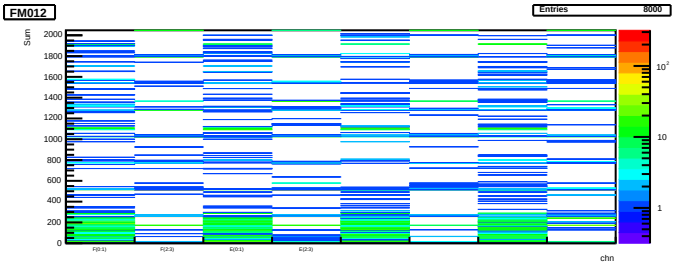

FM007
Entries 8000
Sum
chn

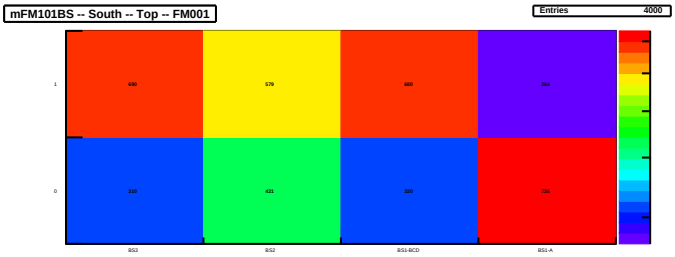

FM008
Entries 8000
Sum
chn

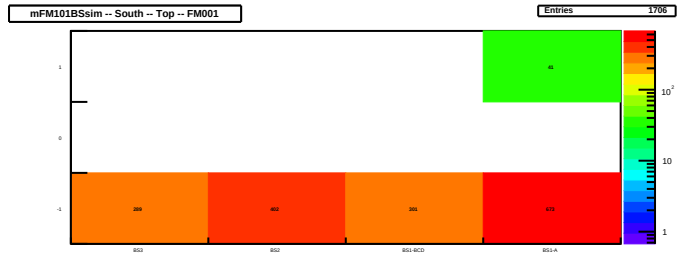

FM009
Entries 8000
Sum
chn

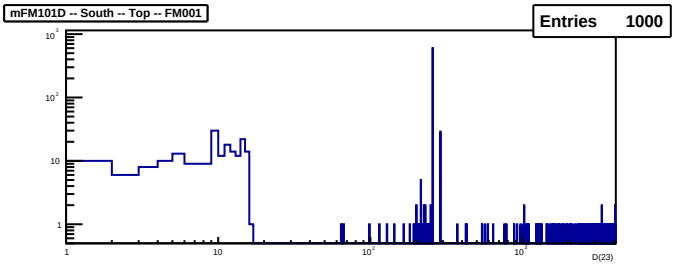

FM010
Entries 8000
Sum
chn

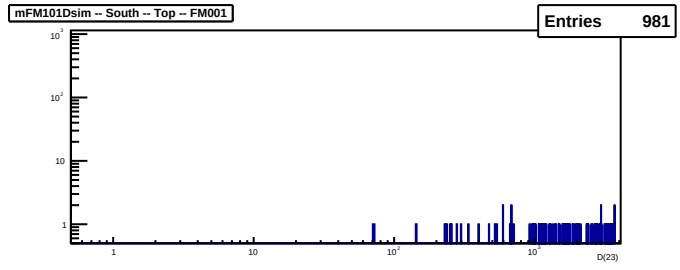

FM011
Entries 8000
Sum
chn

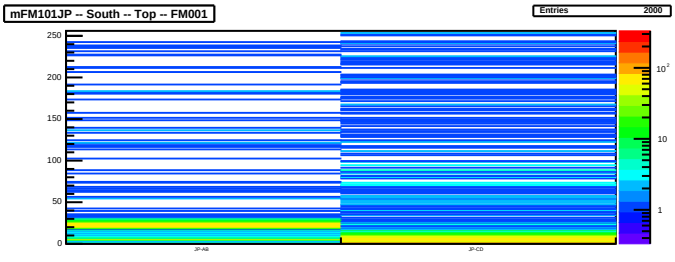

FM012
Entries 8000
Sum
chn

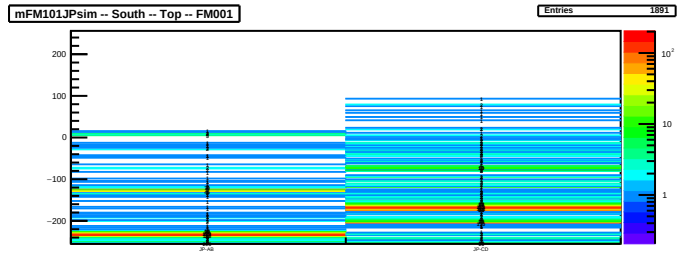

mFM101BS -- South -- Top -- FM001
Entries 4000


mFM101BSsim -- South -- Top -- FM001
Entries 1706


mFM101D -- South -- Top -- FM001
Entries 1000
D(23)


mFM101Dsim -- South -- Top -- FM001
Entries 981
D(23)


mFM101JP -- South -- Top -- FM001
Entries 2000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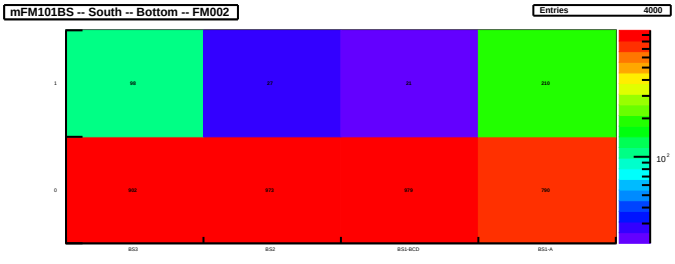
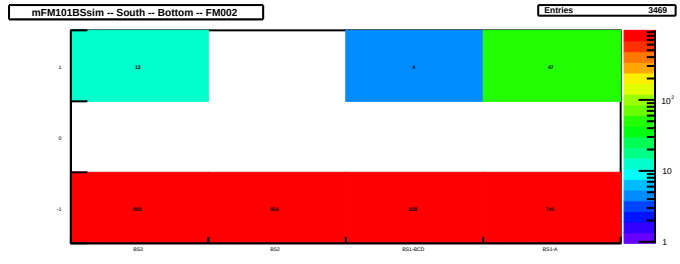
Entries 1891


mFM101BS -- South -- Bottom -- FM002
Entries 4000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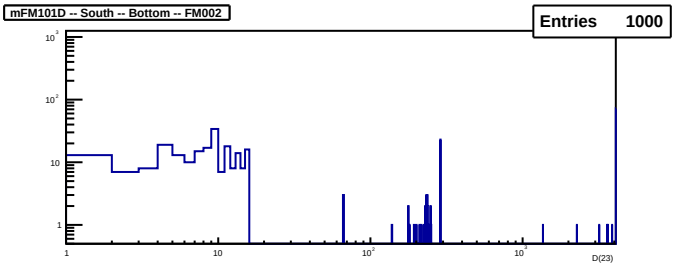
Entries 3469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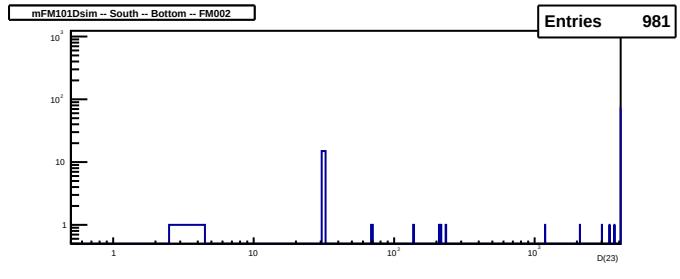
Entries 1000
D(23)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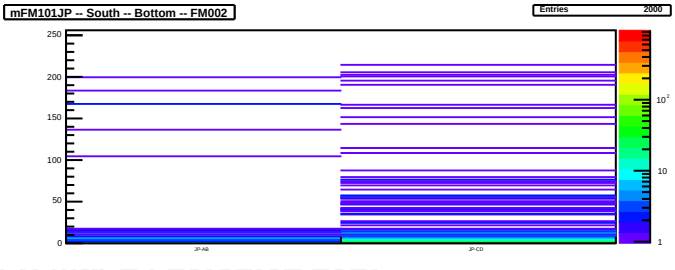
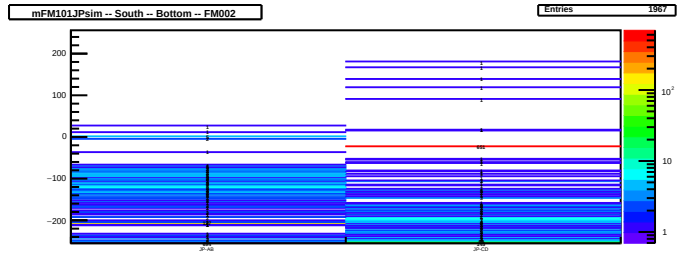
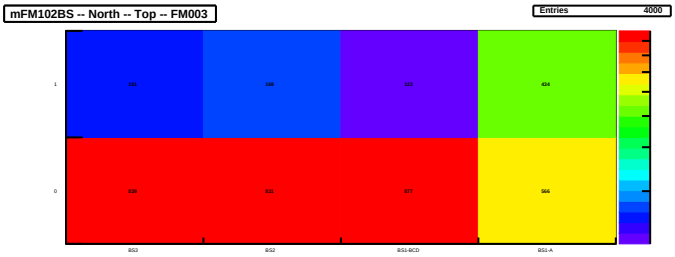
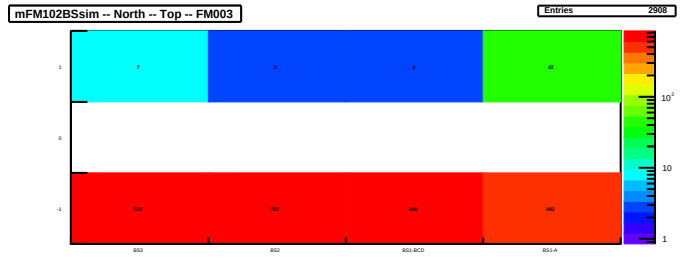
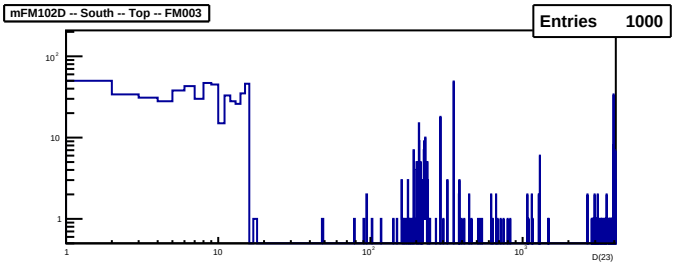
Entries 981
D(23)


mFM101JP -- South -- Bottom -- FM002
Entries 2000


mFM101JPsim -- South -- Bottom -- FM002
Entries 1967


mFM102BS -- North -- Top -- FM003
Entries 4000


mFM102BSsim -- North -- Top -- FM003
Entries 2908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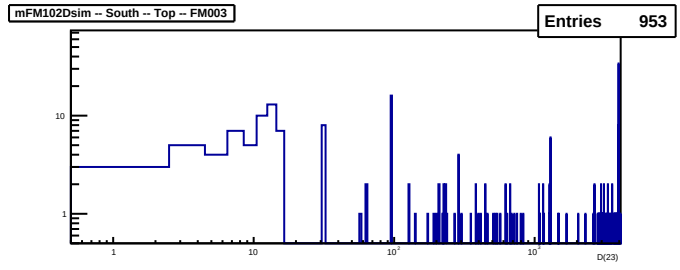
Entries 1000
D(23)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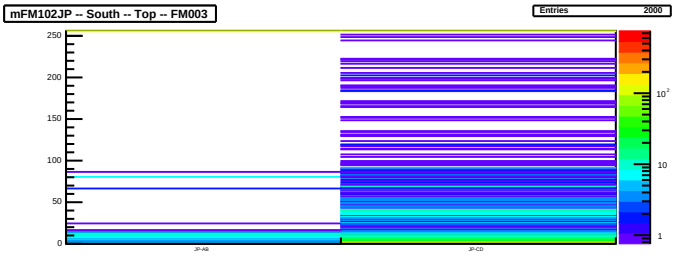
Entries 953
D(23)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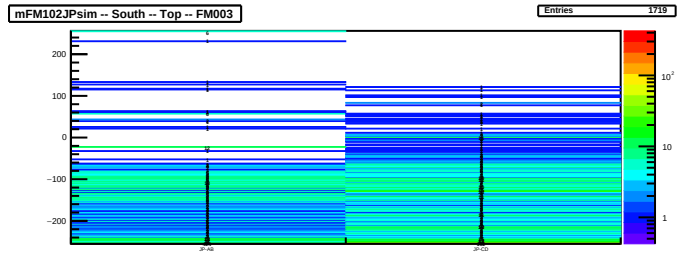
Entries 2000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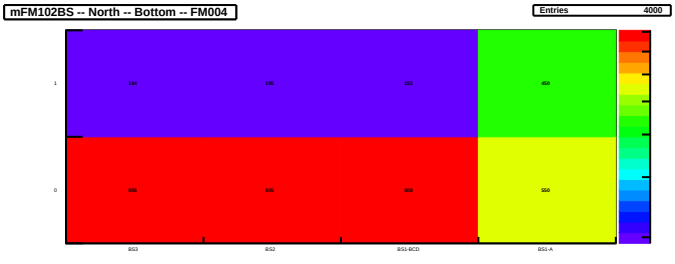
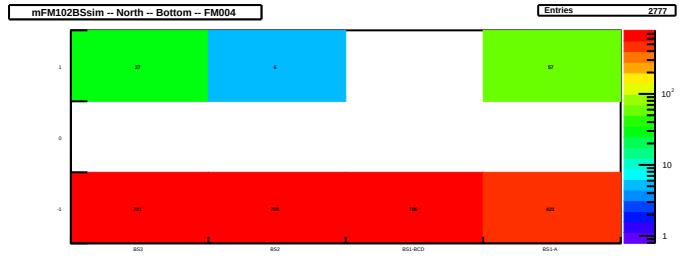
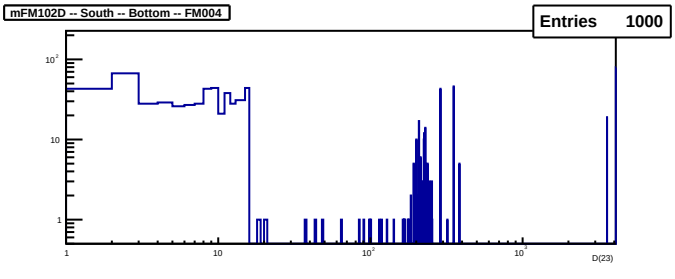
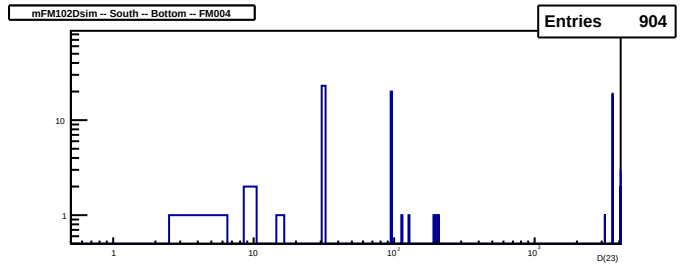
Entries 1719


mFM102BS -- North -- Bottom -- FM004
Entries 4000


mFM102BSsim -- North -- Bottom -- FM004
Entries 2777


mFM102D -- South -- Bottom -- FM004
Entries 1000
D(23)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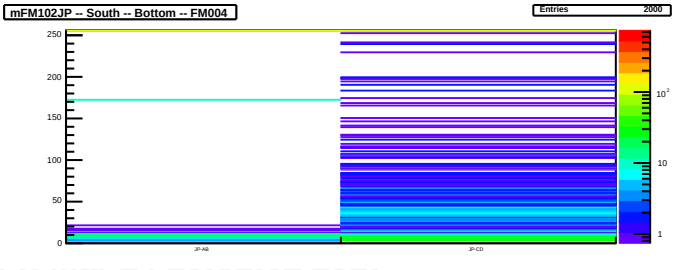
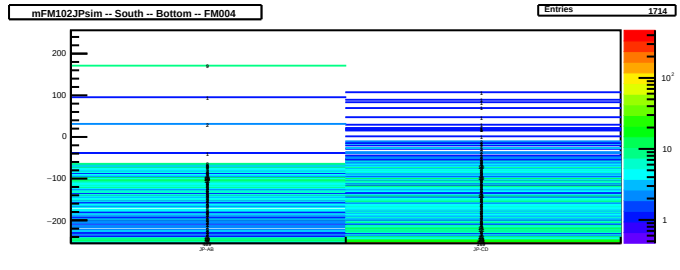
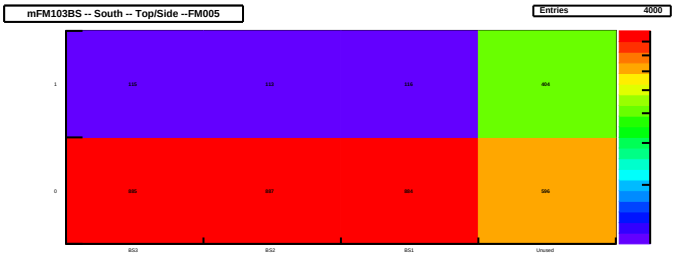
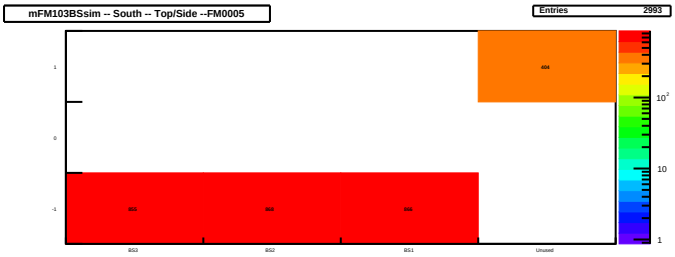
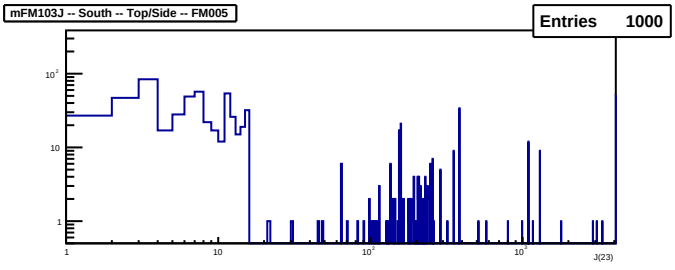
Entries 904
D(23)

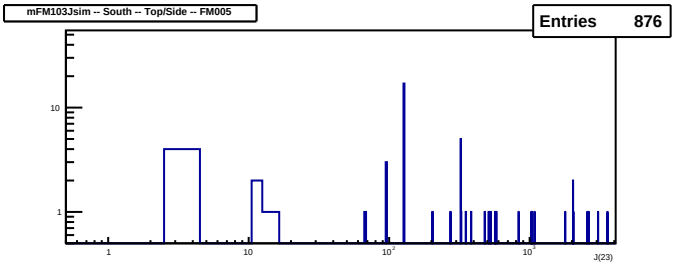

mFM102JP -- South -- Bottom -- FM004
Entries 2000


mFM102JPsim -- South -- Bottom -- FM004
Entries 1714


mFM103BS -- South -- Top/Side --FM005
Entries 4000


mFM103BSsim -- South -- Top/Side --FM0005
Entries 2993


mFM103J -- South -- Top/Side -- FM005
Entries 1000
J(23)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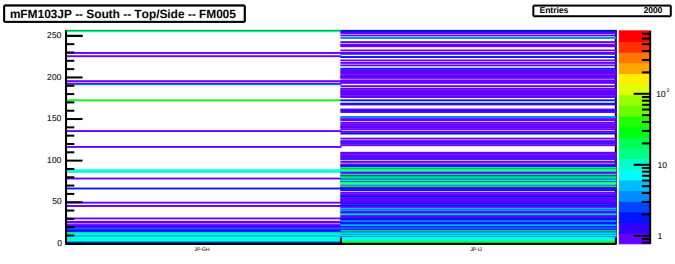
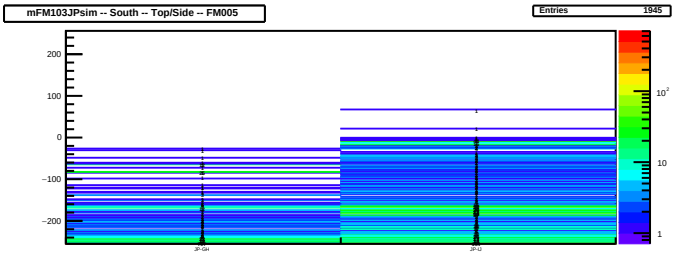
Entries 876
J(23)


mFM103JP -- South -- Top/Side -- FM005
Entries 2000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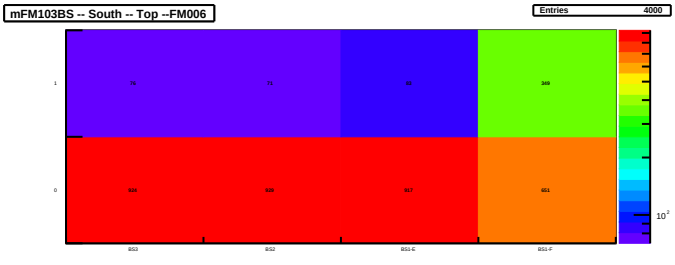
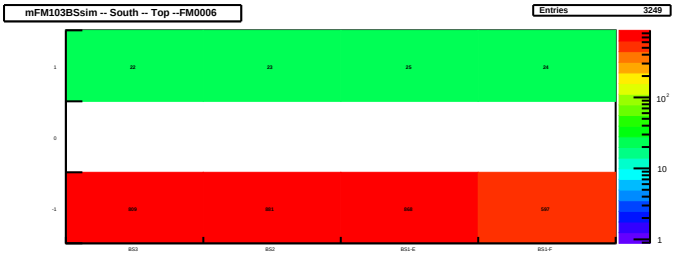
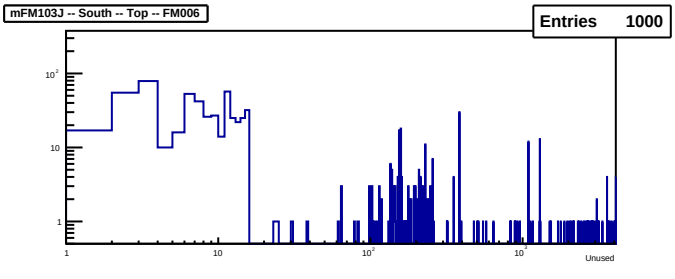
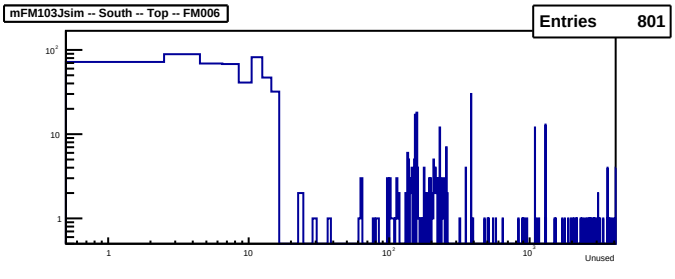
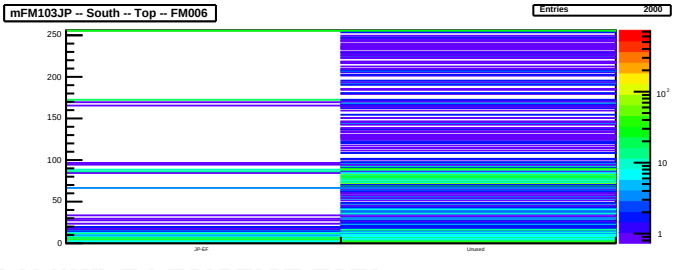
Entries 1945

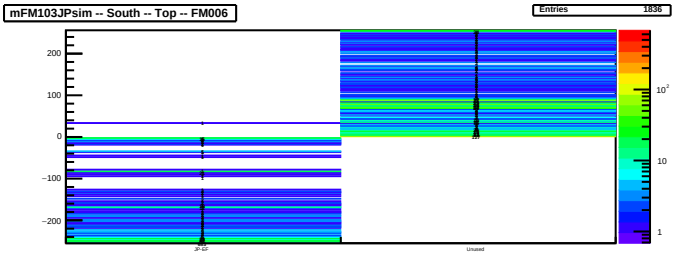

mFM103BS -- South -- Top --FM006
Entries 4000

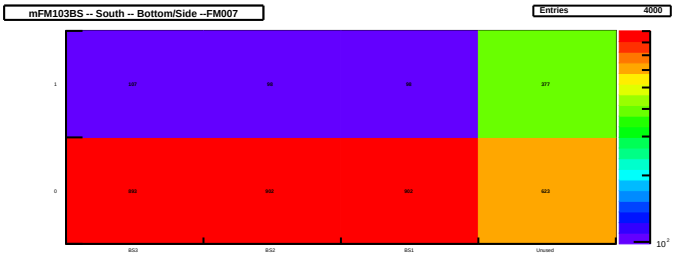

mFM103BSsim -- South -- Top --FM0006
Entries 3249

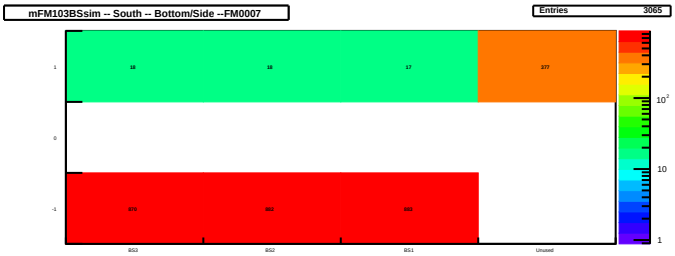

mFM103J -- South -- Top -- FM006
Entries 1000
Unused

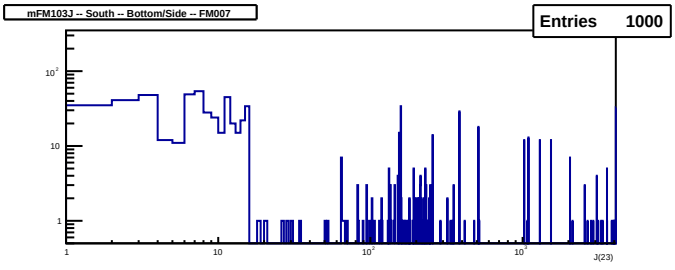

mFM103Jsim -- South -- Top -- FM006
Entries 801
Unused

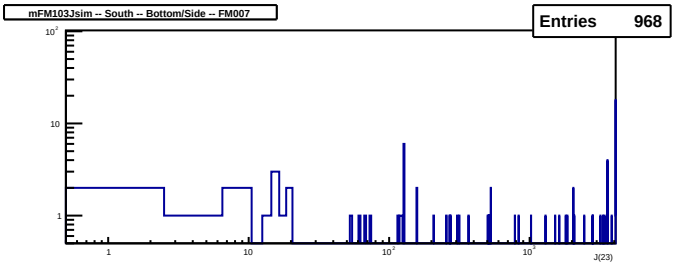

mFM103JP -- South -- Top -- FM006
Entries 2000

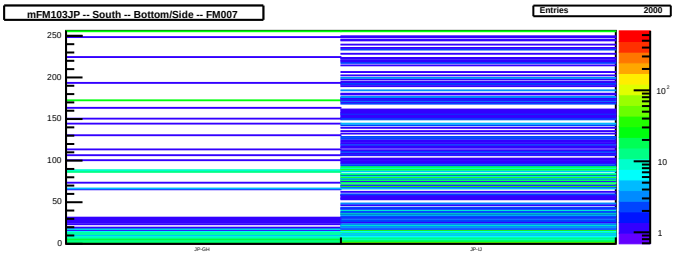

mFM103JPsim -- South -- Top -- FM006
Entries 1836

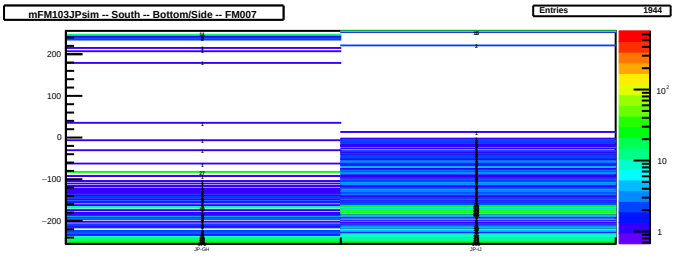

mFM103BS -- South -- Bottom/Side --FM007
Entries 4000
BS3
BS2
BS1
Unused

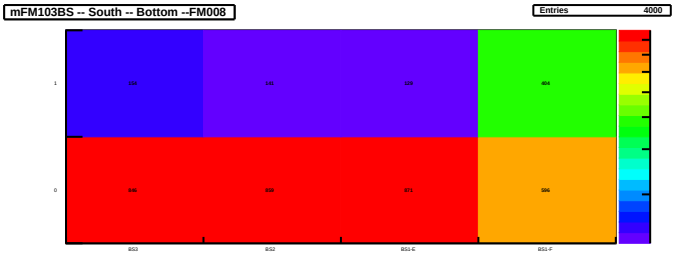

mFM103BSsim -- South -- Bottom/Side --FM0007
Entries 3065
BS3
BS2
BS1
Unused

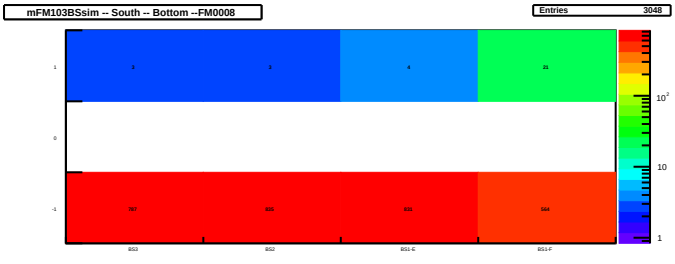

mFM103J -- South -- Bottom/Side -- FM007
Entries 1000
J(23)

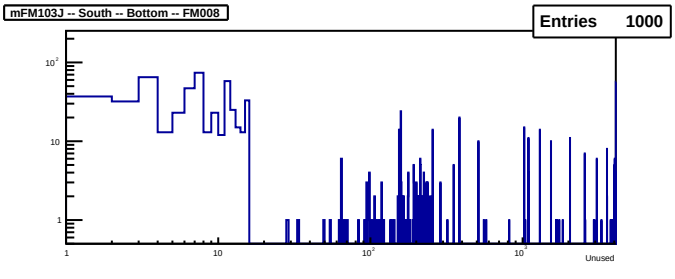

mFM103Jsim -- South -- Bottom/Side -- FM007
Entries 968
J(23)

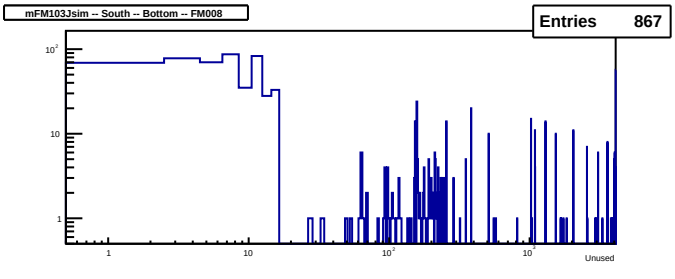

mFM103JP -- South -- Bottom/Side -- FM007
Entries 2000

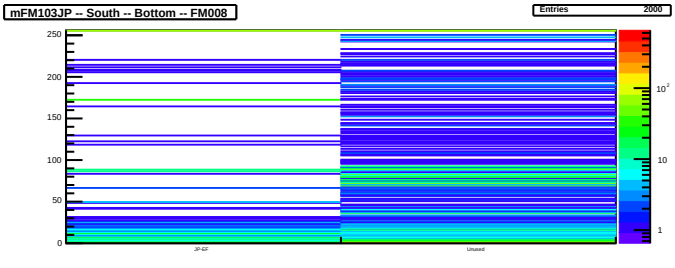

mFM103JPsim -- South -- Bottom/Side -- FM007
Entries 1944

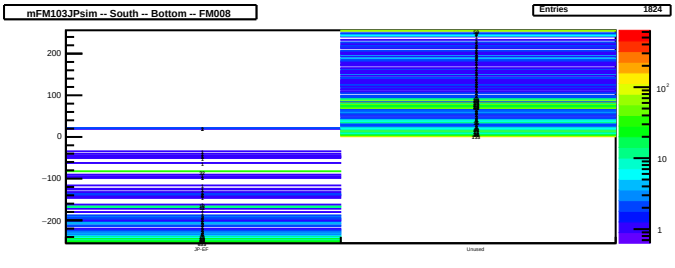

mFM103BS -- South -- Bottom --FM008
Entries 4000
BS3
BS2
BS1-E
BS1-F


mFM103BSsim -- South -- Bottom --FM0008
Entries 3048
BS3
BS2
BS1-E
BS1-F


mFM103J -- South -- Bottom -- FM008
Entries 1000
Unused


mFM103Jsim -- South -- Bottom -- FM008
Entries 867
Unused


mFM103JP -- South -- Bottom -- FM008
Entries 2000
Unused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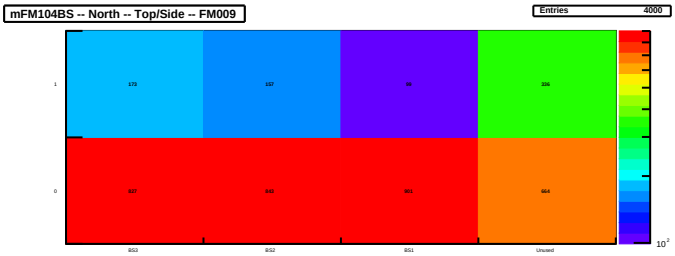
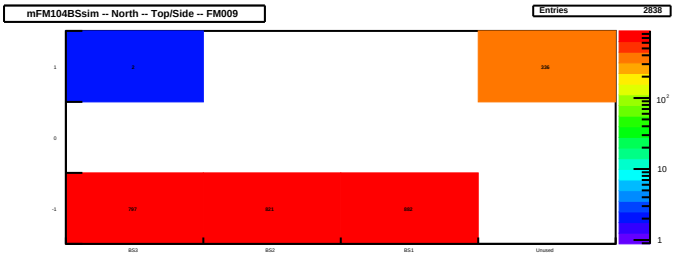
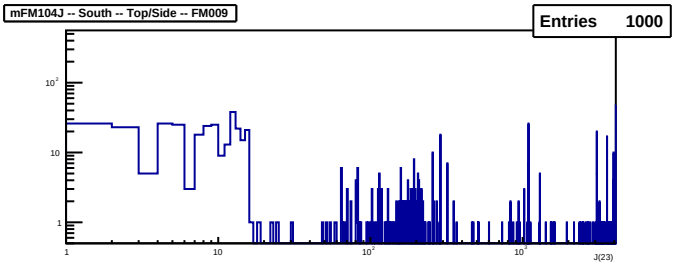
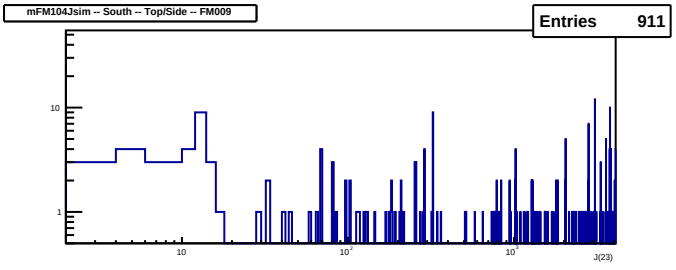
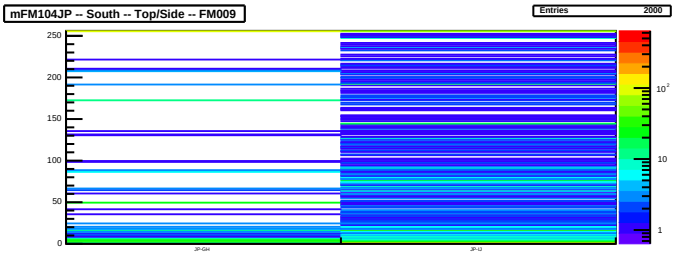
Entries 1824
Unused

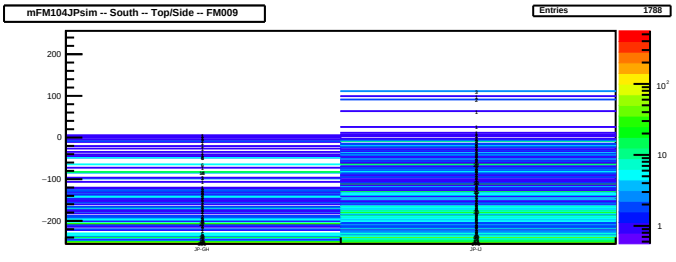

mFM104BS -- North -- Top/Side -- FM009
Entries 4000


mFM104BSsim -- North -- Top/Side -- FM009
Entries 2838


mFM104J -- South -- Top/Side -- FM009
Entries 1000
J(23)


mFM104Jsim -- South -- Top/Side -- FM009
Entries 911
J(23)


mFM104JP -- South -- Top/Side -- FM009
Entries 2000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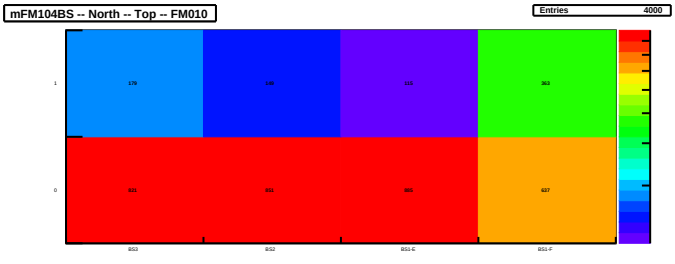
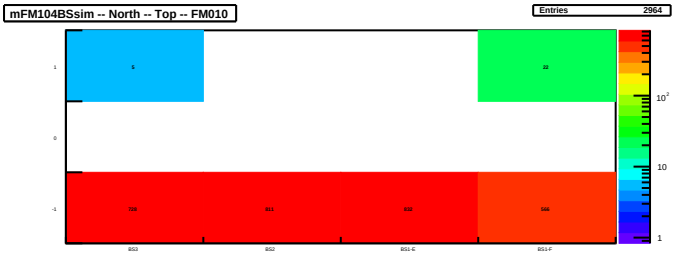
Entries 1788


mFM104BS -- North -- Top -- FM010
Entries 4000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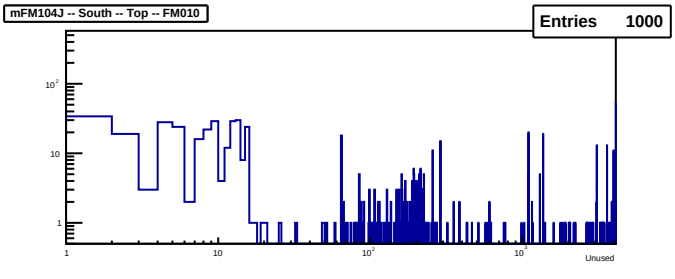
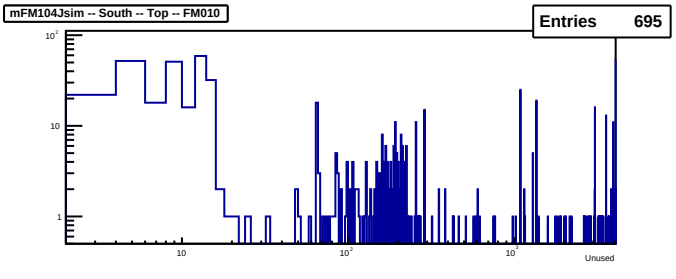
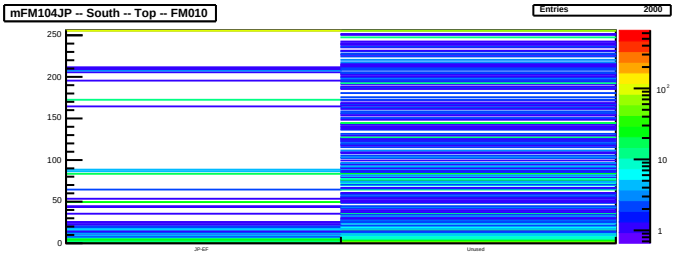
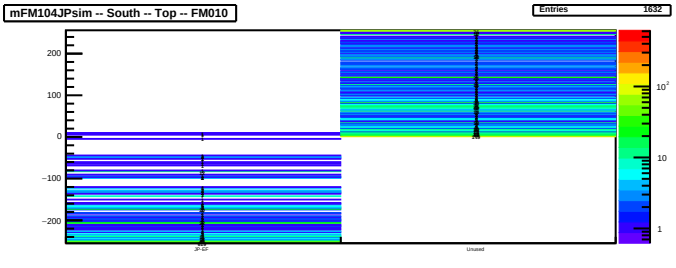
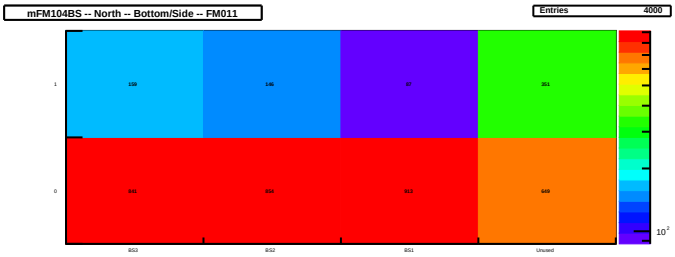
Entries 2964

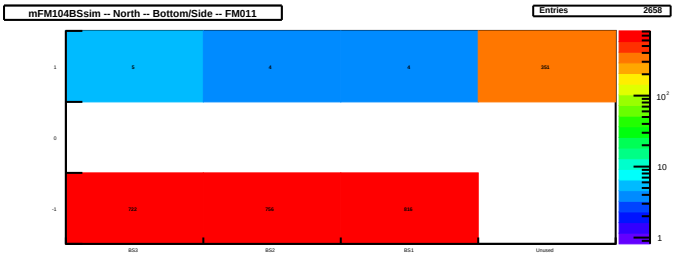

mFM104J -- South -- Top -- FM010
Entries 1000
Unused

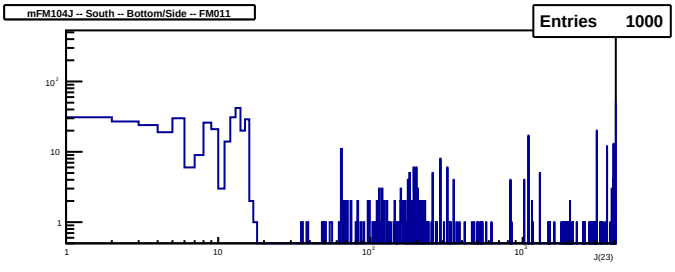

mFM104Jsim -- South -- Top -- FM010
Entries 695
Unused

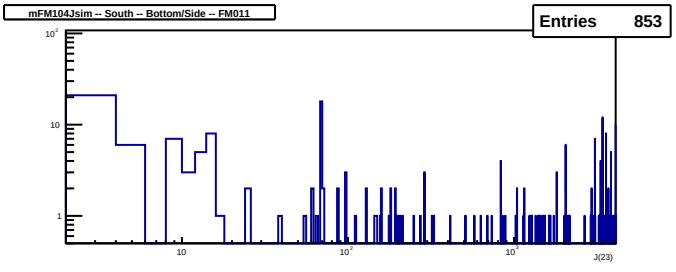

mFM104JP -- South -- Top -- FM010
Entries 2000


mFM104JPsim -- South -- Top -- FM010
Entries 1632


mFM104BS -- North -- Bottom/Side -- FM011
Entries 4000


mFM104BSsim -- North -- Bottom/Side -- FM011
Entries 2658


mFM104J -- South -- Bottom/Side -- FM011
Entries 1000
J(23)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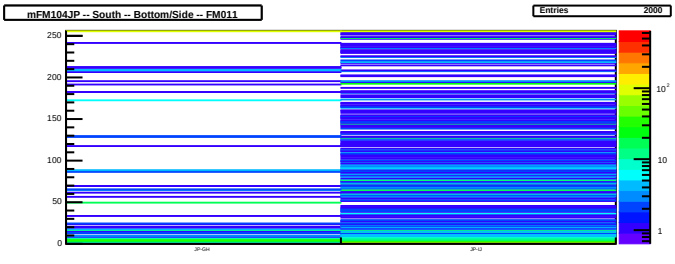
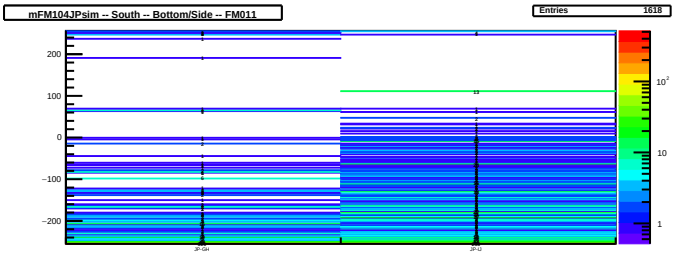
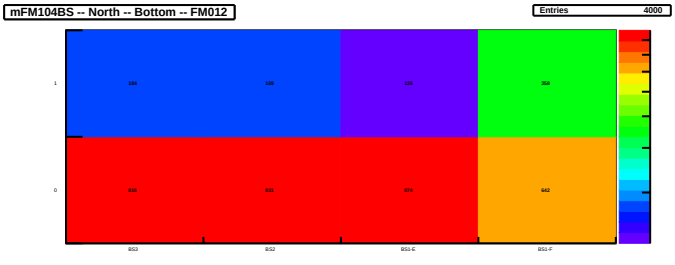
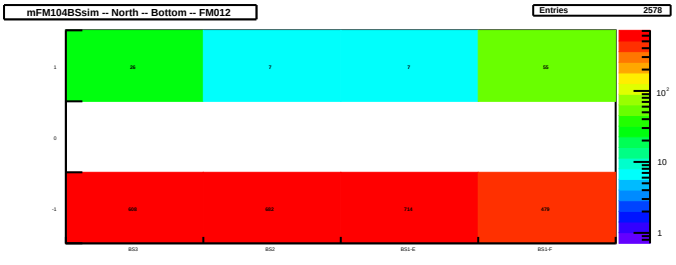
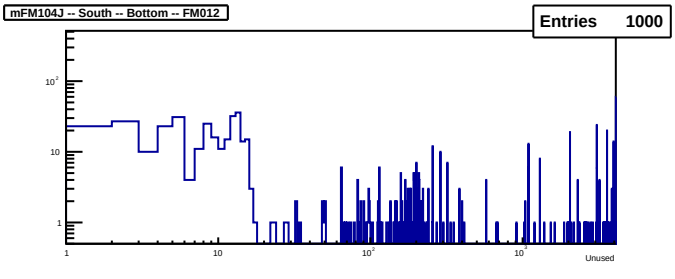
Entries 853
J(23)

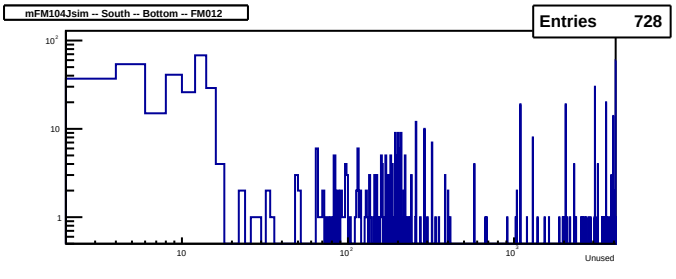

mFM104JP -- South -- Bottom/Side -- FM011
Entries 2000

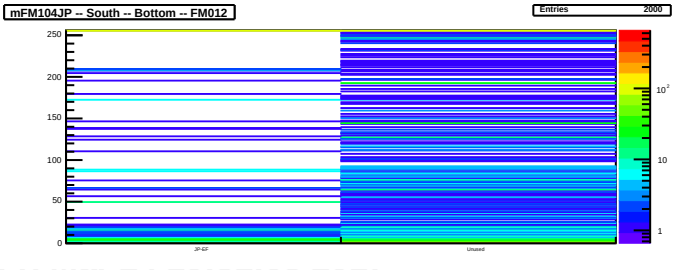

mFM104JPsim -- South -- Bottom/Side -- FM011
Entries 1618

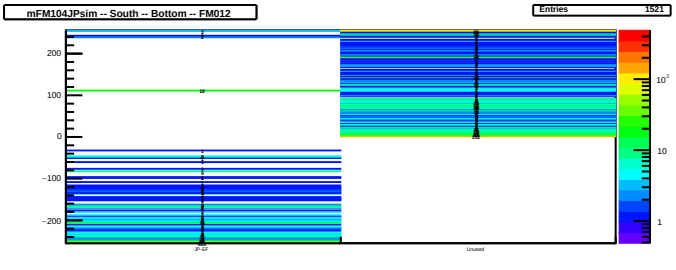

mFM104BS -- North -- Bottom -- FM012
Entries 4000

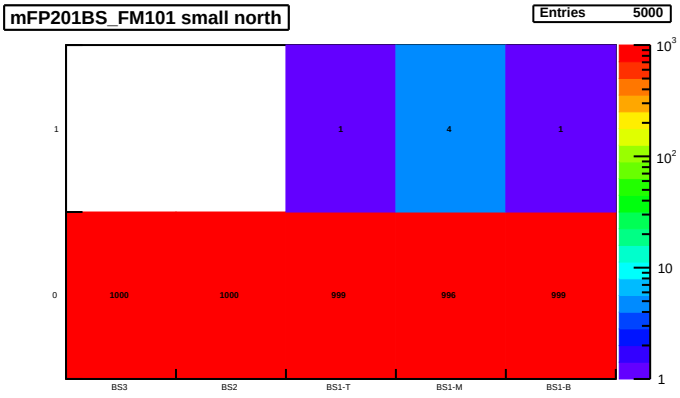

mFM104BSsim -- North -- Bottom -- FM012
Entries 2578

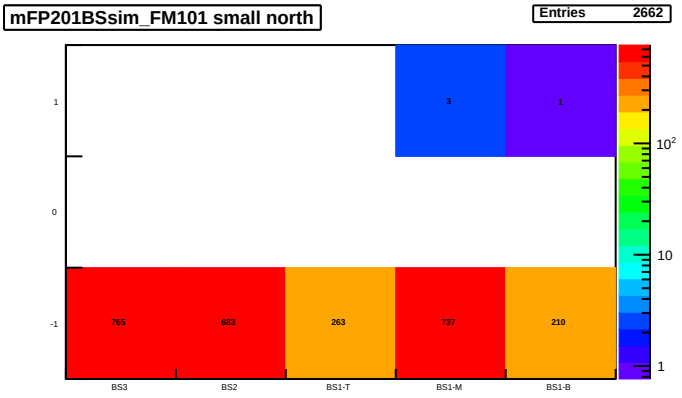

mFM104J -- South -- Bottom -- FM012
Entries 1000
Unused

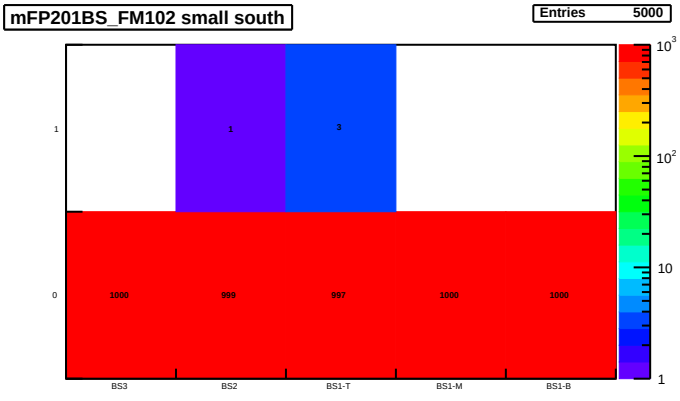

mFM104Jsim -- South -- Bottom -- FM012
Entries 728
Unused

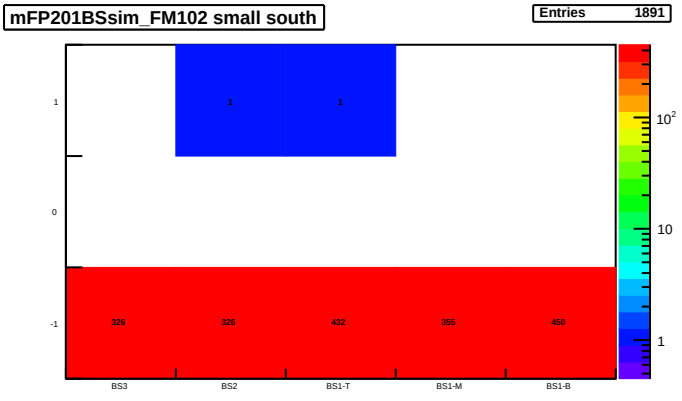

mFM104JP -- South -- Bottom -- FM012
Entries 2000

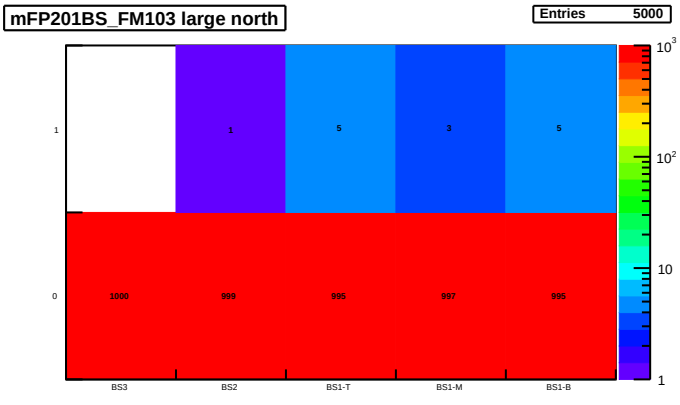

mFM104JPsim -- South -- Bottom -- FM012
Entries 1521

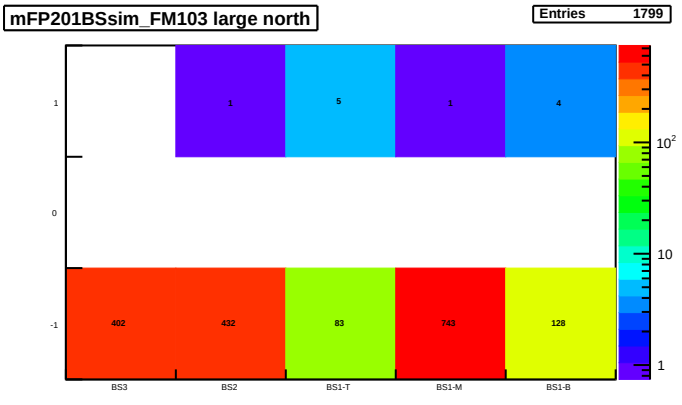

mFP201BS_FM101 small north
Entries 5000
1 4 1
1000 1000 999 996 999
BS3 BS2 BS1-T BS1-M BS1-B

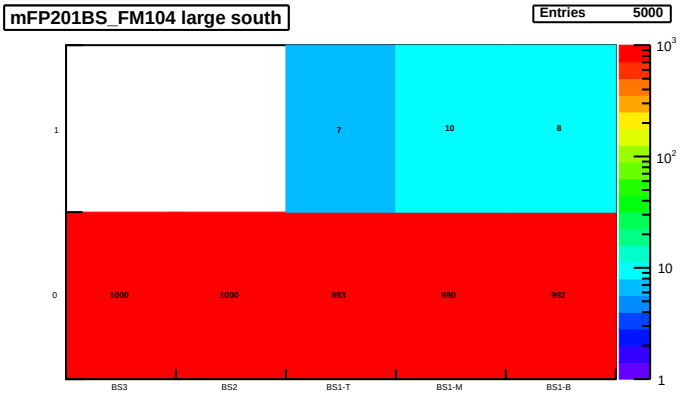

mFP201BSsim_FM101 small north
Entries 2662
3 1
765 683 263 737 210
BS3 BS2 BS1-T BS1-M BS1-B

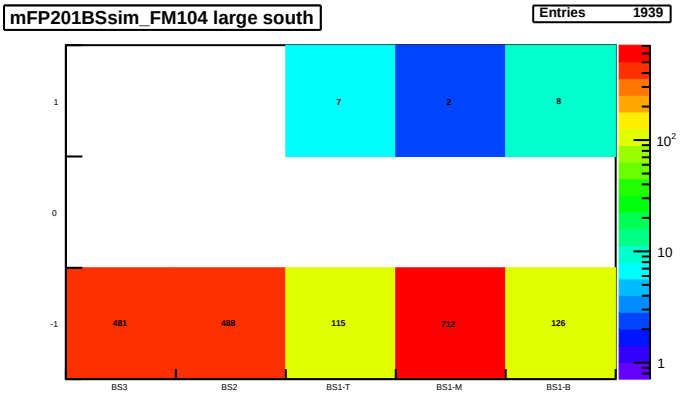

mFP201BS_FM102 small south
Entries 5000
1 3
1000 999 997 1000 1000
BS3 BS2 BS1-T BS1-M BS1-B

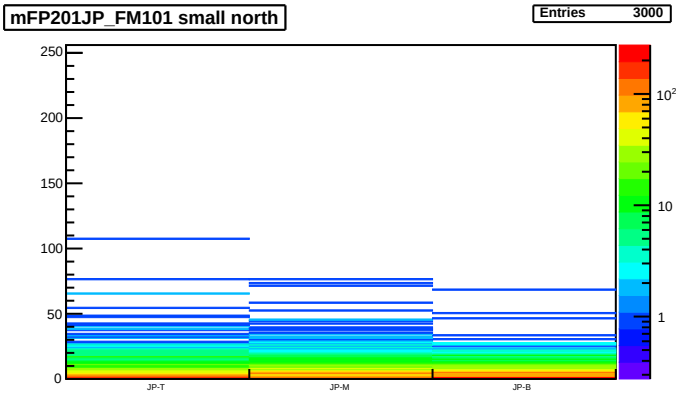

mFP201BSsim_FM102 small south
Entries 1891
1 1
326 326 432 355 450
BS3 BS2 BS1-T BS1-M BS1-B

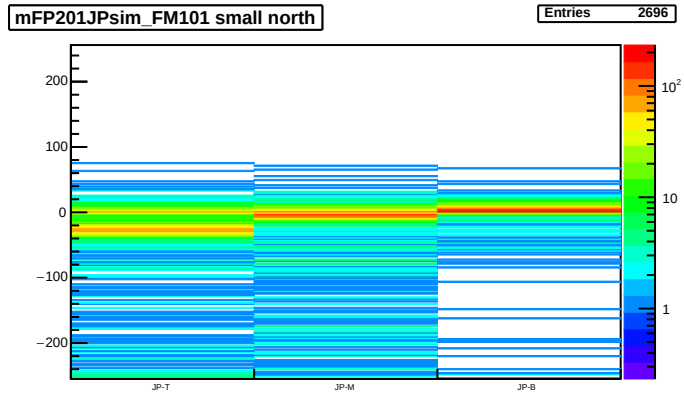

mFP201BS_FM103 large north
Entries 5000
1 5 3 5
1000 999 995 997 995
BS3 BS2 BS1-T BS1-M BS1-B

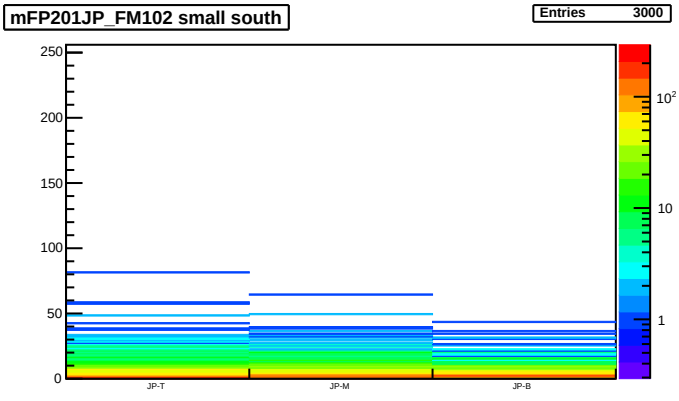

mFP201BSsim_FM103 large north
Entries 1799
1 5 1 4
402 432 83 743 128
BS3 BS2 BS1-T BS1-M BS1-B

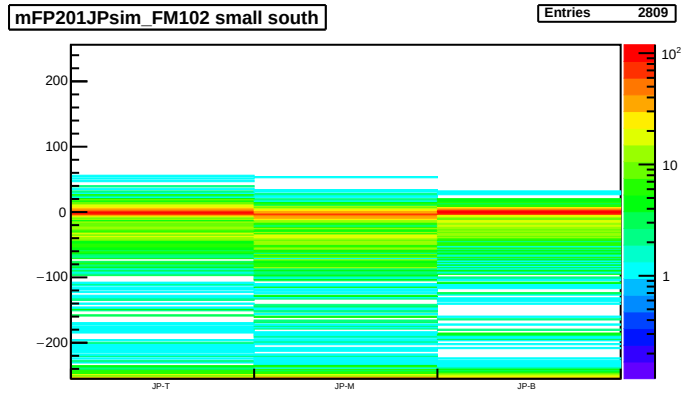

mFP201BS_FM104 large south
Entries 5000
7 10 8
1000 1000 993 990 992
BS3 BS2 BS1-T BS1-M BS1-B

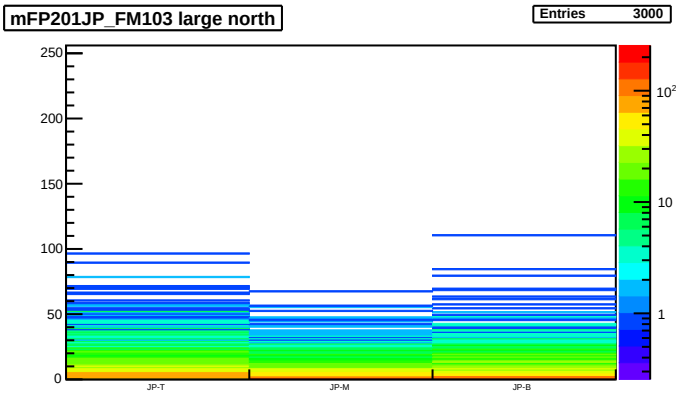

mFP201BSsim_FM104 large south
Entries 1939
7 2 8
481 488 115 712 126
BS3 BS2 BS1-T BS1-M BS1-B

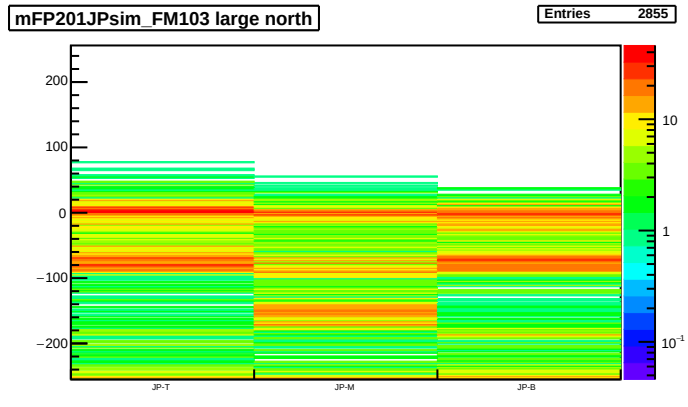

mFP201JP_FM101 small north
Entries 3000
JP-T
JP-M
JP-B

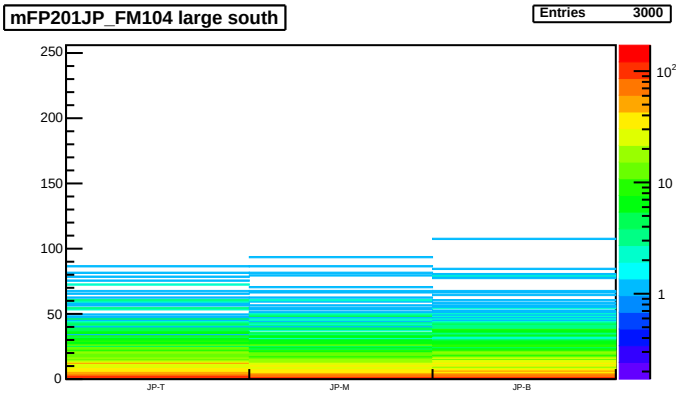

mFP201JPsim_FM101 small north
Entries 2696
JP-T
JP-M
JP-B

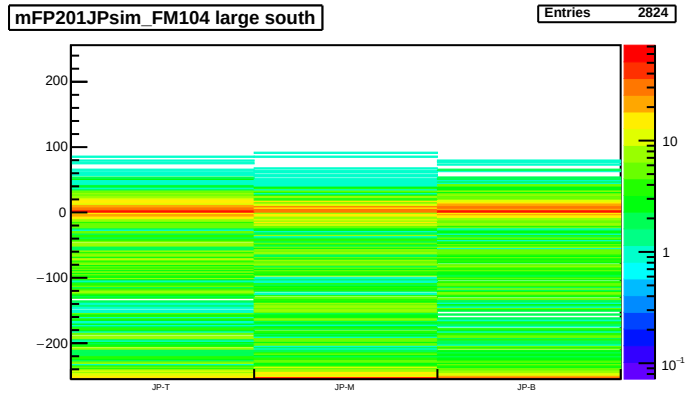

mFP201JP_FM102 small south
Entries 3000
JP-T
JP-M
JP-B

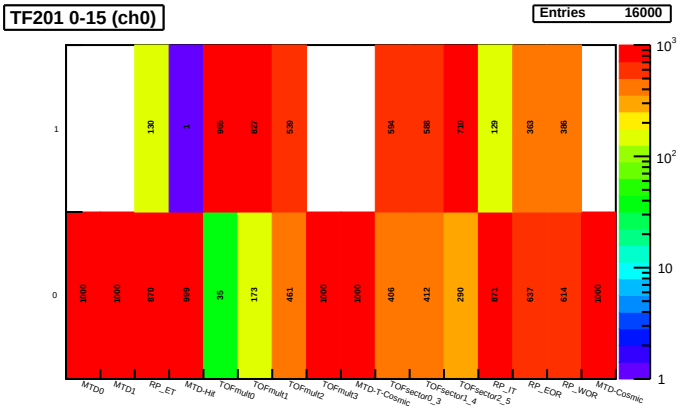

mFP201JPsim_FM102 small south
Entries 2809
JP-T
JP-M
JP-B

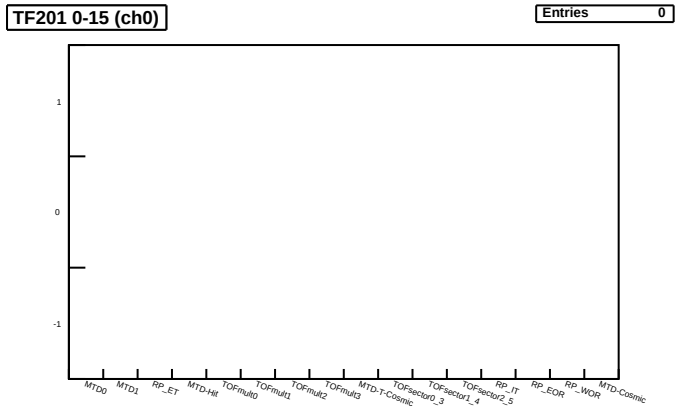

mFP201JP_FM103 large north
Entries 3000
JP-T
JP-M
JP-B

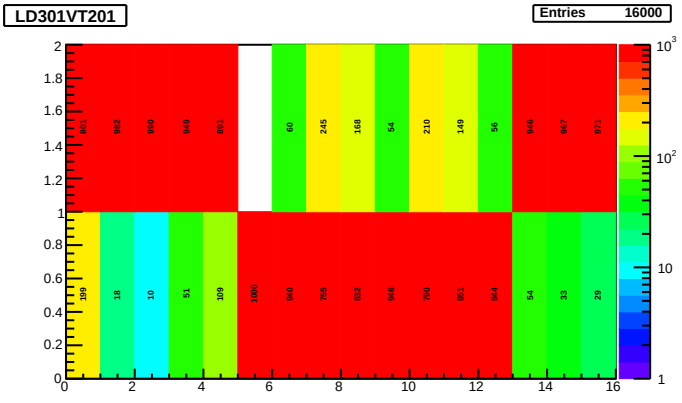

mFP201JPsim_FM103 large north
Entries 2855
JP-T
JP-M
JP-B

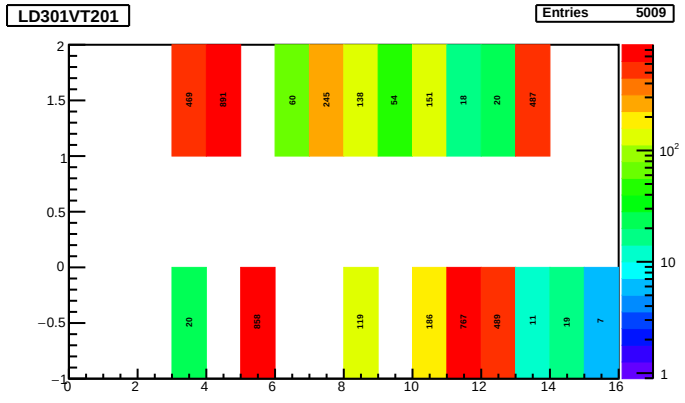

mFP201JP_FM104 large south
Entries 3000
JP-T
JP-M
JP-B

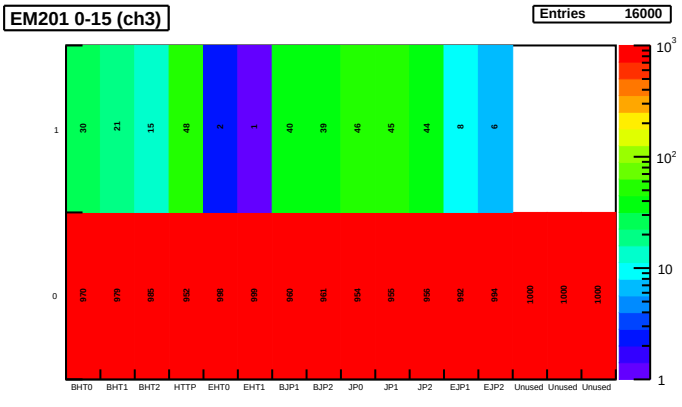

mFP201JPsim_FM104 large south
Entries 2824
JP-T
JP-M
JP-B

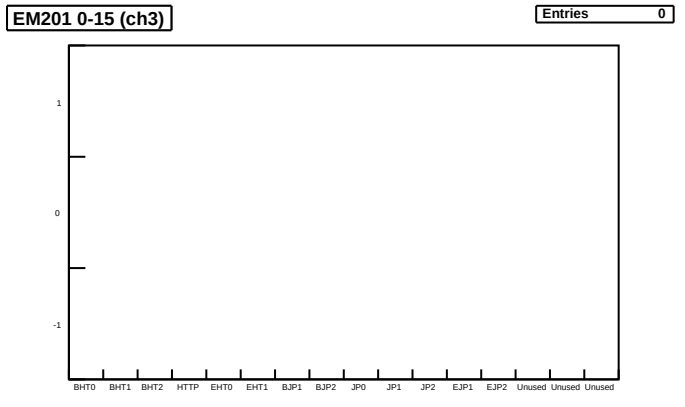

TF201 0-15 (ch0)
Entries 16000
MTD0 MTD1 RP_ET MTD-Hit TOFmult0 TOFmult1 TOFmult2 TOFmult3 MTD-T-Cosmic TOFsector0_3 TOFsector1_4 TOFsector2_5 RP_IT RP_EOR RP_WOR MTD-Cosmic

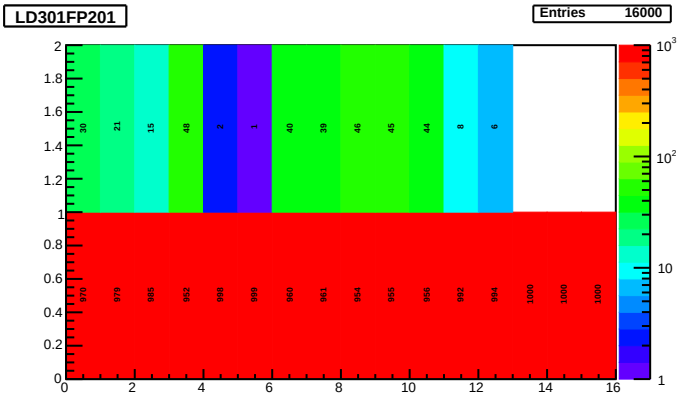

TF201 0-15 (ch0)
Entries 0
MTD0 MTD1 RP_ET MTD-Hit TOFmult0 TOFmult1 TOFmult2 TOFmult3 MTD-T-Cosmic TOFsector0_3 TOFsector1_4 TOFsector2_5 RP_IT RP_EOR RP_WOR MTD-Cosmic

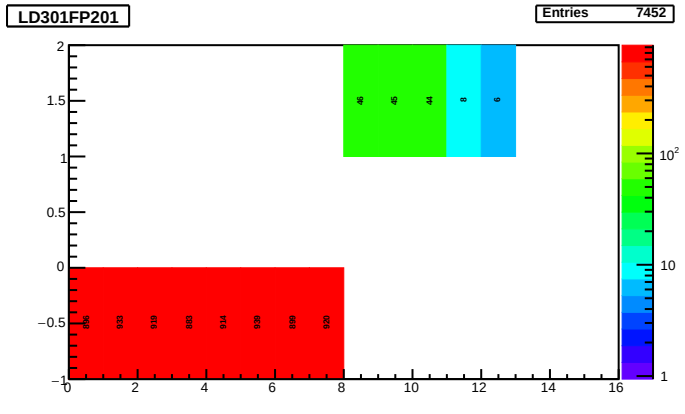

LD301VT201
Entries 16000


LD301VT201
Entries 5009


EM201 0-15 (ch3)
Entries 16000
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


LD301FP201
Entries 16000


LD301FP201
Entries 7452