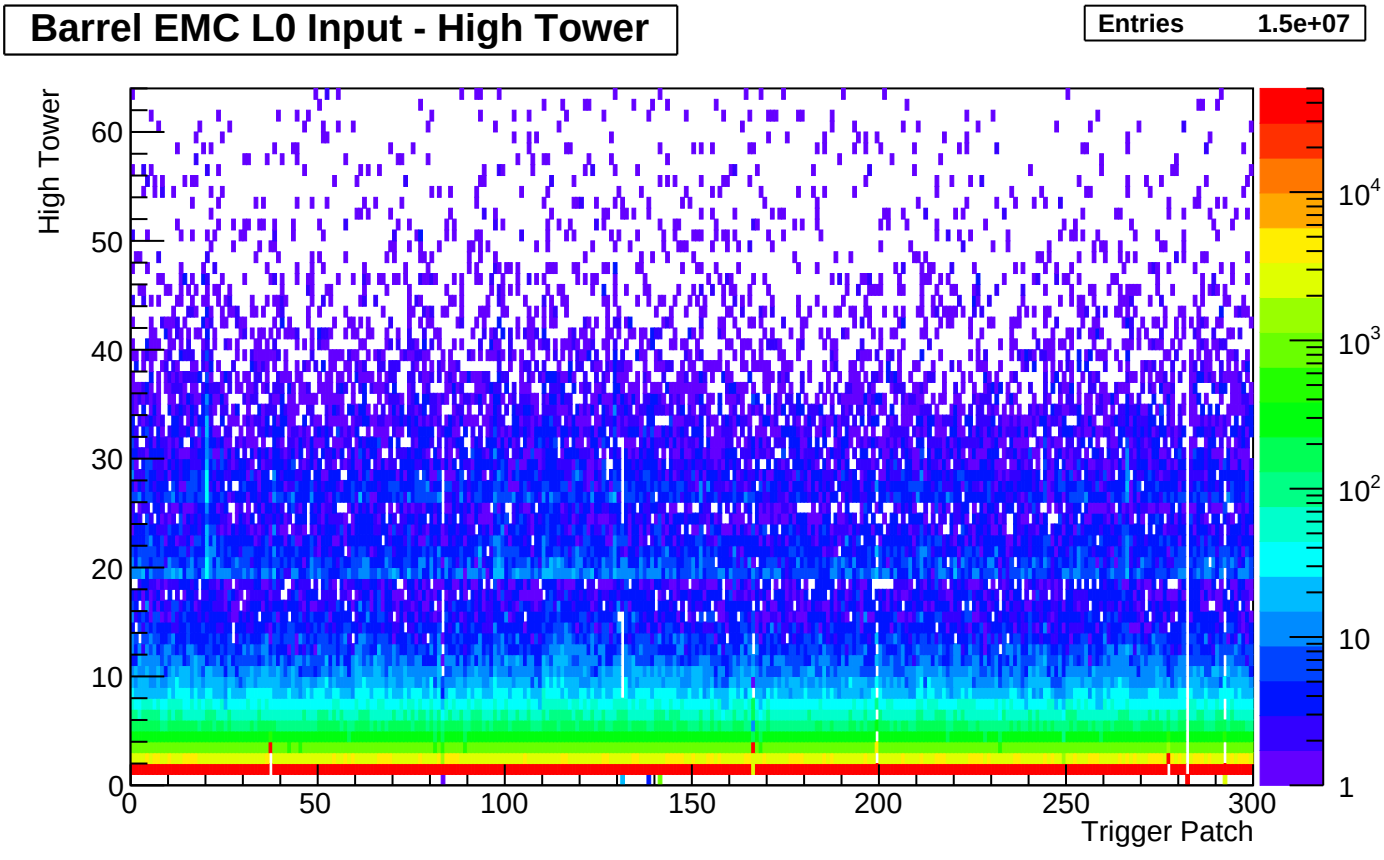
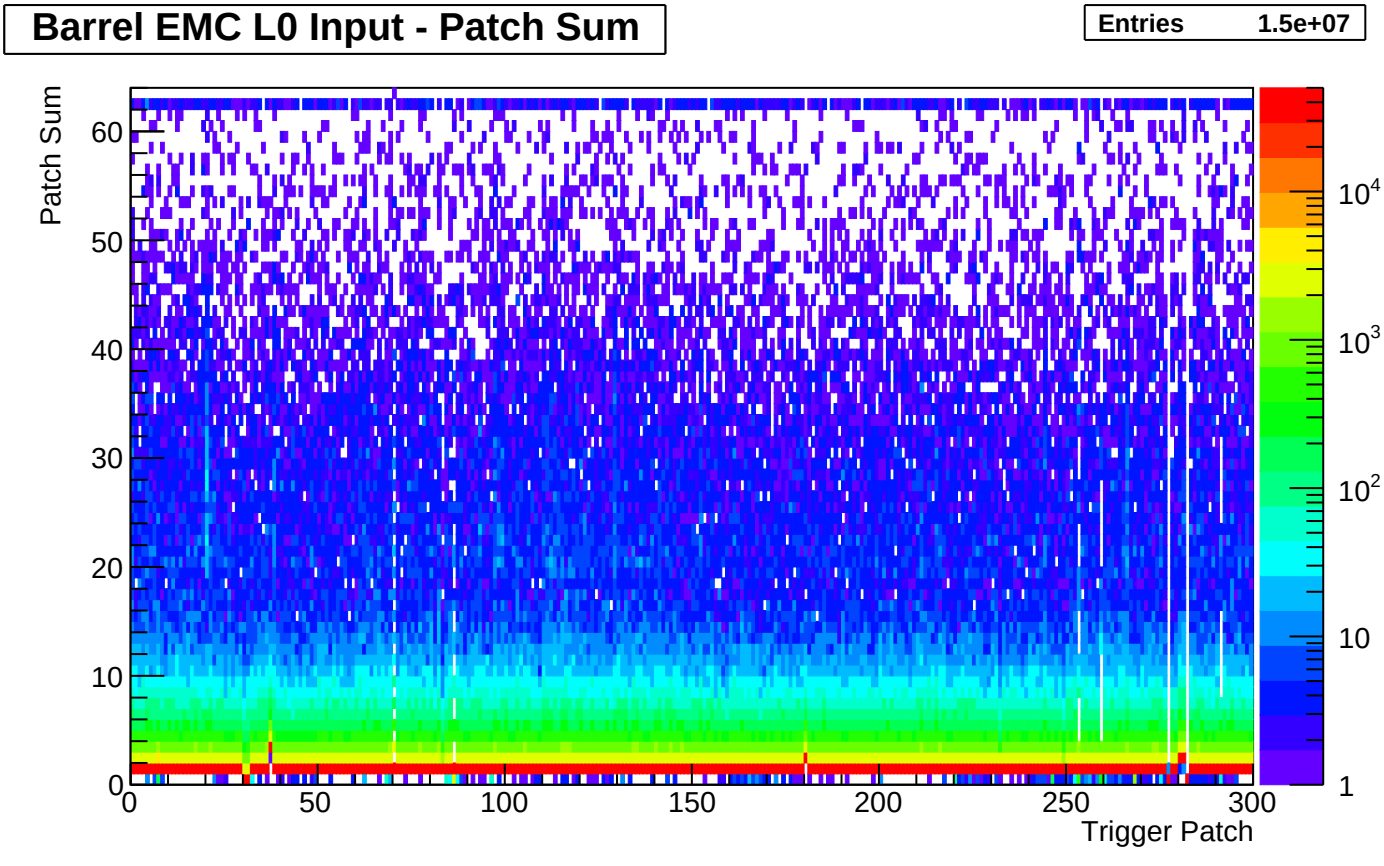
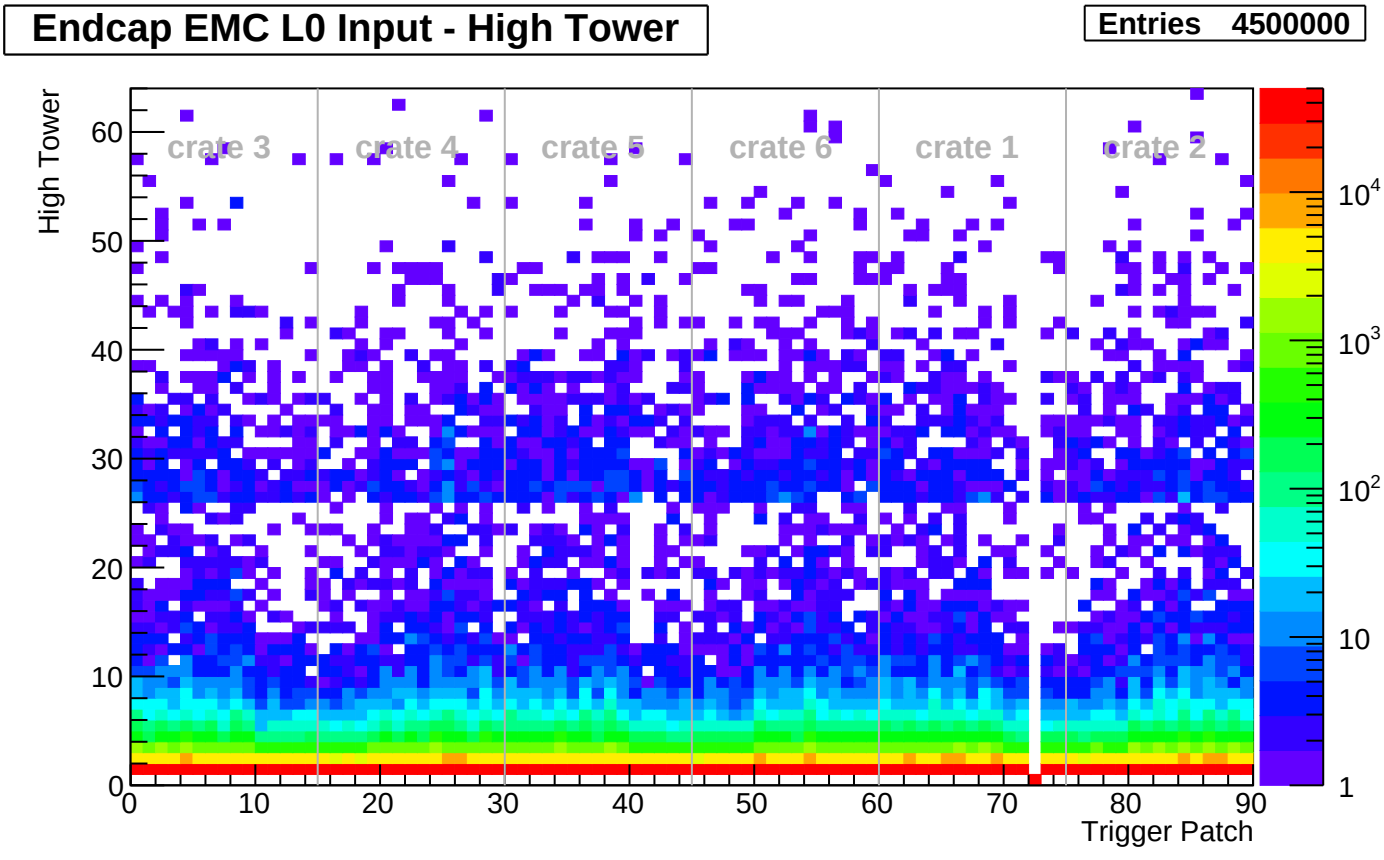
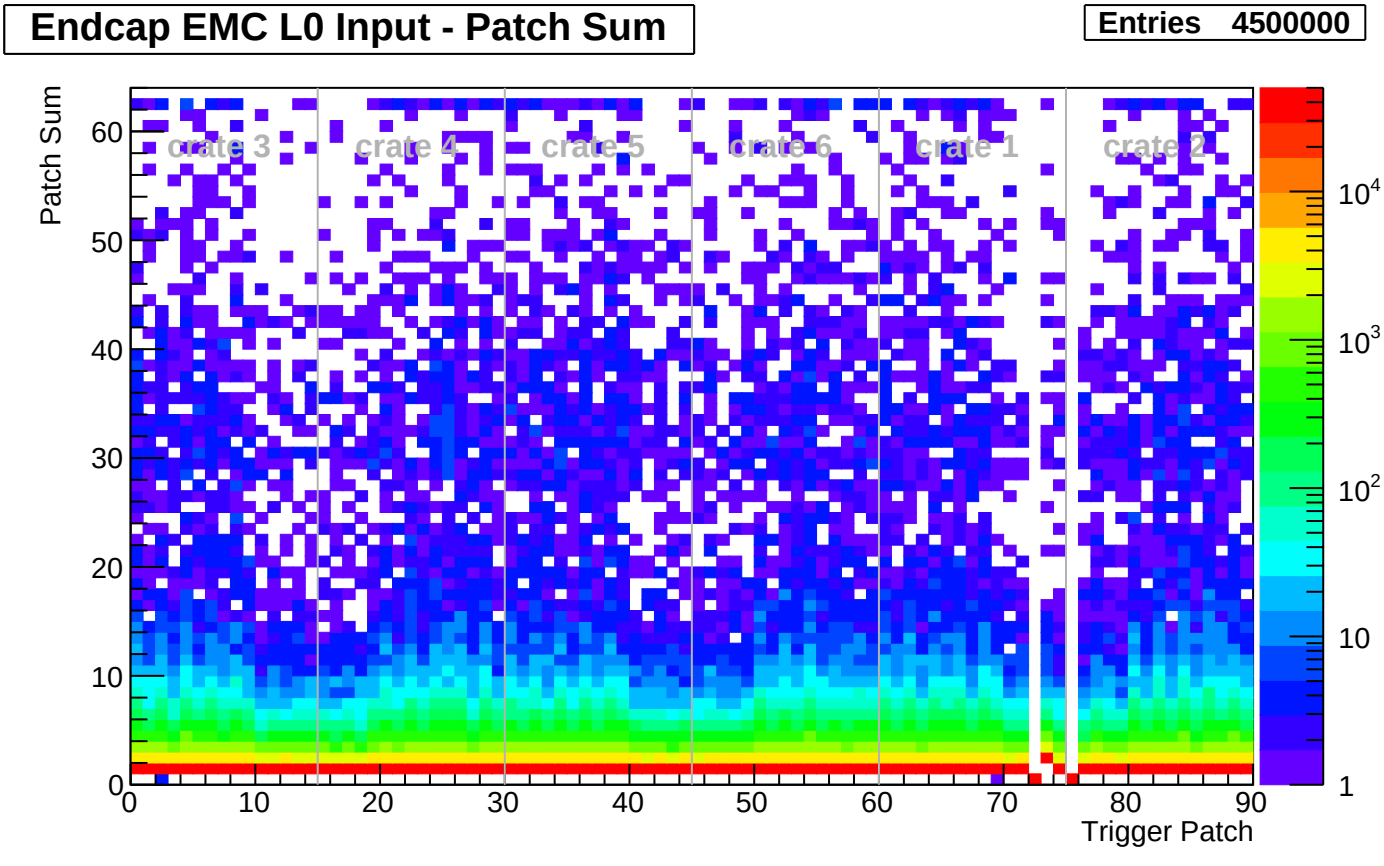
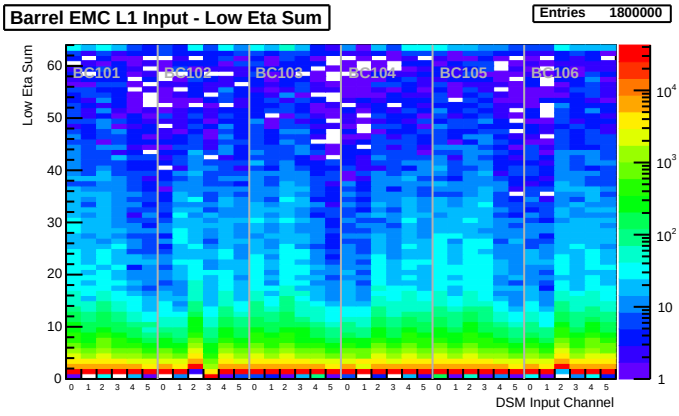
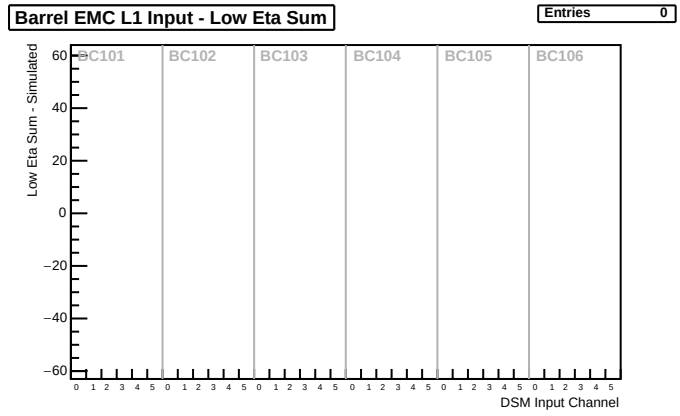

Barrel EMC L0 Input - High Tower
Entries 1.5e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

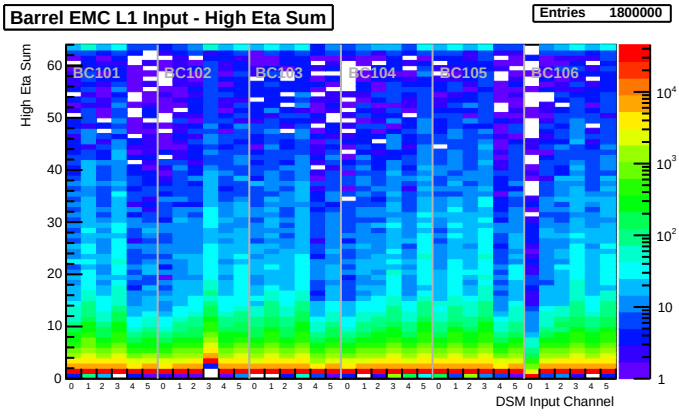

Barrel EMC L0 Input - Patch Sum
Entries 1.5e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

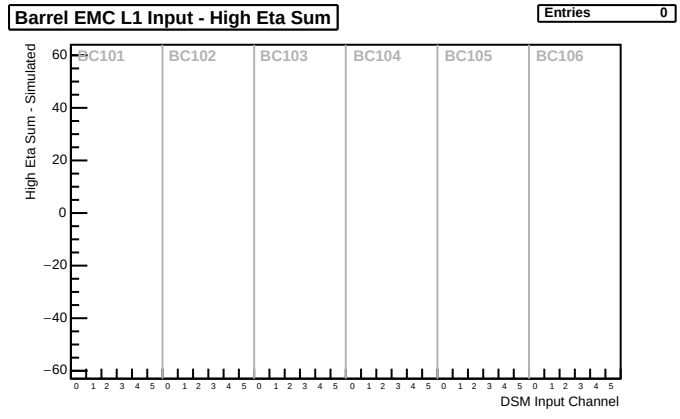

Endcap EMC L0 Input - High Tower
Entries 4500000
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

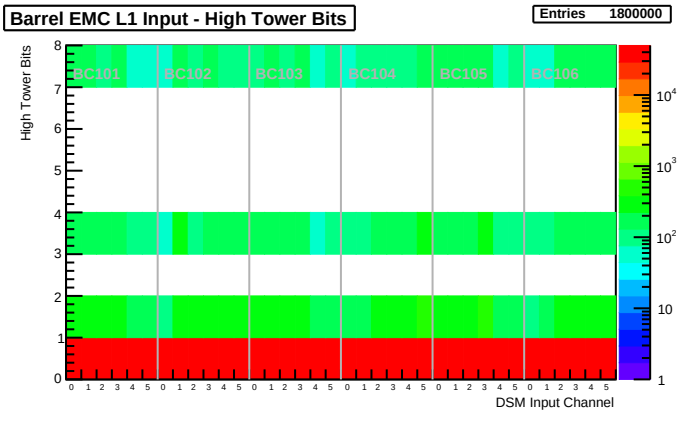

Endcap EMC L0 Input - Patch Sum
Entries 4500000
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

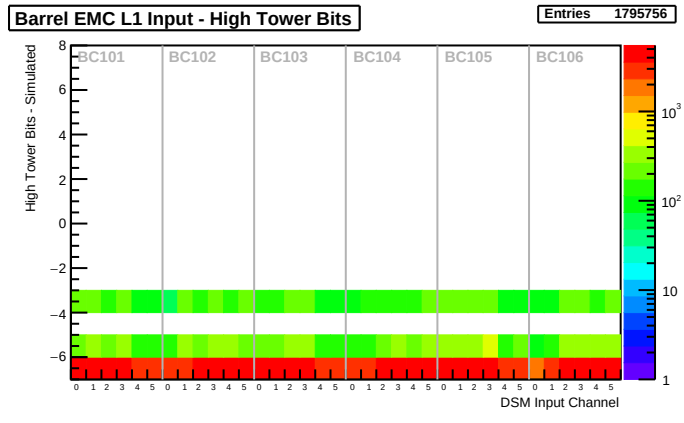

Barrel EMC L1 Input - Low Eta Sum
Entries 1800000
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

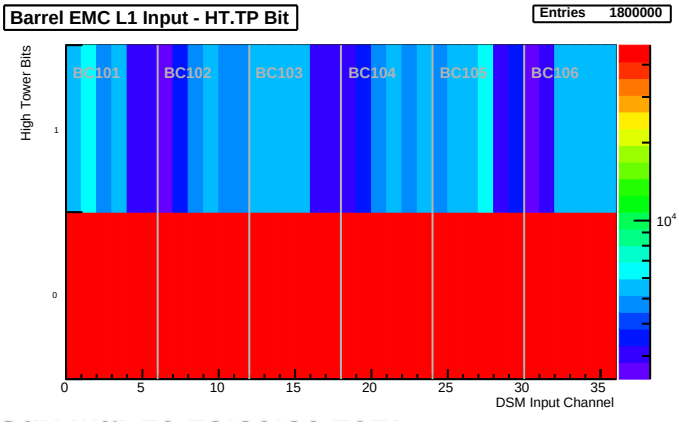

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

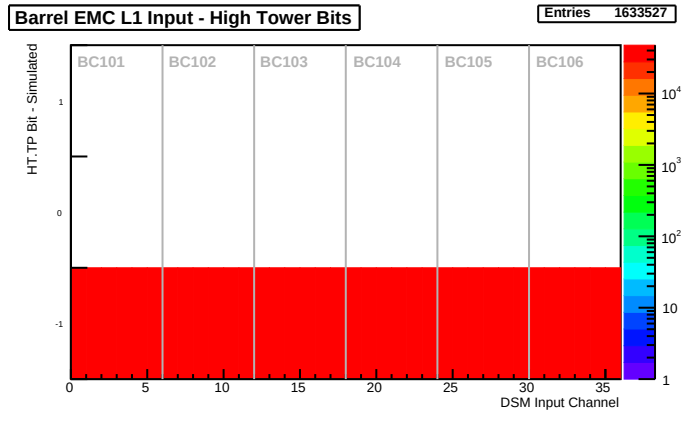

Barrel EMC L1 Input - High Eta Sum
Entries 1800000
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

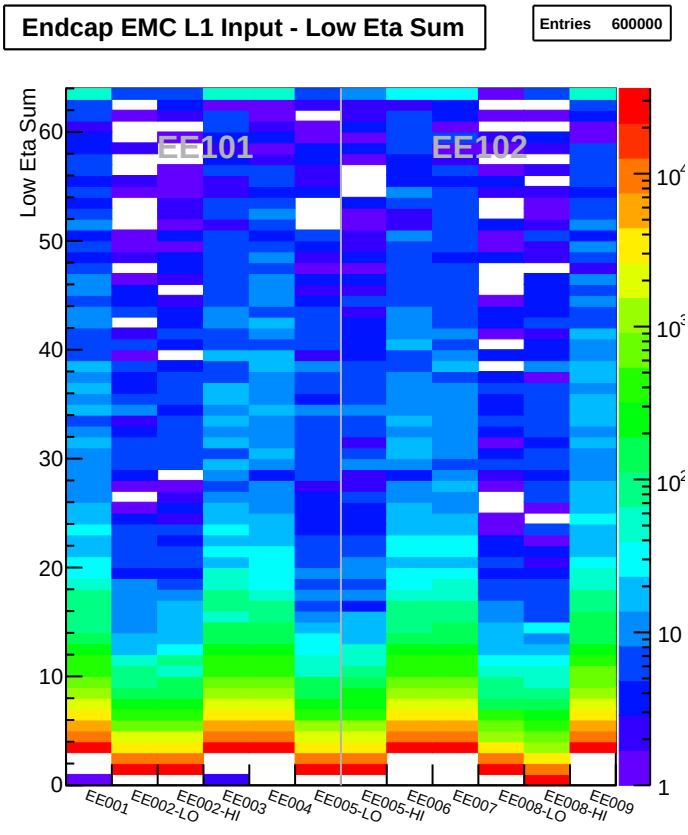

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

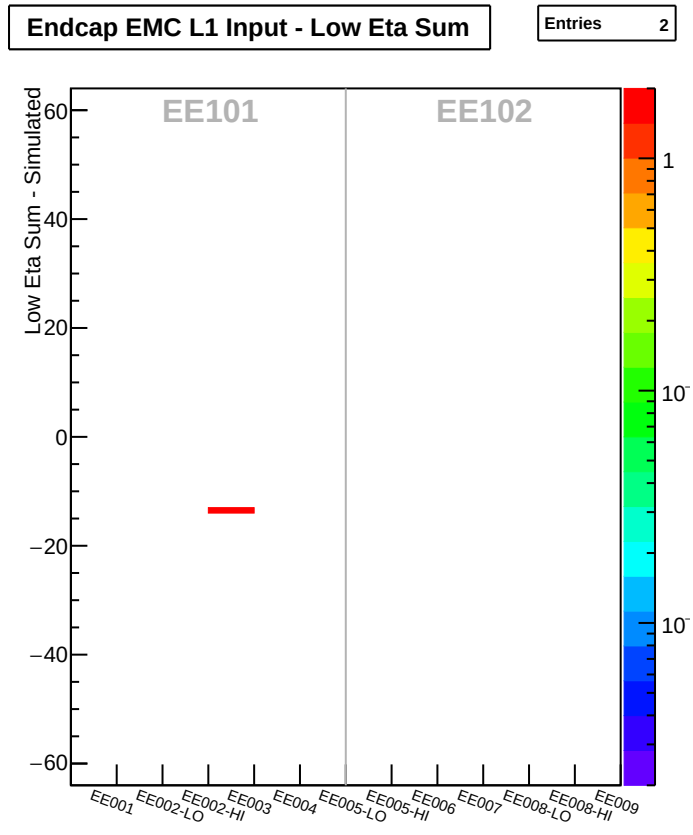

Barrel EMC L1 Input - High Tower Bits
Entries 1800000
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

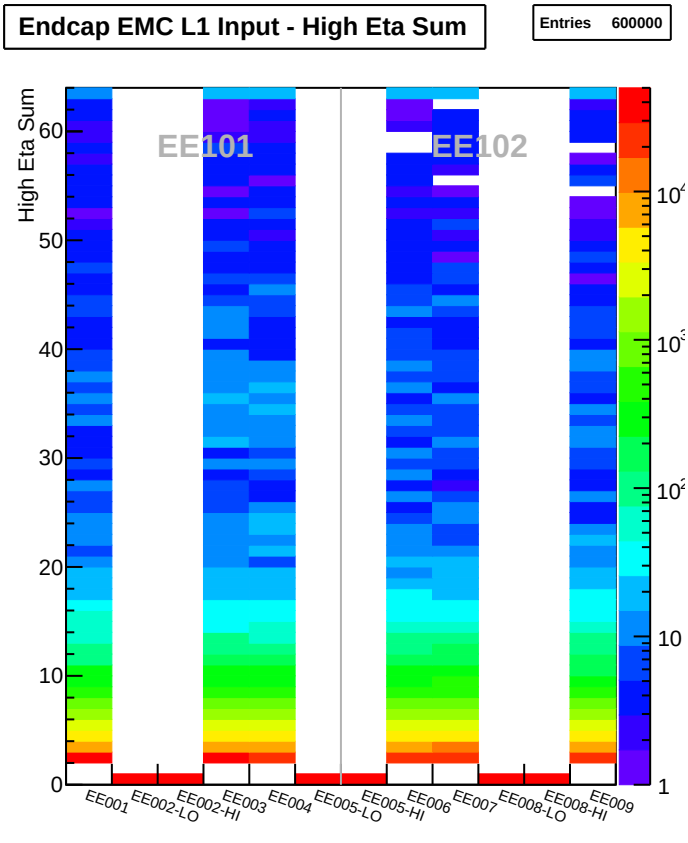

Barrel EMC L1 Input - High Tower Bits
Entries 1795756
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

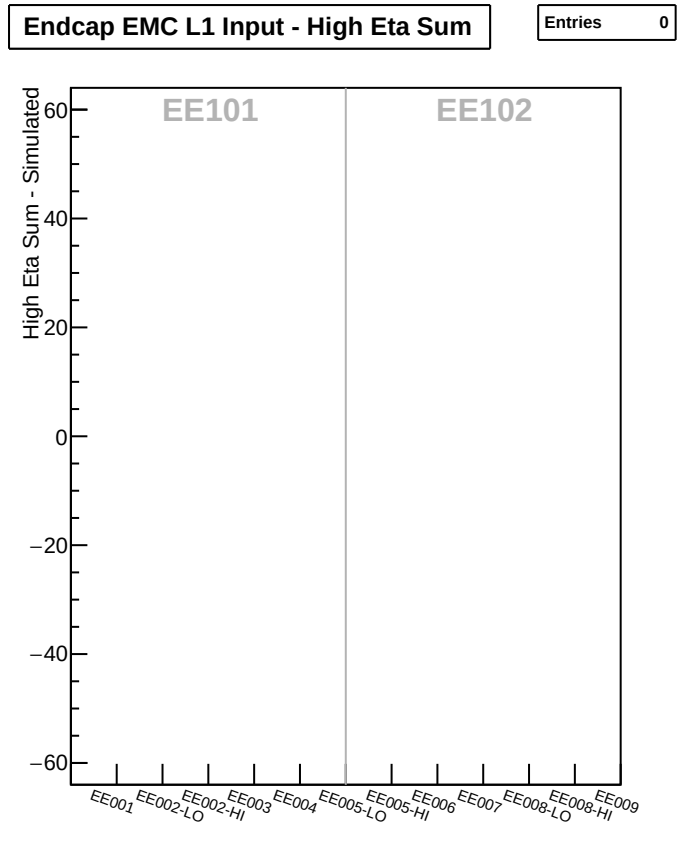

Barrel EMC L1 Input - HT.TP Bit
Entries 1800000
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

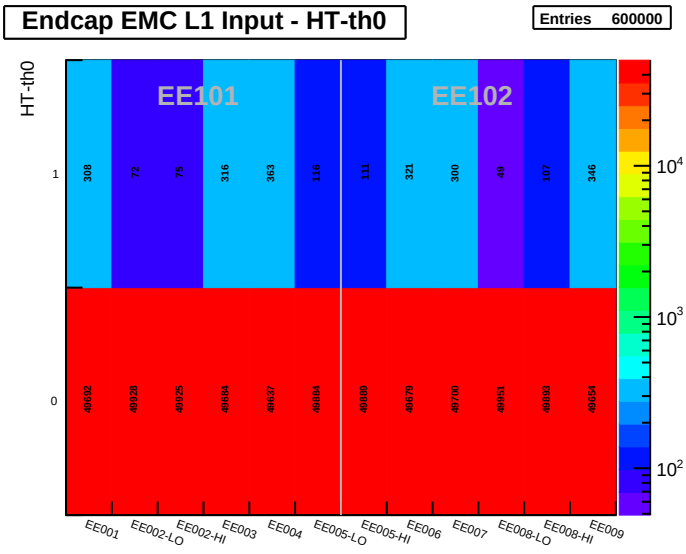

Barrel EMC L1 Input - High Tower Bits
Entries 1633527
HT.TP Bit - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

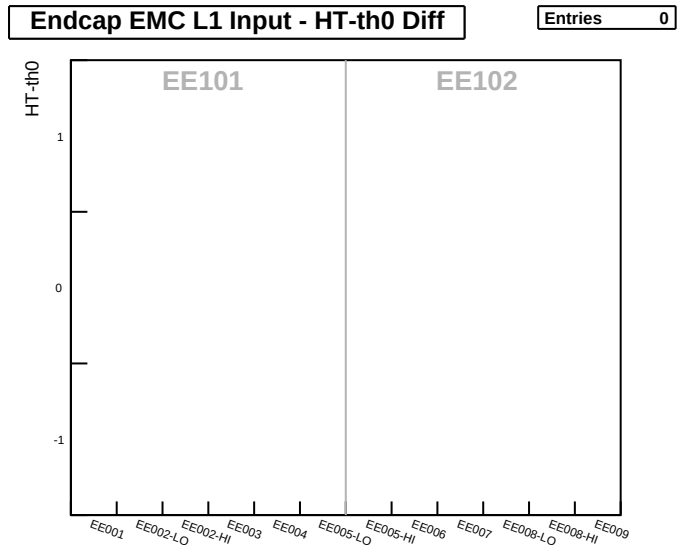

Endcap EMC L1 Input - Low Eta Sum
Entries 600000
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

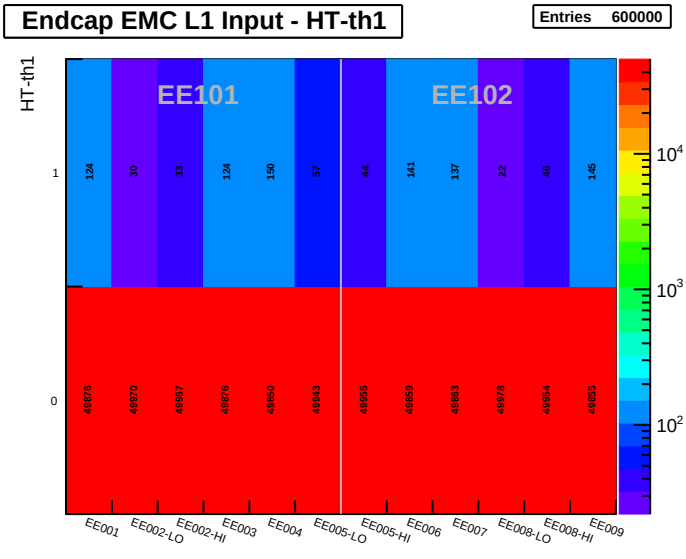

Endcap EMC L1 Input - Low Eta Sum
Entries 2
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

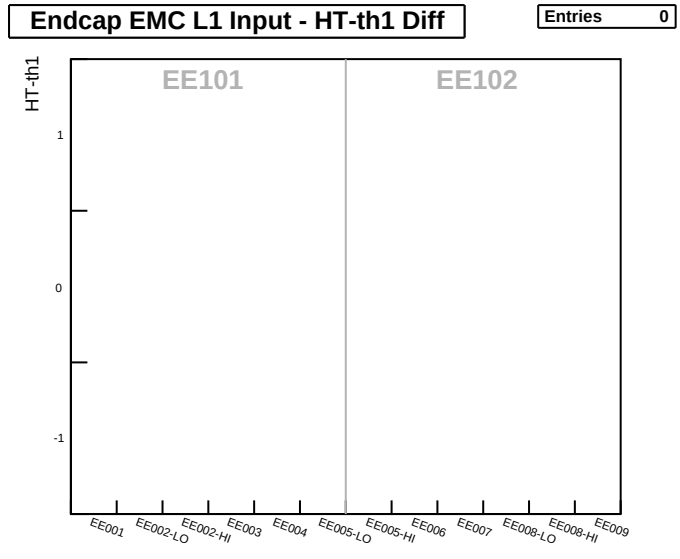

Endcap EMC L1 Input - High Eta Sum
Entries 600000
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

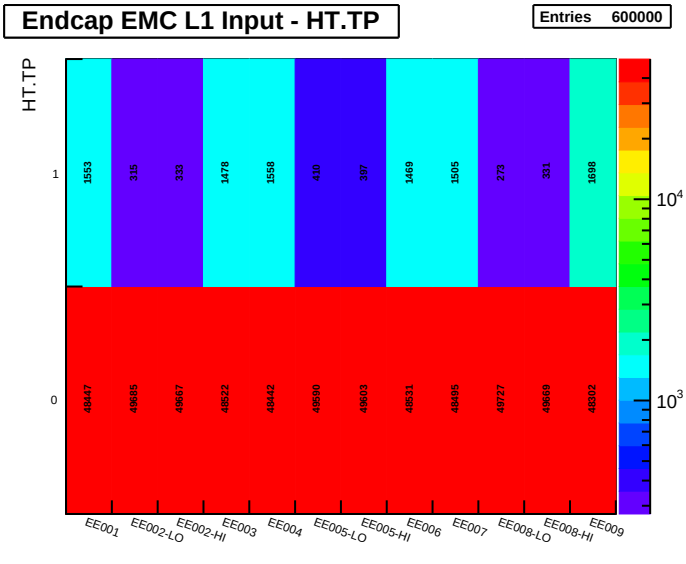

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

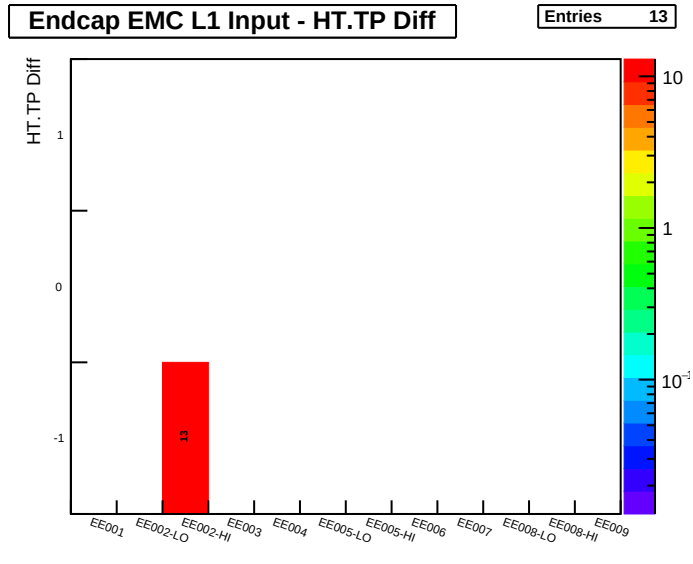

Endcap EMC L1 Input - HT-th0
Entries 600000
HT-th0
EE101
EE102
1
0
308 72 75 316 363 116 111 321 300 49 107 346
49692 49928 49925 49684 49637 49884 49889 49679 49700 49961 49893 49654
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009
10^4
10^3
10^2

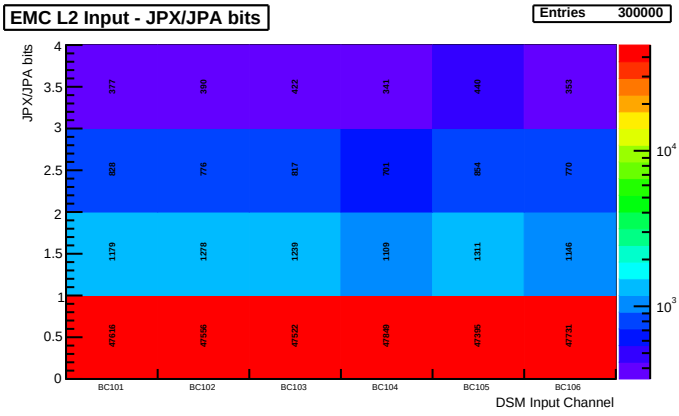

Endcap EMC L1 Input - HT-th0 Diff
Entries 0
HT-th0
EE101
EE102
1
0
-1
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

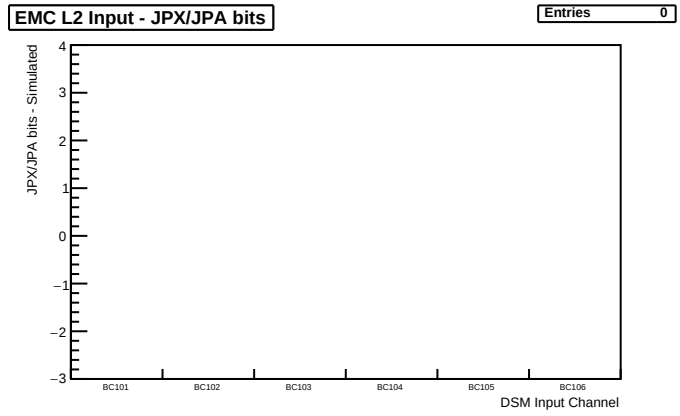

Endcap EMC L1 Input - HT-th1
Entries 600000
HT-th1
EE101
EE102
1
0
124 30 33 124 150 57 44 141 137 22 46 145
49876 49970 49967 49876 49880 49943 49956 49859 49863 49978 49954 49855
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009
10^4
10^3
10^2

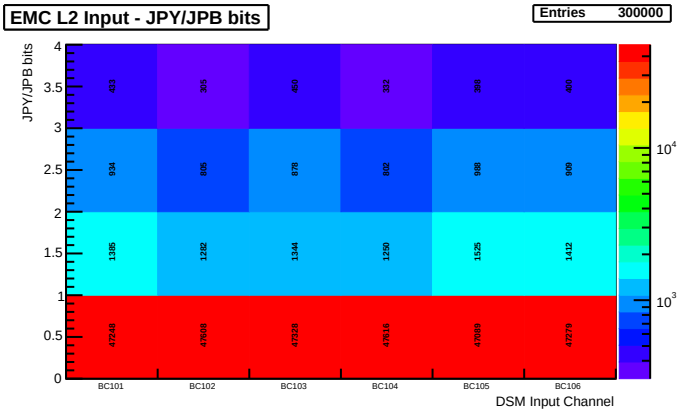

Endcap EMC L1 Input - HT-th1 Diff
Entries 0
HT-th1
EE101
EE102
1
0
-1
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

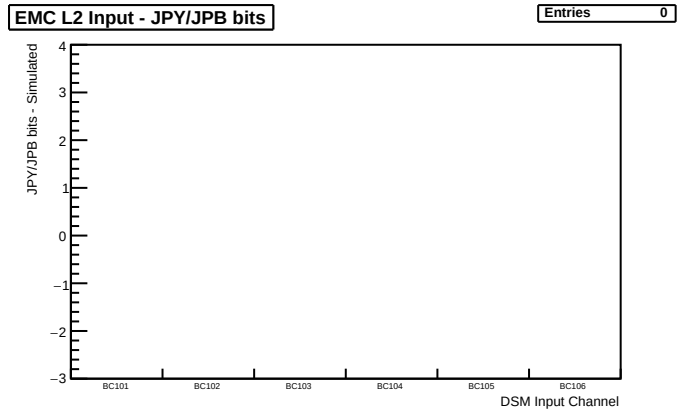

Endcap EMC L1 Input - HT.TP
Entries 600000
HT.TP
1
0
1553 315 333 1478 1558 410 397 1469 1505 273 331 1698
48447 49685 49667 48522 48442 49590 49603 48531 48495 49727 49669 48302
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009
10^4
10^3

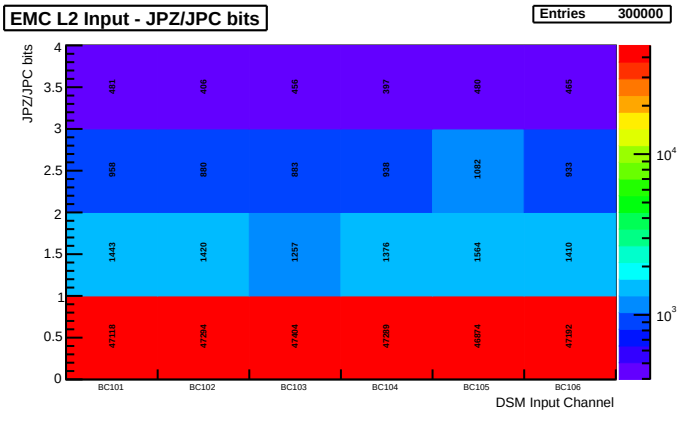

Endcap EMC L1 Input - HT.TP Diff
Entries 13
HT.TP Diff
1
0
-1
13
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009
10
1
10^{-1}

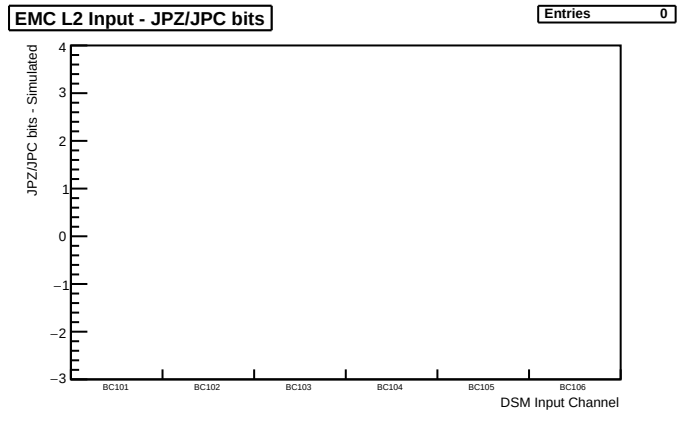

EMC L2 Input - JPX/JPA bits
Entries 300000
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106
377 390 422 341 440 353
828 776 817 701 854 770
1179 1278 1239 1109 1311 1146
47618 47556 47522 47649 47395 47731

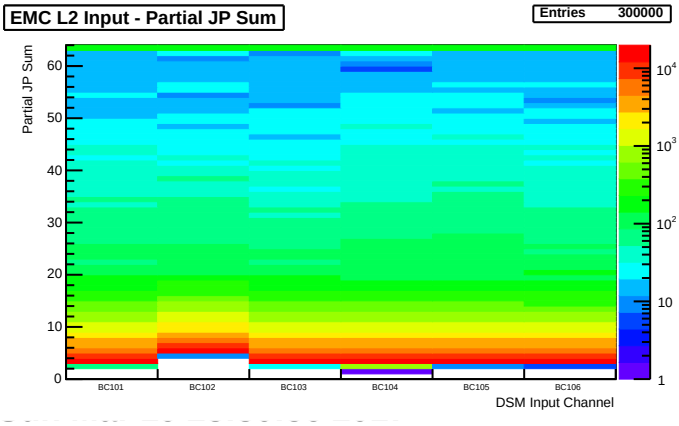

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106

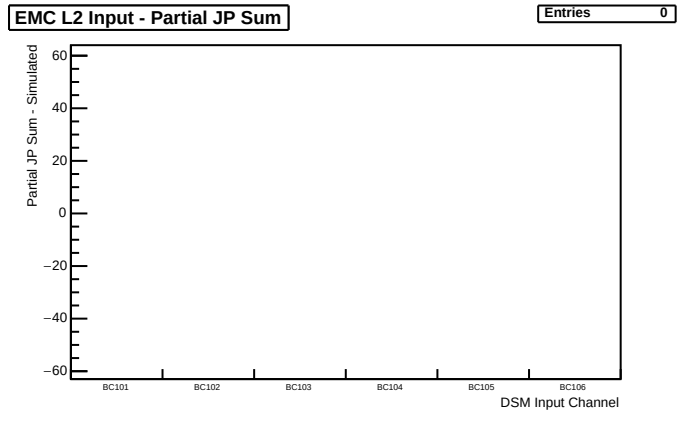

EMC L2 Input - JPY/JPB bits
Entries 300000
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106
433 305 450 322 388 400
934 805 878 802 888 909
1395 1282 1344 1250 1525 1412
47248 47608 47328 47616 47059 47279

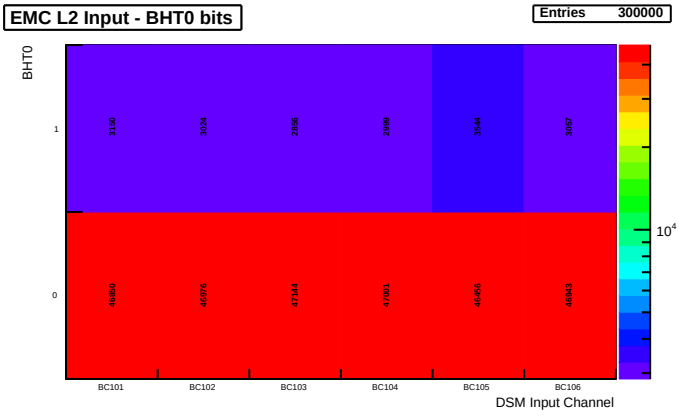

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106

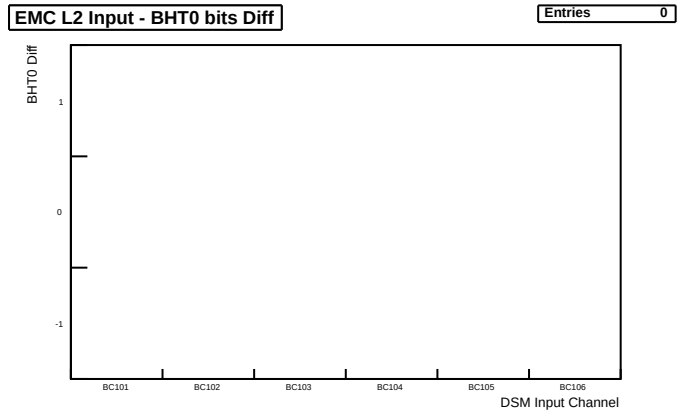

EMC L2 Input - JPZ/JPC bits
Entries 300000
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106
461 406 456 397 430 465
958 880 853 938 1082 933
1443 1420 1257 1376 1564 1410
47118 47294 47404 47289 46874 47192

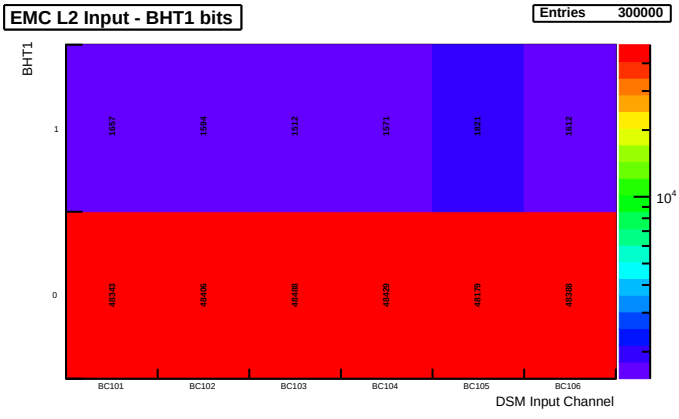

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106

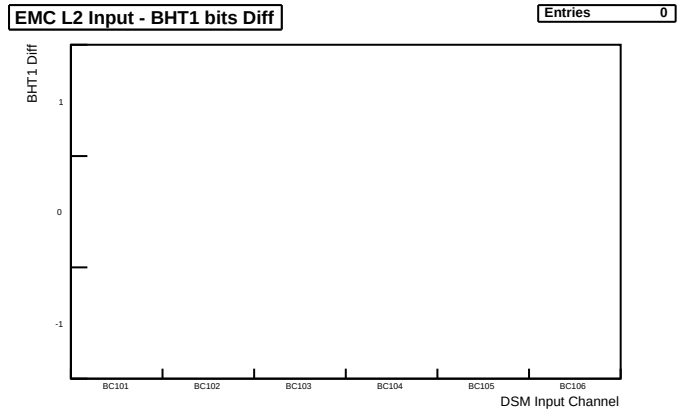

EMC L2 Input - Partial JP Sum
Entries 300000
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106

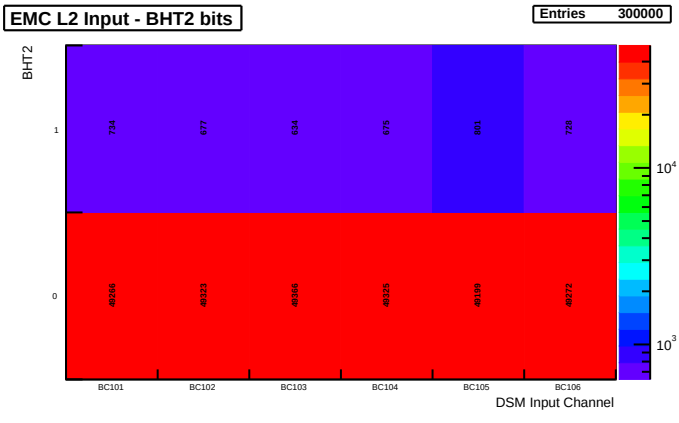

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106

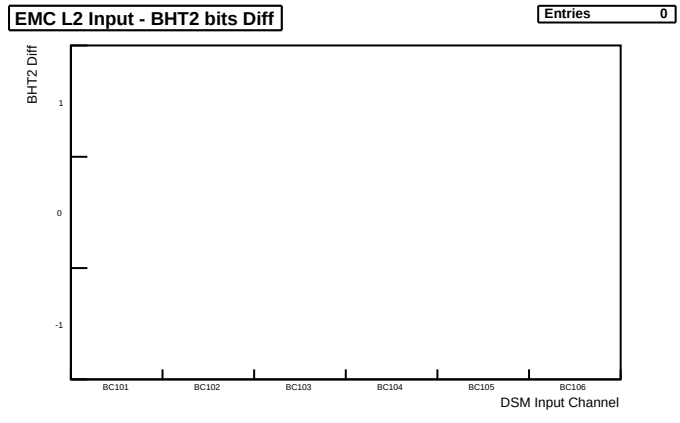

EMC L2 Input - BHT0 bits
Entries 300000
BHT0
1
0
3130
3024
2856
2999
3544
3057
46850
46976
47144
47001
46456
46943
10^4
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

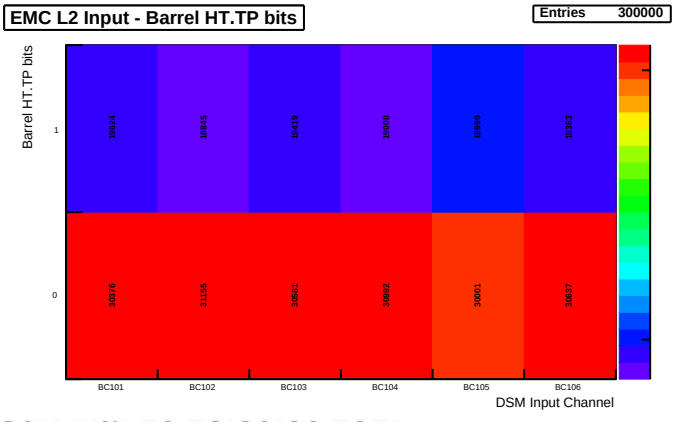

EMC L2 Input - BHT0 bits Diff
Entries 0
BHT0 Diff
1
0
-1
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

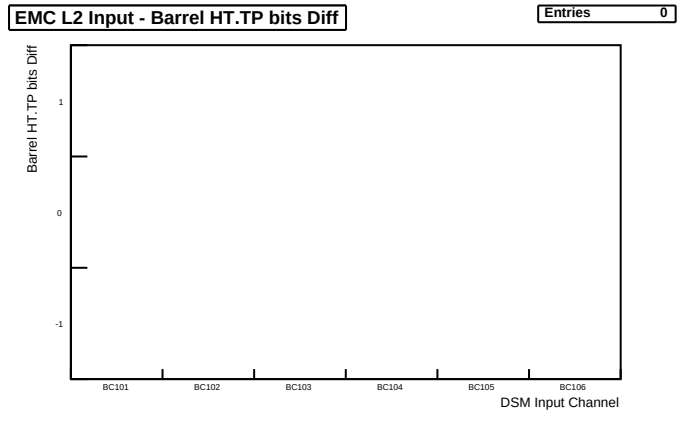

EMC L2 Input - BHT1 bits
Entries 300000
BHT1
1
0
1657
1594
1512
1571
1821
1612
48343
48406
48488
48429
48179
48388
10^4
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

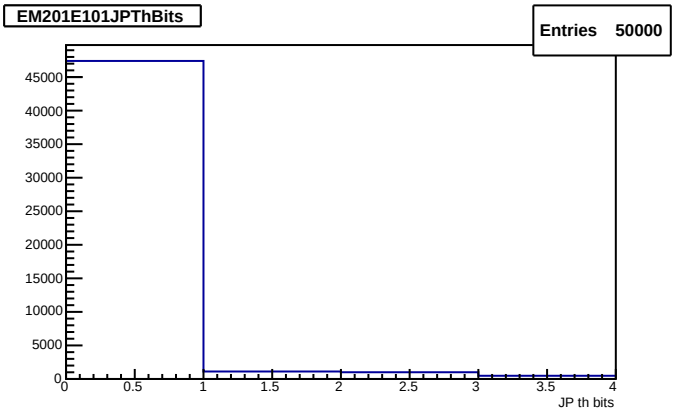

EMC L2 Input - BHT1 bits Diff
Entries 0
BHT1 Diff
1
0
-1
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

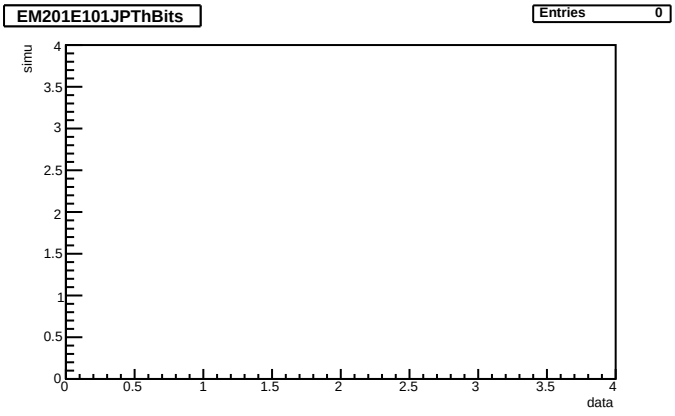

EMC L2 Input - BHT2 bits
Entries 300000
BHT2
1
0
734
677
634
675
801
728
49266
49323
49366
49325
49199
49272
10^4
10^3
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

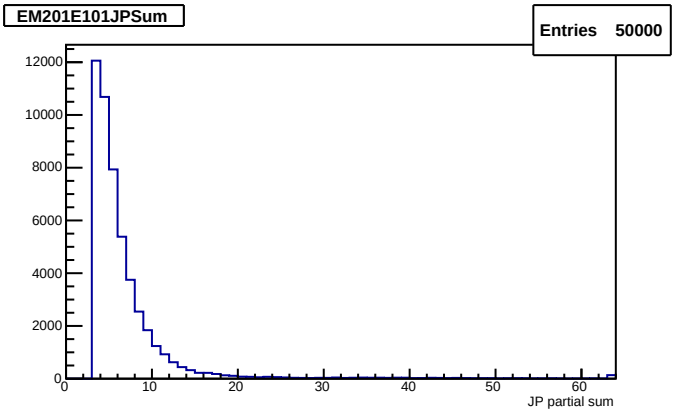

EMC L2 Input - BHT2 bits Diff
Entries 0
BHT2 Diff
1
0
-1
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

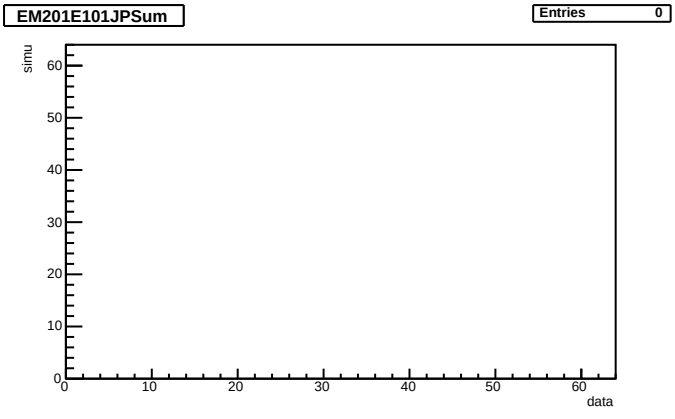

EMC L2 Input - Barrel HT.TP bits
Entries 300000
Barrel HT.TP bits
1
0
19624
19845
19419
19608
19999
19363
30376
30155
30581
30392
30001
30637
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

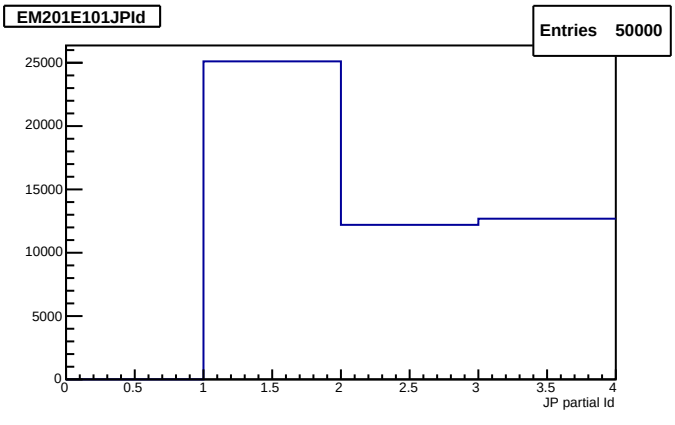

EMC L2 Input - Barrel HT.TP bits Diff
Entries 0
Barrel HT.TP bits Diff
1
0
-1
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

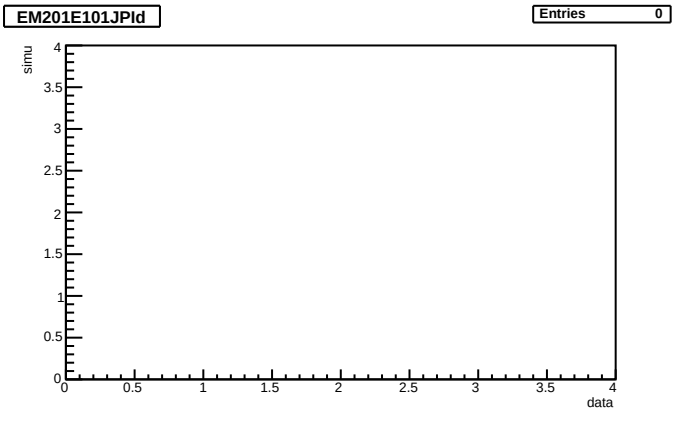

EM201E101JPThBits
Entries 50000
JP th bits

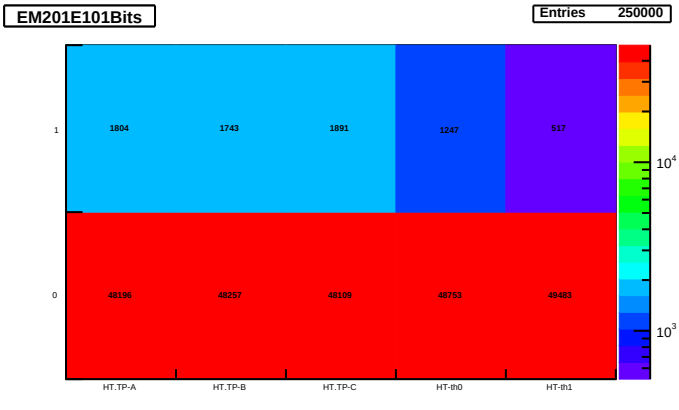

EM201E101JPThBits
Entries 0
simu
data

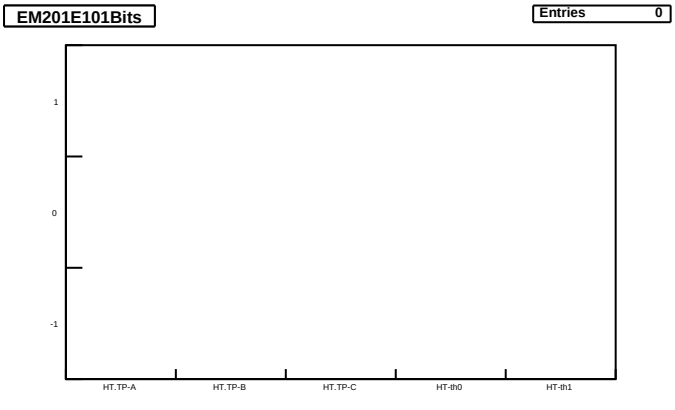

EM201E101JPSum
Entries 50000
JP partial sum

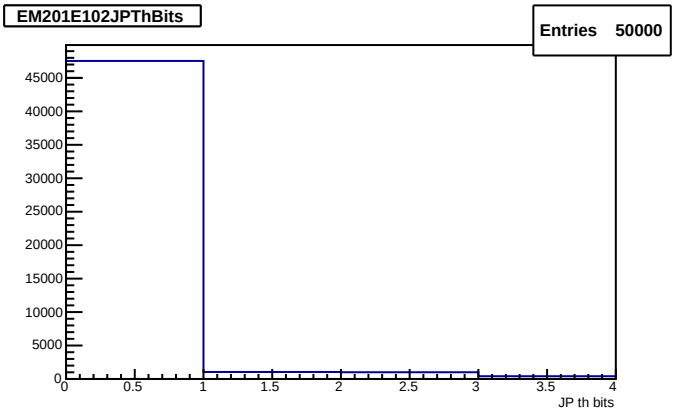

EM201E101JPSum
Entries 0
simu
data

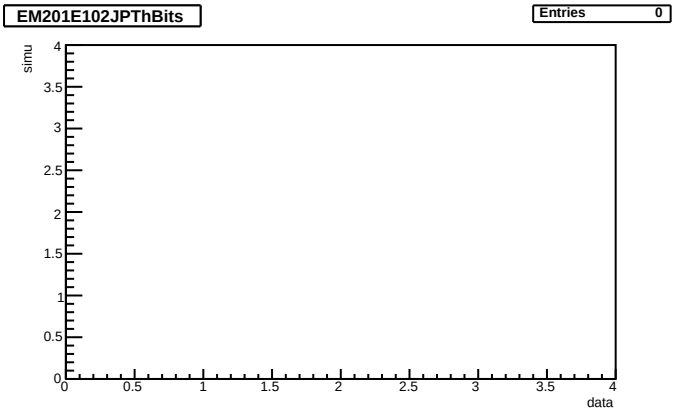

EM201E101JPId
Entries 50000
JP partial Id

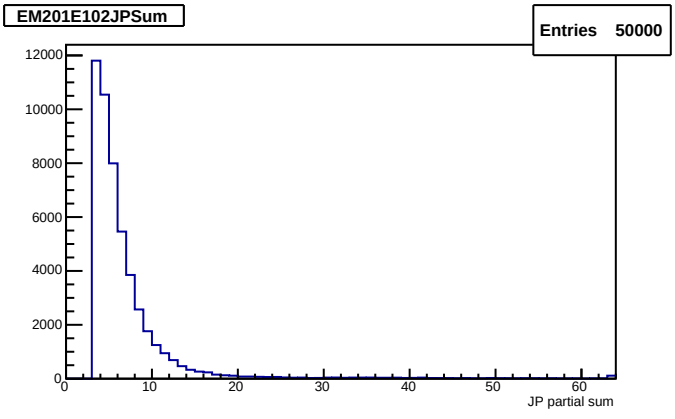

EM201E101JPId
Entries 0
simu
data

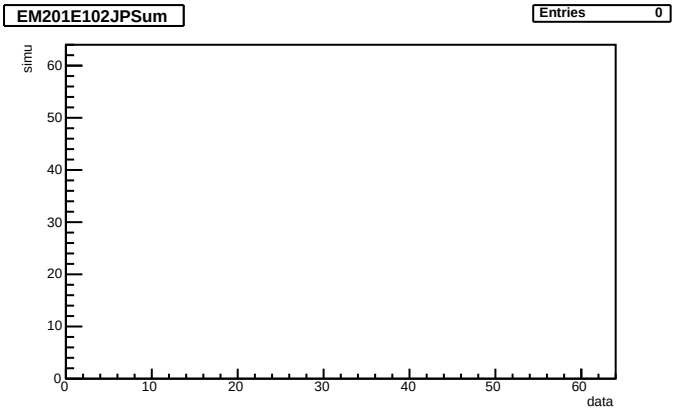

EM201E101Bits
Entries 250000
1804
1743
1891
1247
517
48196
48257
48109
48753
49483
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

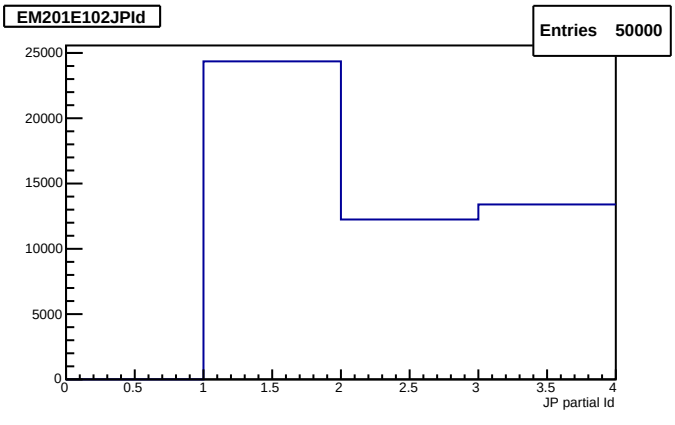

EM201E101Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

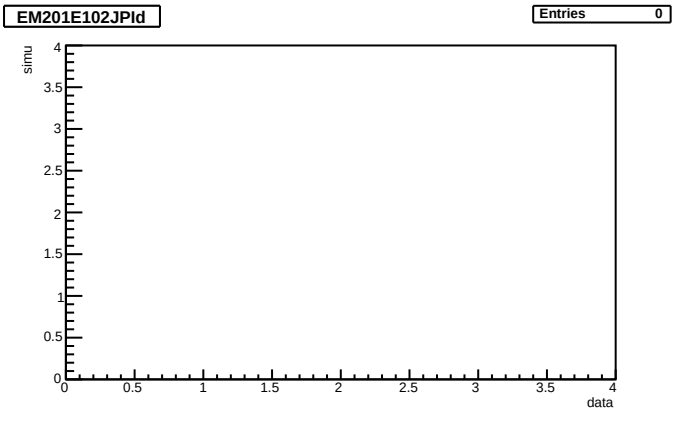

EM201E102JPThBits
Entries 50000
JP th bits

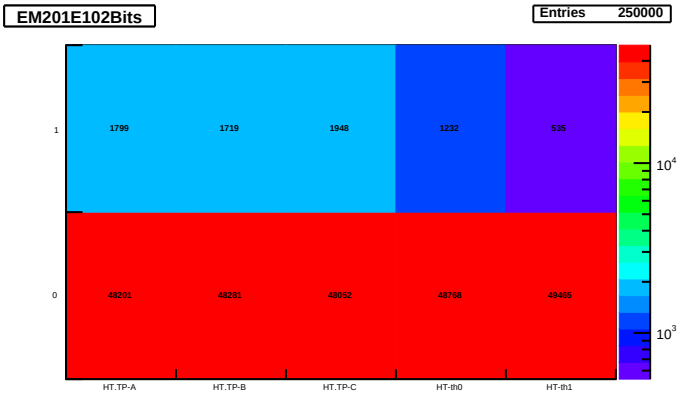

EM201E102JPThBits
Entries 0
simu
data

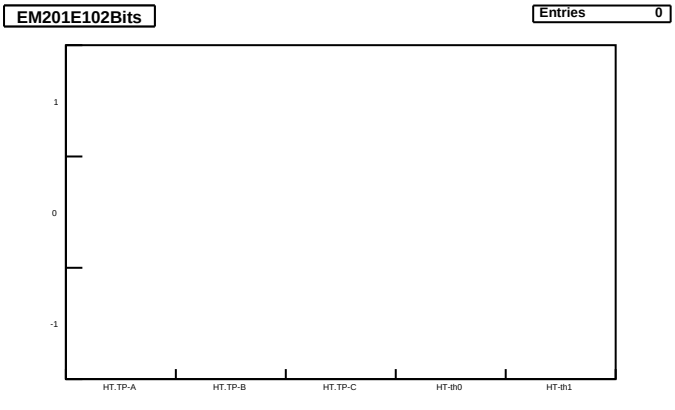

EM201E102JPSum
Entries 50000
JP partial sum

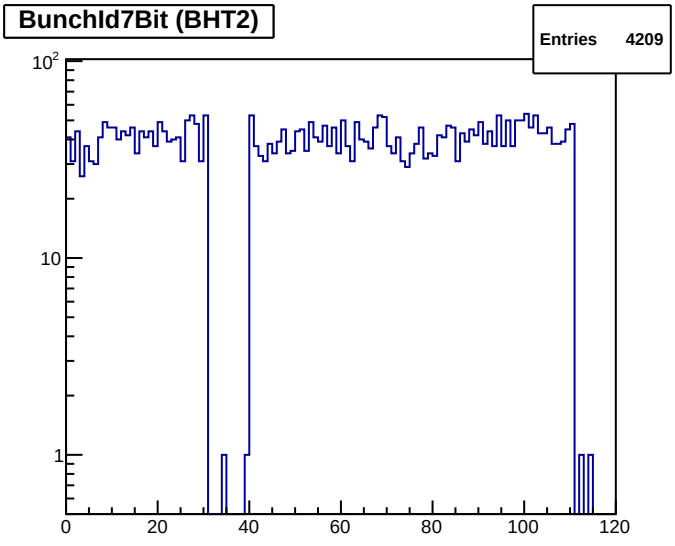

EM201E102JPSum
Entries 0
simu
data

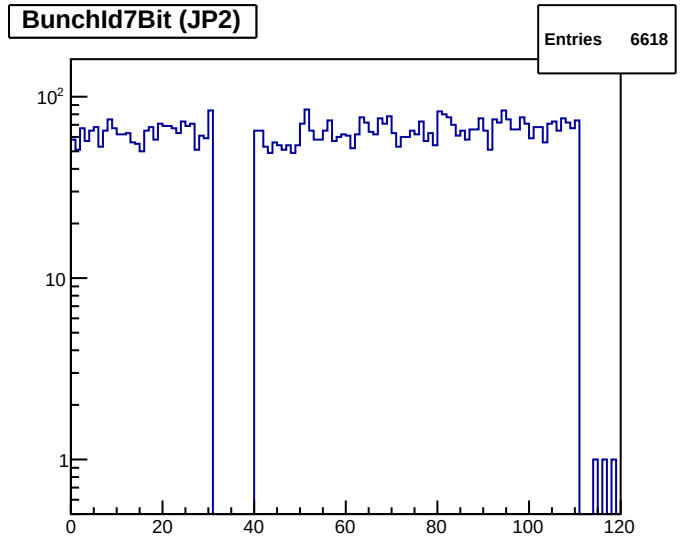

EM201E102JPId
Entries 50000
JP partial Id

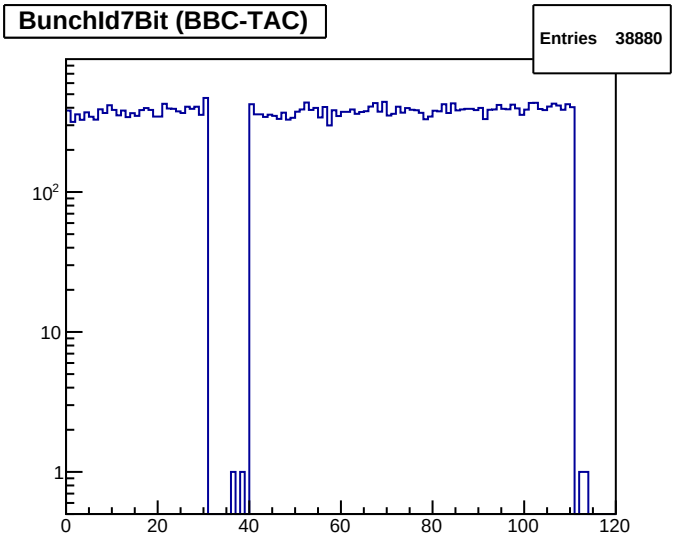

EM201E102JPId
Entries 0
simu
data

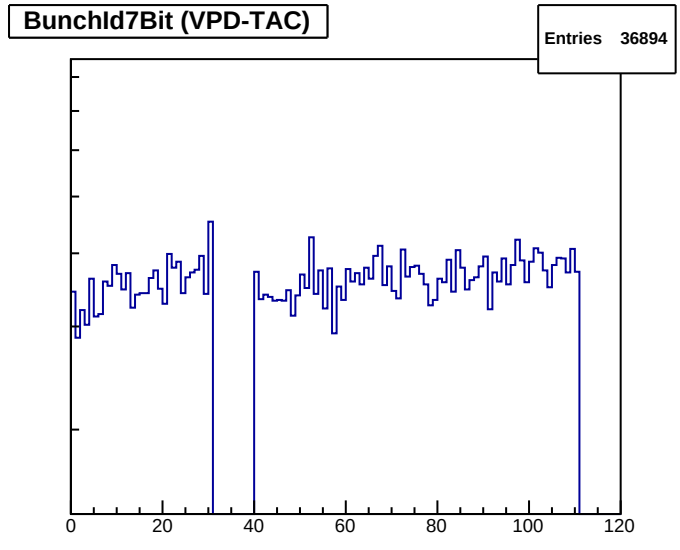

EM201E102Bits
Entries 250000
1799
1719
1948
1232
535
48201
48281
48052
48768
49465
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

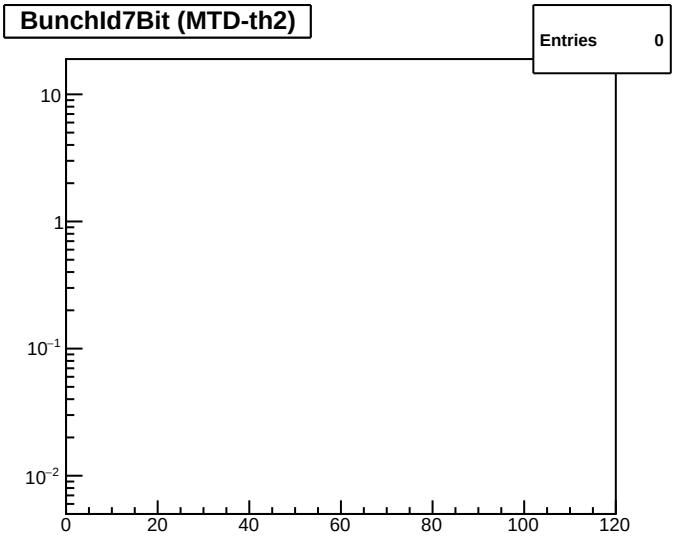

EM201E102Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

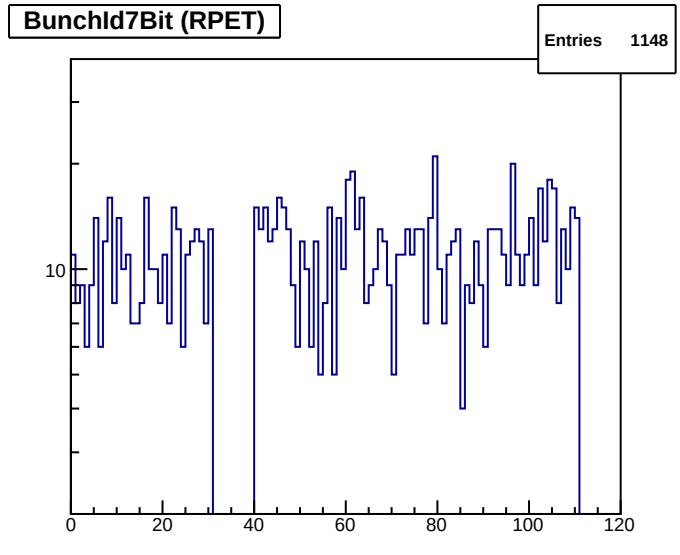

BunchId7Bit (BHT2)
Entries 4209
10^2
10
1
0
20
40
60
80
100
120

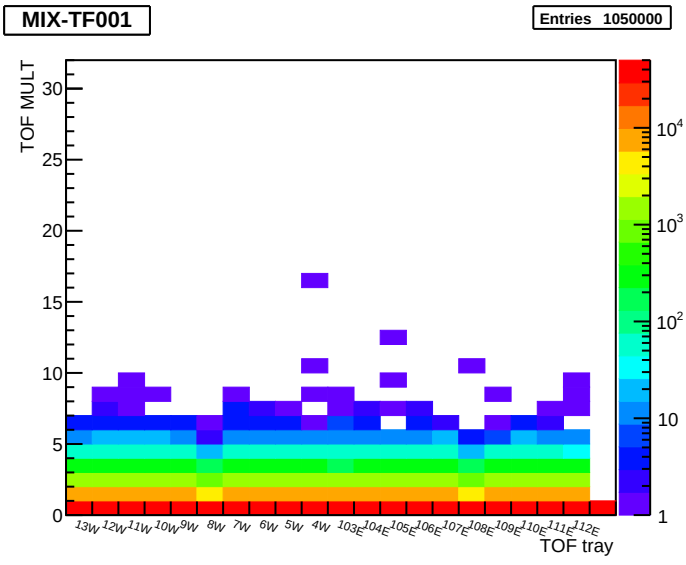

BunchId7Bit (JP2)
Entries 6618
10^2
10
1
0
20
40
60
80
100
120

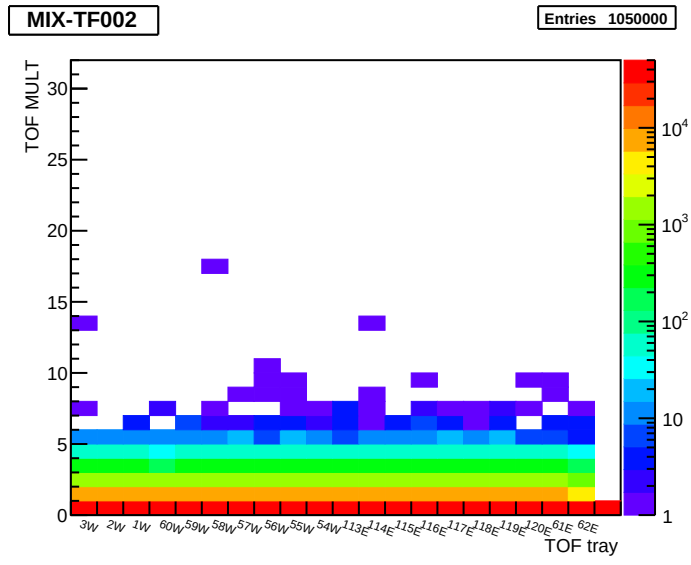

BunchId7Bit (BBC-TAC)
Entries 38880
10^2
10
1
0
20
40
60
80
100
120

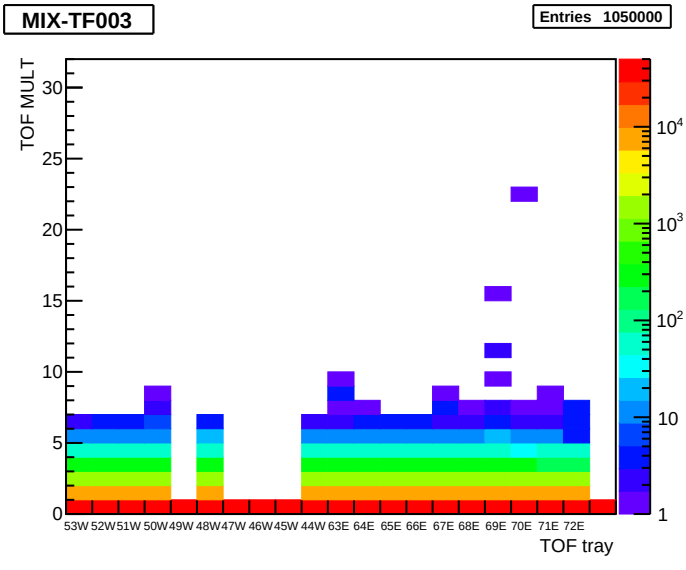

BunchId7Bit (VPD-TAC)
Entries 36894
0
20
40
60
80
100
120

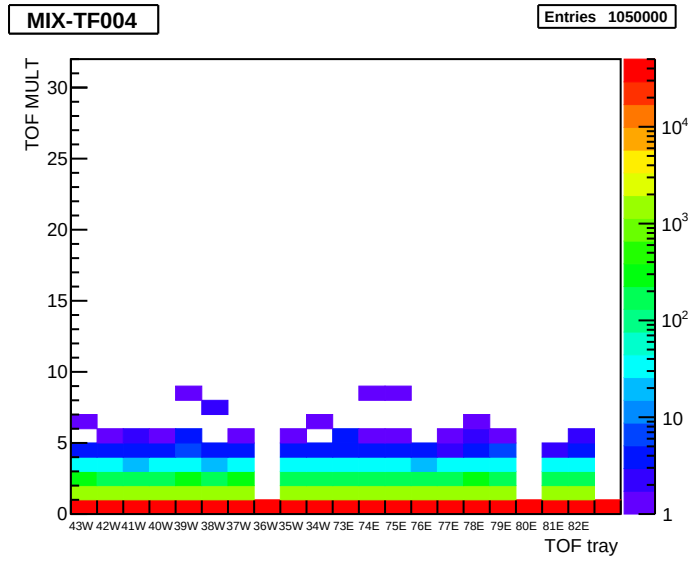

BunchId7Bit (MTD-th2)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

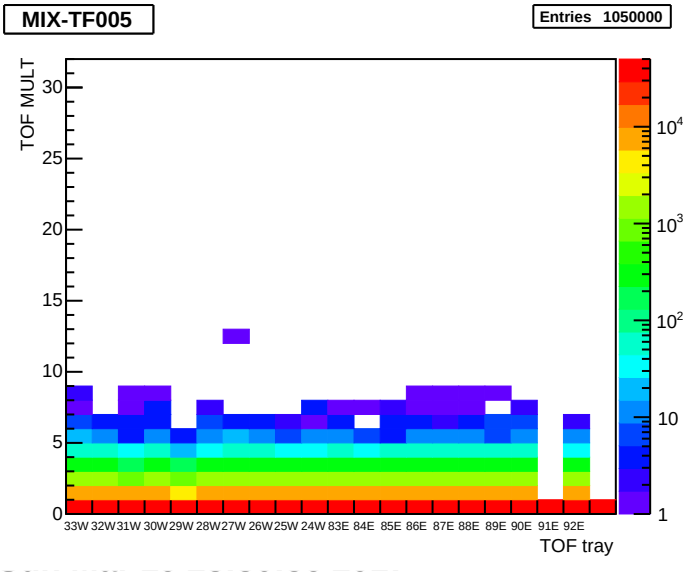

BunchId7Bit (RPET)
Entries 1148
10
0
20
40
60
80
100
120

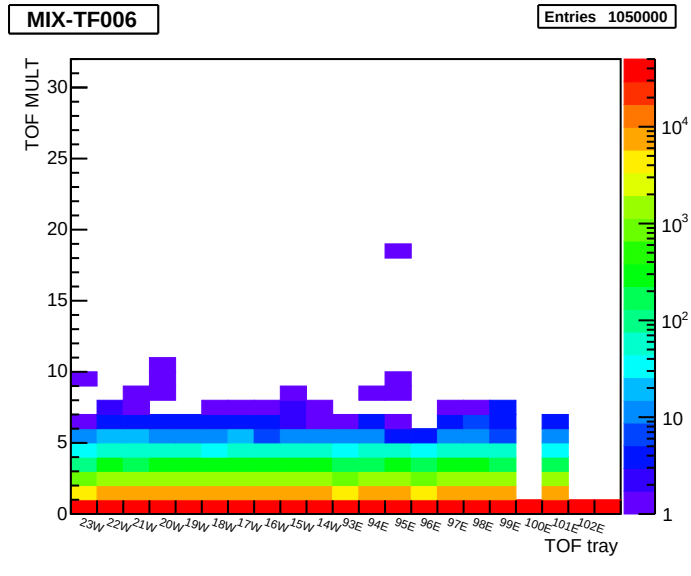

MIX-TF001
Entries 1050000
TOF MULT
TOF tray

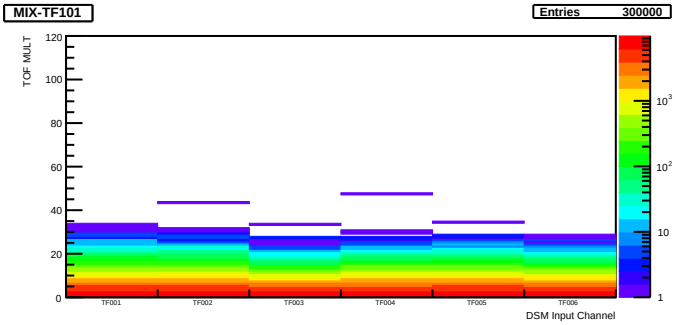

MIX-TF002
Entries 1050000
TOF MULT
TOF tray

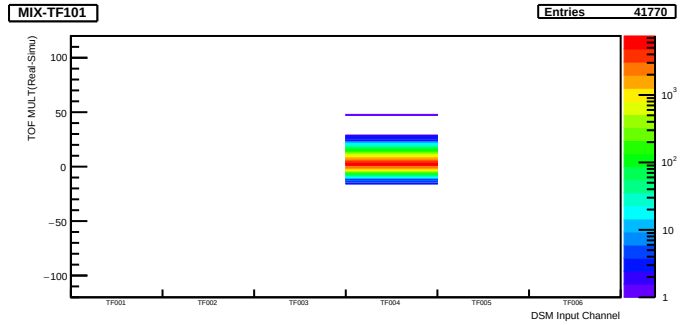

MIX-TF003
Entries 1050000
TOF MULT
TOF tray

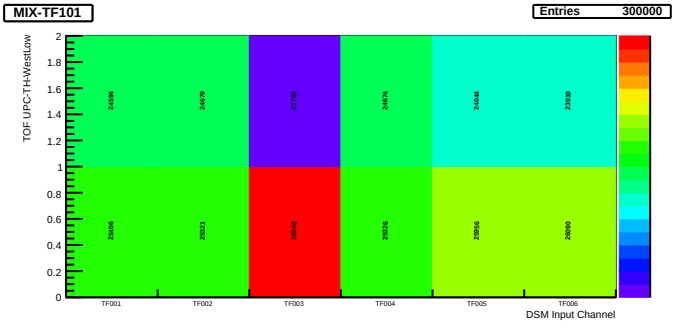

MIX-TF004
Entries 1050000
TOF MULT
TOF tray

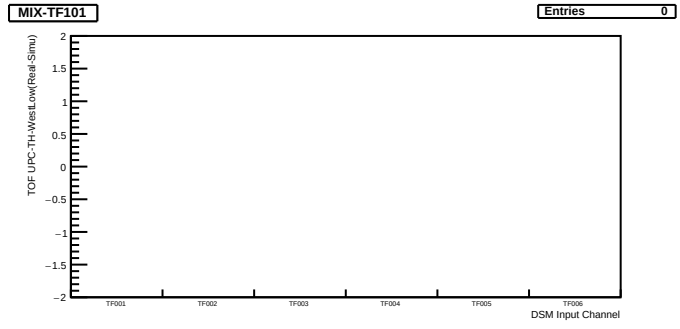

MIX-TF005
Entries 1050000
TOF MULT
TOF tray

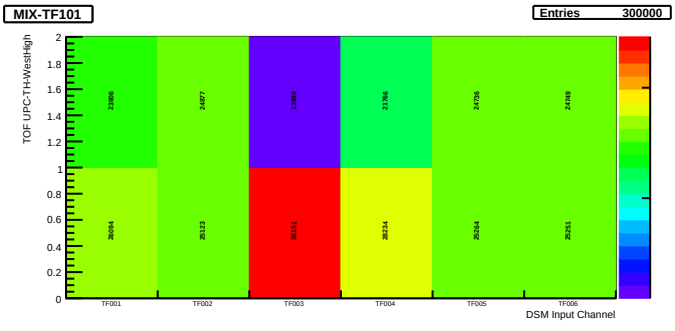

MIX-TF006
Entries 1050000
TOF MULT
TOF tray

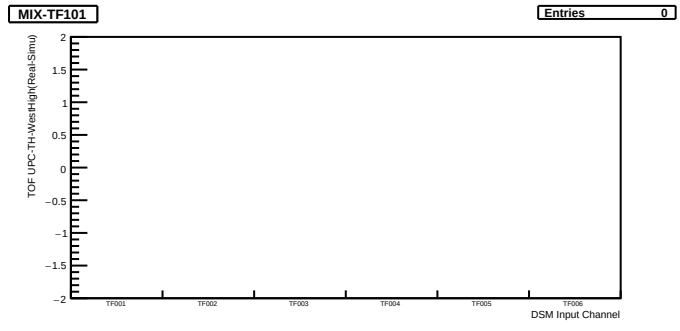

MIX-TF101
Entries 300000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

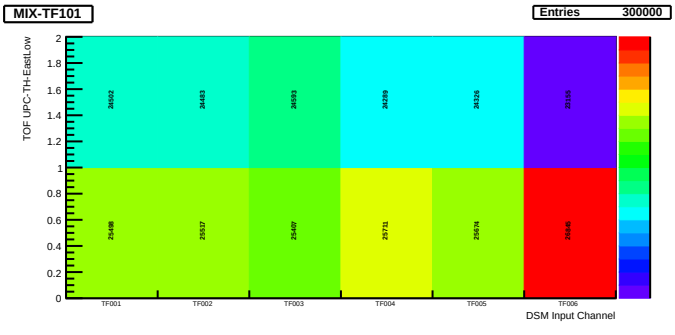

MIX-TF101
Entries 41770
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

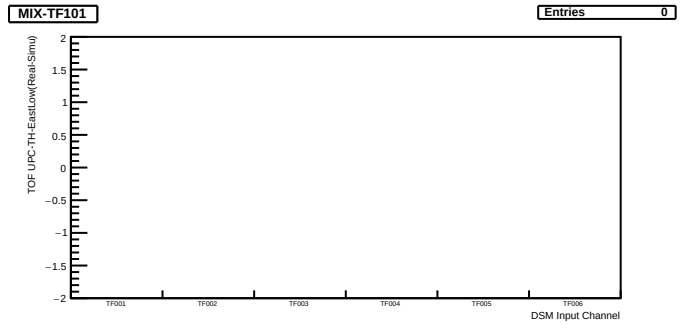

MIX-TF101
Entries 300000
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

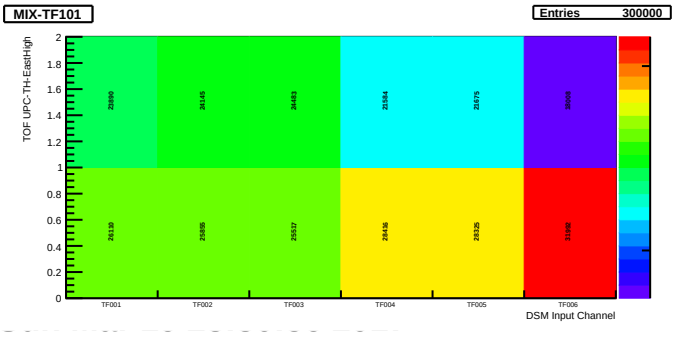

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

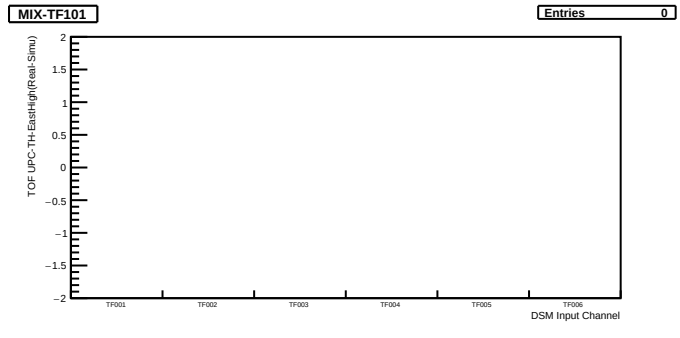

MIX-TF101
Entries 300000
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

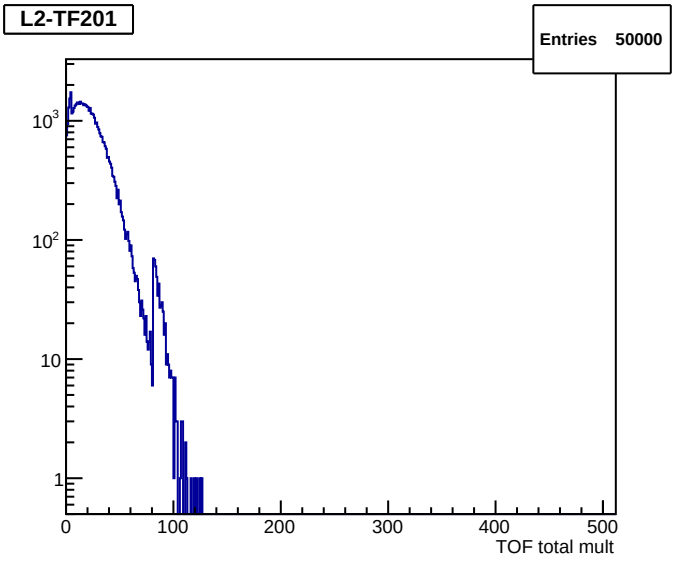

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

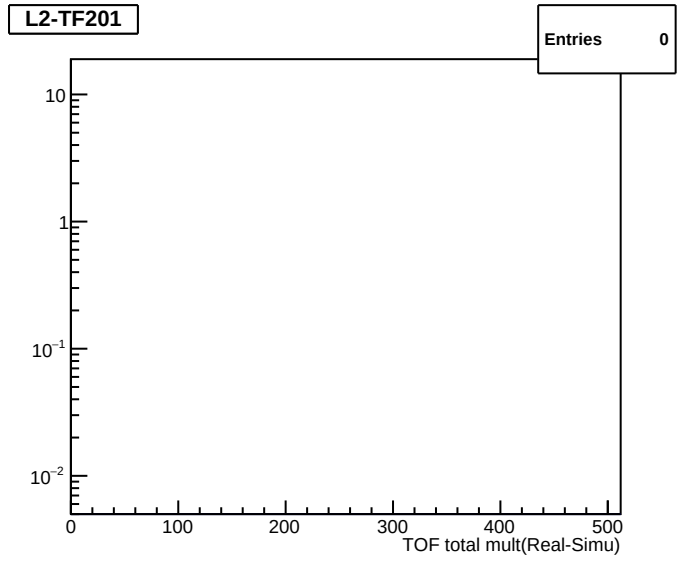

MIX-TF101
Entries 300000
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

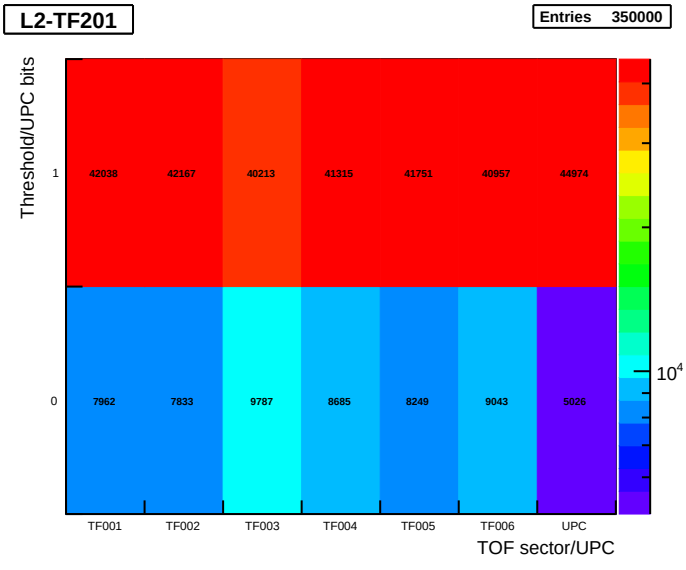

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

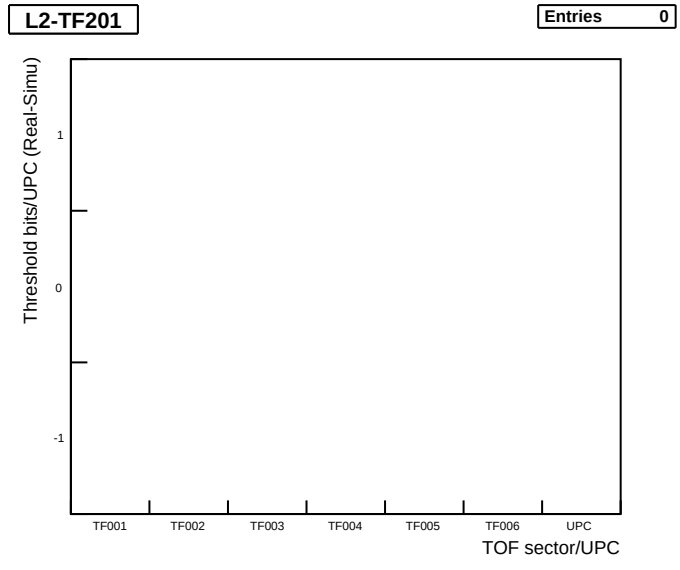

MIX-TF101
Entries 300000
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

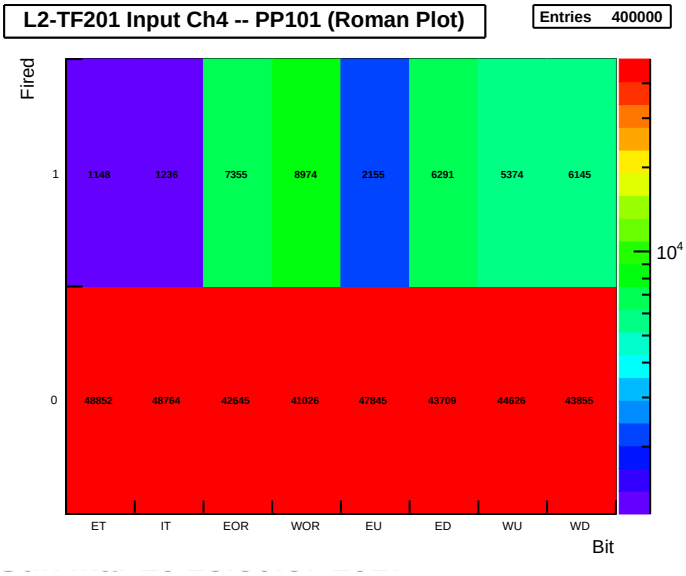

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

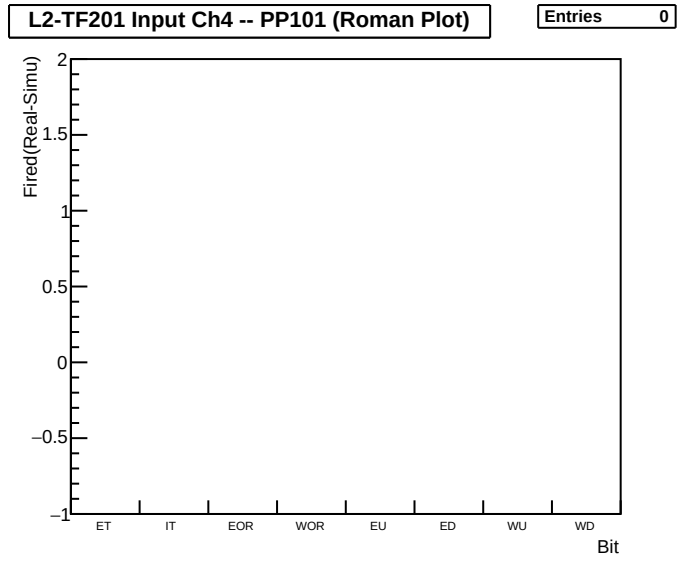

L2-TF201
Entries 50000
TOF total mult

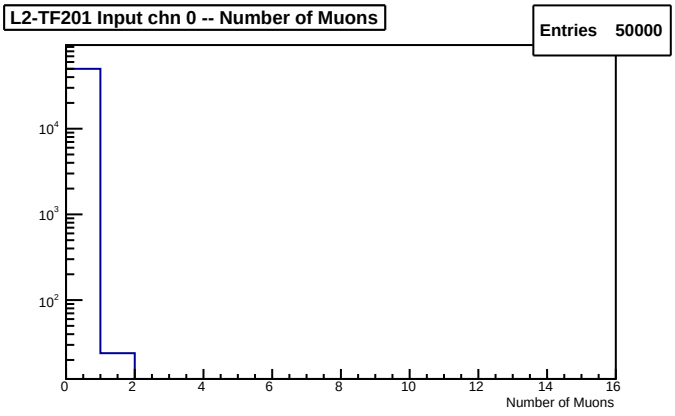

L2-TF201
Entries 0
TOF total mult(Real-Simu)

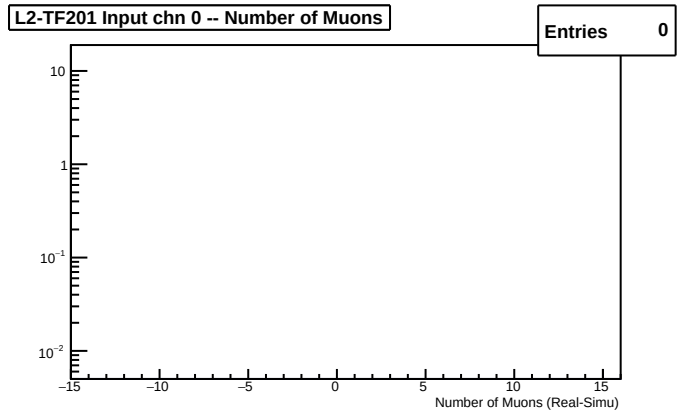

L2-TF201
Entries 350000
Threshold/UPC bits
42038 42167 40213 41315 41751 40957 44974
7962 7833 9787 8685 8249 9043 5026
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

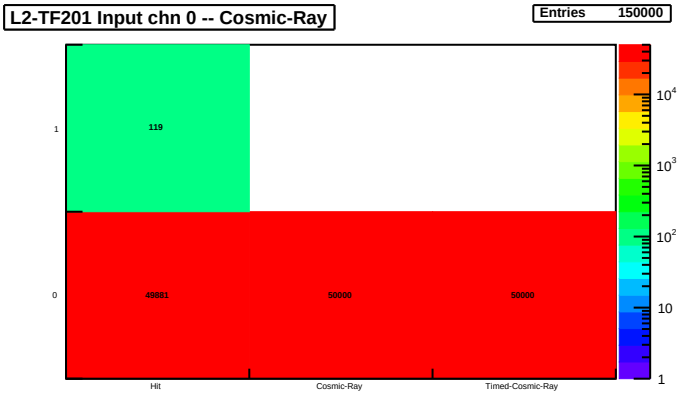

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

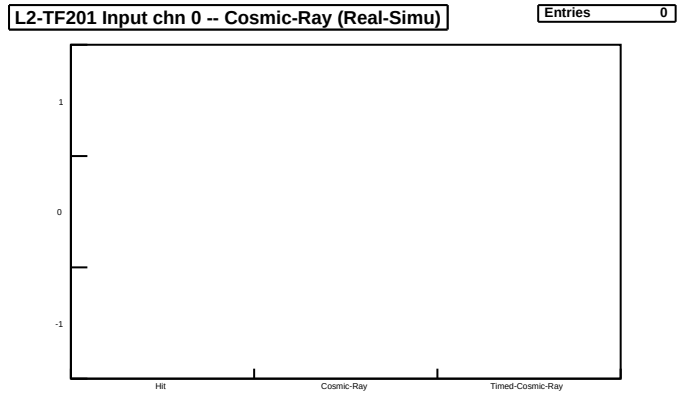

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 400000
Fired
1148 1236 7355 8974 2155 6291 5374 6145
48852 48764 42645 41026 47845 43709 44626 43855
ET IT EOR WOR EU ED WU WD
Bit

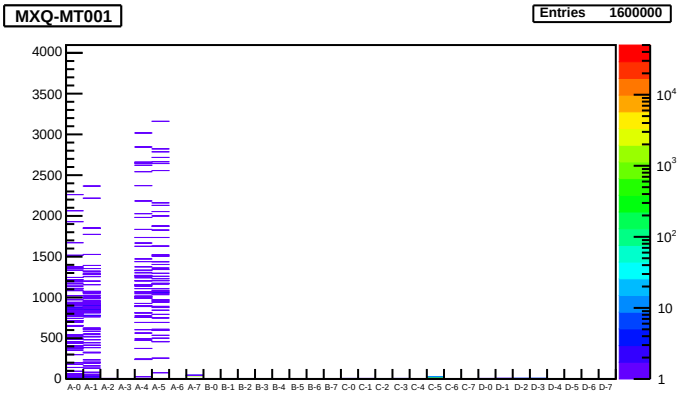

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 0
Fired(Real-Simu)
ET IT EOR WOR EU ED WU WD
Bit

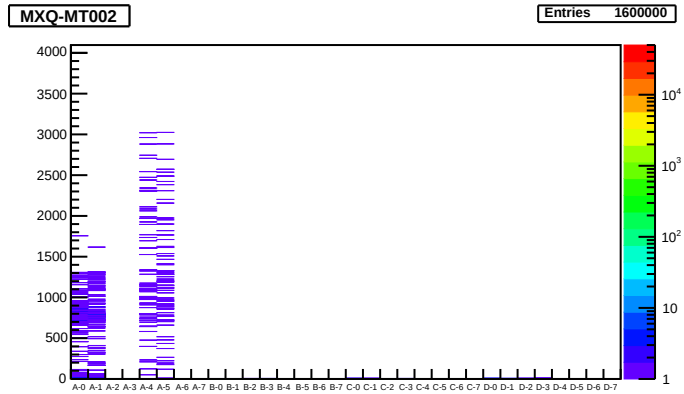

L2-TF201 Input chn 0 -- Number of Muons
Entries 50000
Number of Muons

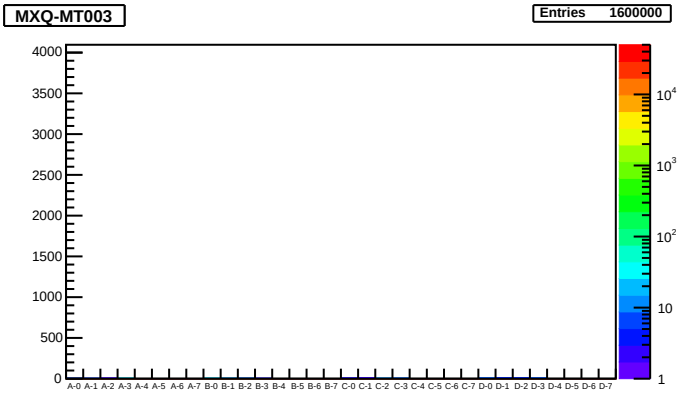

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

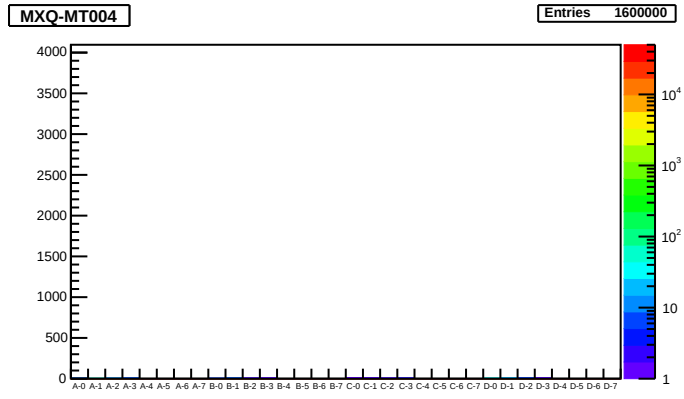

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 150000
119
49881
50000
50000
Hit
Cosmic-Ray
Timed-Cosmic-Ray

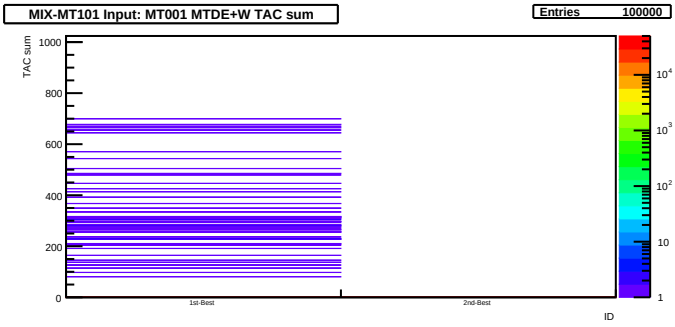

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

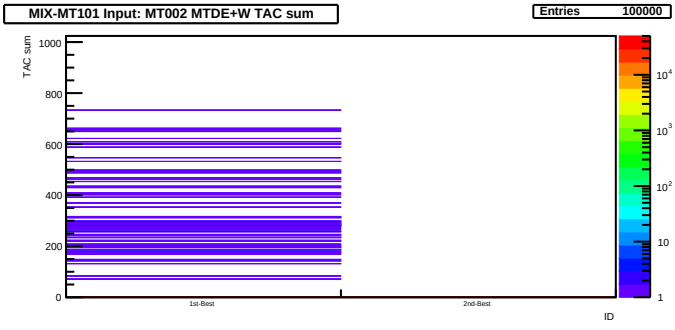

MXQ-MT001
Entries 1600000
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^4
10^3
10^2
10
1

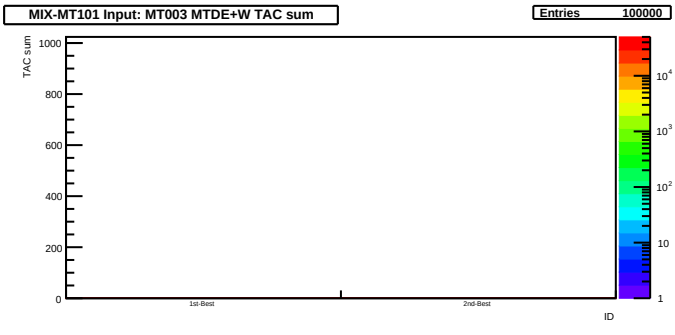

MXQ-MT002
Entries 1600000
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^4
10^3
10^2
10
1

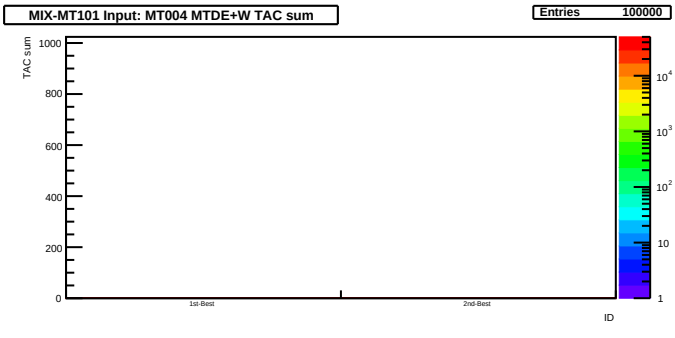

MXQ-MT003
Entries 1600000
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^4
10^3
10^2
10
1

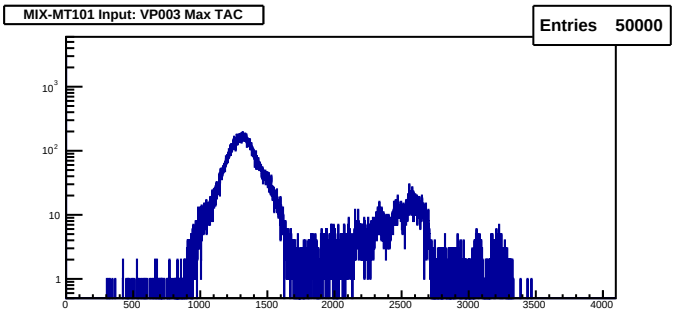

MXQ-MT004
Entries 1600000
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^4
10^3
10^2
10
1

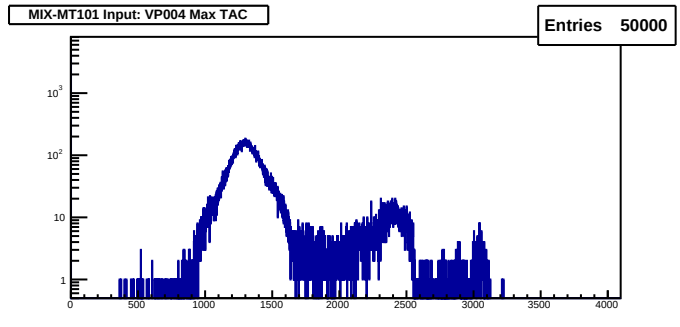

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 100000
TAC sum
1st-Best
2nd-Best
ID

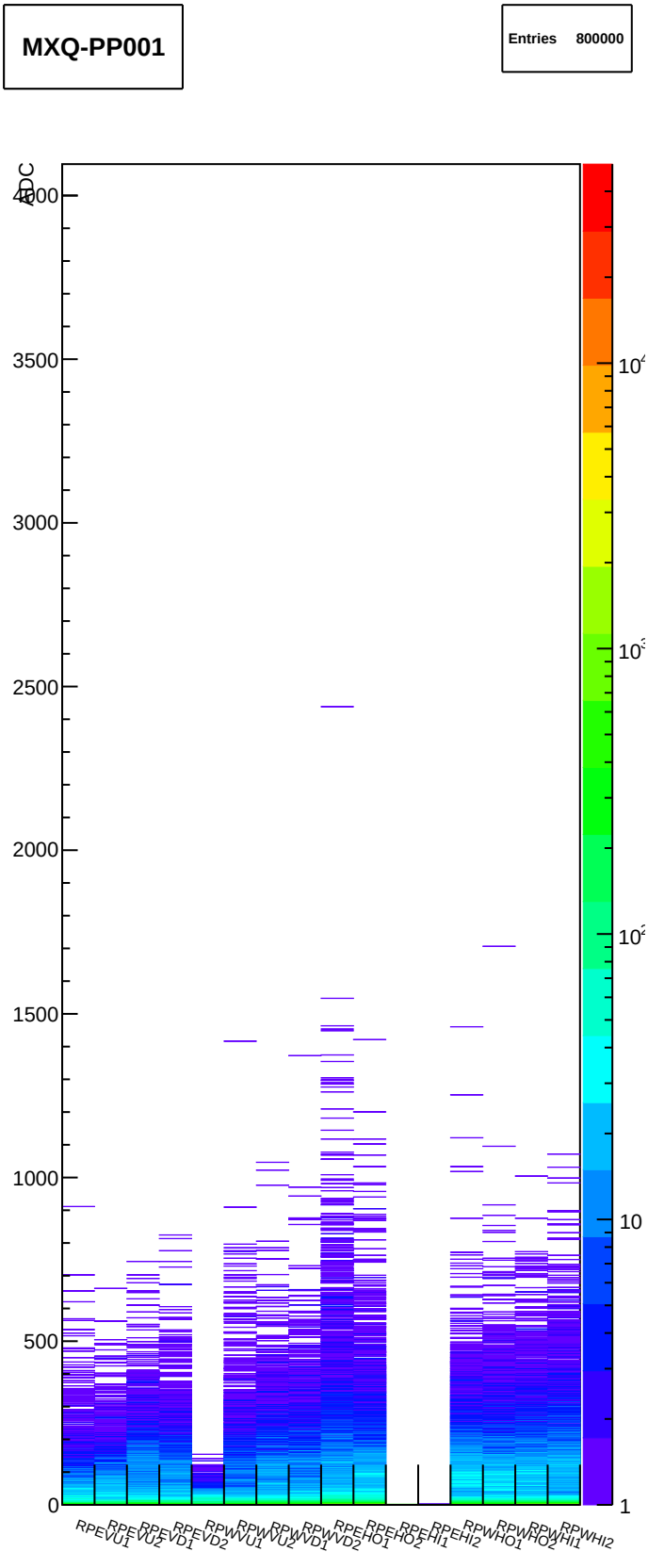

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 100000
TAC sum
1st-Best
2nd-Best
ID

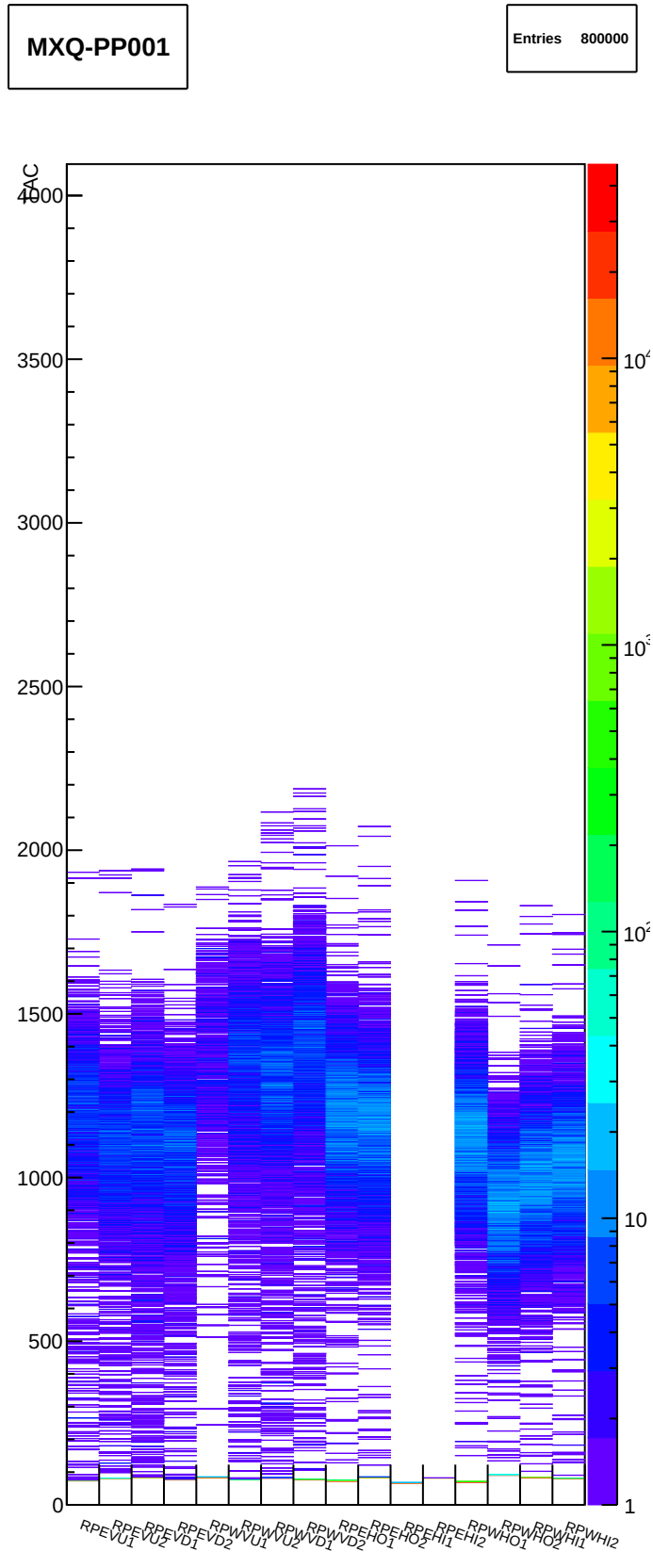

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 100000
TAC sum
1st-Best
2nd-Best
ID

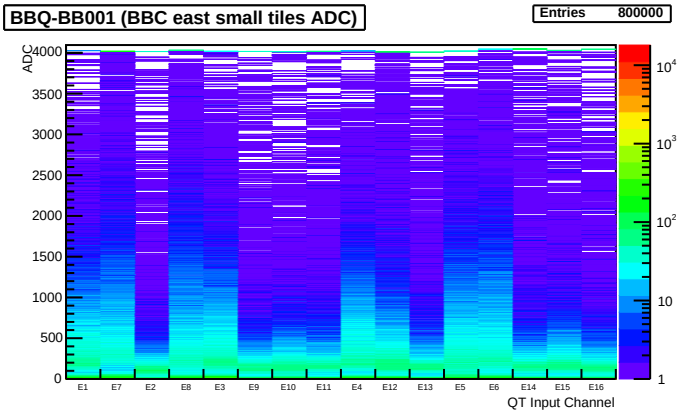

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 100000
TAC sum
1st-Best
2nd-Best
ID

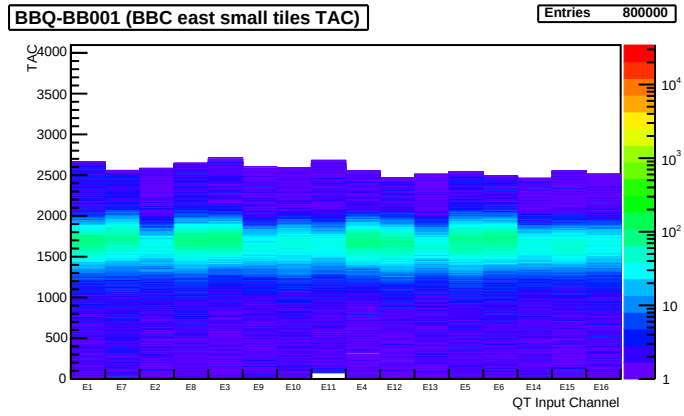

MIX-MT101 Input: VP003 Max TAC
Entries 50000

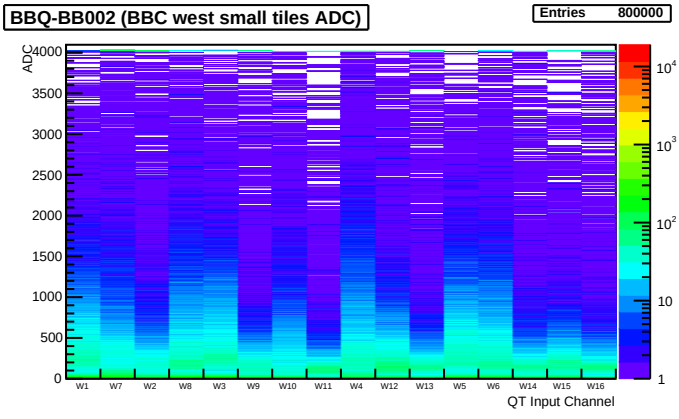

MIX-MT101 Input: VP004 Max TAC
Entries 50000

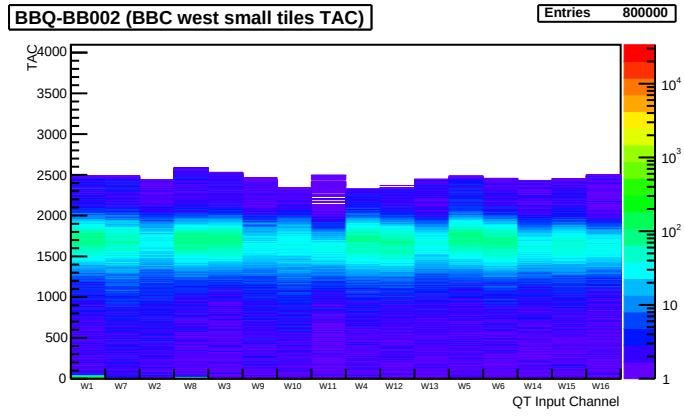

MXQ-PP001
Entries 800000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

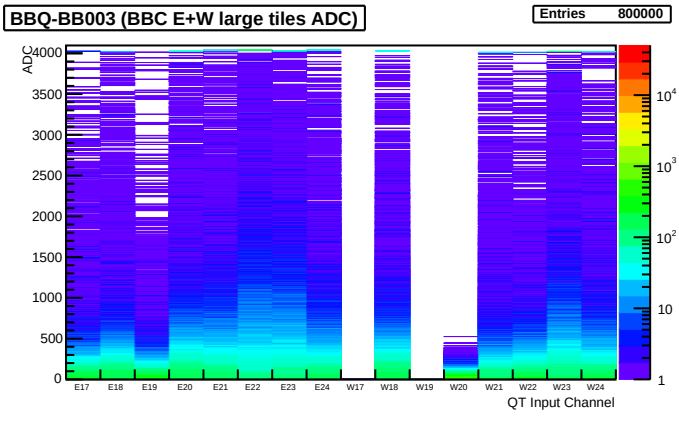

MXQ-PP001
Entries 800000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

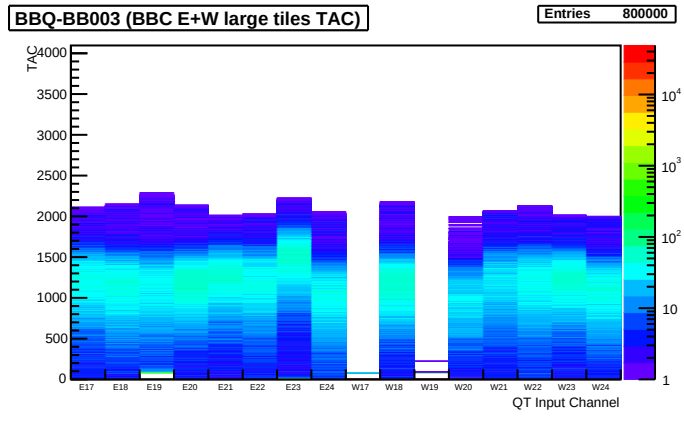

BBQ-BB001 (BBC east small tiles ADC)
Entries 800000
ADC
QT Input Channel

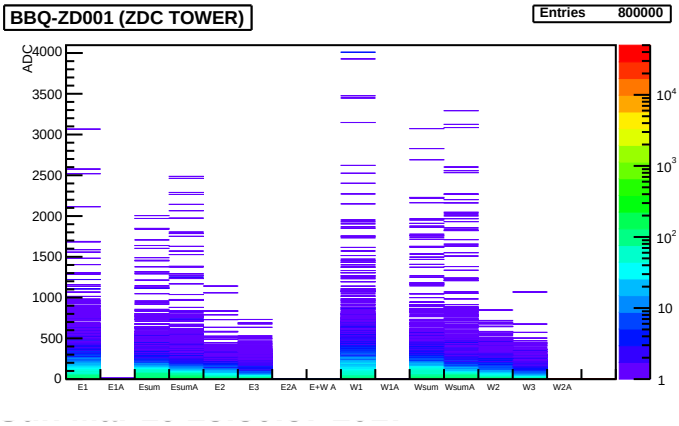

BBQ-BB001 (BBC east small tiles TAC)
Entries 800000
TAC
QT Input Channel

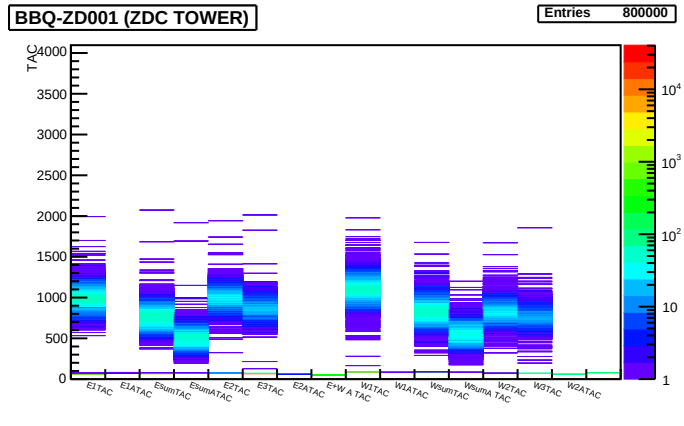

BBQ-BB002 (BBC west small tiles ADC)
Entries 800000
ADC
QT Input Channel

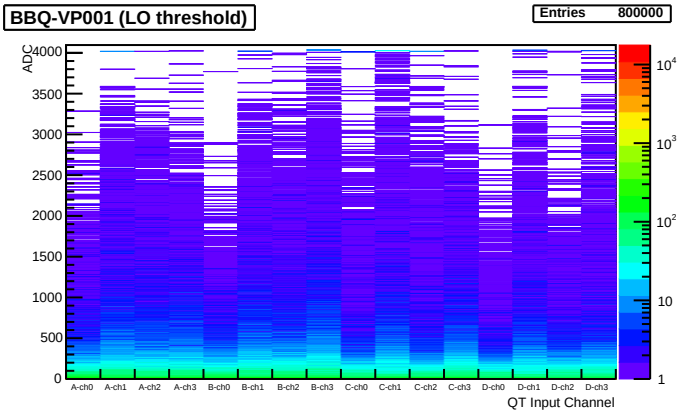

BBQ-BB002 (BBC west small tiles TAC)
Entries 800000
TAC
QT Input Channel

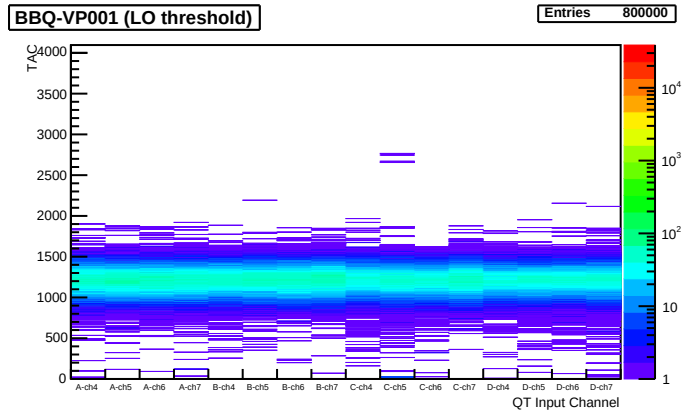

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 800000
ADC
QT Input Channel

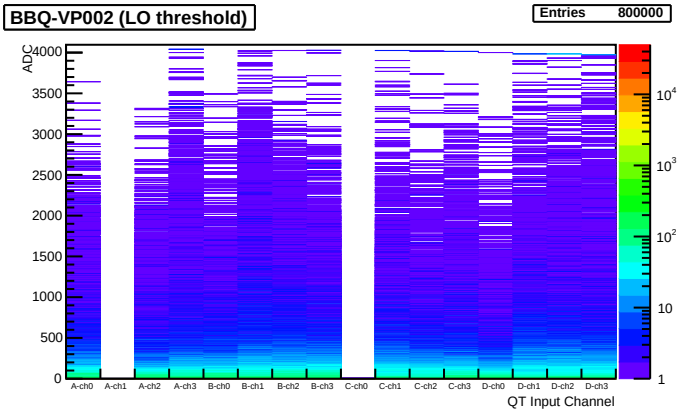

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 800000
TAC
QT Input Channel

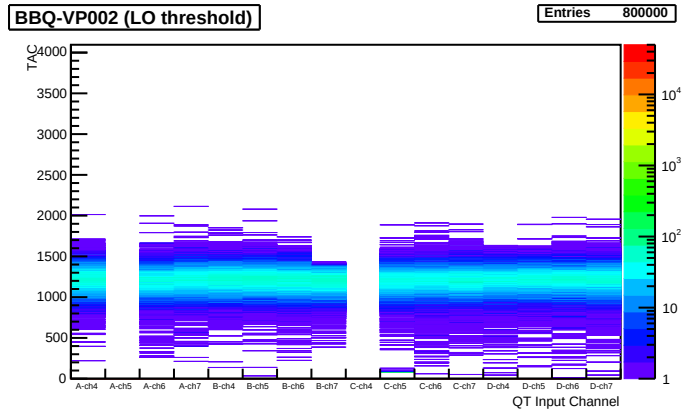

BBQ-ZD001 (ZDC TOWER)
Entries 800000
ADC

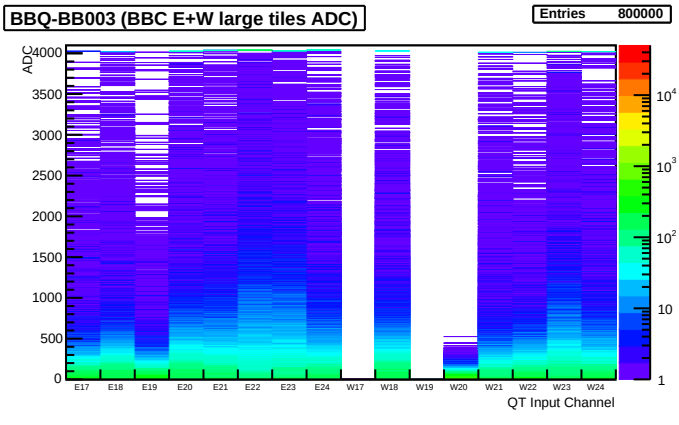

BBQ-ZD001 (ZDC TOWER)
Entries 800000
TAC

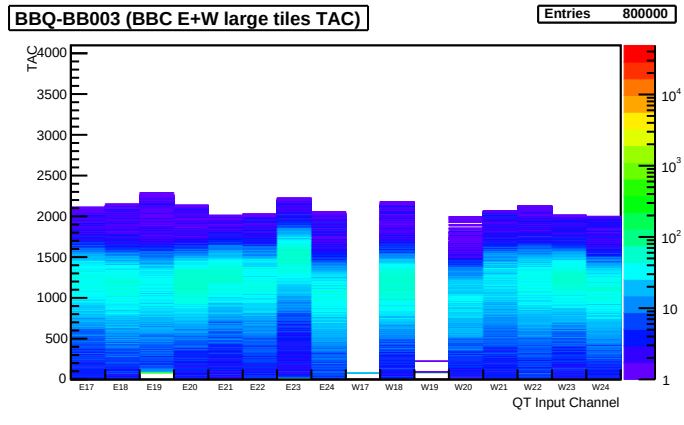

BBQ-VP001 (LO threshold)
Entries 800000
ADC
QT Input Channel

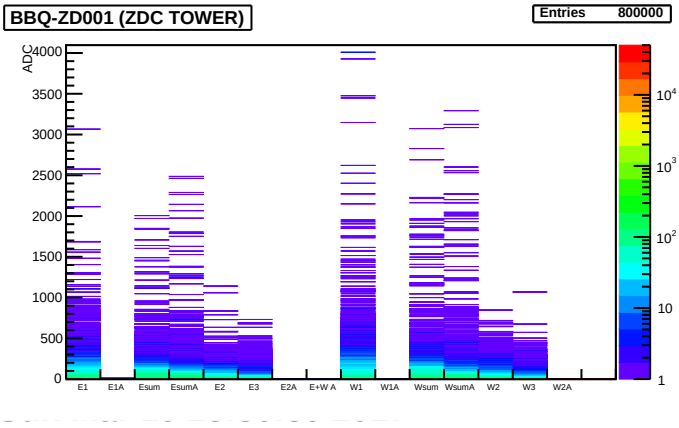

BBQ-VP001 (LO threshold)
Entries 800000
TAC
QT Input Channel

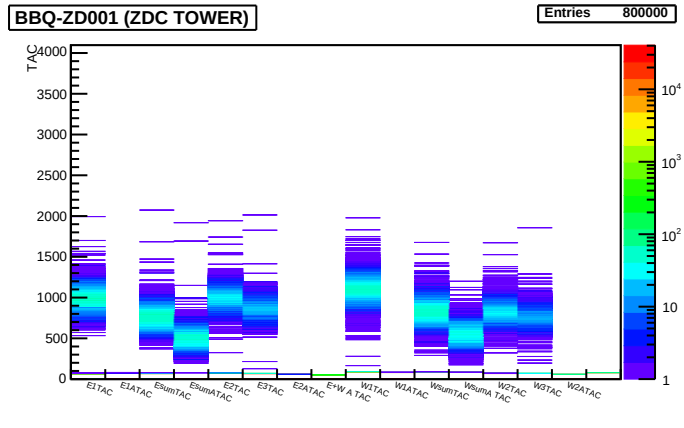

BBQ-VP002 (LO threshold)
Entries 800000
ADC
QT Input Channel

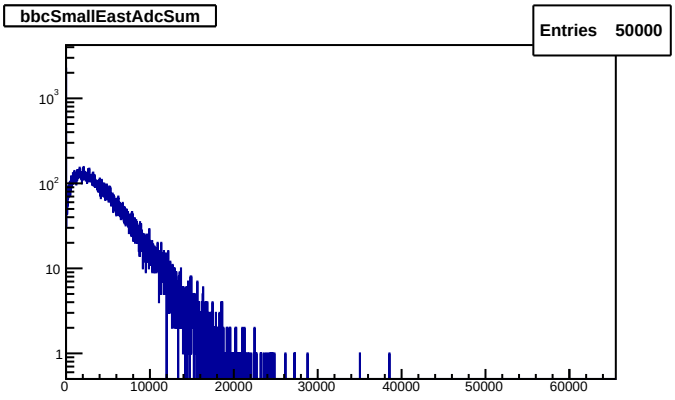

BBQ-VP002 (LO threshold)
Entries 800000
TAC
QT Input Channel

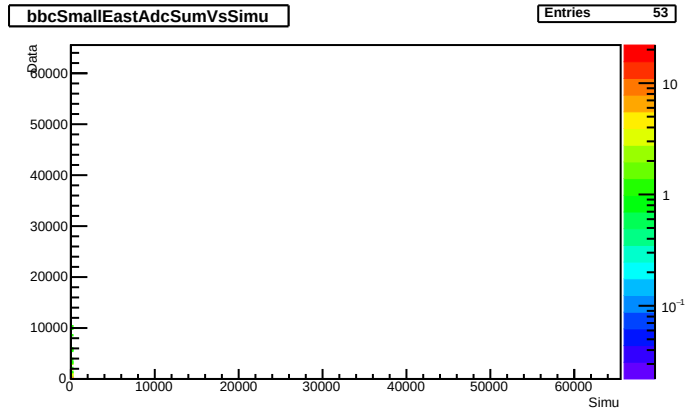

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 800000
ADC
QT Input Channel

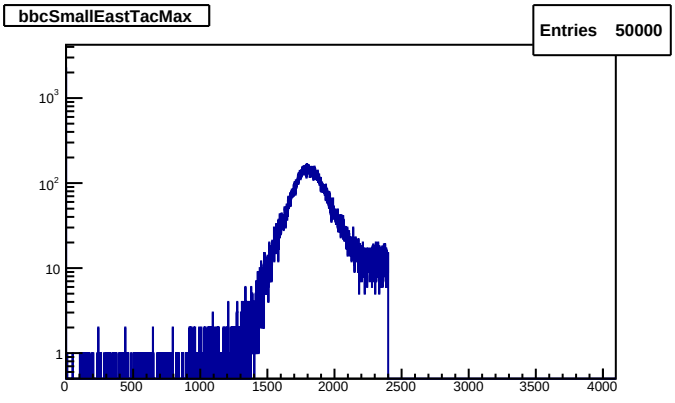

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 800000
TAC
QT Input Channel

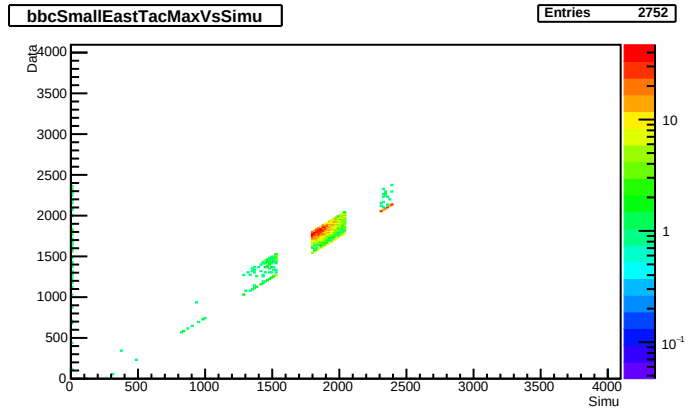

BBQ-ZD001 (ZDC TOWER)
Entries 800000
ADC

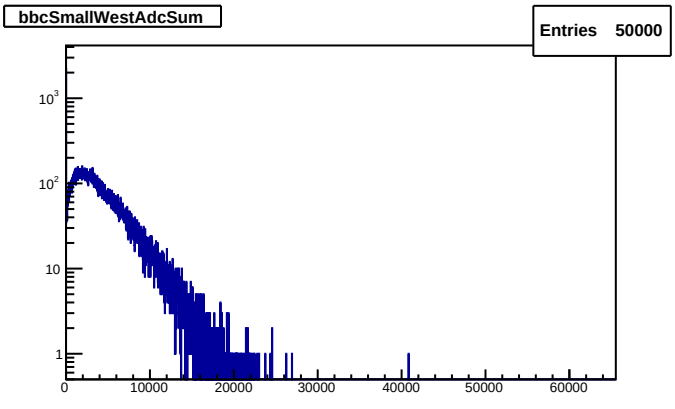

BBQ-ZD001 (ZDC TOWER)
Entries 800000
TAC

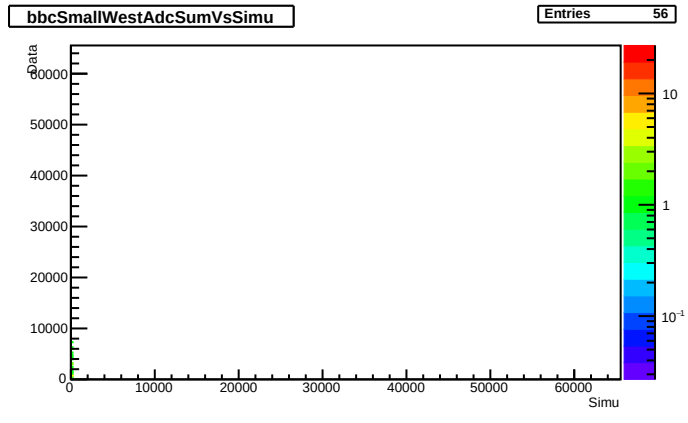

bbcSmallEastAdcSum
Entries 50000

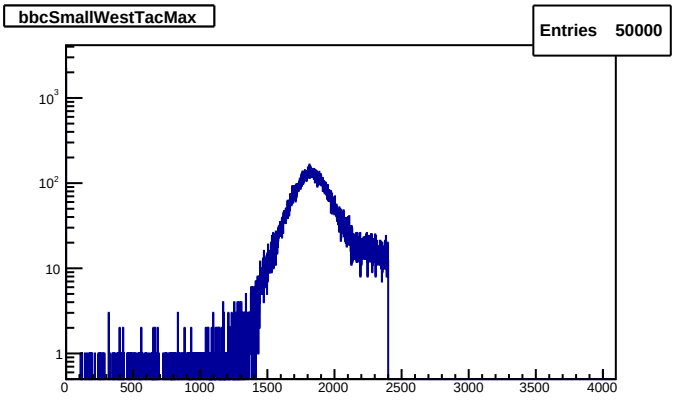

bbcSmallEastAdcSumVsSimu
Entries 53
Data
Simu

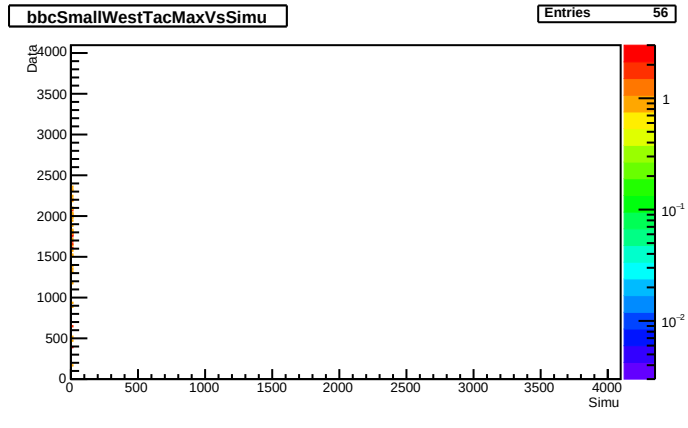

bbcSmallEastTacMax
Entries 50000

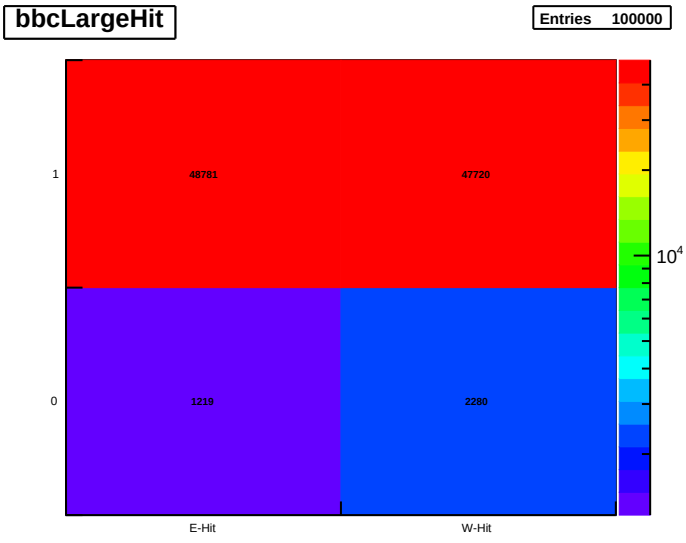

bbcSmallEastTacMaxVsSimu
Entries 2752
Data
Simu

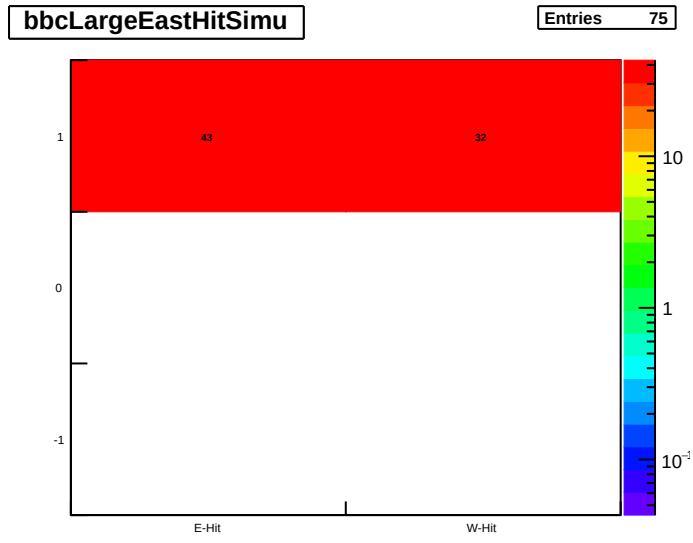

bbcSmallWestAdcSum
Entries 50000

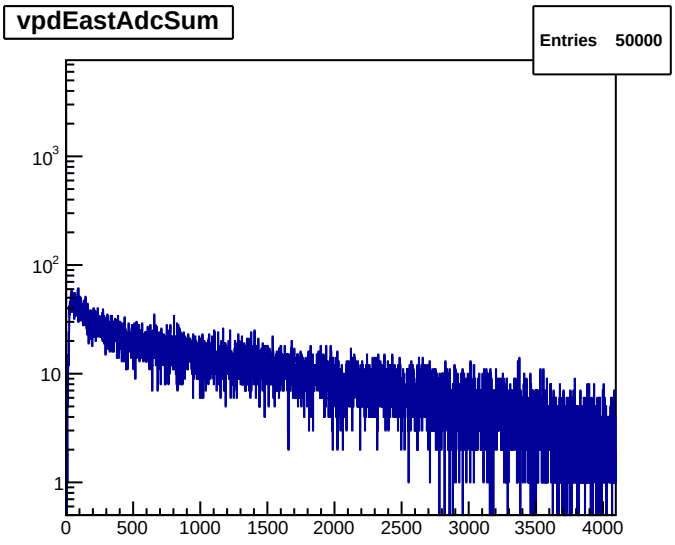

bbcSmallWestAdcSumVsSimu
Entries 56
Data
Simu

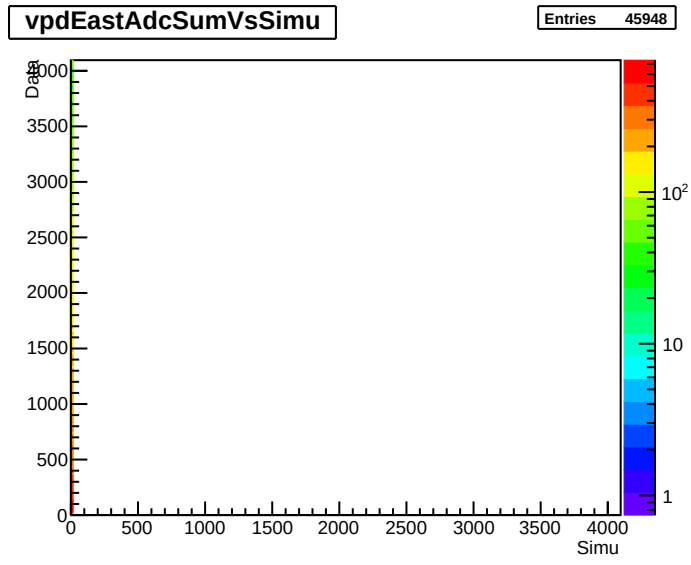

bbcSmallWestTacMax
Entries 50000

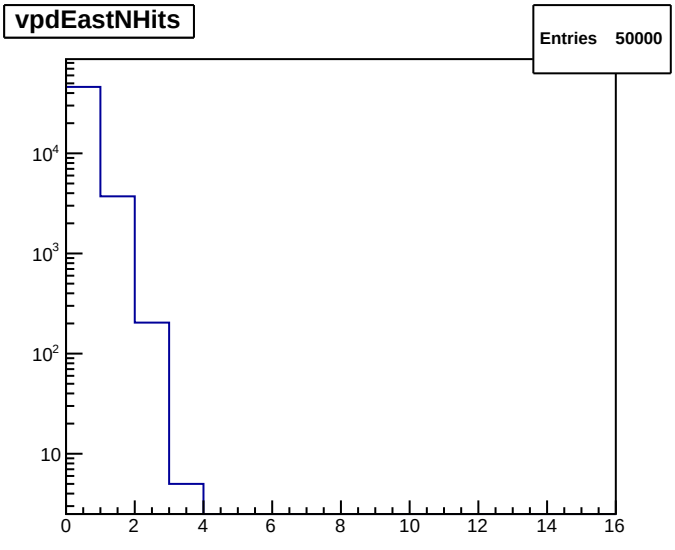

bbcSmallWestTacMaxVsSimu
Entries 56
Data
Simu

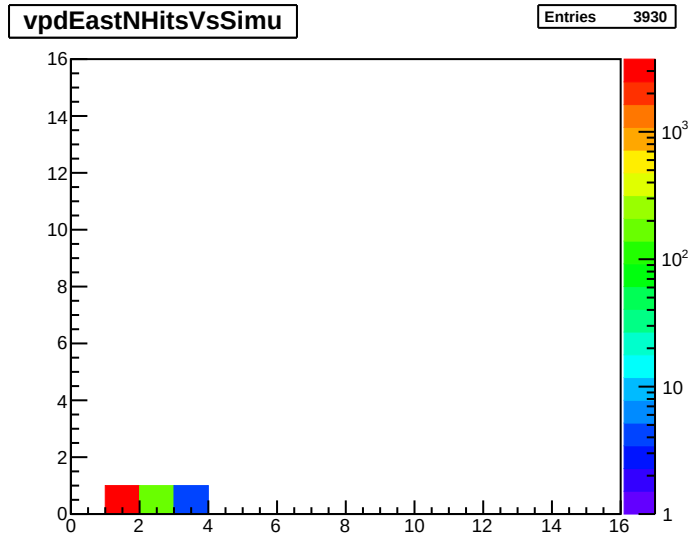

bbcLargeHit
Entries 100000
1
0
48781
47720
1219
2280
E-Hit
W-Hit
10^4

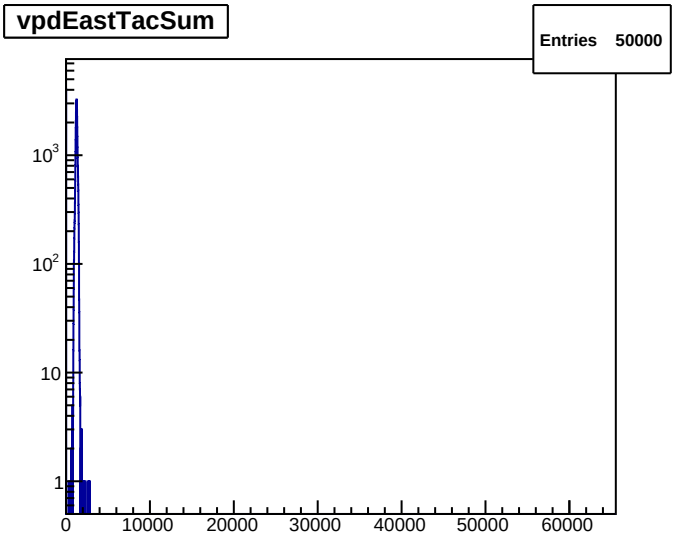

bbcLargeEastHitSimu
Entries 75
1
0
-1
43
32
E-Hit
W-Hit
10
1
10^{-1}

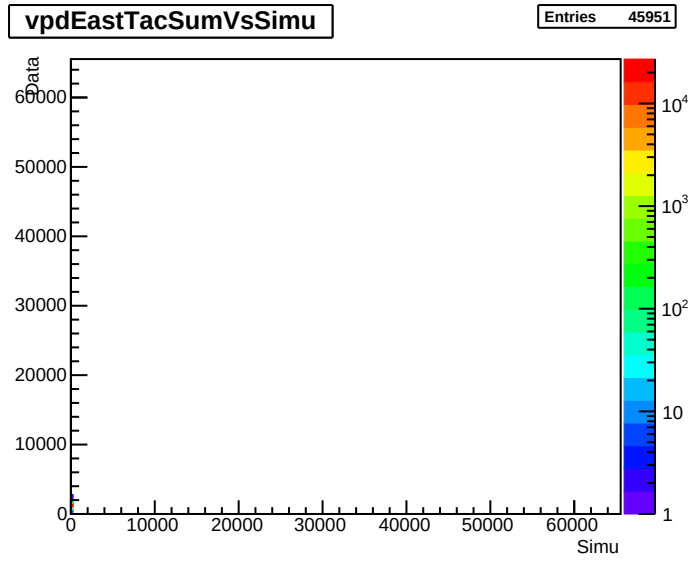

vpdEastAdcSum
Entries 50000

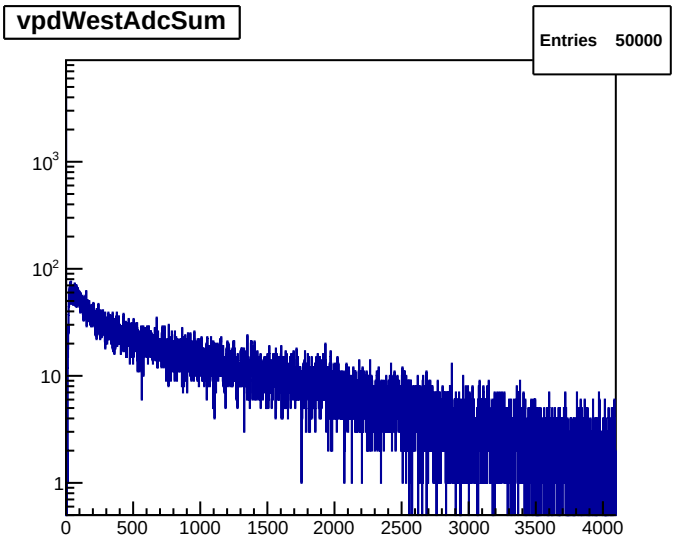

vpdEastAdcSumVsSimu
Entries 45948
Data
Simu

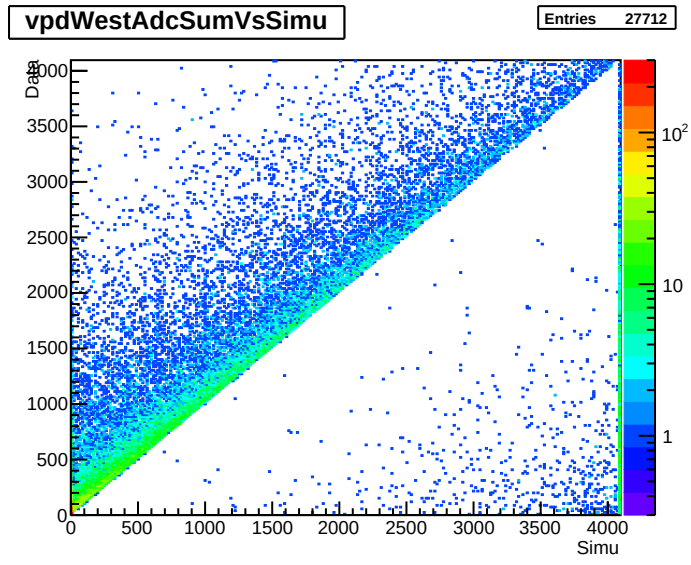

vpdEastNHits
Entries 50000

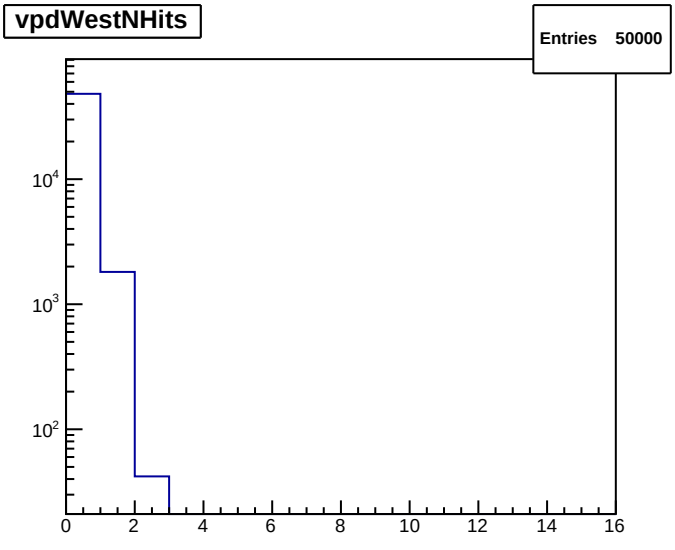

vpdEastNHitsVsSimu
Entries 3930

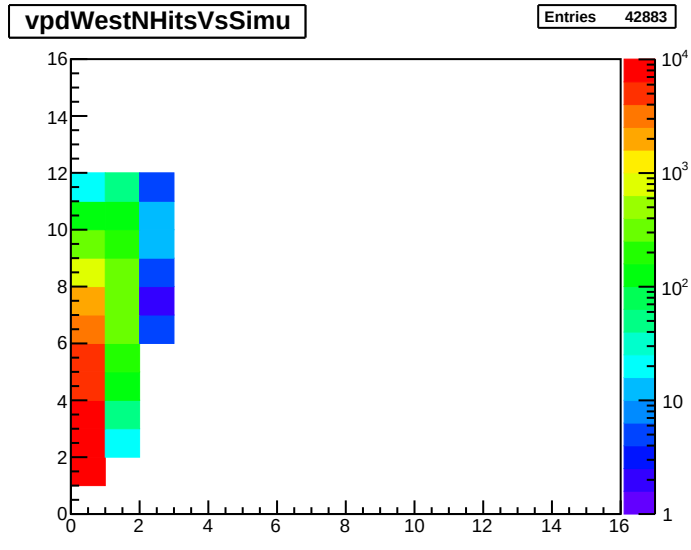

vpdEastTacSum
Entries 50000

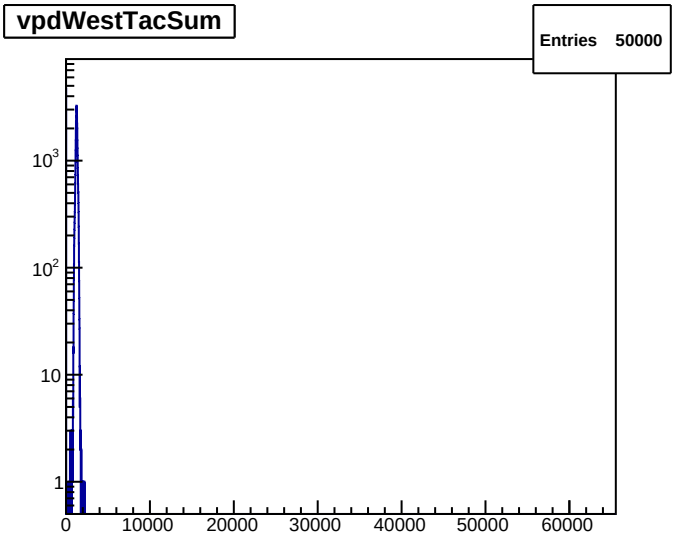

vpdEastTacSumVsSimu
Entries 45951
Data
Simu

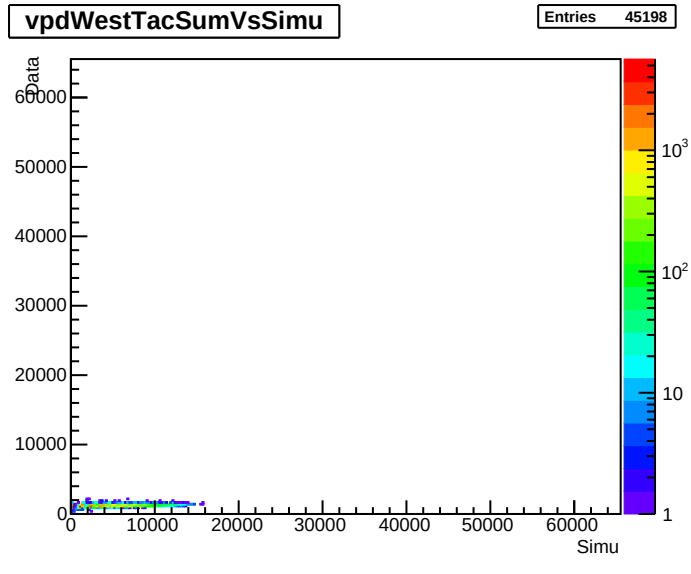

vpdWestAdcSum
Entries 50000

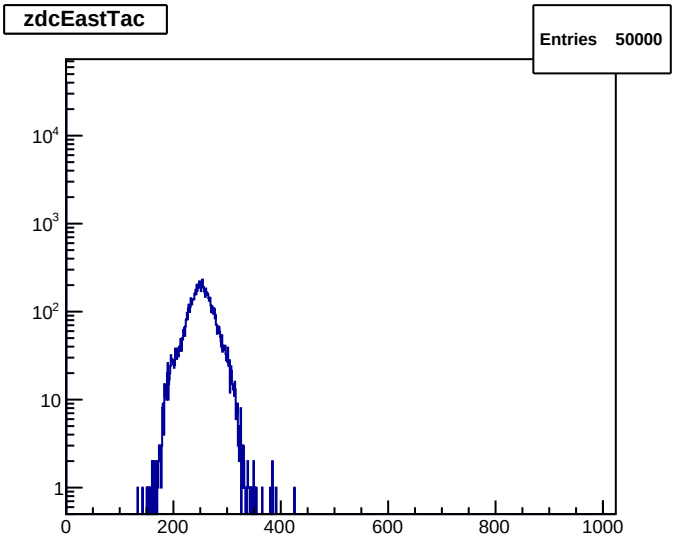

vpdWestAdcSumVsSimu
Entries 27712
Data
Simu

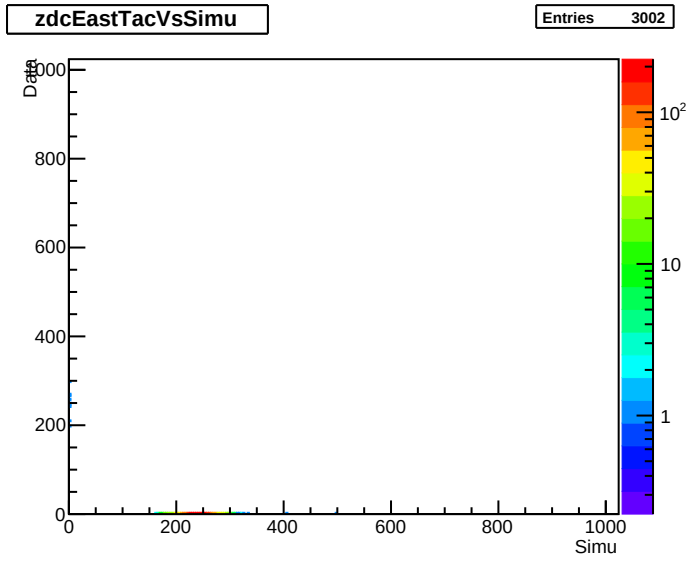

vpdWestNHits
Entries 50000

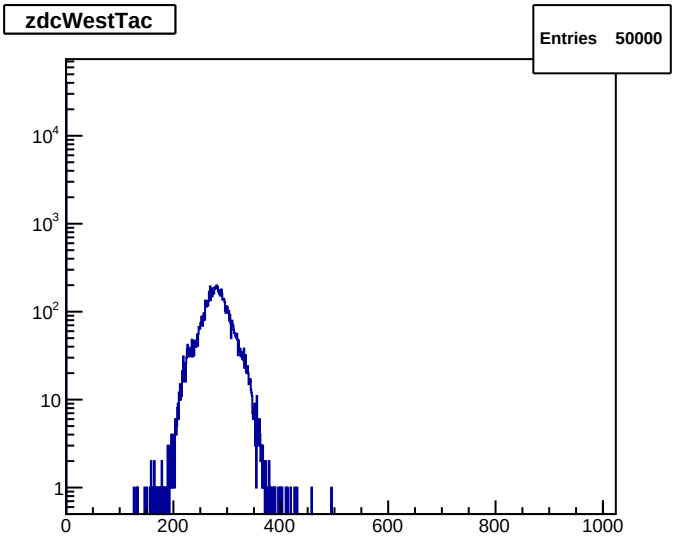

vpdWestNHitsVsSimu
Entries 42883

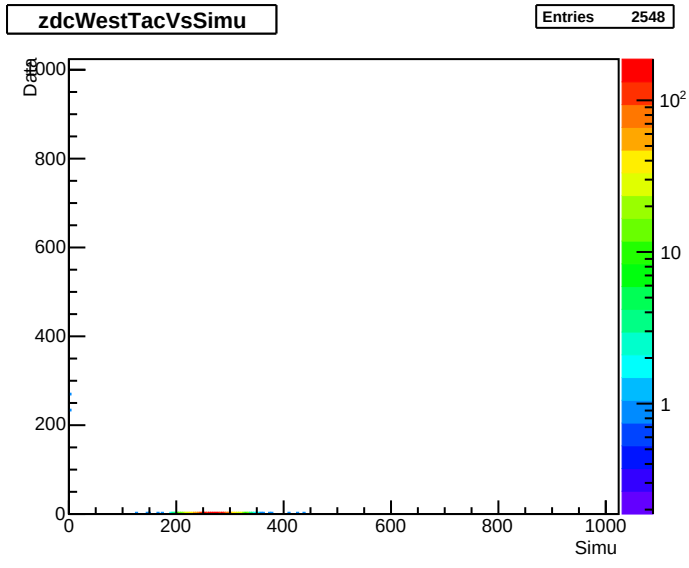

vpdWestTacSum
Entries 50000

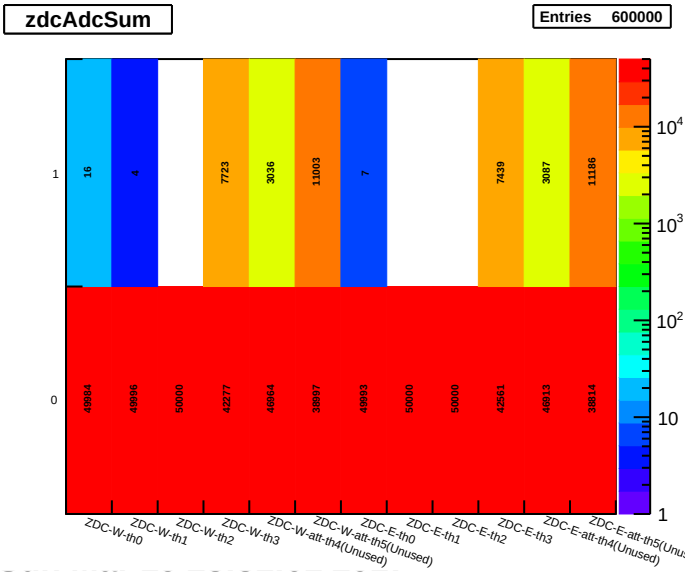

vpdWestTacSumVsSimu
Entries 45198
Data
Simu

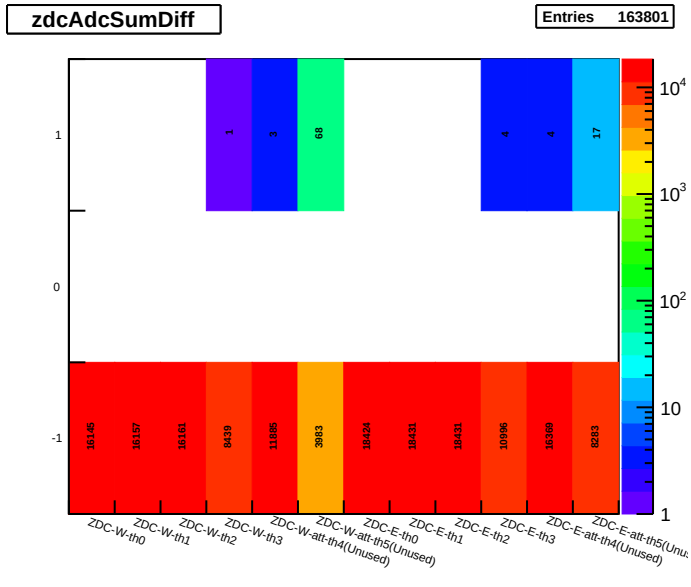

zdcEastTac
Entries 50000

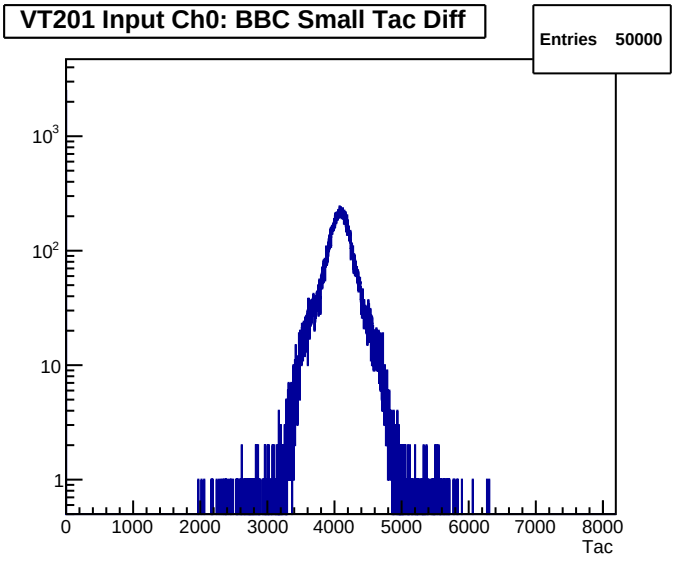

zdcEastTacVsSimu
Entries 3002
Data
Simu

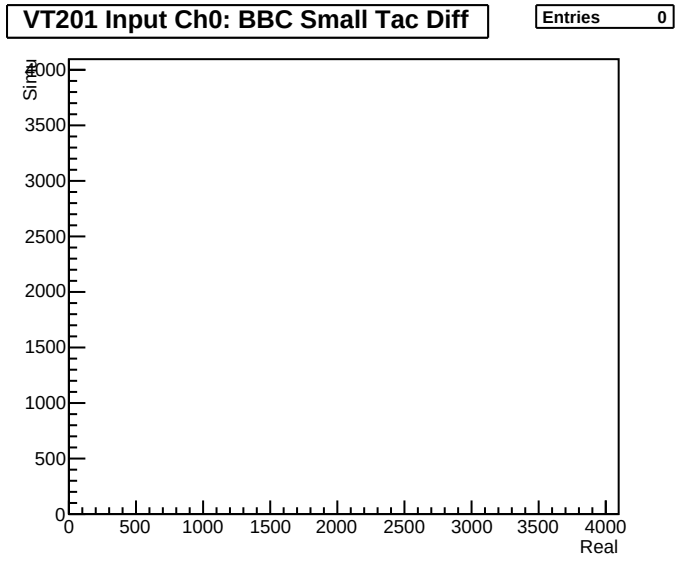

zdcWestTac
Entries 50000

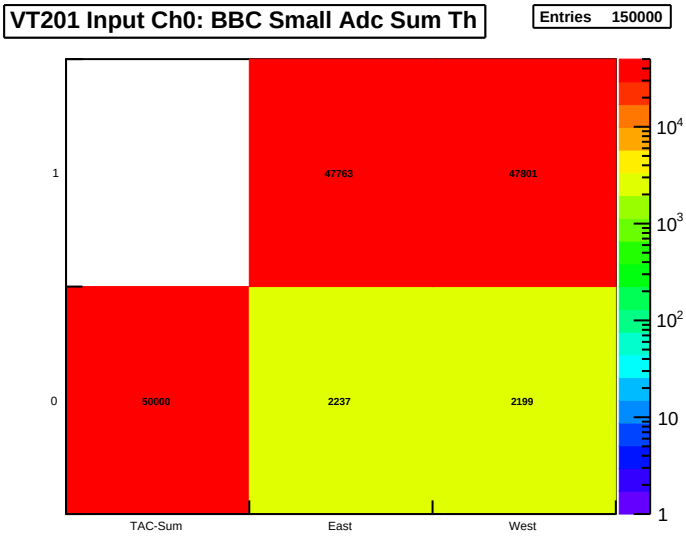

zdcWestTacVsSimu
Entries 2548
Data
Simu

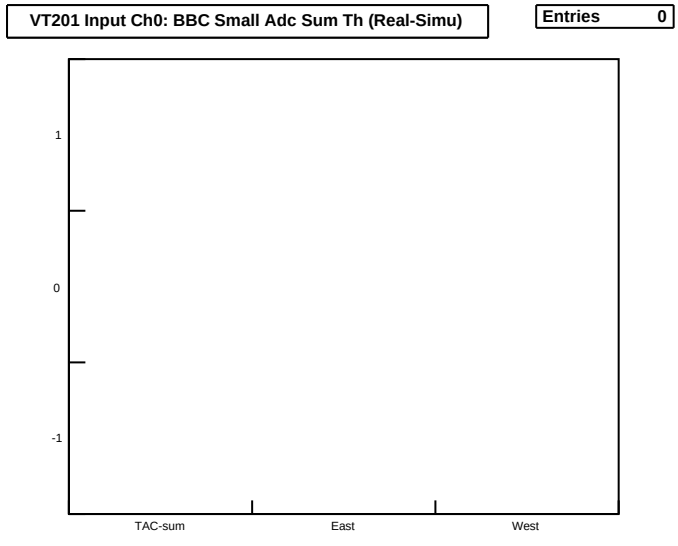

zdcAdcSum
Entries 600000
16
4
7723
3036
11003
7
7439
3087
11186
49984
49996
50000
42277
46864
38997
49993
50000
50000
42561
46913
38814
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

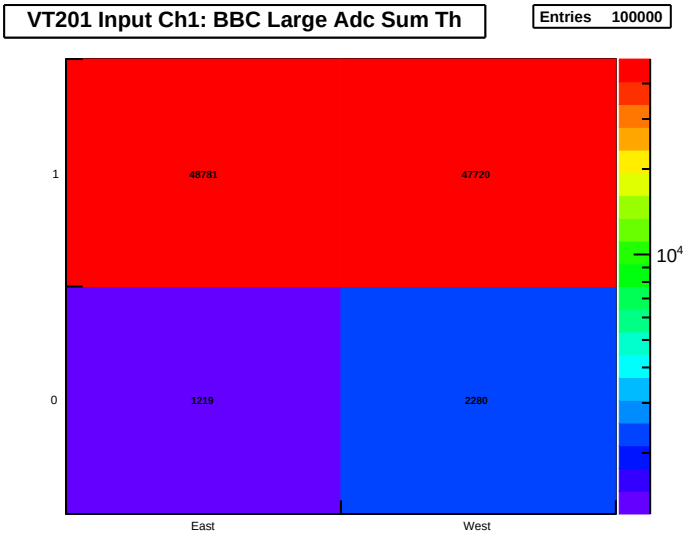

zdcAdcSumDiff
Entries 163801
1
3
68
4
4
17
16145
16157
16161
8439
11885
3983
18424
18431
18431
10996
16369
8283
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

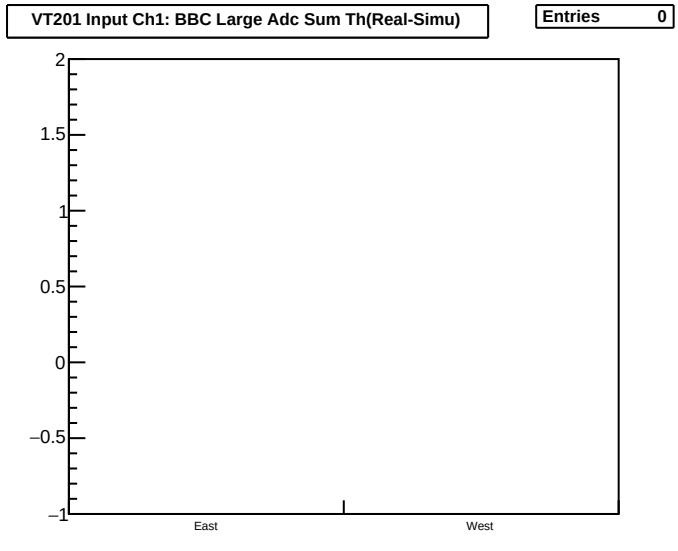

VT201 Input Ch0: BBC Small Tac Diff
Entries 50000
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

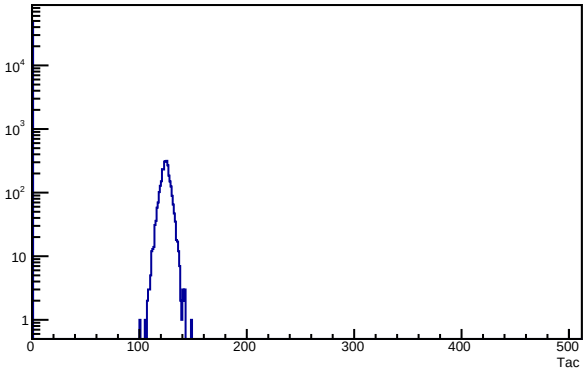

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 150000
47763
47801
50000
2237
2199
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

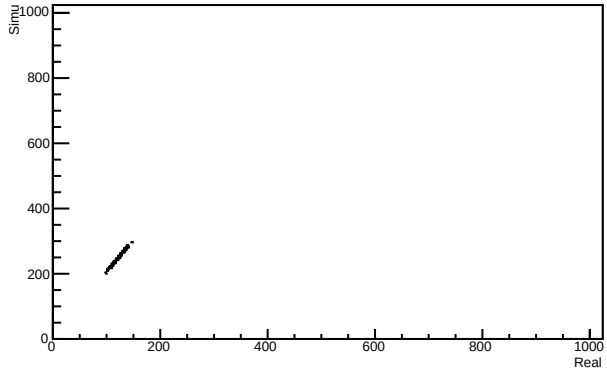

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 100000
48781
47720
1219
2280
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

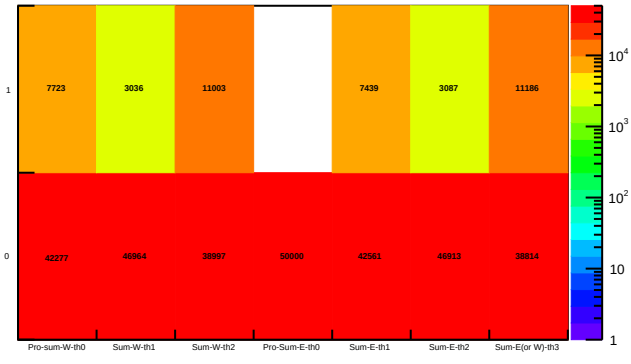
VT201 Input Ch2: ZDC Tac Diff



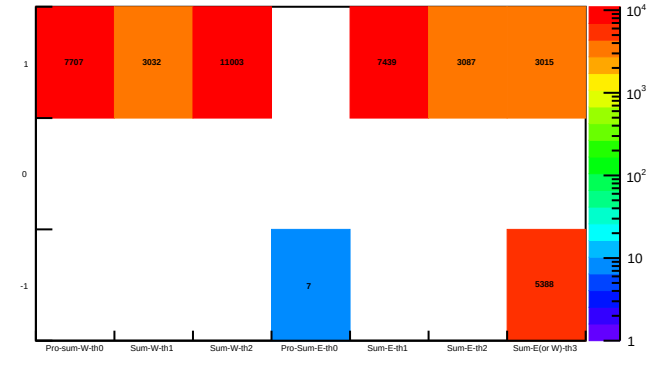
VT201 Input Ch2: ZDC Tac Diff



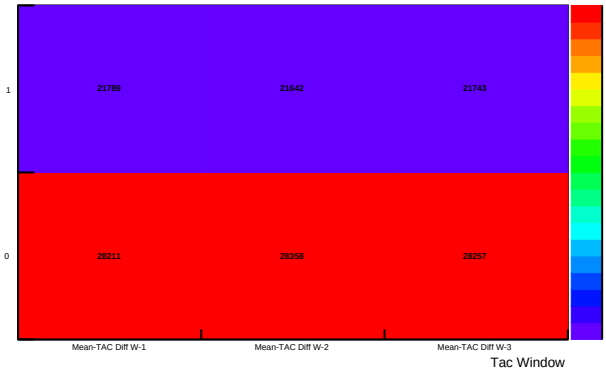
VT201 Input Ch2: ZDC Adc Sum Th



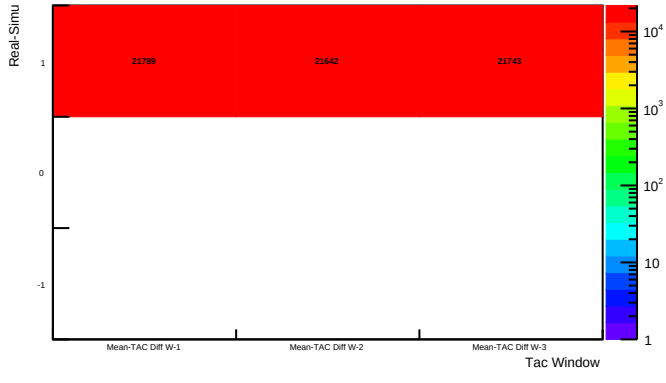
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



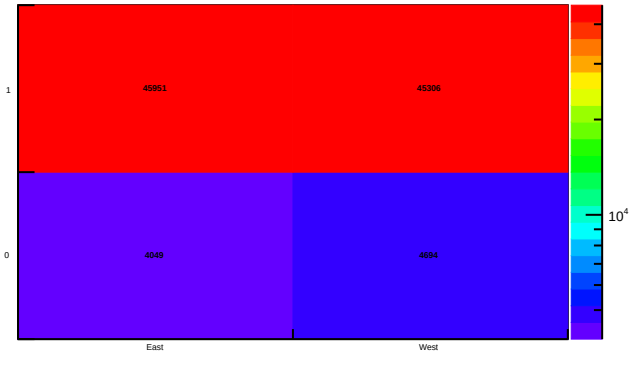
VT201 Input Ch4: VPD Tac Diff



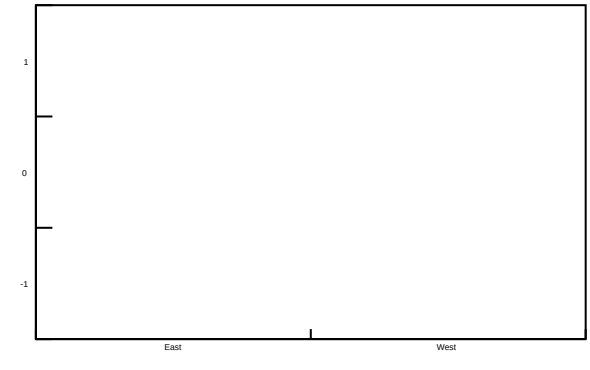
VT201 Input Ch4: VPD Tac Diff

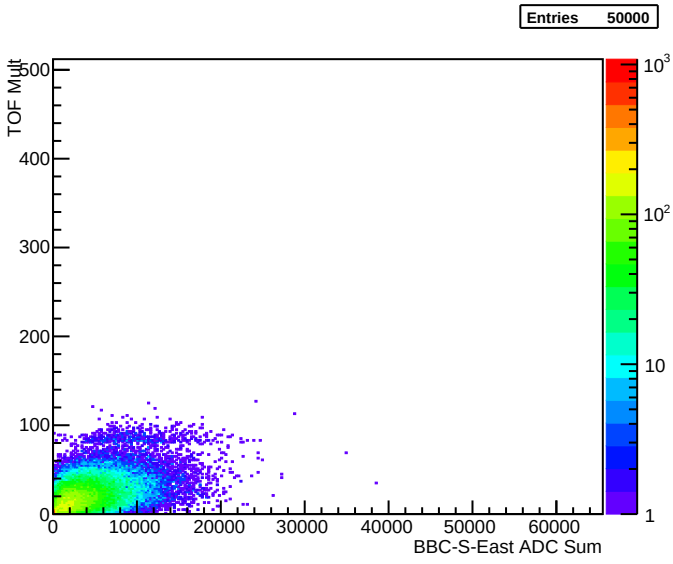


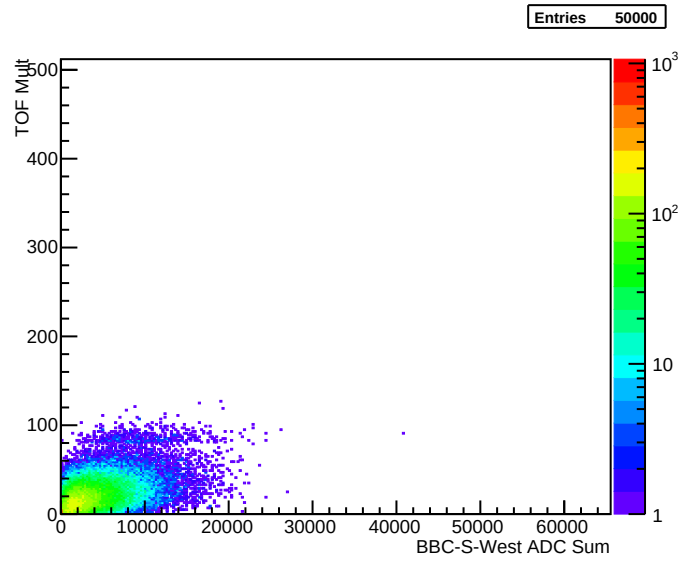
VT201 Input Ch4: VPD Adc Sum Th

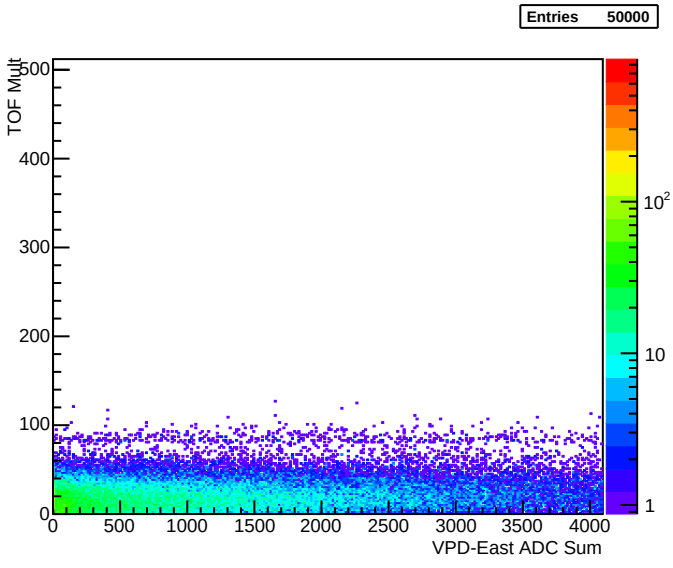


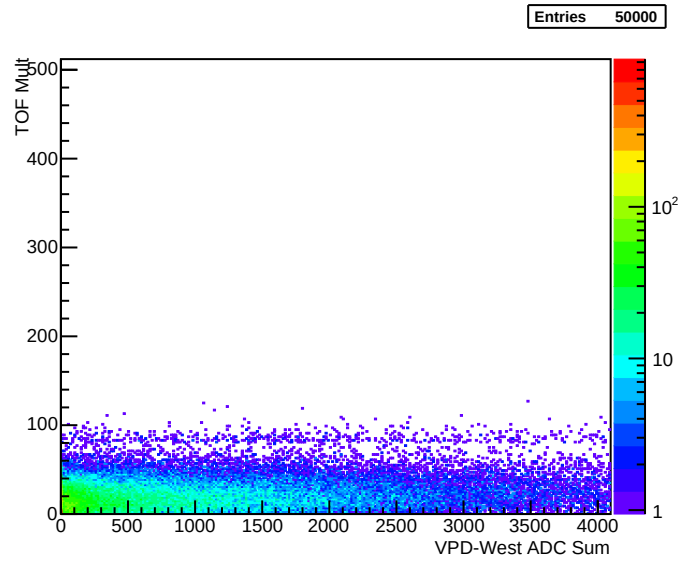
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

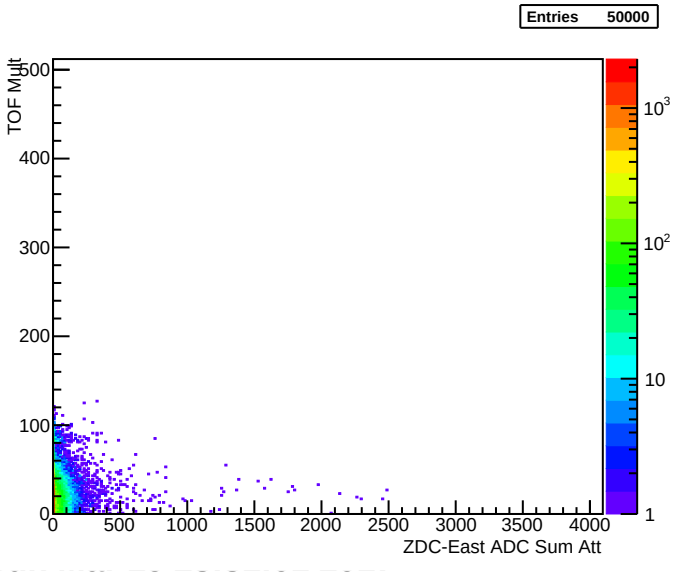


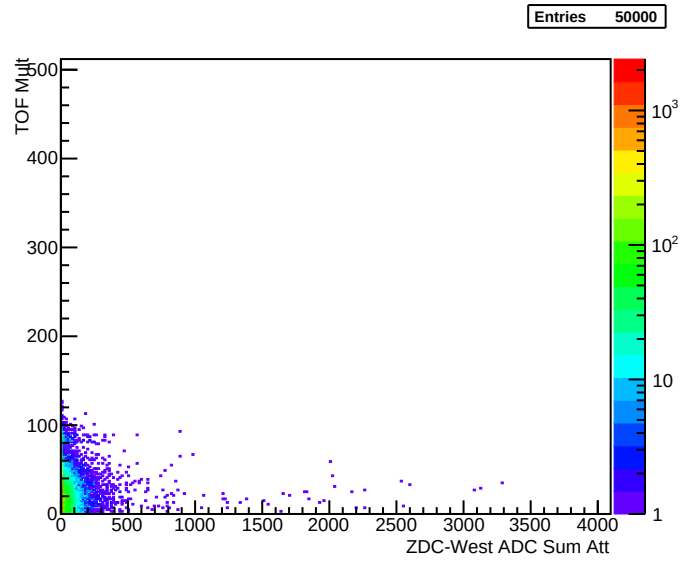

Entries 50000
TOF Mult
BBC-S-East ADC Sum

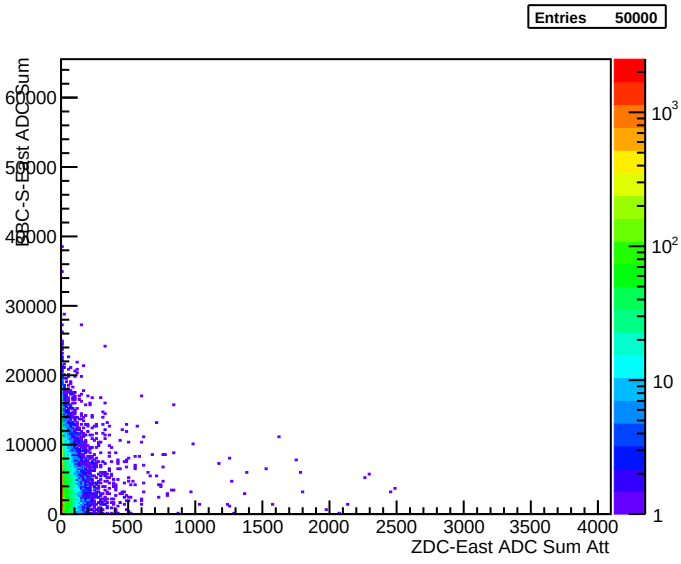

Entries 50000
TOF Mult
BBC-S-West ADC Sum

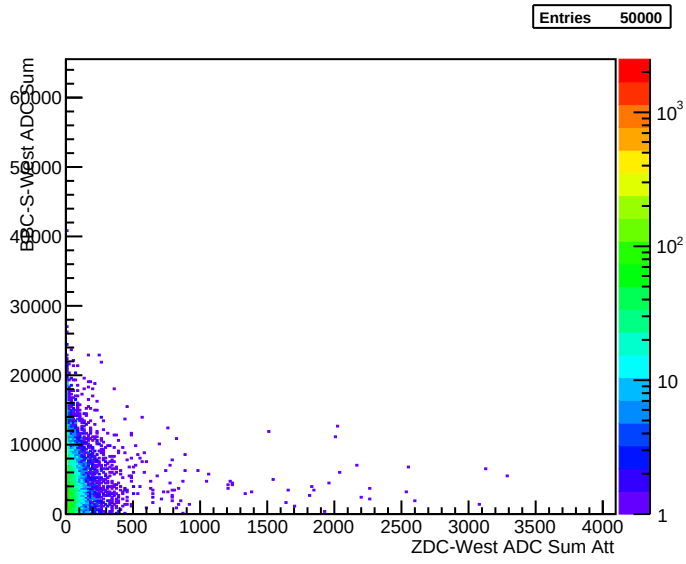

Entries 50000
TOF Mult
VPD-East ADC Sum

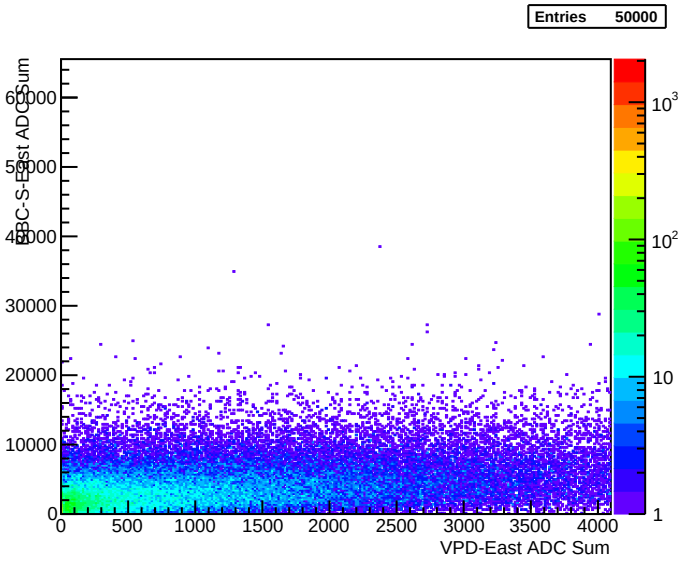

Entries 50000
TOF Mult
VPD-West ADC Sum

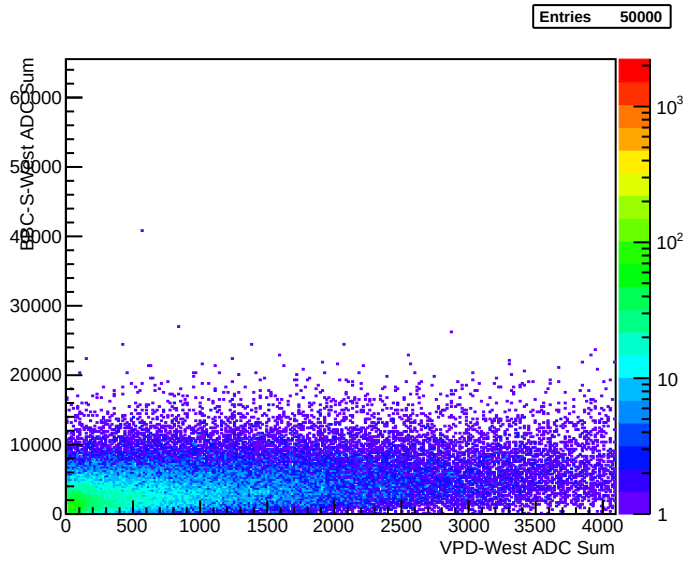

Entries 50000
TOF Mult
ZDC-East ADC Sum Att

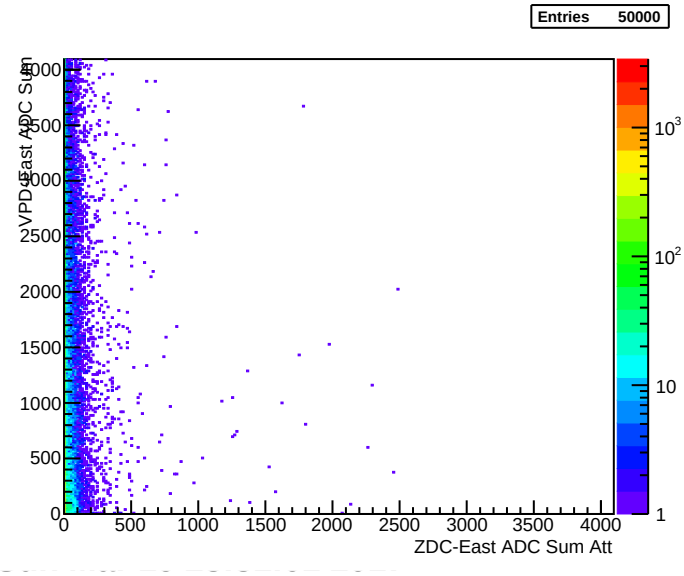

Entries 50000
TOF Mult
ZDC-West ADC Sum Att

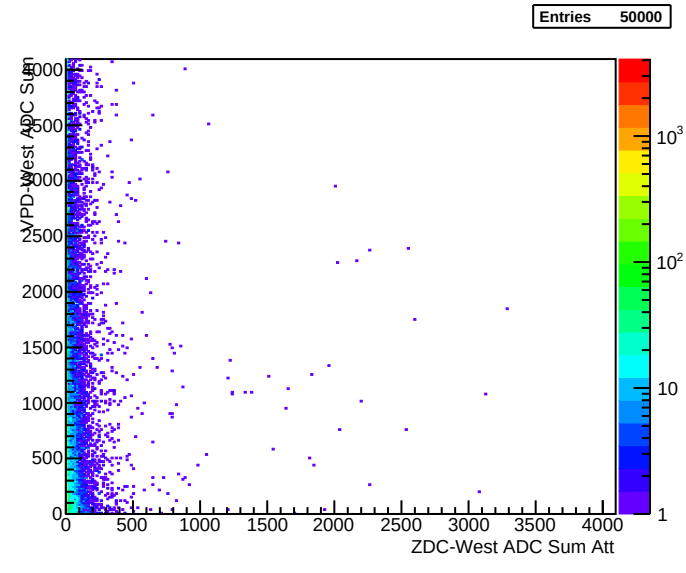

Entries 50000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

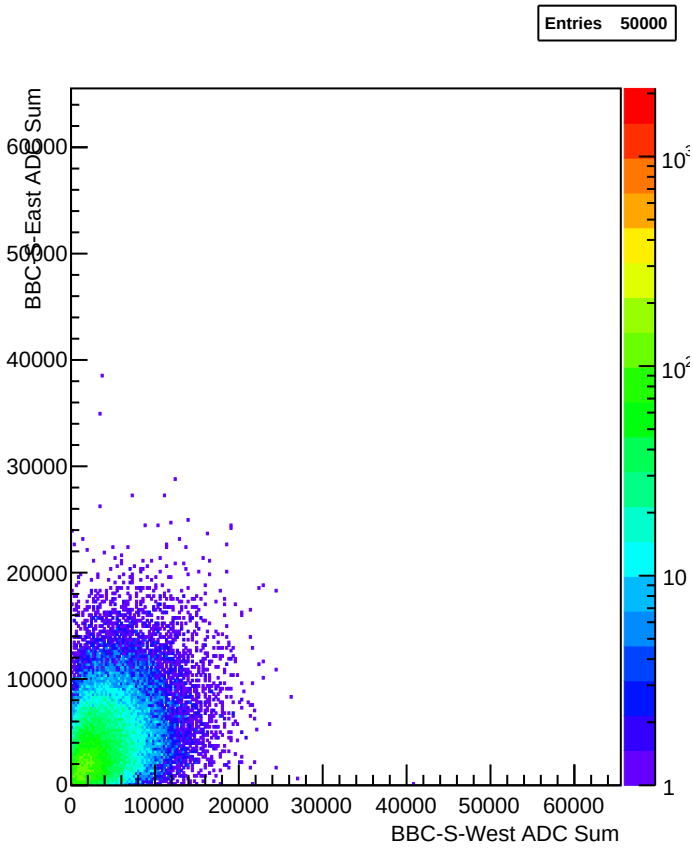

Entries 50000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

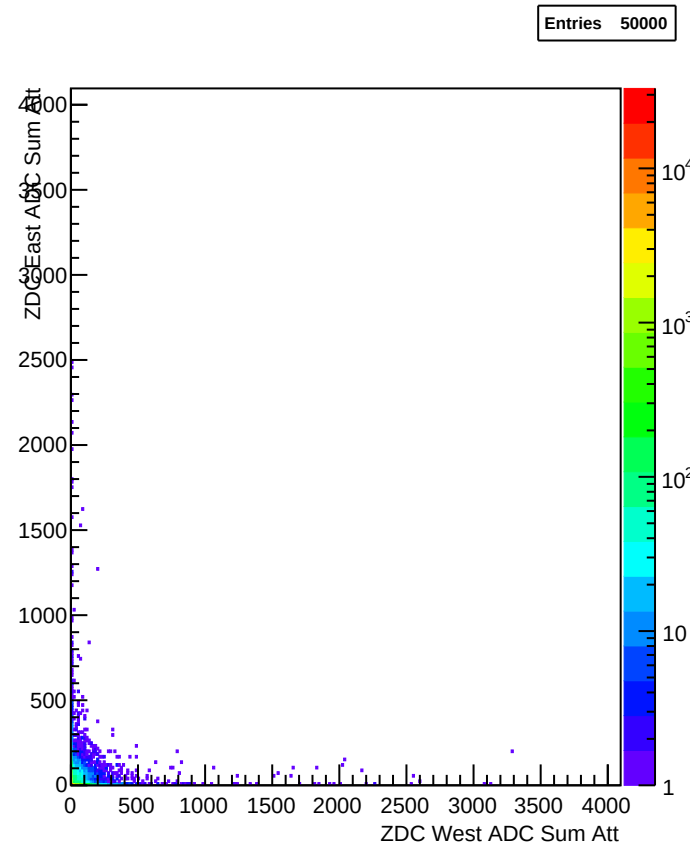

Entries 50000
BBC-S-East ADC Sum
VPD-East ADC Sum

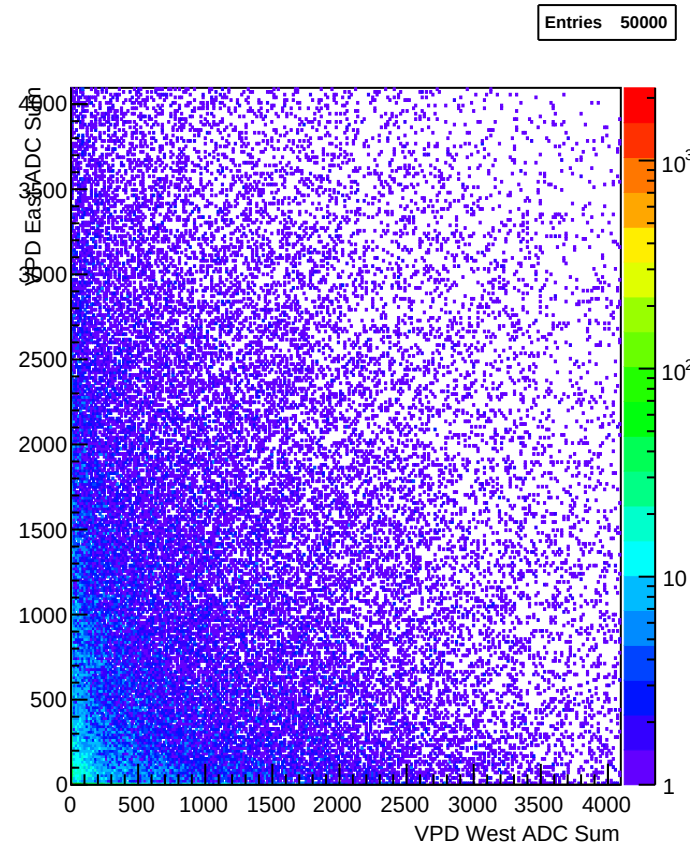

Entries 50000
BBC-S-West ADC Sum
VPD-West ADC Sum

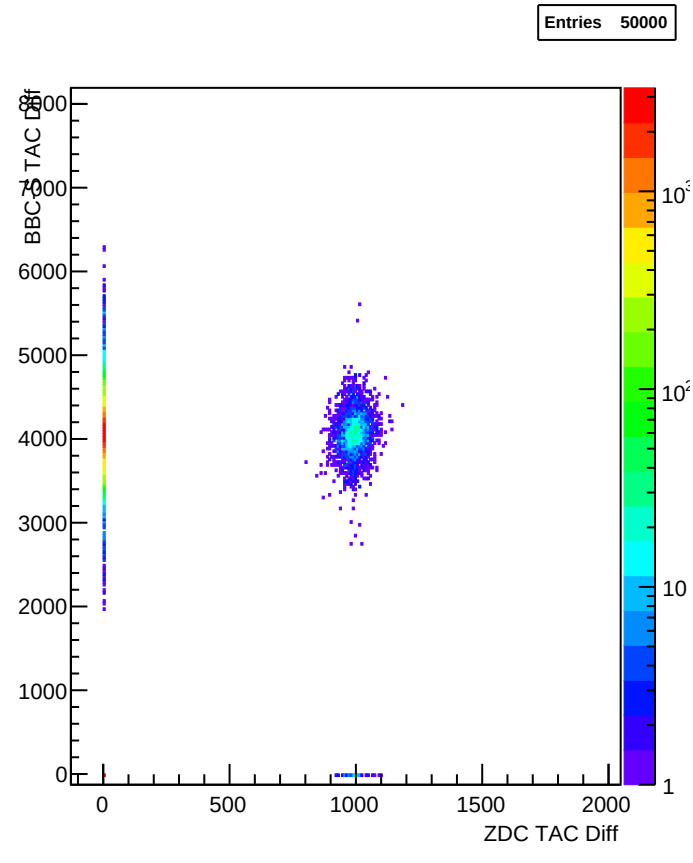

Entries 50000
VPD-East ADC Sum
ZDC-East ADC Sum Att

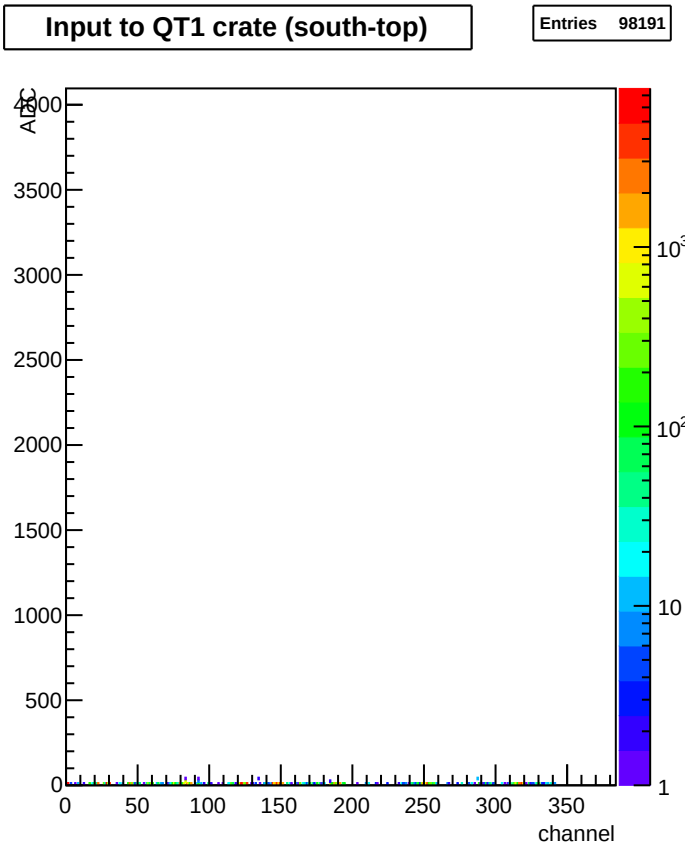

Entries 50000
VPD-West ADC Sum
ZDC-West ADC Sum Att

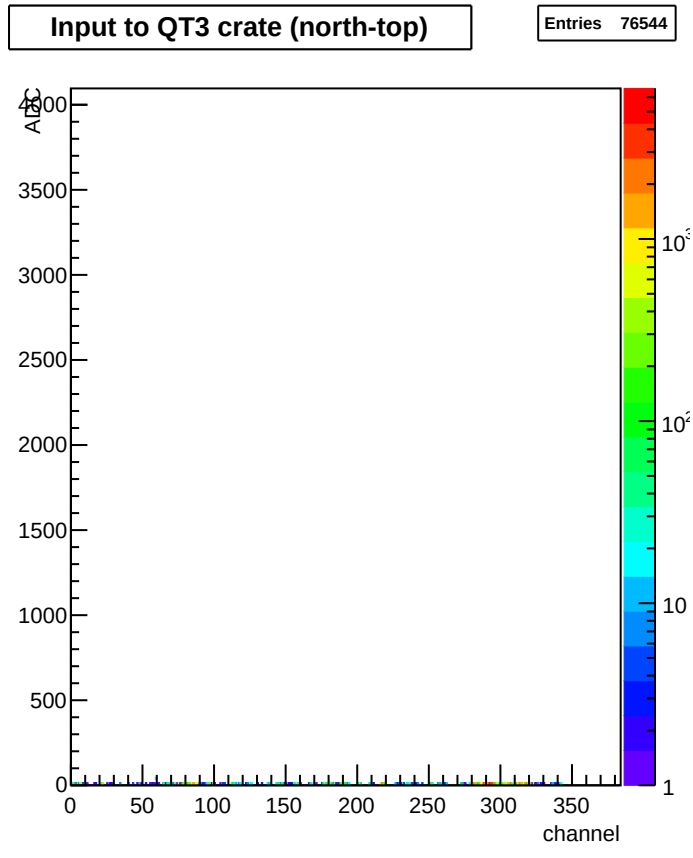

Entries 50000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

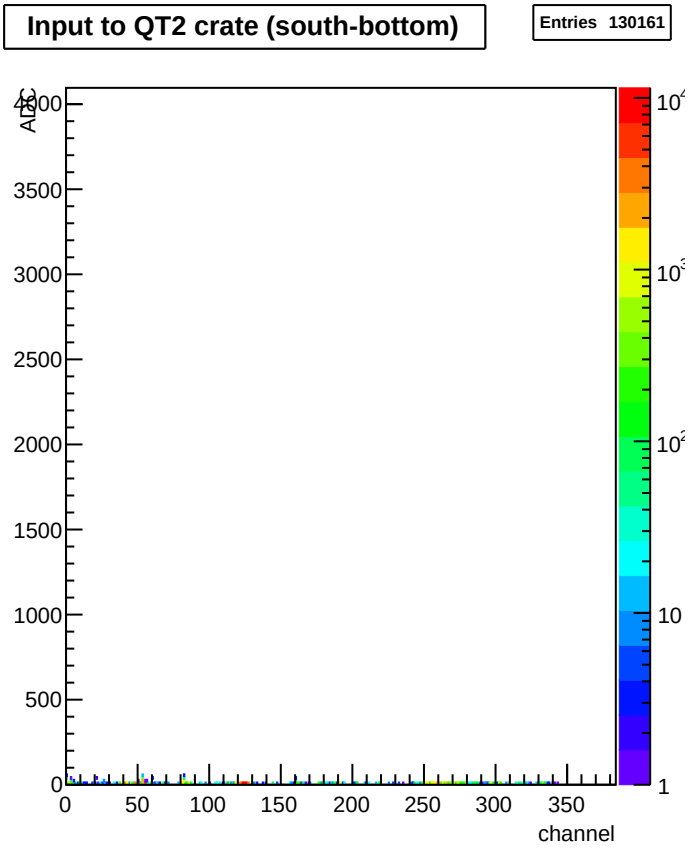

Entries 50000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

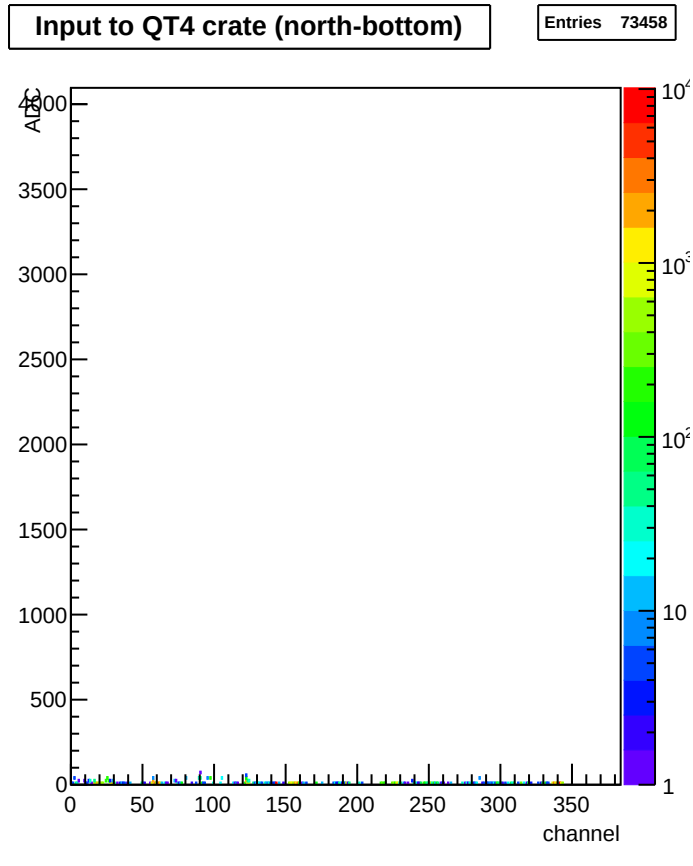

Entries 50000
VPD East ADC Sum
VPD West ADC Sum

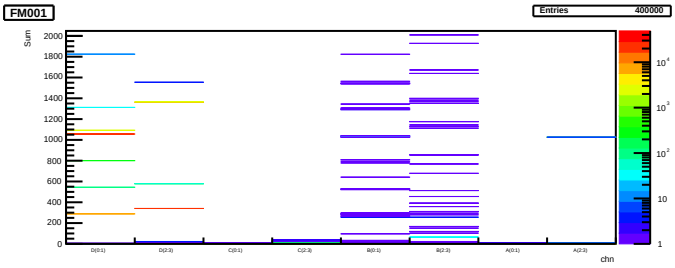

Entries 50000
BBC-S TAC Diff
ZDC TAC Diff

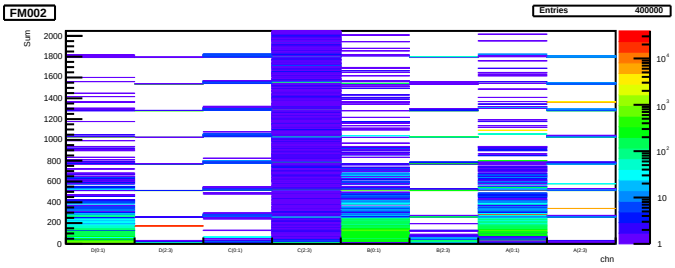

Input to QT1 crate (south-top)
Entries 98191
ADC
channel

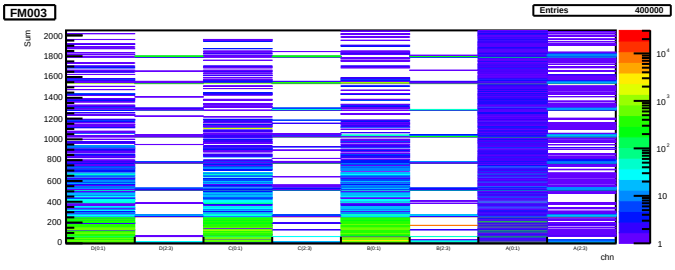

Input to QT3 crate (north-top)
Entries 76544
ADC
channel

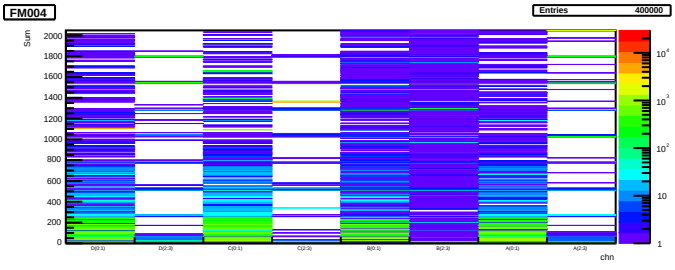

Input to QT2 crate (south-bottom)
Entries 130161
ADC
channel

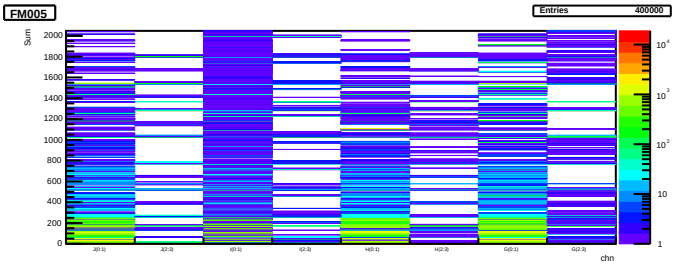

Input to QT4 crate (north-bottom)
Entries 73458
ADC
channel

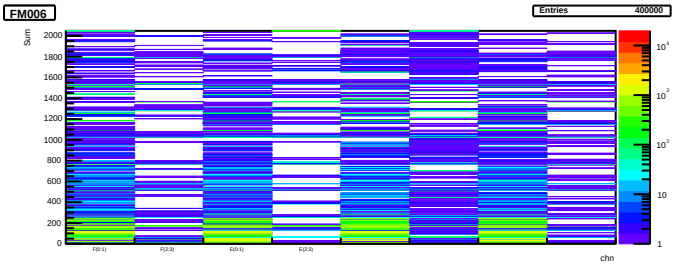

FM001
Entries 400000
Sum
chn

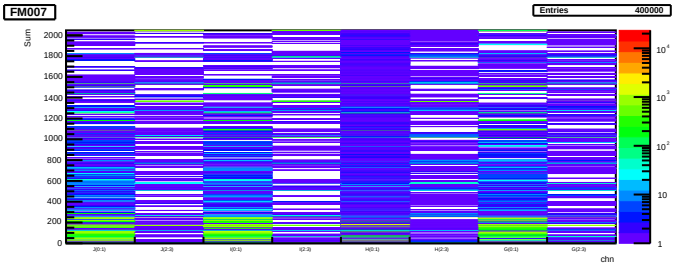

FM002
Entries 400000
Sum
chn

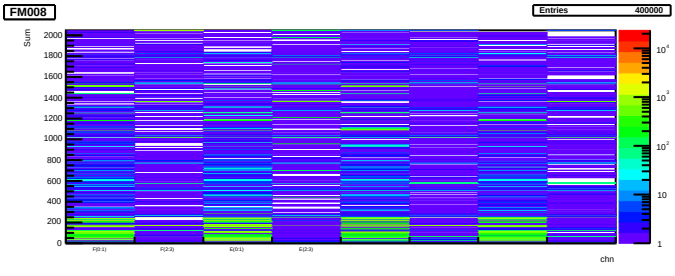

FM003
Entries 400000
Sum
chn

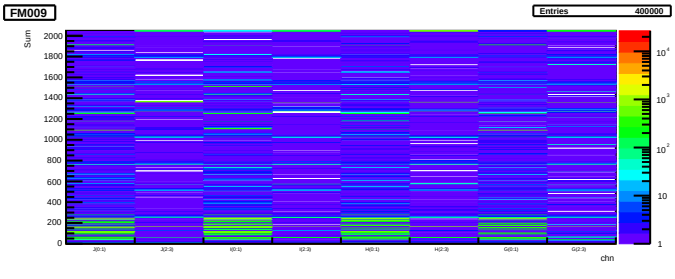

FM004
Entries 400000
Sum
chn

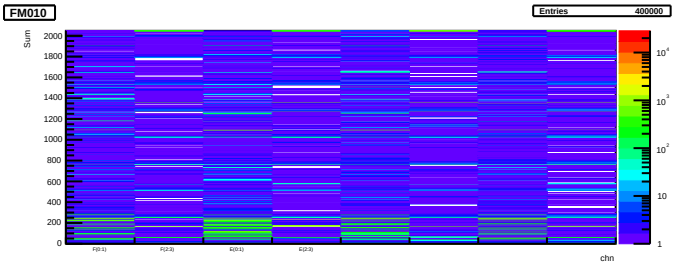

FM005
Entries 400000
Sum
chn

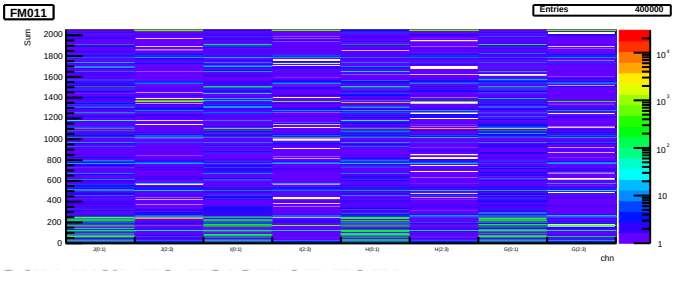

FM006
Entries 400000
Sum
chn

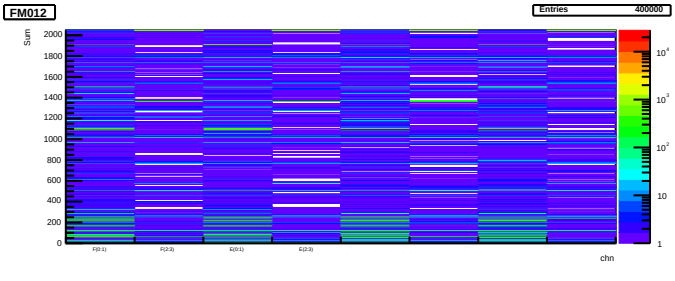

FM007
Entries 400000
Sum
chn

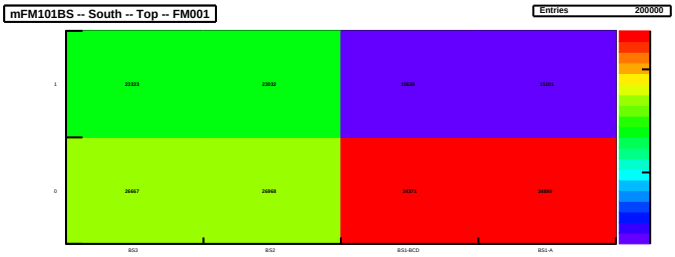

FM008
Entries 400000
Sum
chn

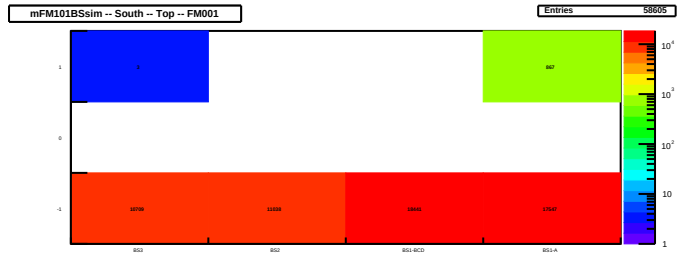

FM009
Entries 400000
Sum
chn

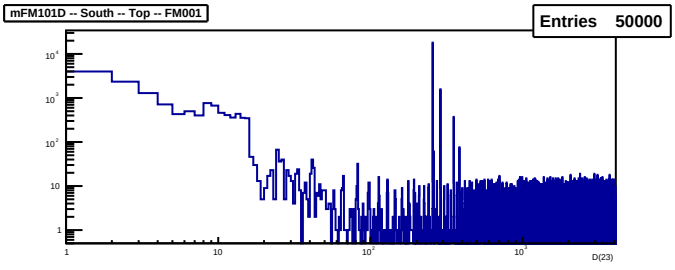

FM010
Entries 400000
Sum
chn


FM011
Entries 400000
Sum
chn


FM012
Entries 400000
Sum
chn


mFM101BS -- South -- Top -- FM001
Entries 200000


mFM101BSsim -- South -- Top -- FM001
Entries 58605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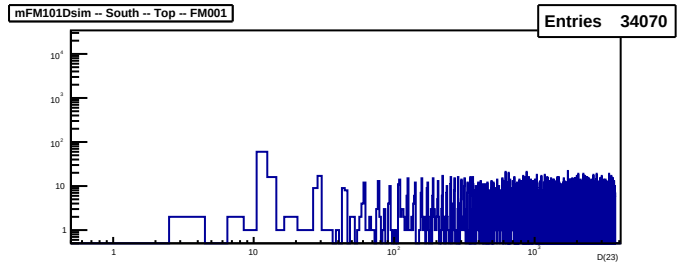
Entries 50000
D(23)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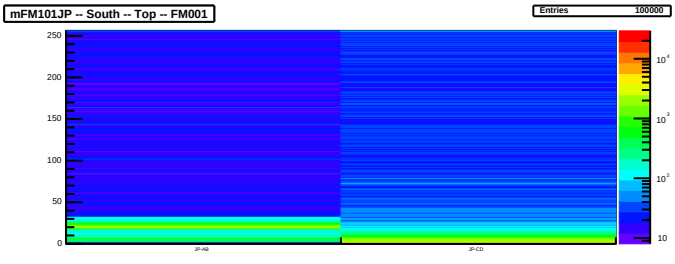
D(23)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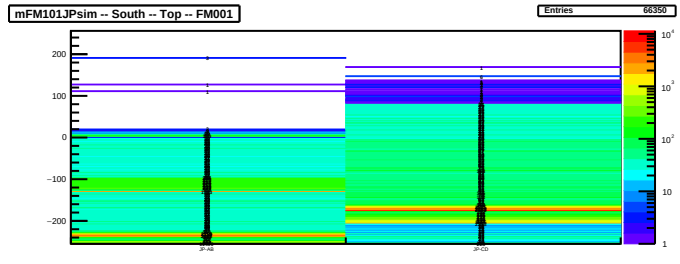
Entries 100000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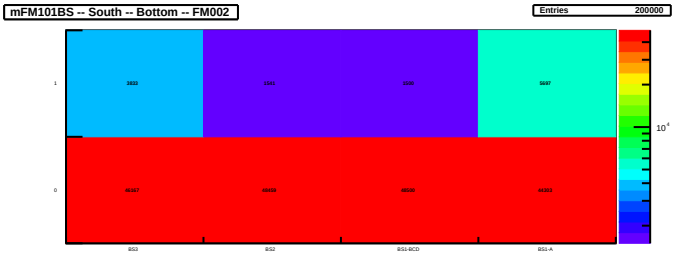
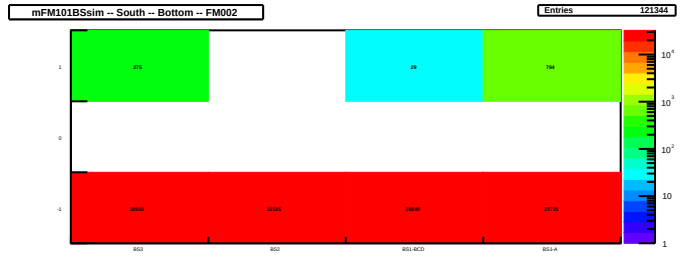
Entries 66350


mFM101BS -- South -- Bottom -- FM002
Entries 200000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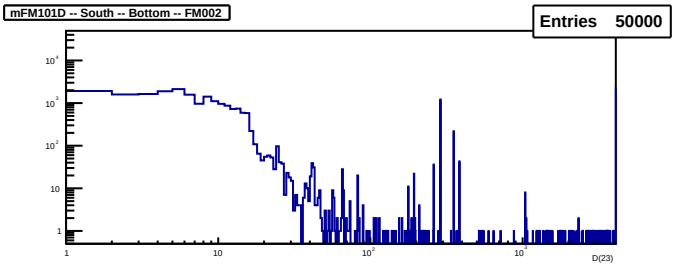
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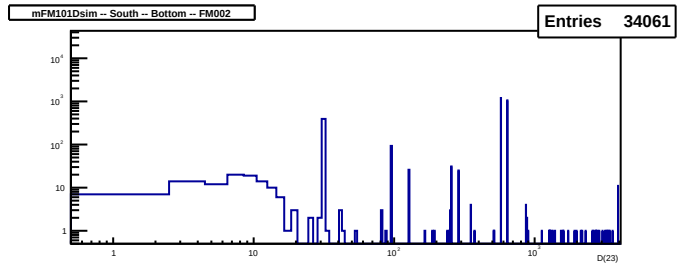
Entries 50000
D(23)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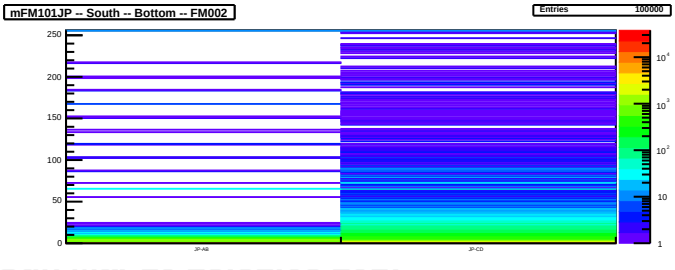
Entries 34061
D(23)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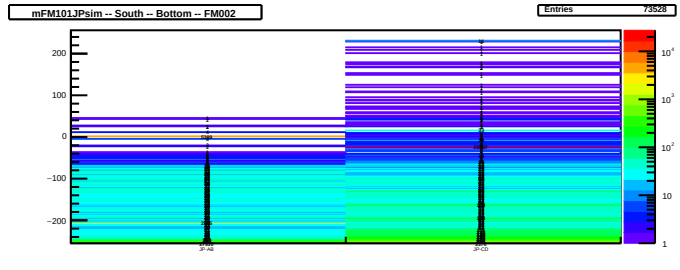
Entries 100000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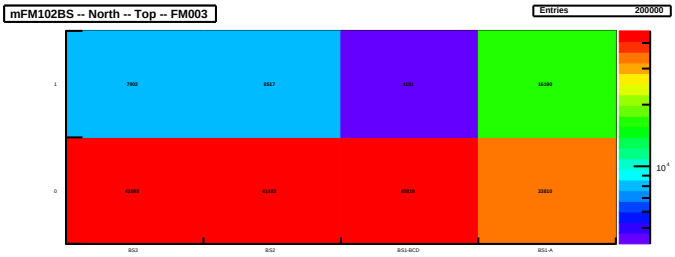
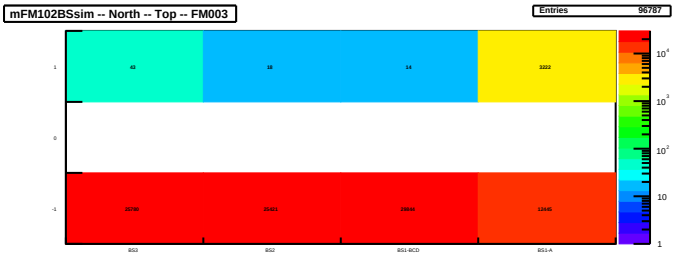

mFM102BS -- North -- Top -- FM003
Entries 200000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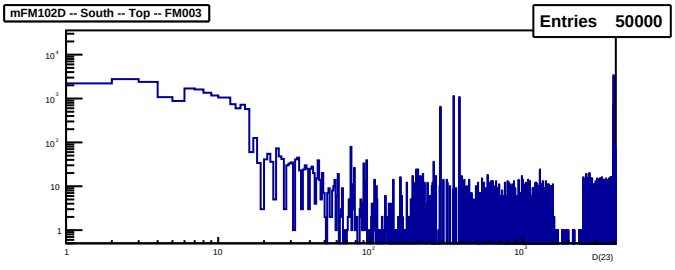
Entries 96787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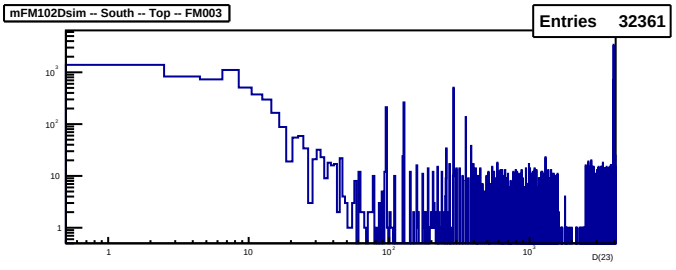
Entries 50000
D(23)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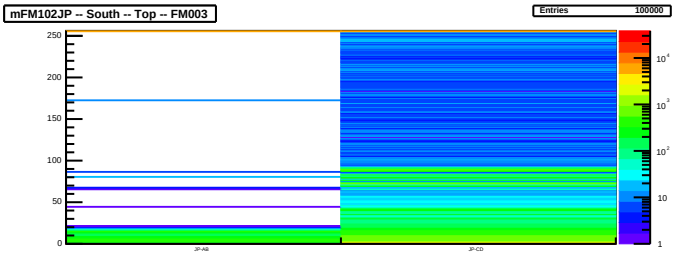
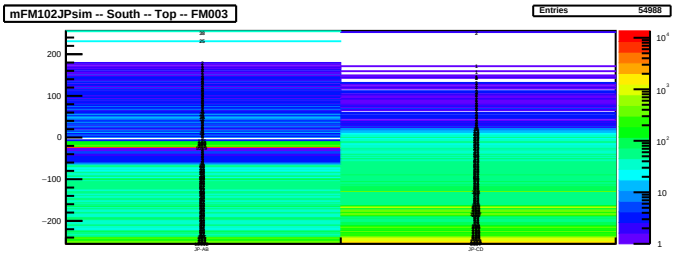
Entries 32361
D(23)


mFM102JP -- South -- Top -- FM003
Entries 100000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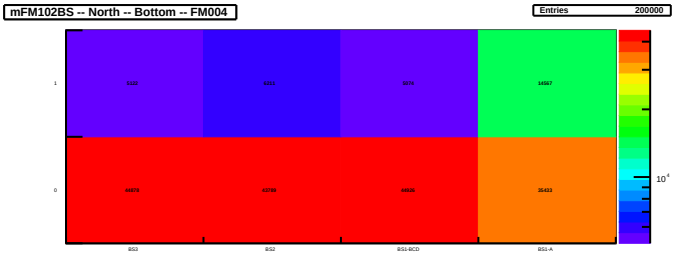
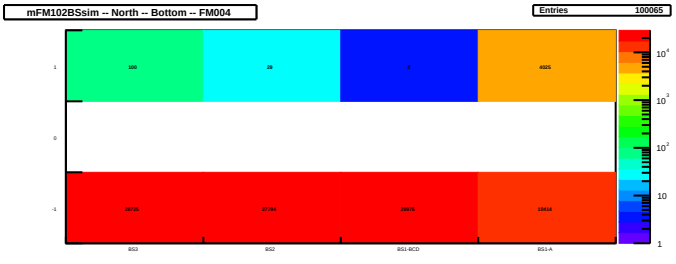

mFM102BS -- North -- Bottom -- FM004
Entries 200000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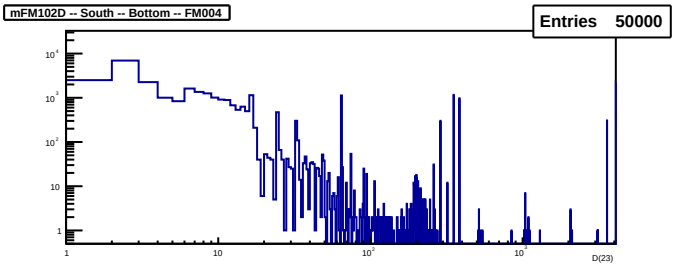
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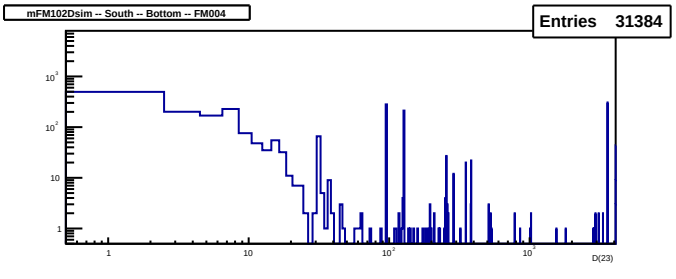
Entries 50000
D(23)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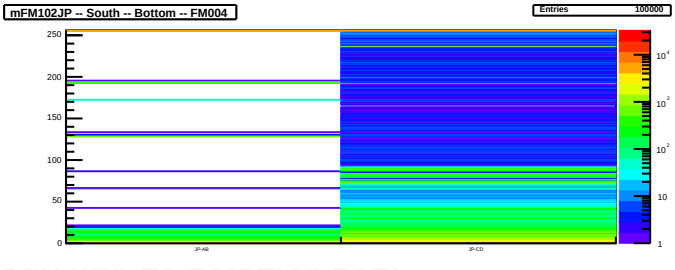
D(23)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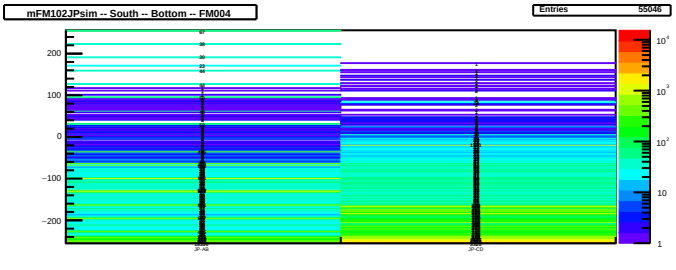
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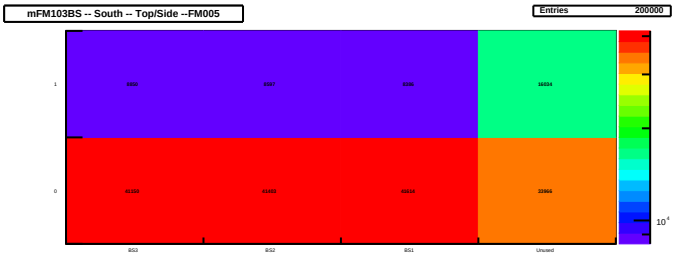
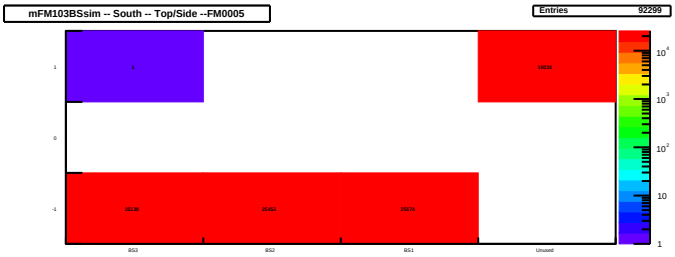

mFM103BS -- South -- Top/Side --FM005
Entries 200000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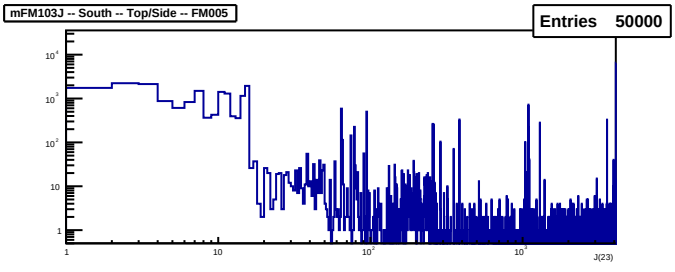
Entries 92299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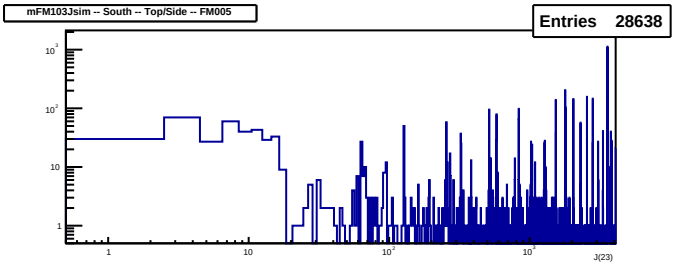
Entries 50000
J(23)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Entries 28638
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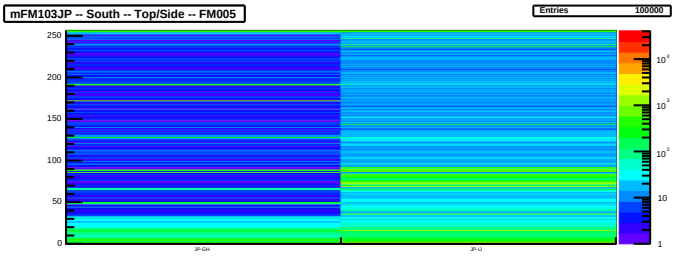
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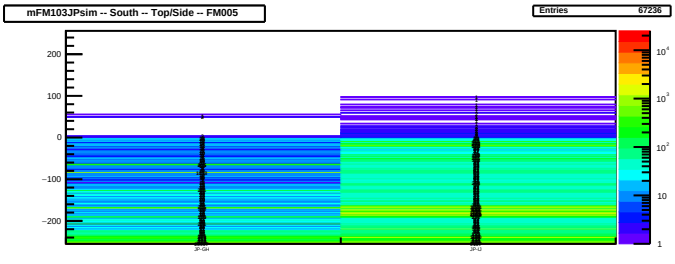
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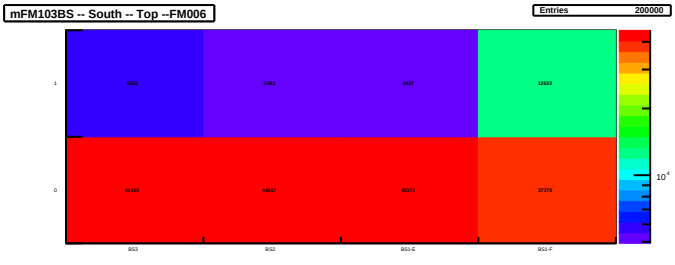
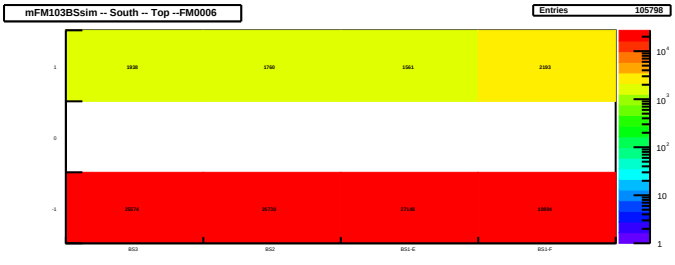

mFM103BS -- South -- Top --FM006
Entries 200000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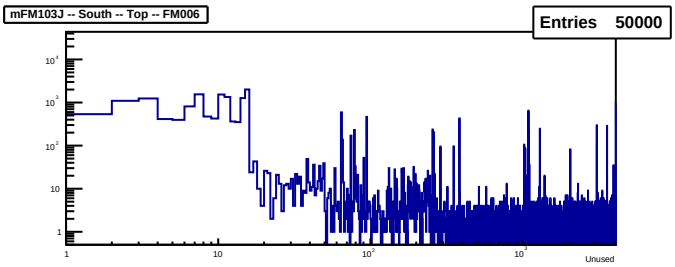
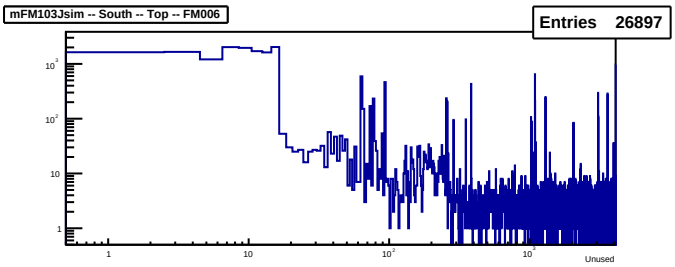
Entries 105798


mFM103J -- South -- Top -- FM006
Entries 50000
Unused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Entries 26897
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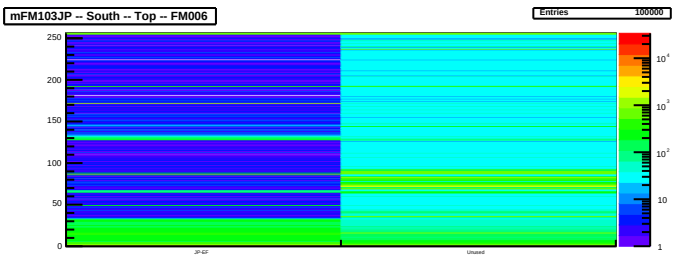
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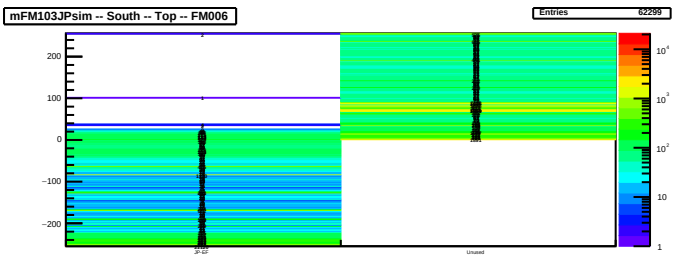
Entries 100000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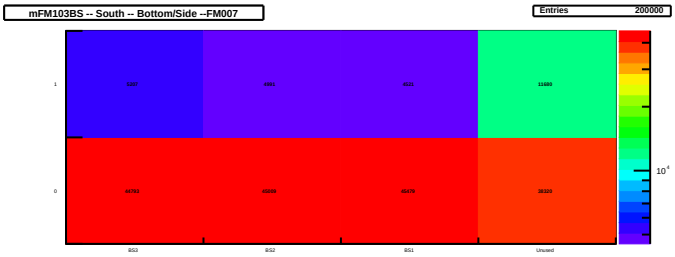
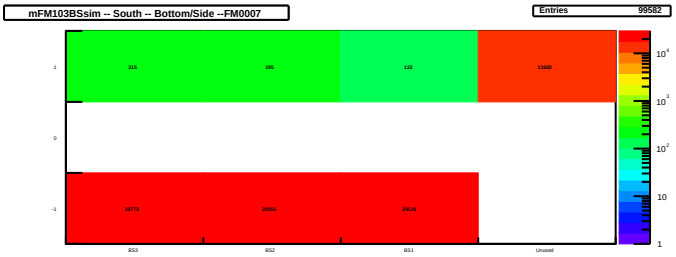
Entries 62299


mFM103BS -- South -- Bottom/Side --FM007
Entries 200000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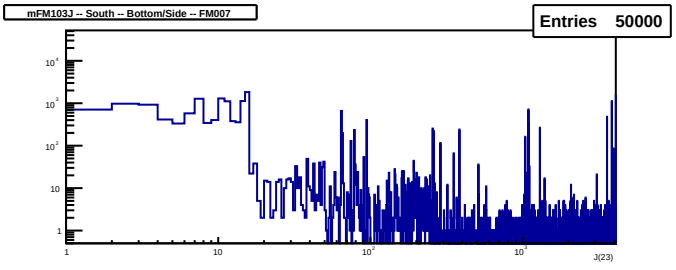
Entries 99582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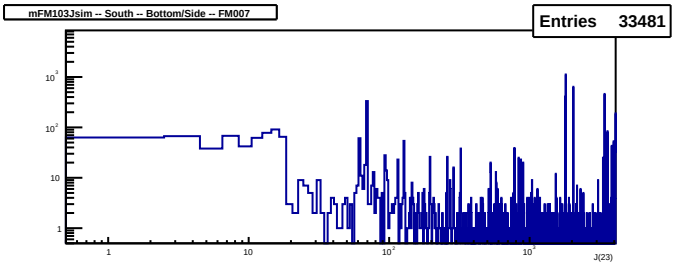
Entries 50000
J(23)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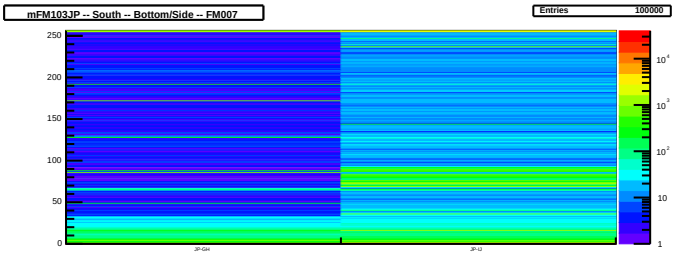
Entries 33481
J(23)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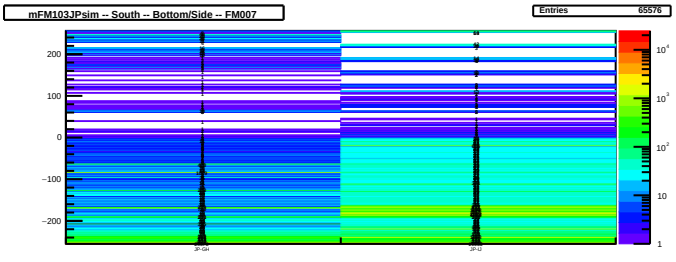
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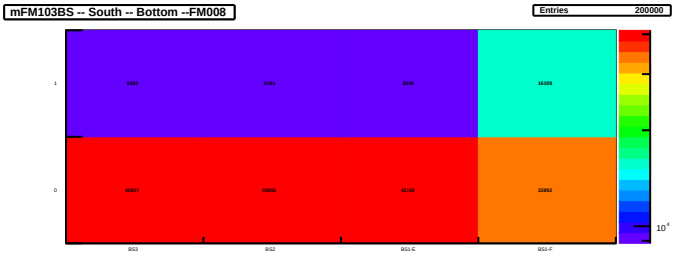
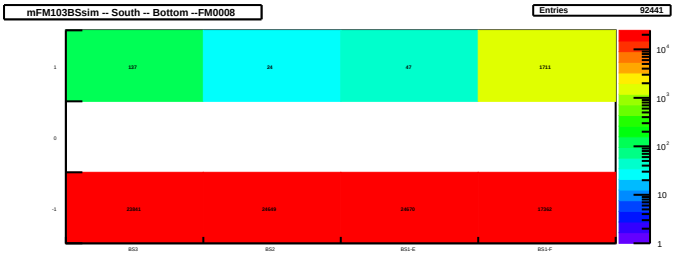
Entries 65576


mFM103BS -- South -- Bottom --FM008
Entries 200000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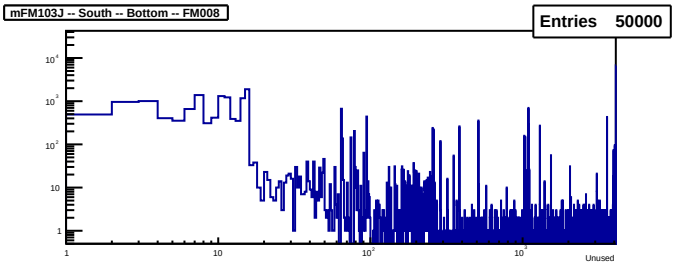
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Entries 50000
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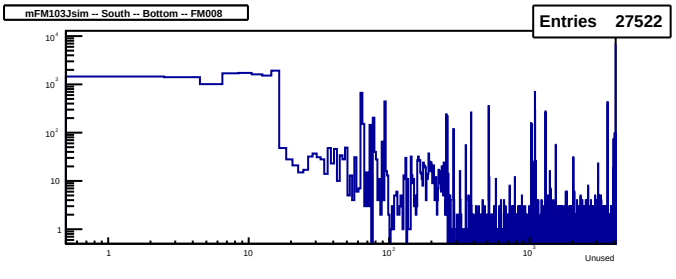
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Entries 27522
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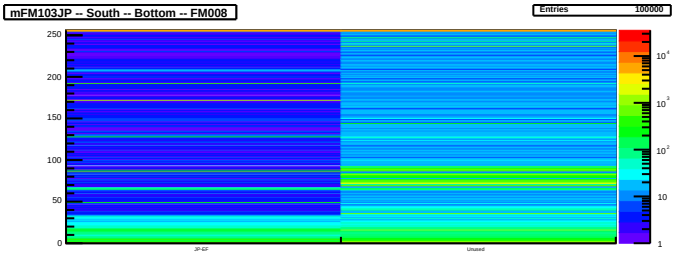
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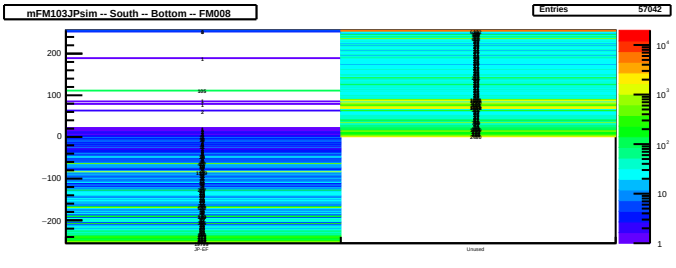
Entries 100000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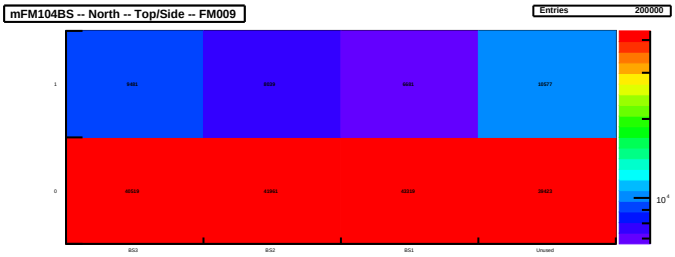
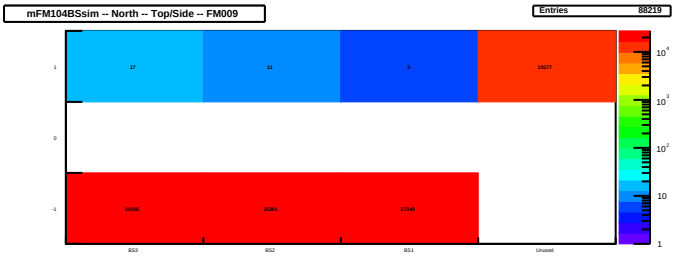
Entries 57042


mFM104BS -- North -- Top/Side -- FM009
Entries 200000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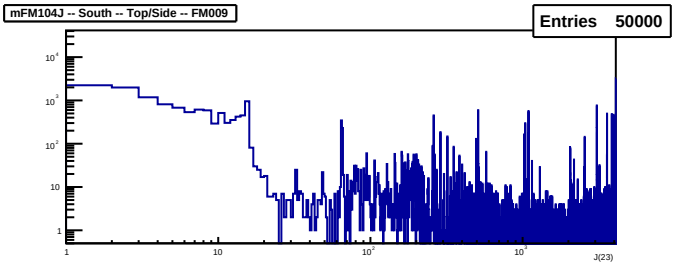
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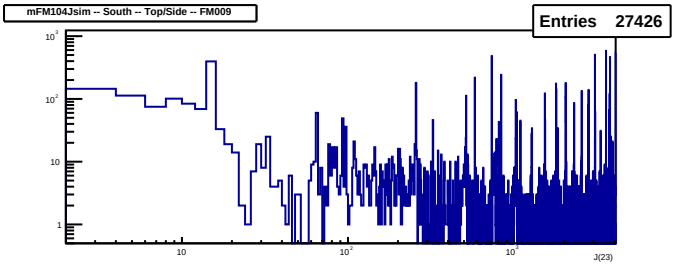
Entries 50000
J(23)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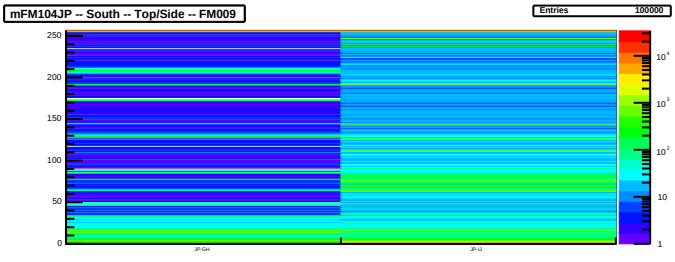
Entries 27426
J(23)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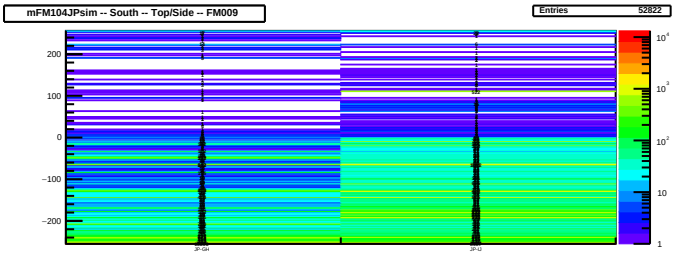
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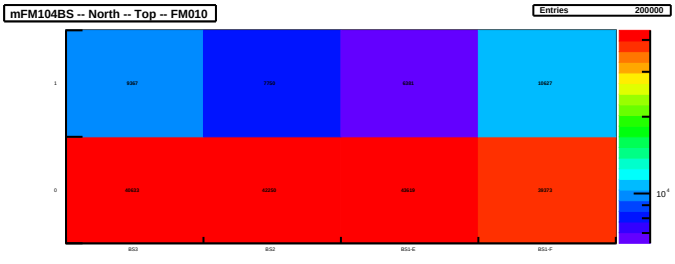
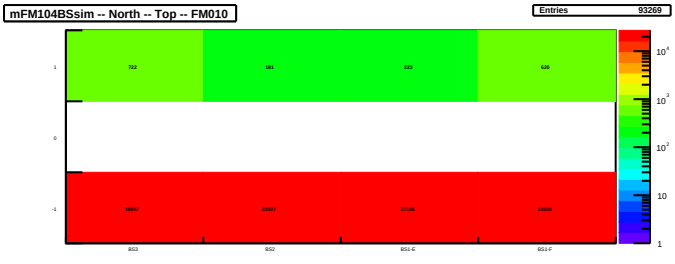
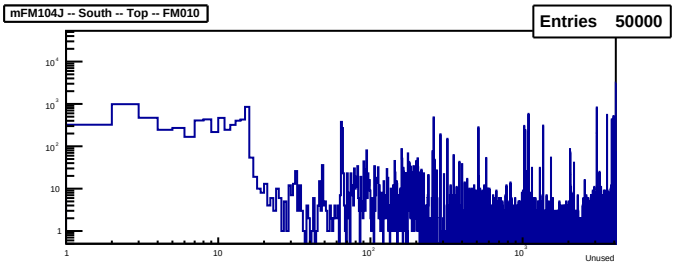
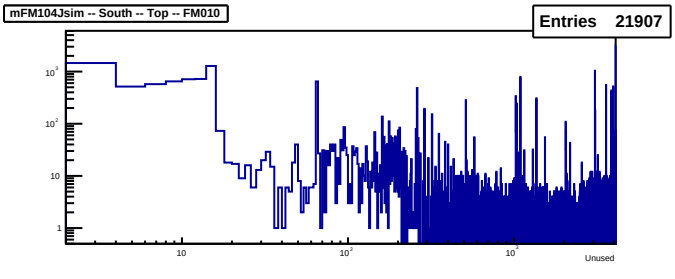
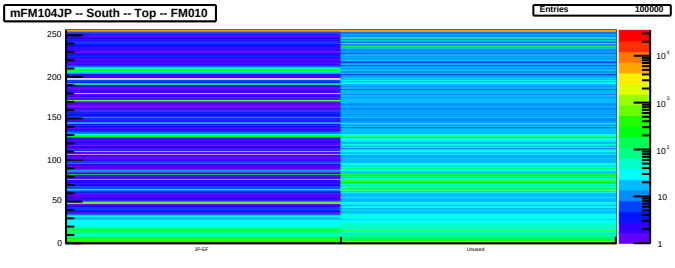
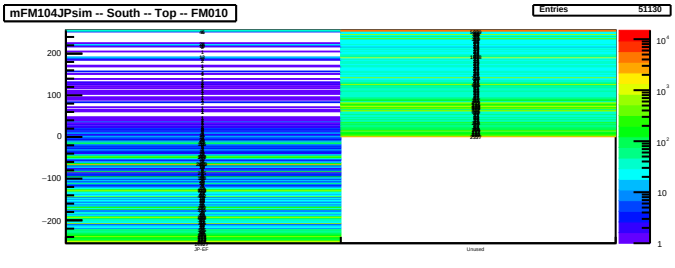

mFM104BS -- North -- Top -- FM010
Entries 200000

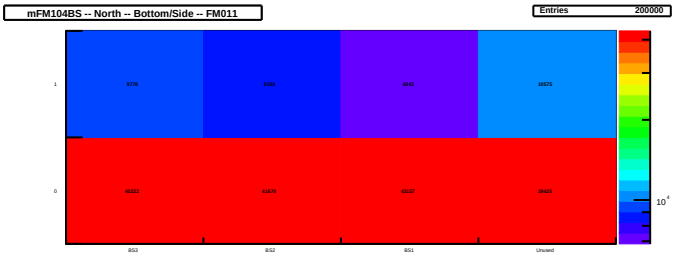

mFM104BSsim -- North -- Top -- FM010
Entries 93269

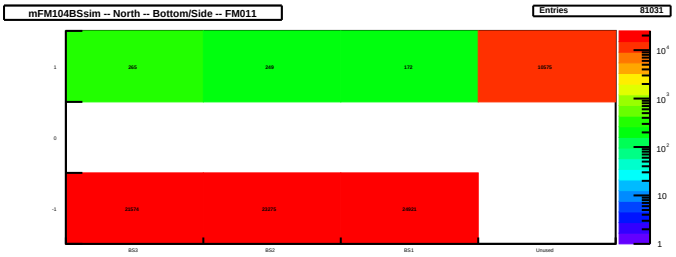

mFM104J -- South -- Top -- FM010
Entries 50000
Unused

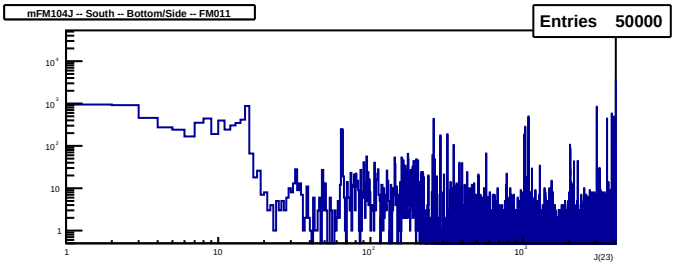

mFM104Jsim -- South -- Top -- FM010
Entries 21907
Unused

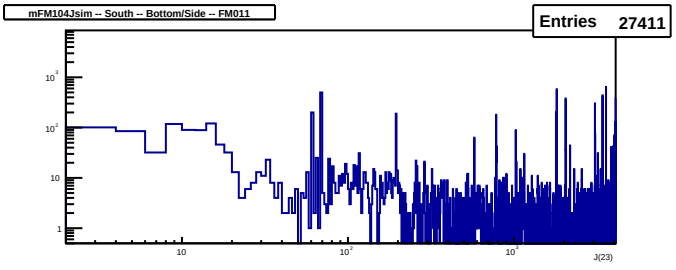

mFM104JP -- South -- Top -- FM010
Entries 100000

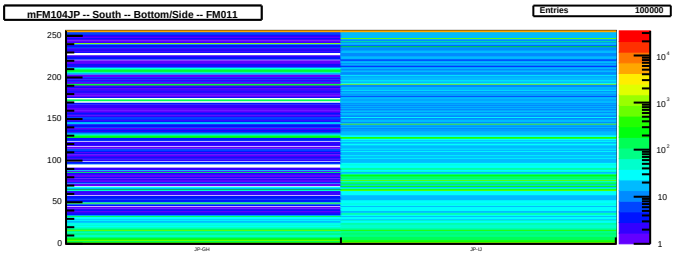

mFM104JPsim -- South -- Top -- FM010
Entries 51130

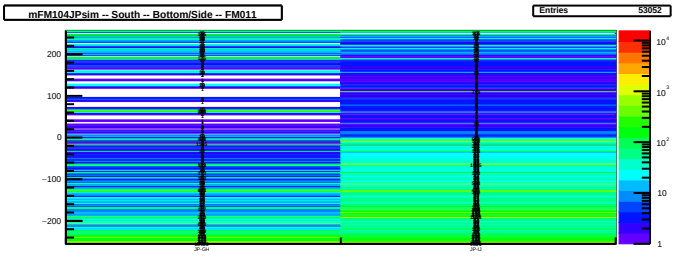

mFM104BS -- North -- Bottom/Side -- FM011
Entries 200000

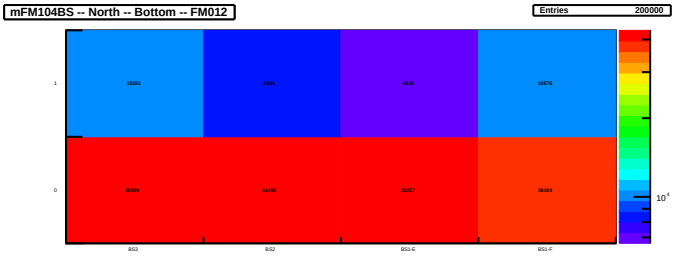

mFM104BSsim -- North -- Bottom/Side -- FM011
Entries 81031

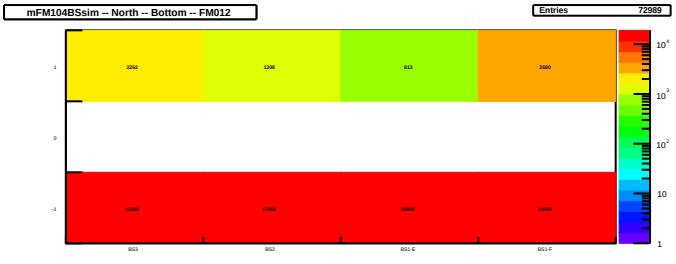

mFM104J -- South -- Bottom/Side -- FM011
Entries 50000
J(23)

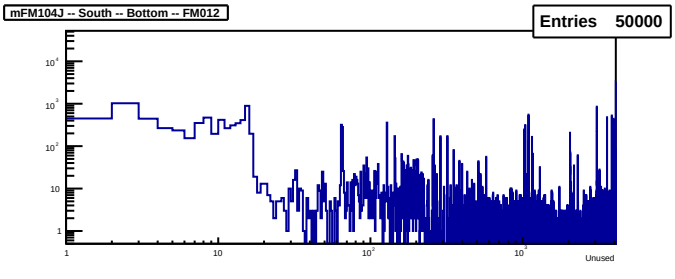

mFM104Jsim -- South -- Bottom/Side -- FM011
Entries 27411
J(23)

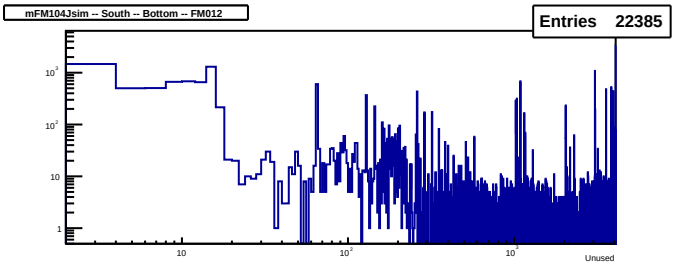

mFM104JP -- South -- Bottom/Side -- FM011
Entries 100000

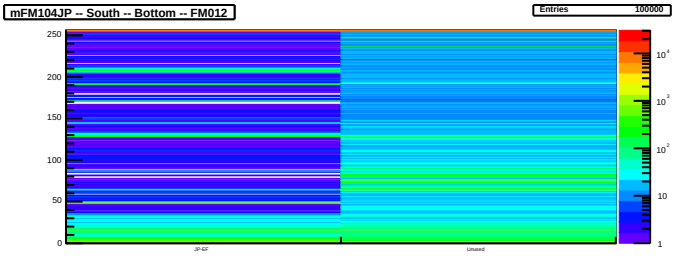

mFM104JPsim -- South -- Bottom/Side -- FM011
Entries 53052

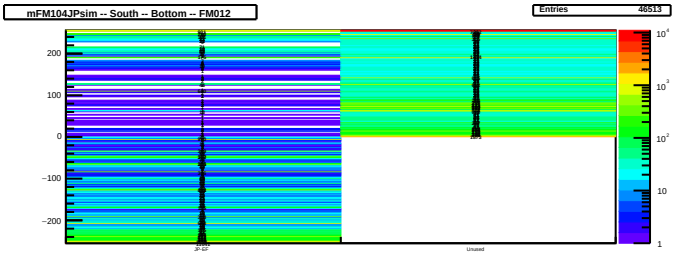

mFM104BS -- North -- Bottom -- FM012
Entries 200000

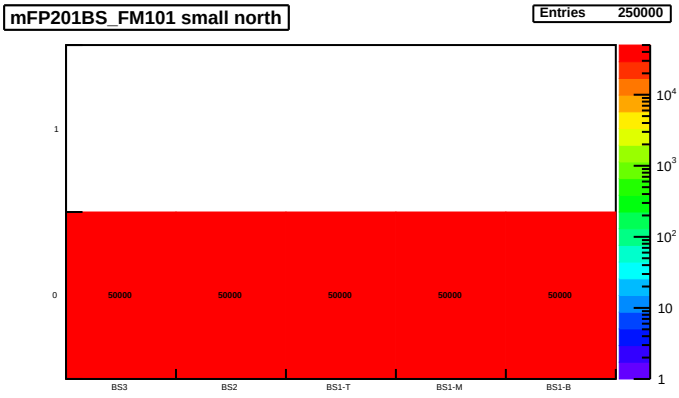

mFM104BSsim -- North -- Bottom -- FM012
Entries 72989

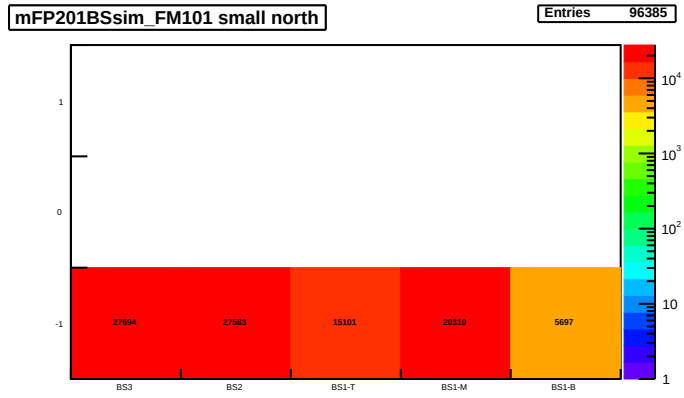

mFM104J -- South -- Bottom -- FM012
Entries 50000
Unused

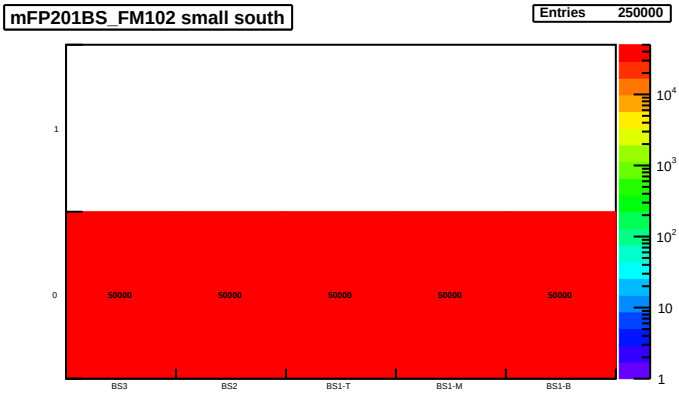

mFM104Jsim -- South -- Bottom -- FM012
Entries 22385
Unused

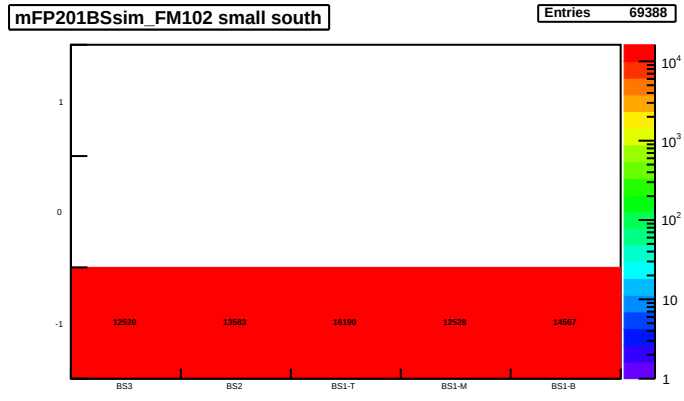

mFM104JP -- South -- Bottom -- FM012
Entries 100000

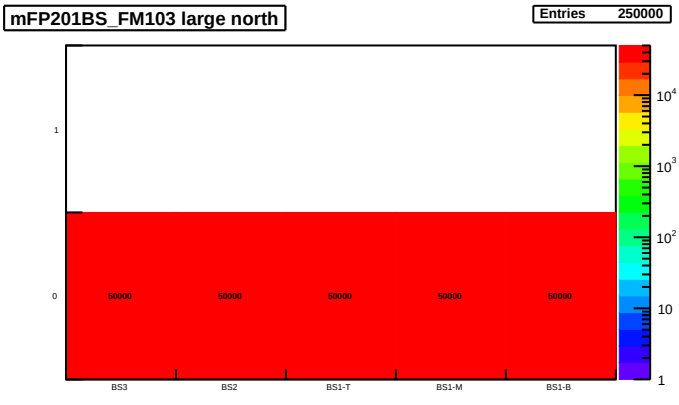

mFM104JPsim -- South -- Bottom -- FM012
Entries 46513

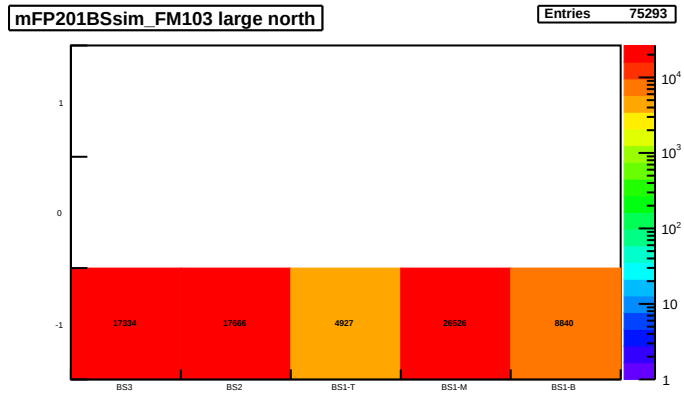

mFP201BS_FM101 small north
Entries 250000
50000
50000
50000
50000
50000
BS3
BS2
BS1-T
BS1-M
BS1-B

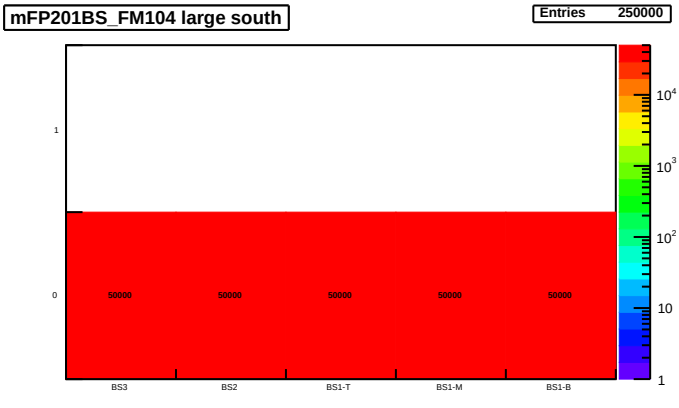

mFP201BSsim_FM101 small north
Entries 96385
27694
27583
15101
20310
5697
BS3
BS2
BS1-T
BS1-M
BS1-B

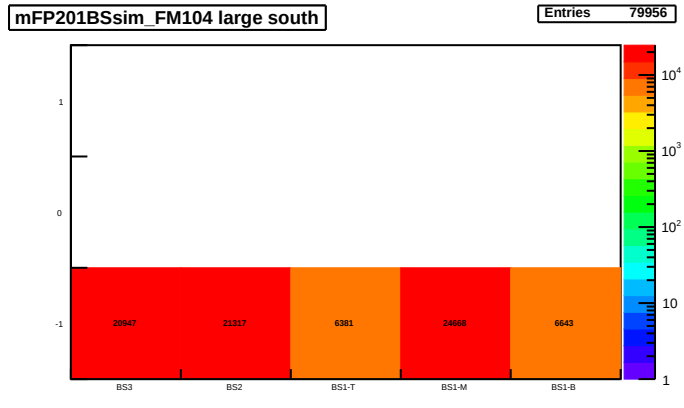

mFP201BS_FM102 small south
Entries 250000
50000
50000
50000
50000
50000
BS3
BS2
BS1-T
BS1-M
BS1-B

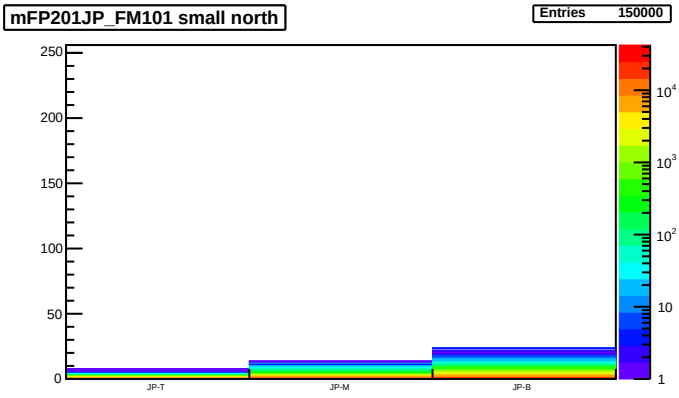

mFP201BSsim_FM102 small south
Entries 69388
12520
13583
16190
12528
14567
BS3
BS2
BS1-T
BS1-M
BS1-B

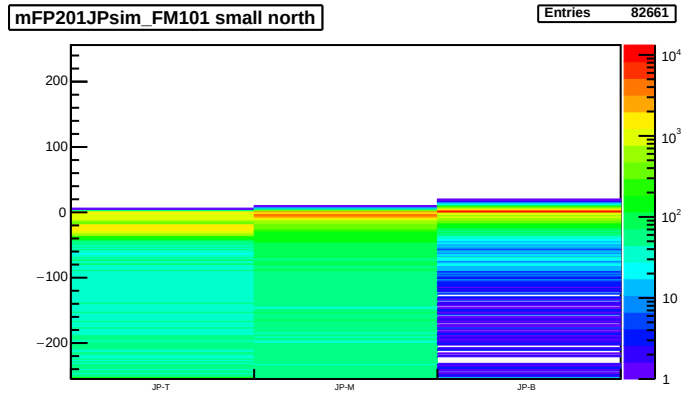

mFP201BS_FM103 large north
Entries 250000
50000
50000
50000
50000
50000
BS3
BS2
BS1-T
BS1-M
BS1-B

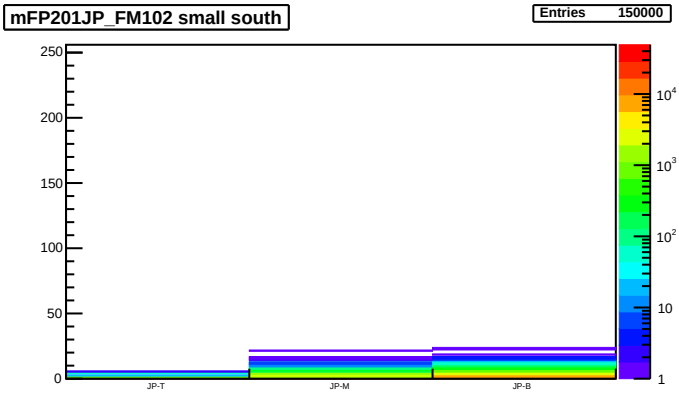

mFP201BSsim_FM103 large north
Entries 75293
17334
17666
4927
26526
8840
BS3
BS2
BS1-T
BS1-M
BS1-B

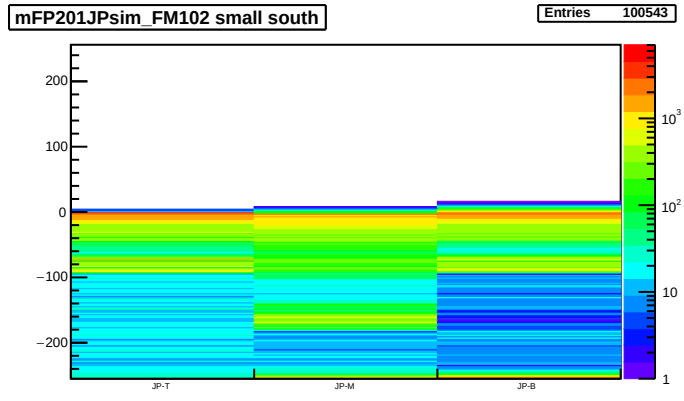

mFP201BS_FM104 large south
Entries 250000
50000
50000
50000
50000
50000
BS3
BS2
BS1-T
BS1-M
BS1-B

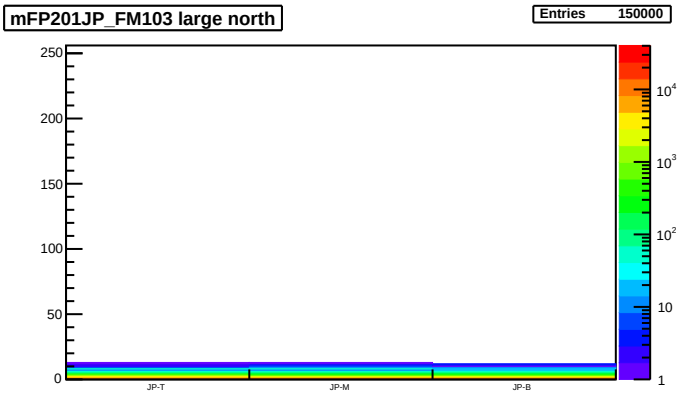

mFP201BSsim_FM104 large south
Entries 79956
20947
21317
6381
24668
6643
BS3
BS2
BS1-T
BS1-M
BS1-B

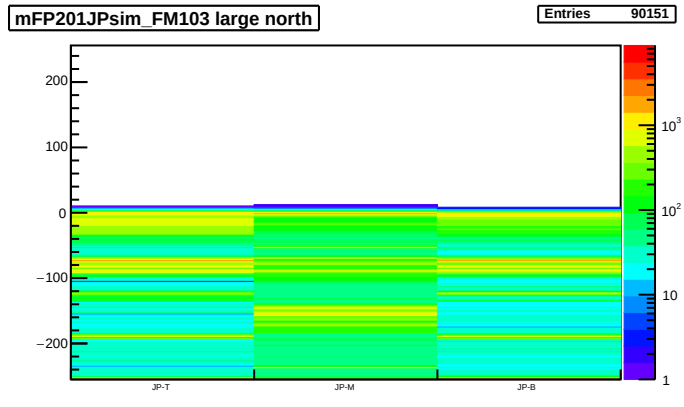

mFP201JP_FM101 small north
Entries 150000
JP-T
JP-M
JP-B

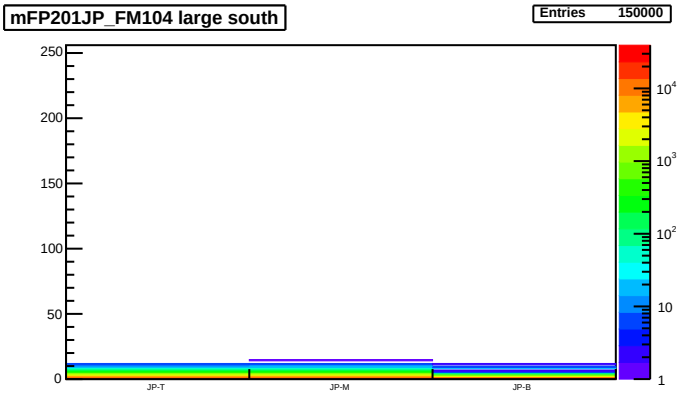

mFP201JPsim_FM101 small north
Entries 82661
JP-T
JP-M
JP-B

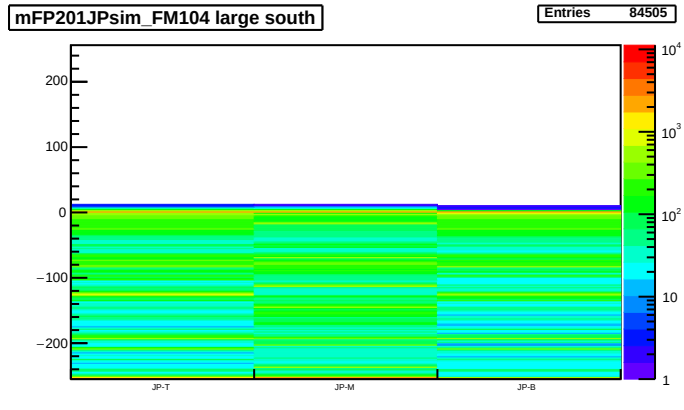

mFP201JP_FM102 small south
Entries 150000
JP-T
JP-M
JP-B

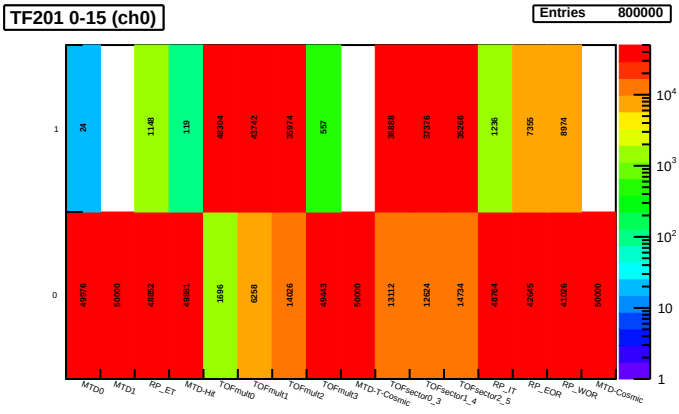

mFP201JPsim_FM102 small south
Entries 100543
JP-T
JP-M
JP-B

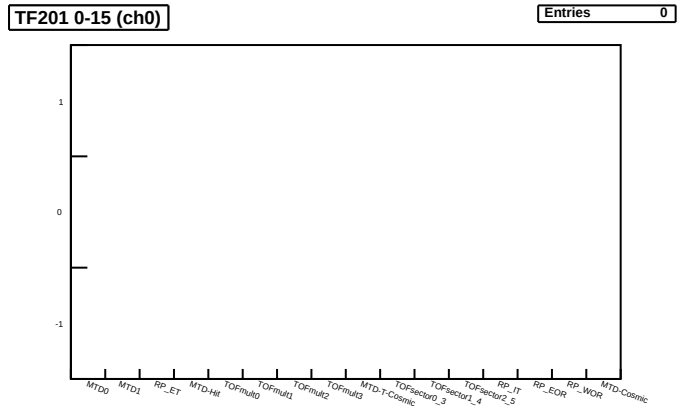

mFP201JP_FM103 large north
Entries 150000
JP-T
JP-M
JP-B

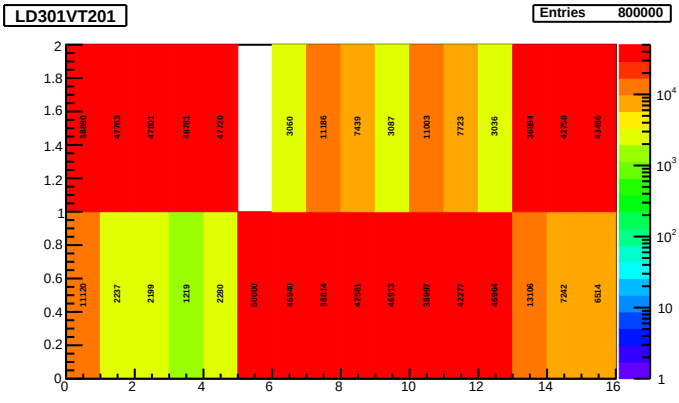

mFP201JPsim_FM103 large north
Entries 90151
JP-T
JP-M
JP-B

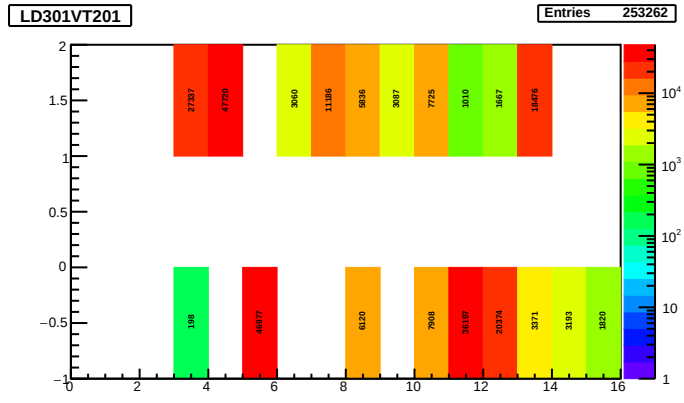

mFP201JP_FM104 large south
Entries 150000
JP-T
JP-M
JP-B

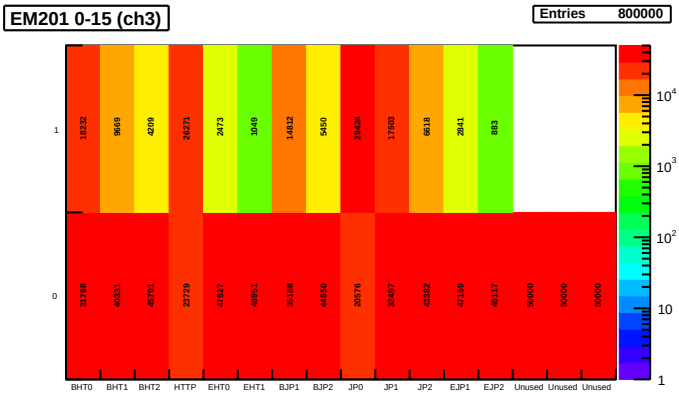

mFP201JPsim_FM104 large south
Entries 84505
JP-T
JP-M
JP-B

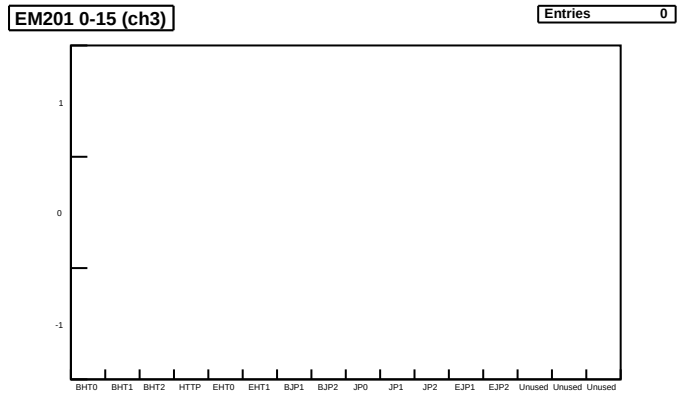

TF201 0-15 (ch0)
Entries 800000
MTD0 MTD1 RP_ET MTD-Hit TOFmult0 TOFmult1 TOFmult2 TOFmult3 MTD-T-Cosmic TOFsector0_3 TOFsector1_4 TOFsector2_5 RP_IT RP_EOR RP_WOR MTD-Cosmic

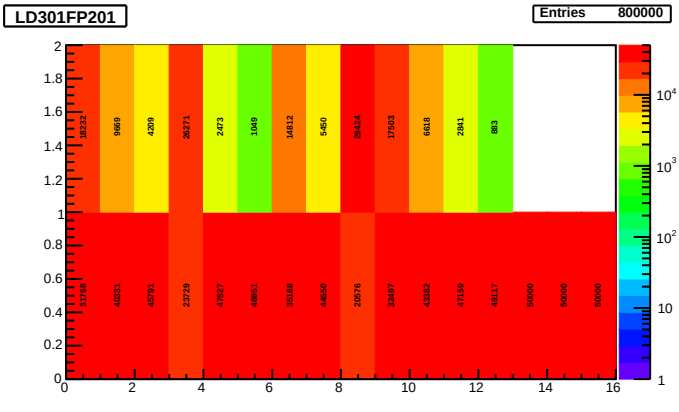

TF201 0-15 (ch0)
Entries 0
MTD0 MTD1 RP_ET MTD-Hit TOFmult0 TOFmult1 TOFmult2 TOFmult3 MTD-T-Cosmic TOFsector0_3 TOFsector1_4 TOFsector2_5 RP_IT RP_EOR RP_WOR MTD-Cosmic

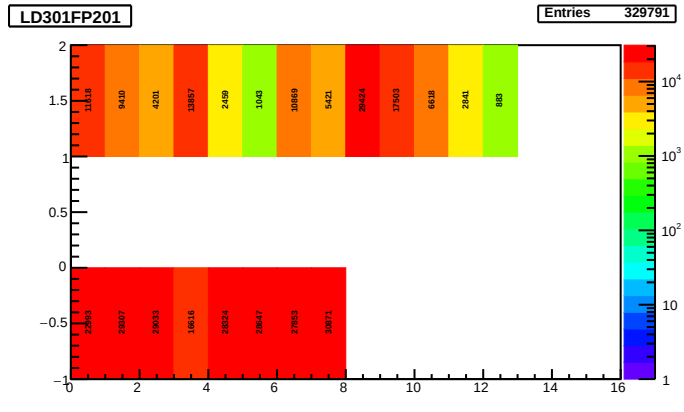

LD301VT201
Entries 800000


LD301VT201
Entries 253262


EM201 0-15 (ch3)
Entries 800000
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


LD301FP201
Entries 800000


LD301FP201
Entries 329791