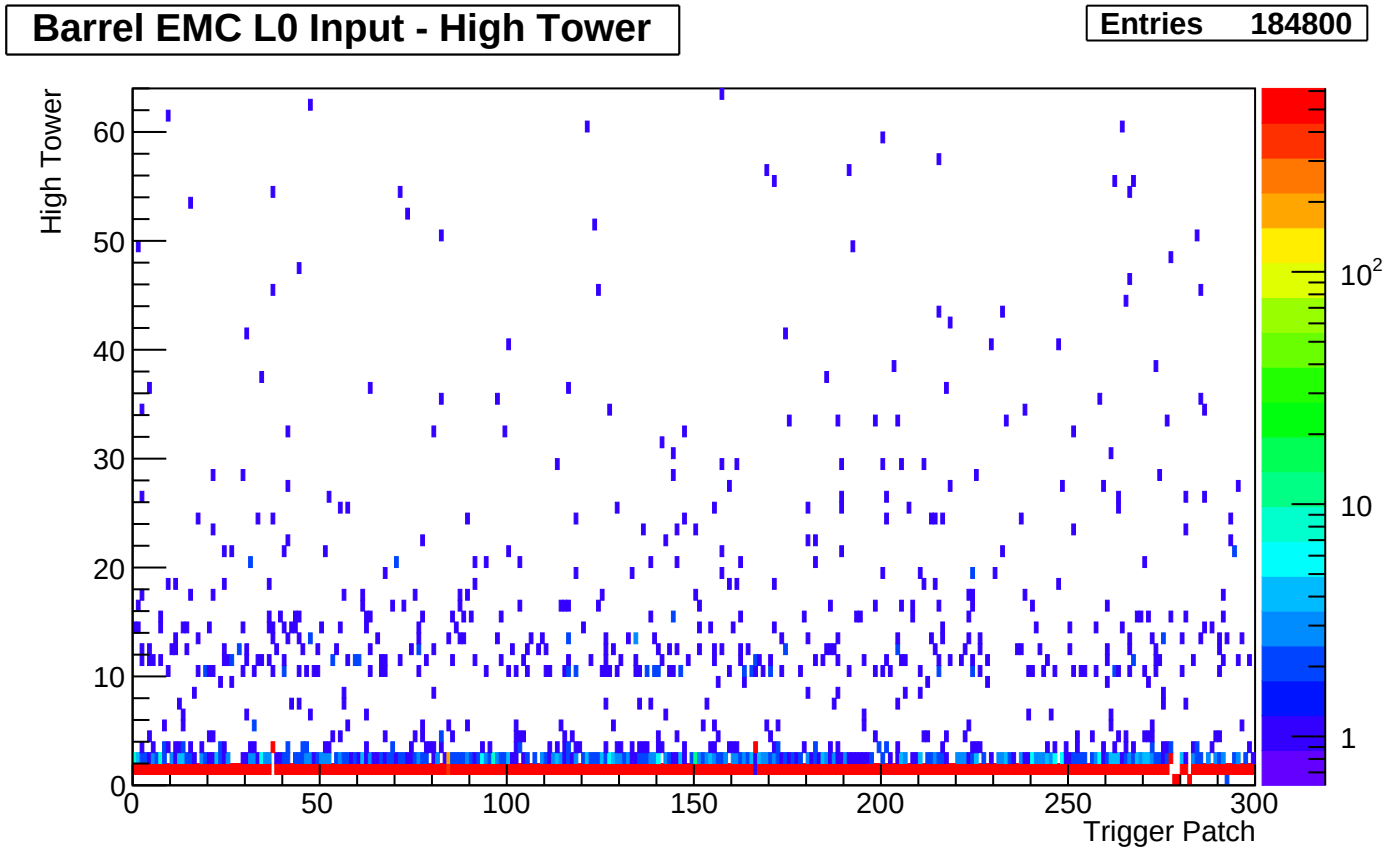
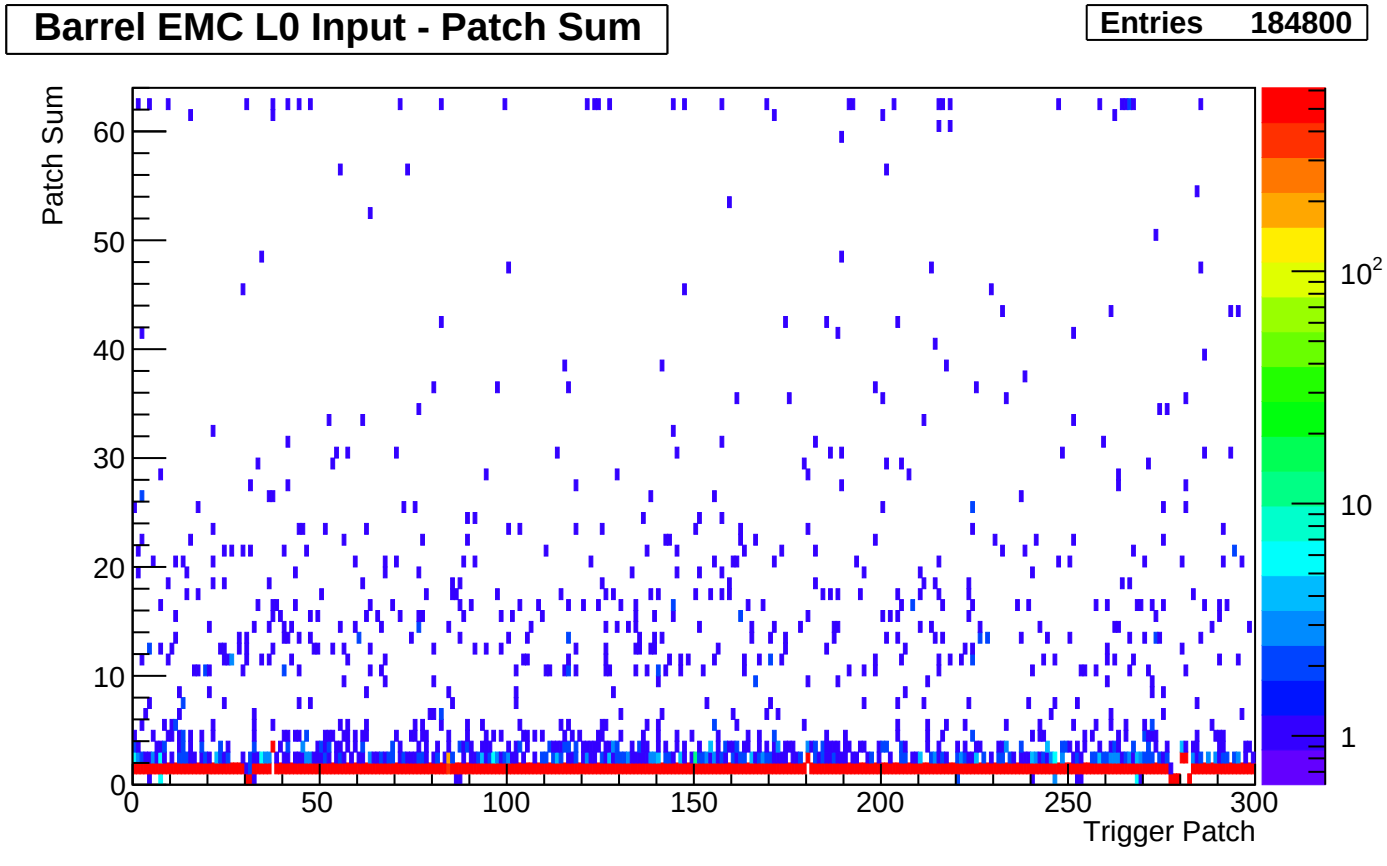
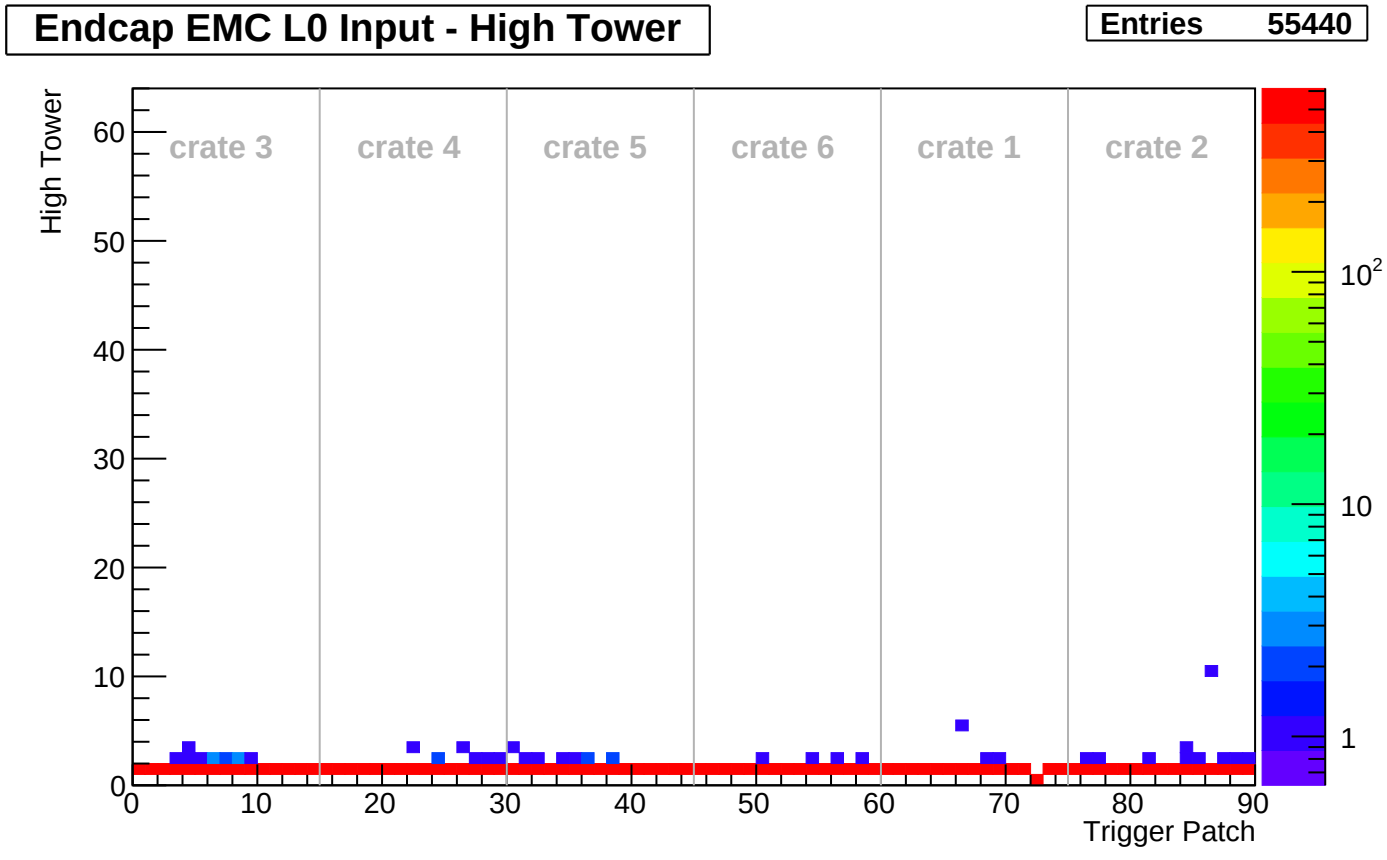
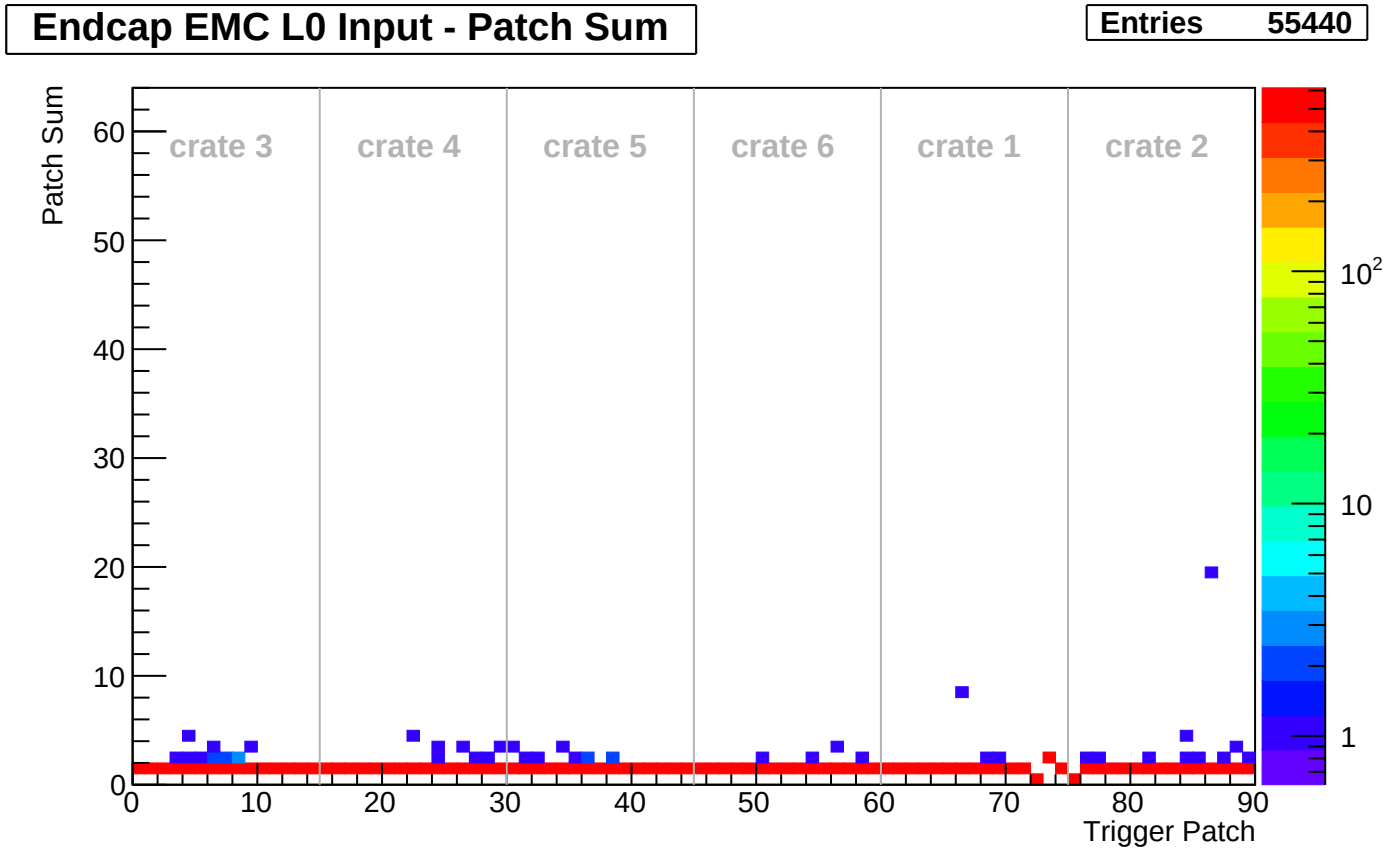
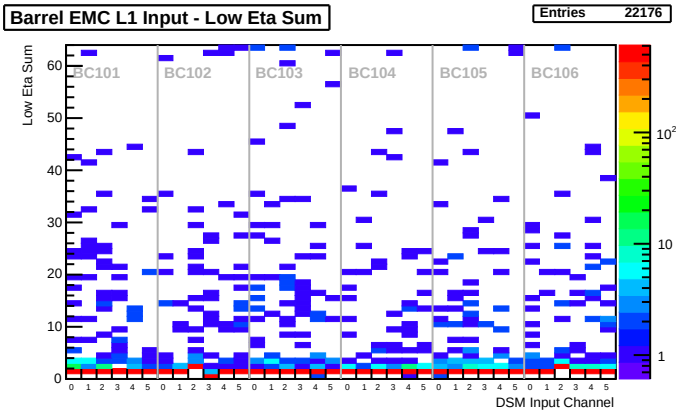
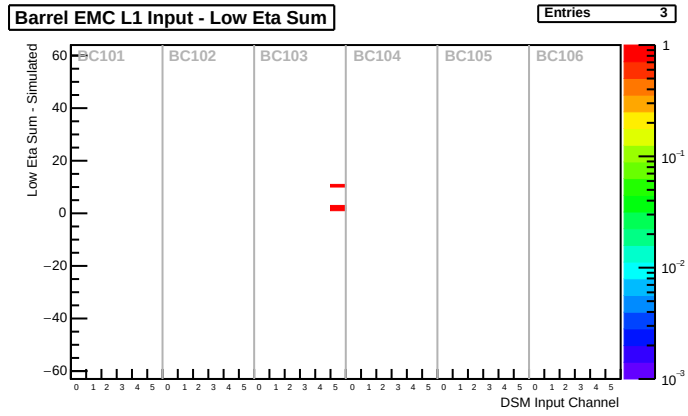

Barrel EMC L0 Input - High Tower
Entries 184800
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2

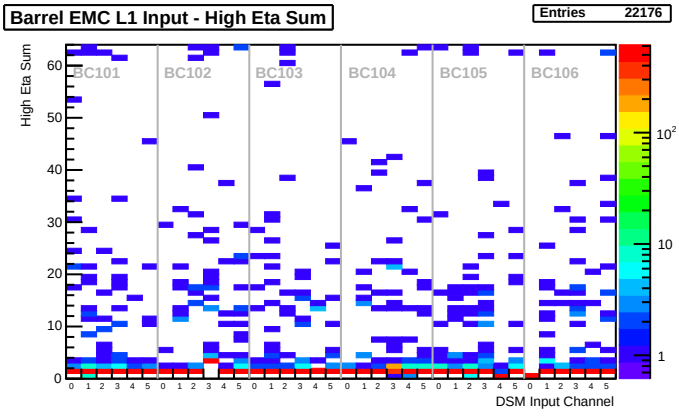

Barrel EMC L0 Input - Patch Sum
Entries 184800
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2

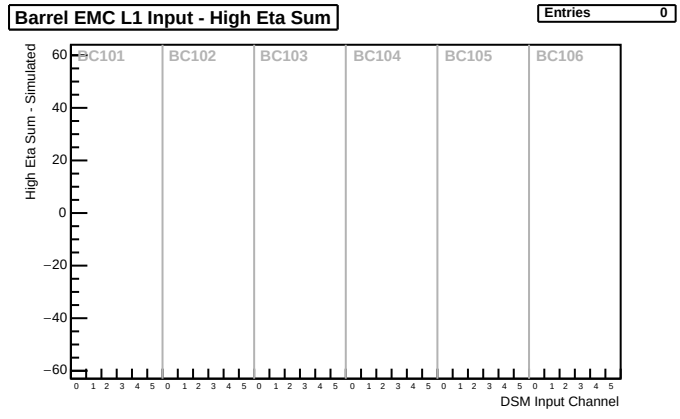

Endcap EMC L0 Input - High Tower
Entries 55440
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2

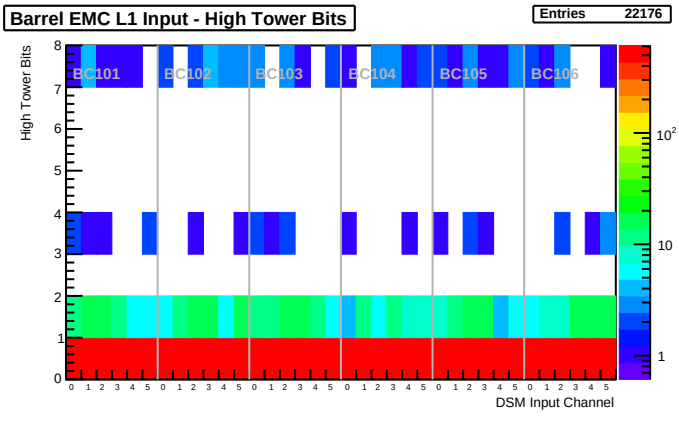

Endcap EMC L0 Input - Patch Sum
Entries 55440
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2

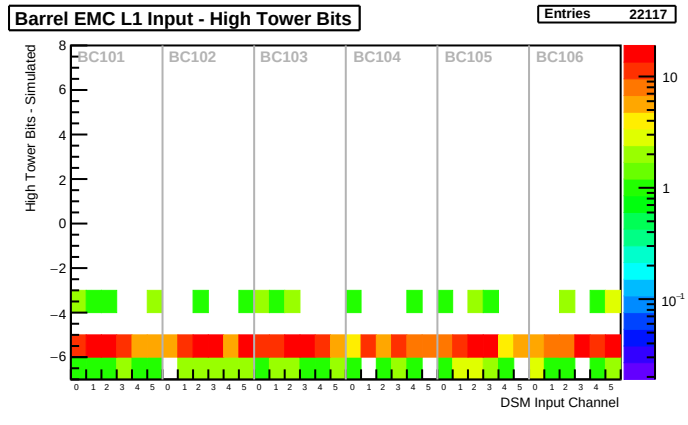

Barrel EMC L1 Input - Low Eta Sum
Entries 22176
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

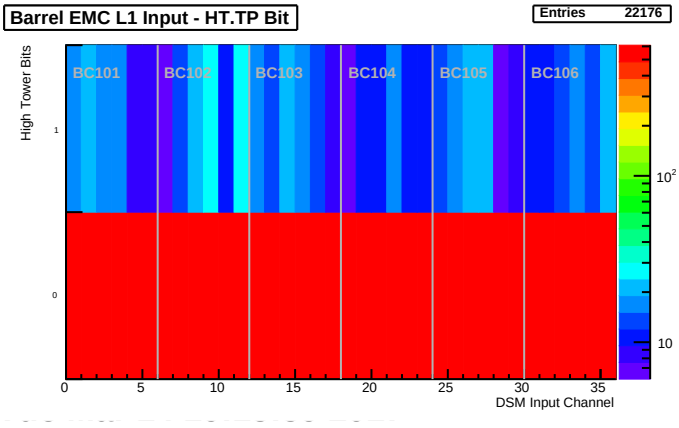

Barrel EMC L1 Input - Low Eta Sum
Entries 3
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

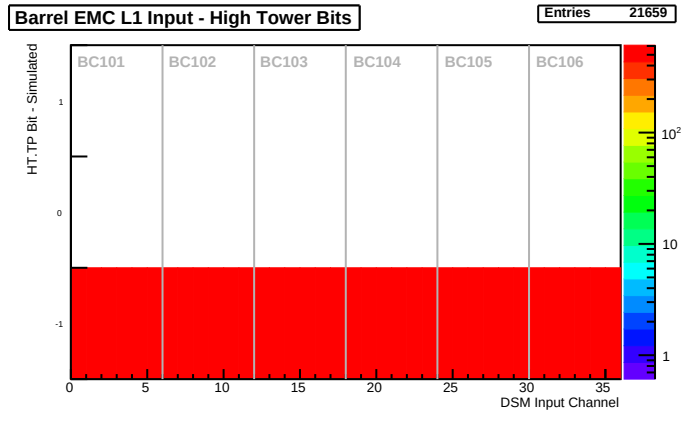

Barrel EMC L1 Input - High Eta Sum
Entries 22176
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

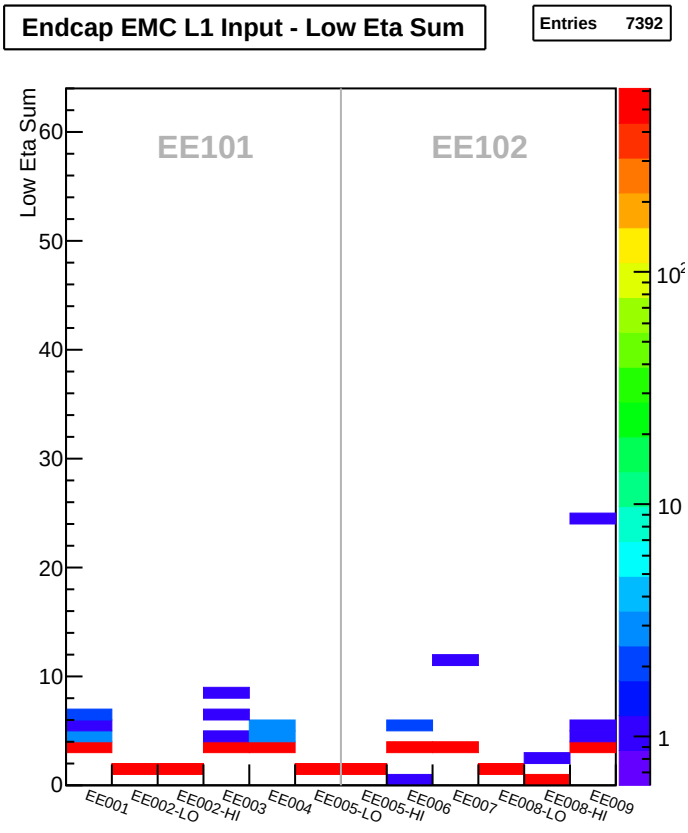

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

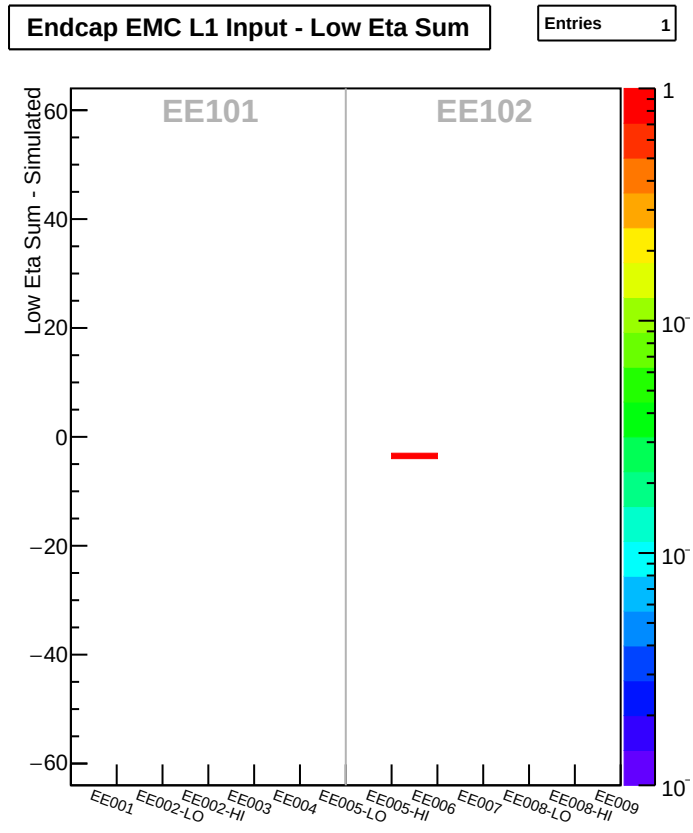

Barrel EMC L1 Input - High Tower Bits
Entries 22176
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

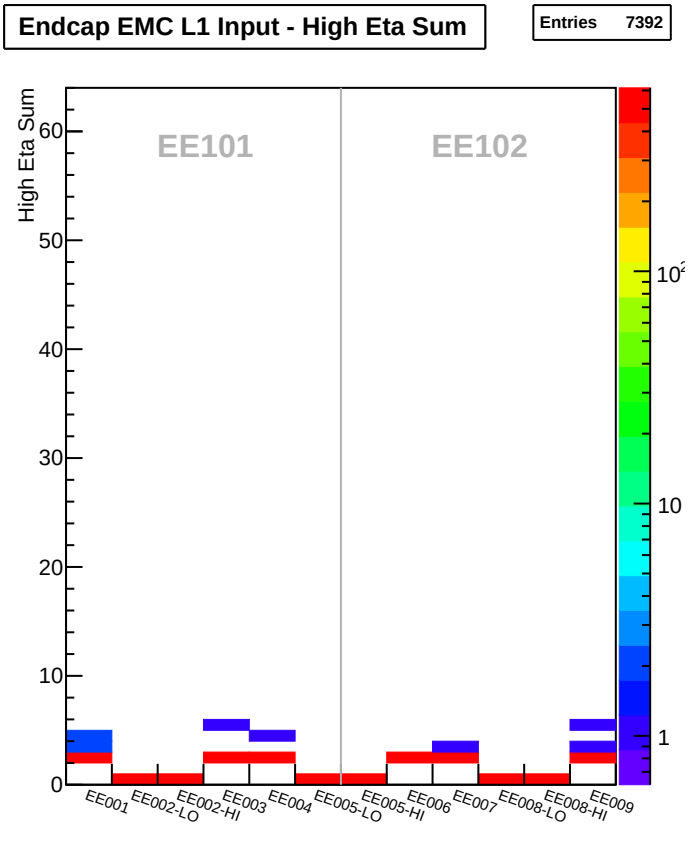

Barrel EMC L1 Input - High Tower Bits
Entries 22117
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

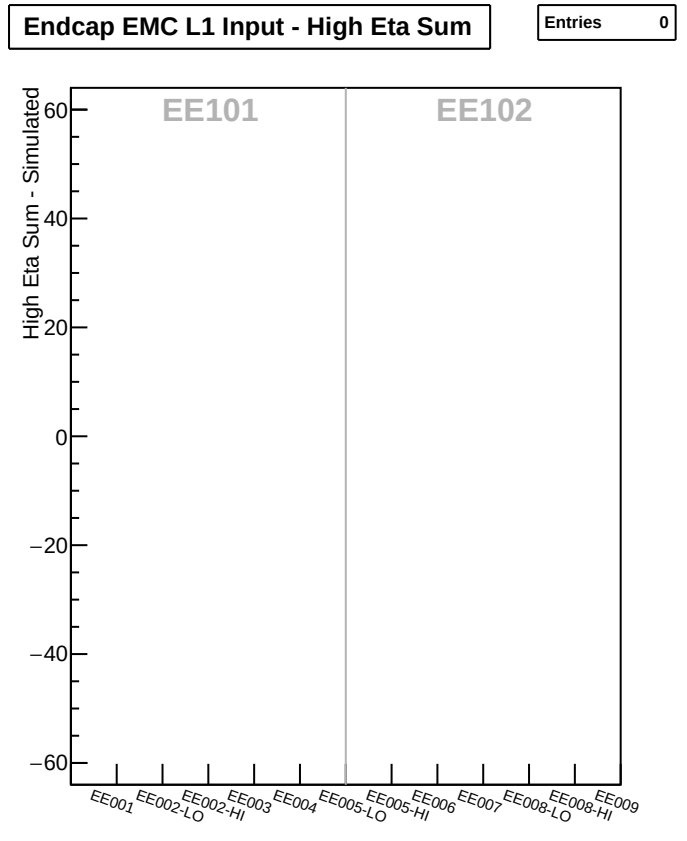

Barrel EMC L1 Input - HT.TP Bit
Entries 22176
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

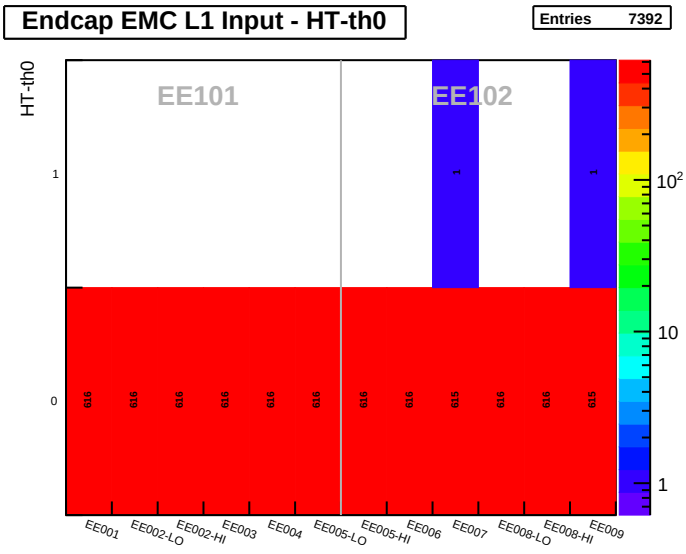

Barrel EMC L1 Input - High Tower Bits
Entries 21659
HT.TP Bit - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

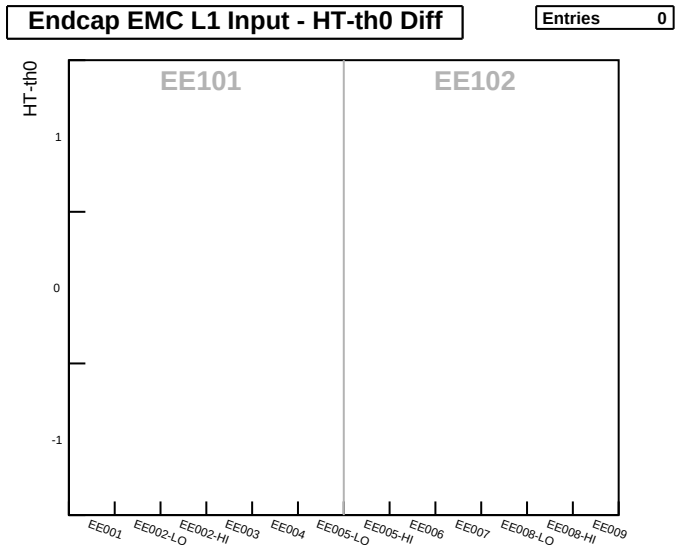

Endcap EMC L1 Input - Low Eta Sum
Entries 7392
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

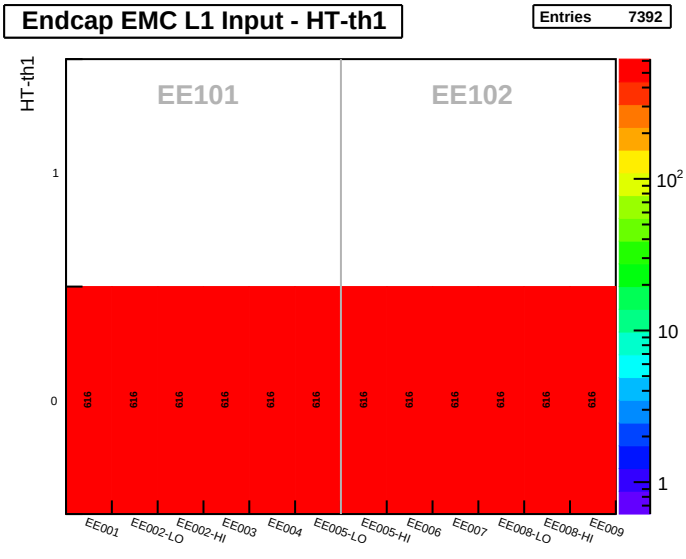

Endcap EMC L1 Input - Low Eta Sum
Entries 1
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

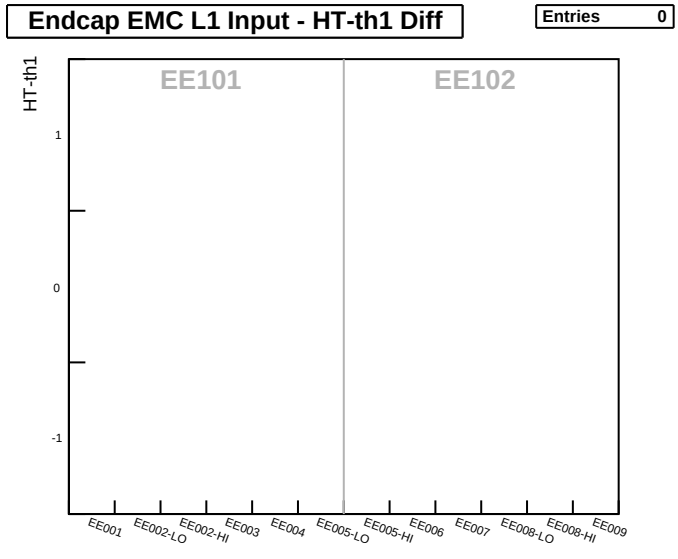

Endcap EMC L1 Input - High Eta Sum
Entries 7392
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

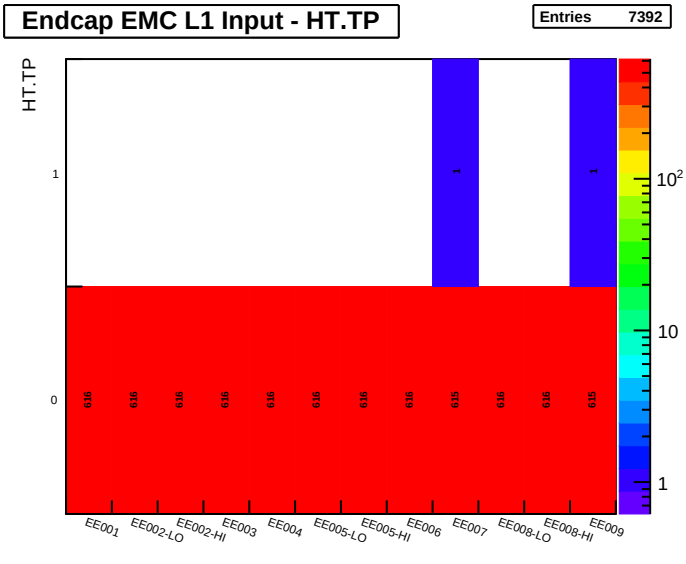

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

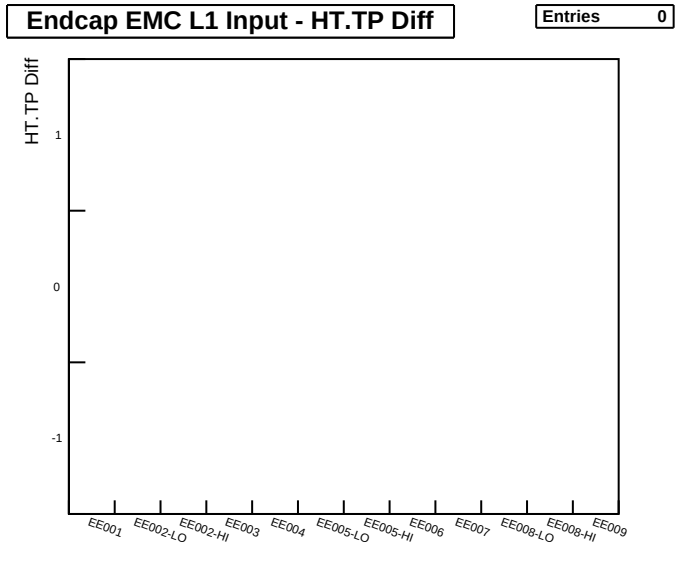

Endcap EMC L1 Input - HT-th0
Entries 7392
HT-th0
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

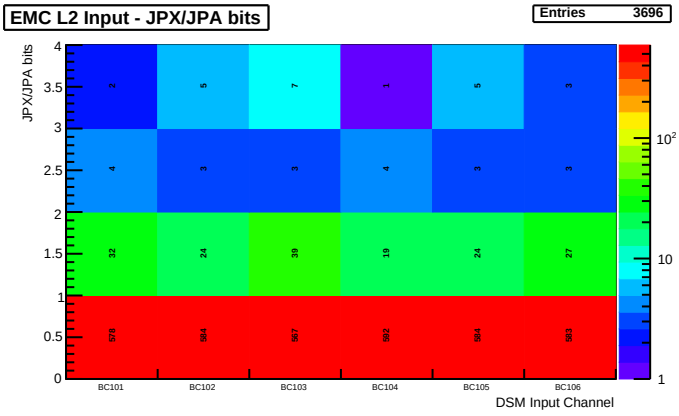

Endcap EMC L1 Input - HT-th0 Diff
Entries 0
HT-th0
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

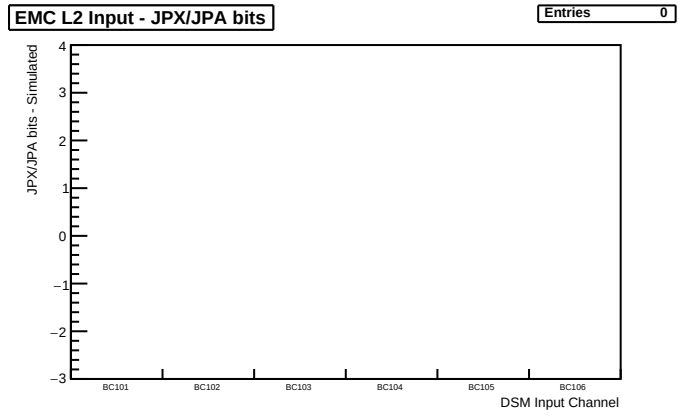

Endcap EMC L1 Input - HT-th1
Entries 7392
HT-th1
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

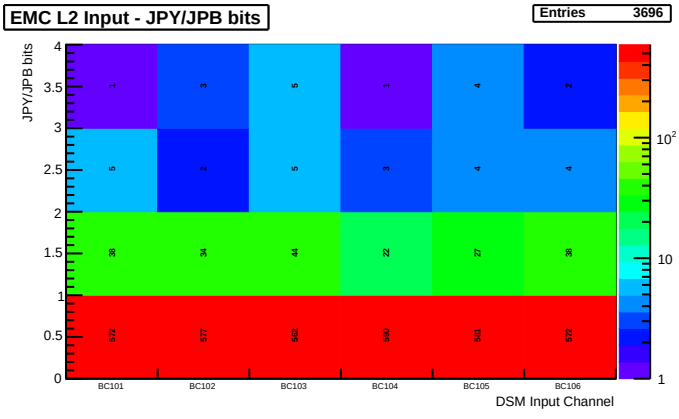

Endcap EMC L1 Input - HT-th1 Diff
Entries 0
HT-th1
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

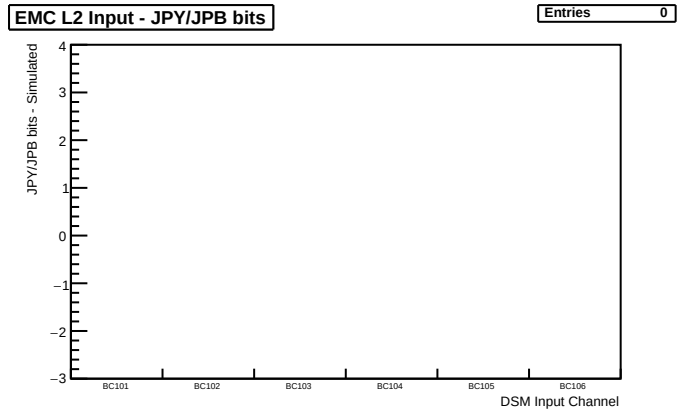

Endcap EMC L1 Input - HT.TP
Entries 7392
HT.TP
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

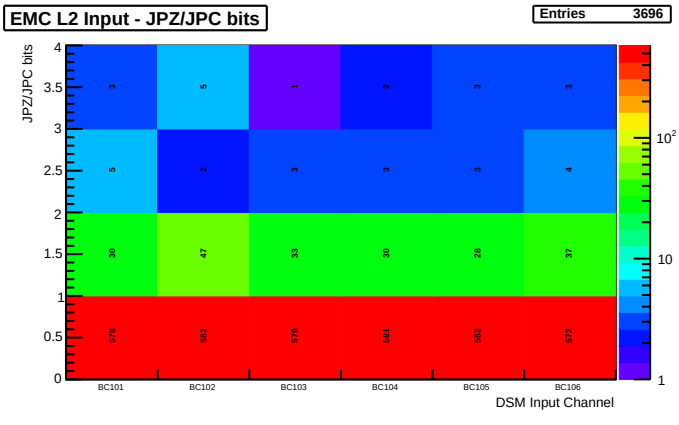

Endcap EMC L1 Input - HT.TP Diff
Entries 0
HT.TP Diff
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

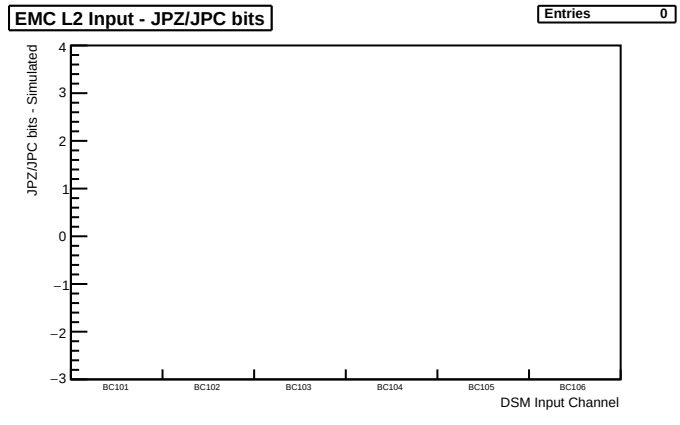

EMC L2 Input - JPX/JPA bits
Entries 3696
JPX/JPA bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

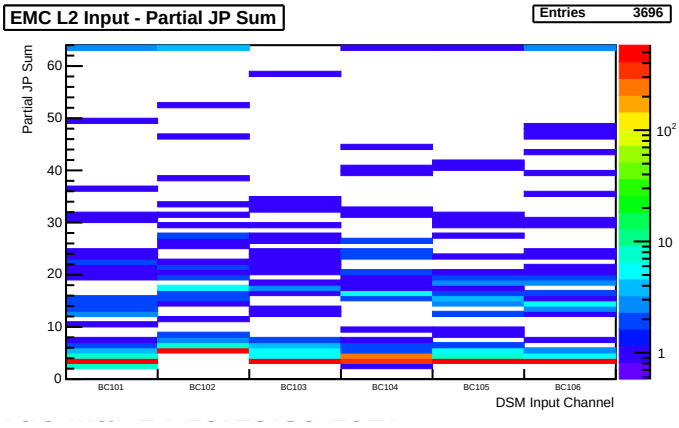

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

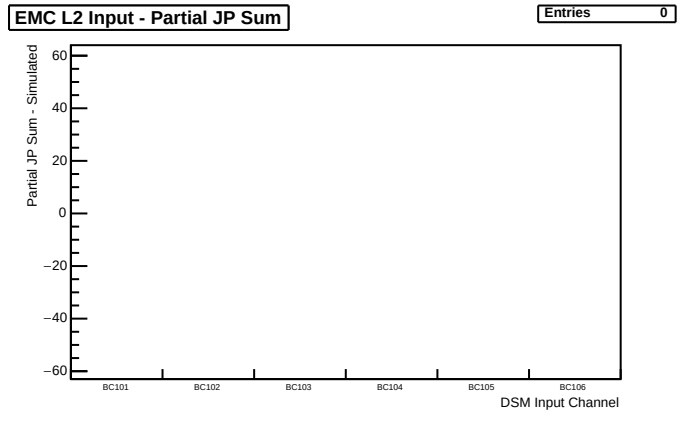

EMC L2 Input - JPY/JPB bits
Entries 3696
JPY/JPB bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

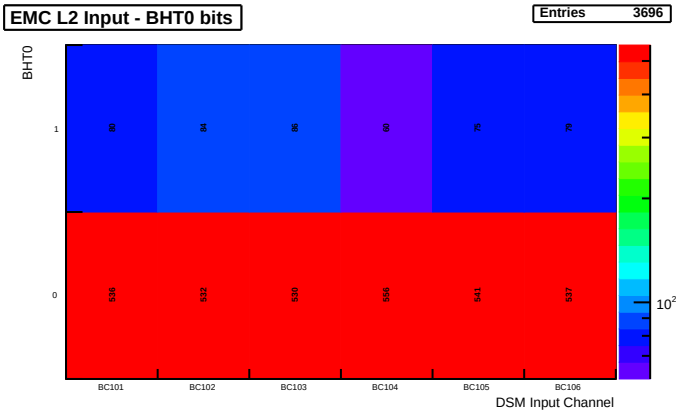

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

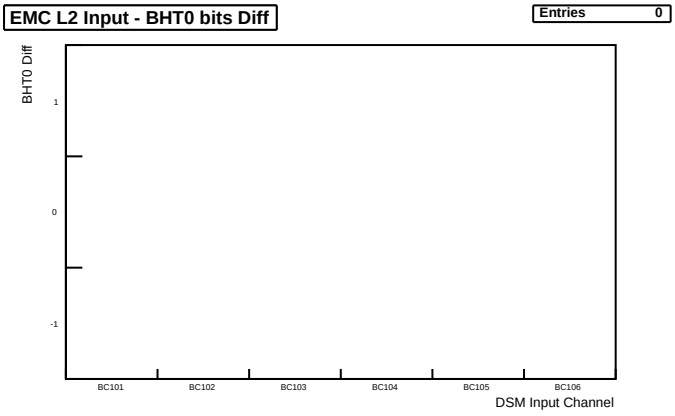

EMC L2 Input - JPZ/JPC bits
Entries 3696
JPZ/JPC bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

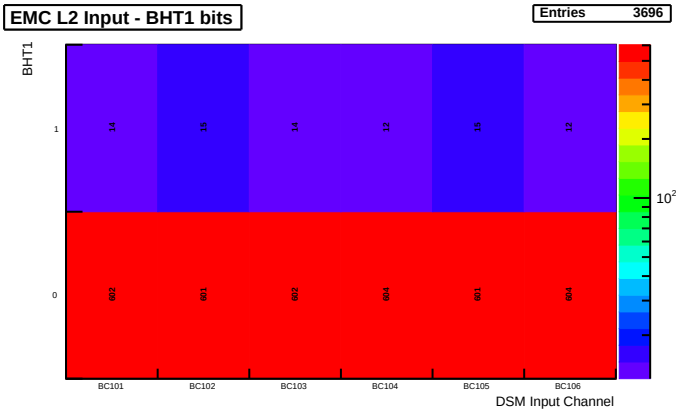

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

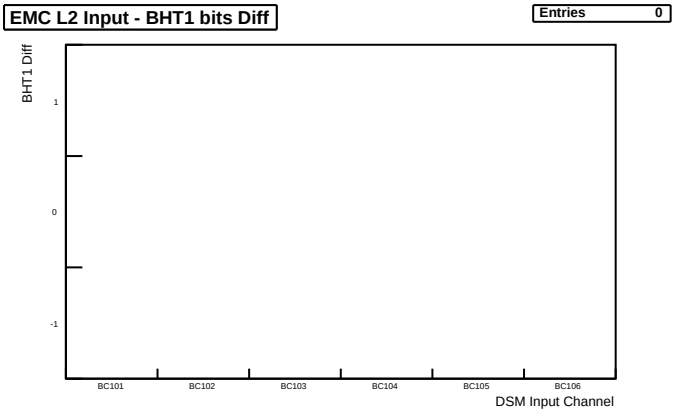

EMC L2 Input - Partial JP Sum
Entries 3696
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

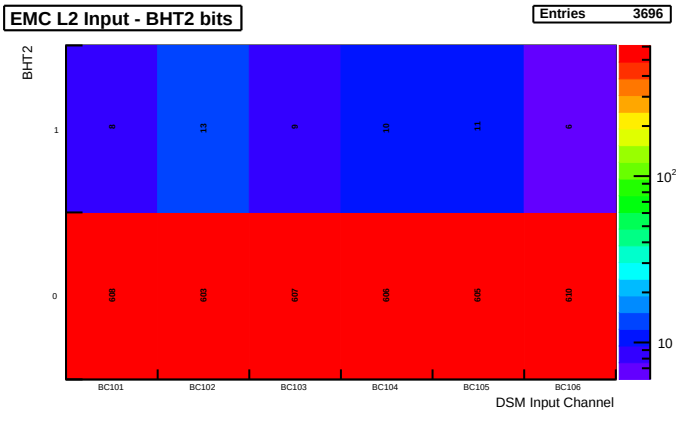

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

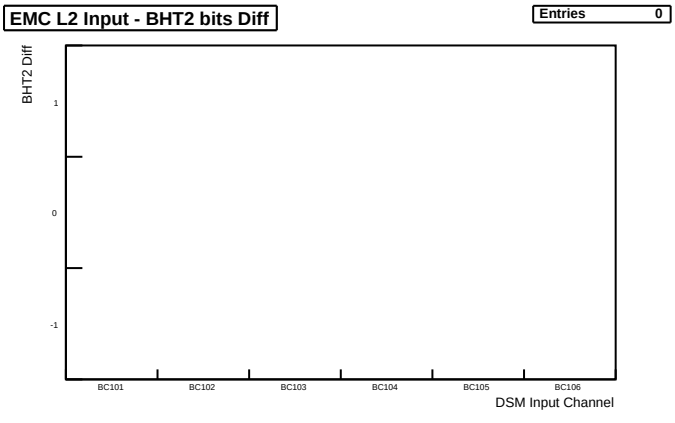

EMC L2 Input - BHT0 bits
Entries 3696
BHT0
1
0
80
84
86
60
75
70
536
532
530
556
541
537
10^2
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

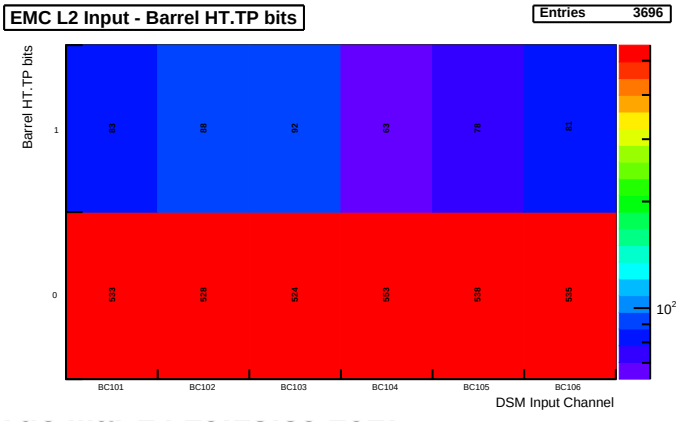

EMC L2 Input - BHT0 bits Diff
Entries 0
BHT0 Diff
1
0
-1
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

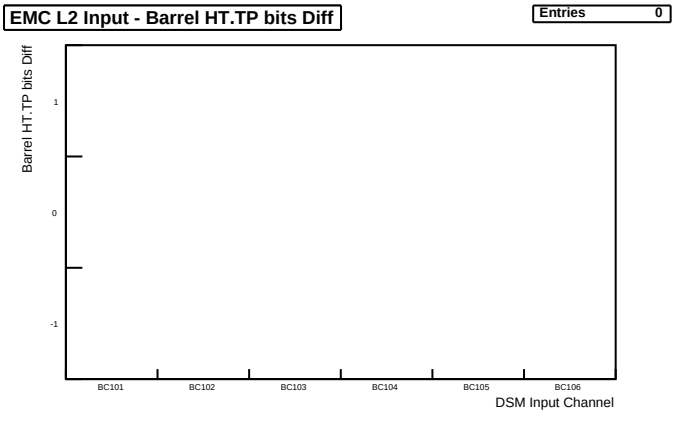

EMC L2 Input - BHT1 bits
Entries 3696
BHT1
1
0
14
15
14
12
15
12
602
601
602
604
601
604
10^2
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

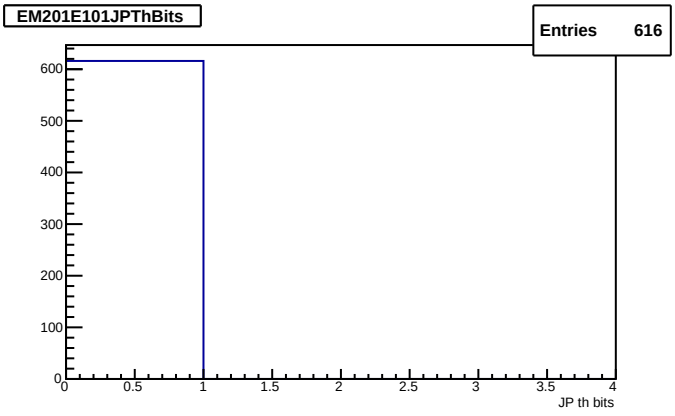

EMC L2 Input - BHT1 bits Diff
Entries 0
BHT1 Diff
1
0
-1
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

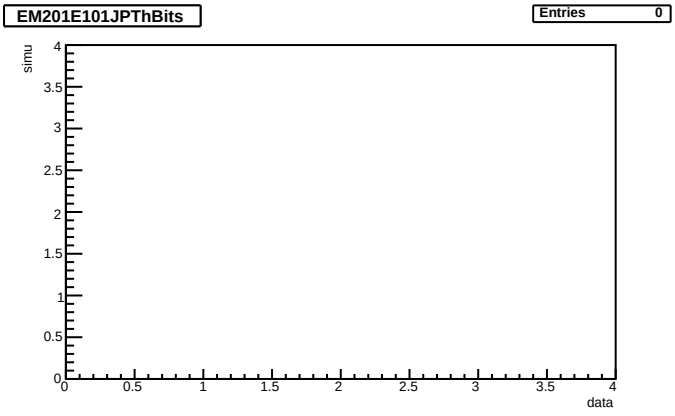

EMC L2 Input - BHT2 bits
Entries 3696
BHT2
1
0
8
13
9
10
11
6
608
603
607
606
605
610
10^2
10
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

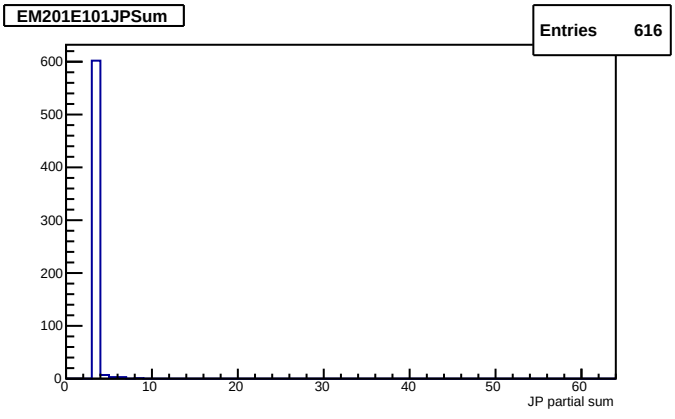

EMC L2 Input - BHT2 bits Diff
Entries 0
BHT2 Diff
1
0
-1
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

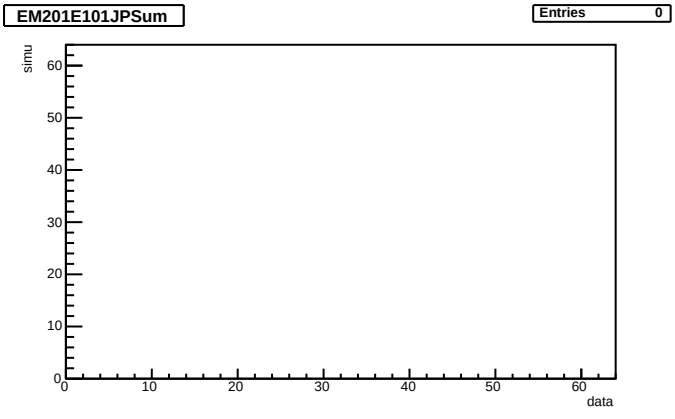

EMC L2 Input - Barrel HT.TP bits
Entries 3696
Barrel HT.TP bits
1
0
83
88
92
63
78
81
533
528
524
553
538
535
10^2
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

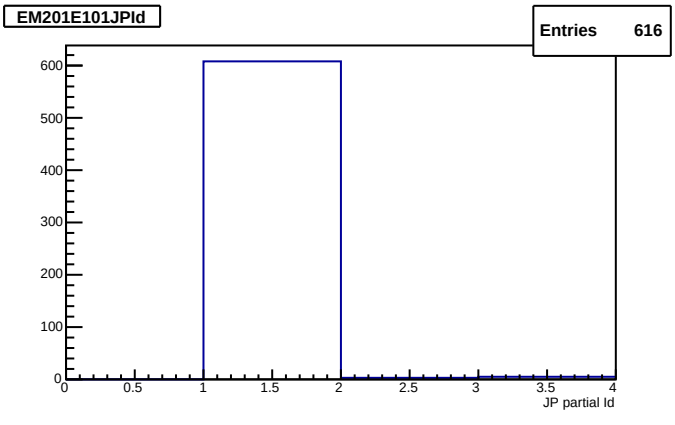

EMC L2 Input - Barrel HT.TP bits Diff
Entries 0
Barrel HT.TP bits Diff
1
0
-1
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

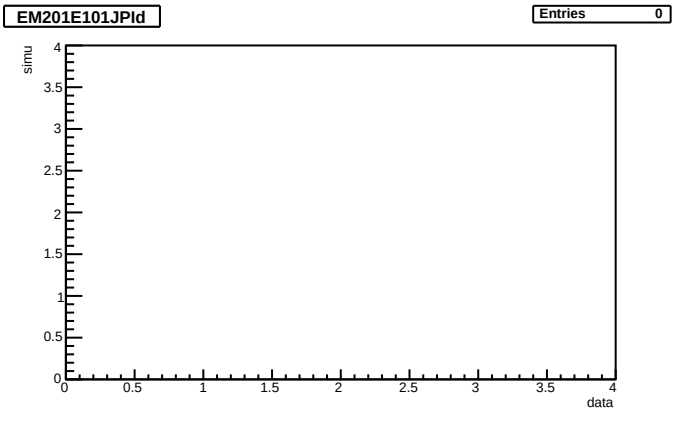

EM201E101JPThBits
Entries 616
JP th bits

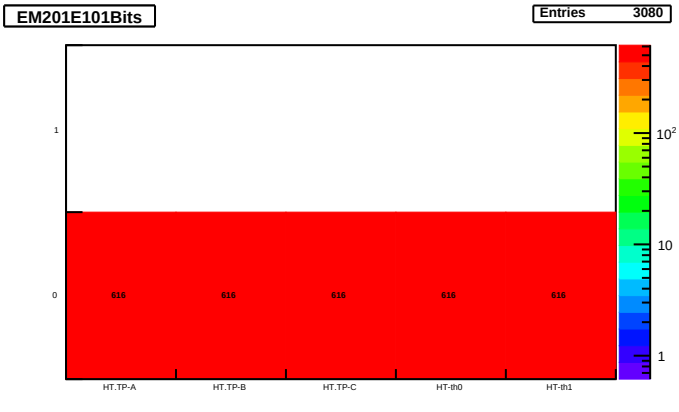

EM201E101JPThBits
Entries 0
simu
data

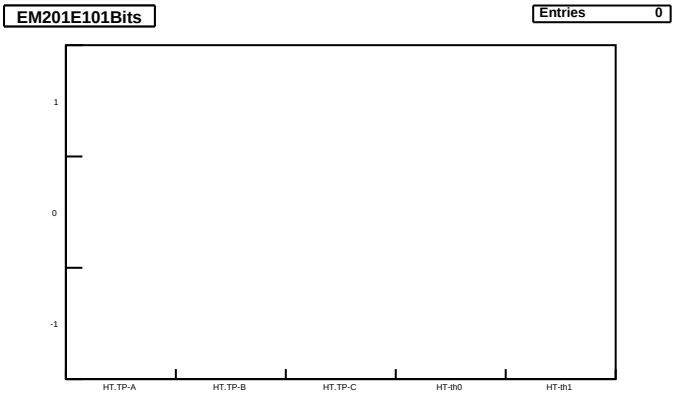

EM201E101JPSum
Entries 616
JP partial sum

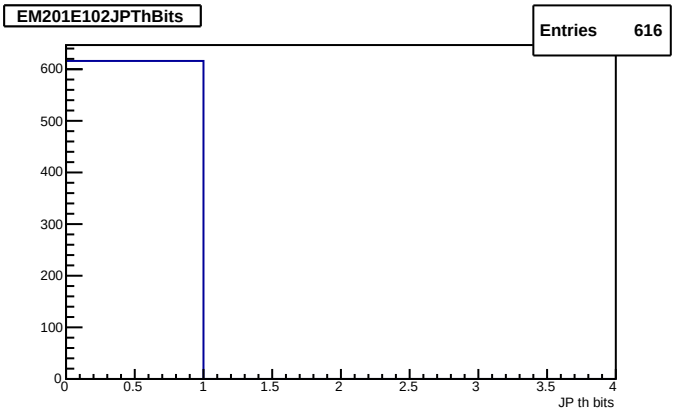

EM201E101JPSum
Entries 0
simu
data

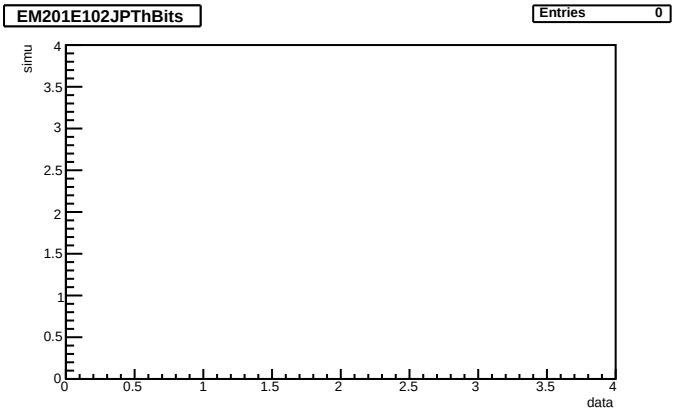

EM201E101JPId
Entries 616
JP partial Id

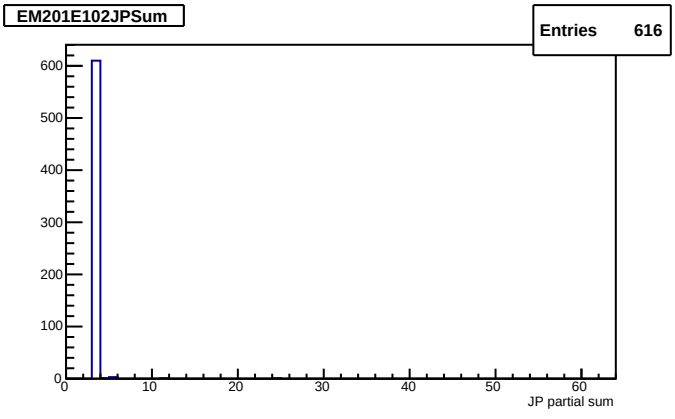

EM201E101JPId
Entries 0
simu
data

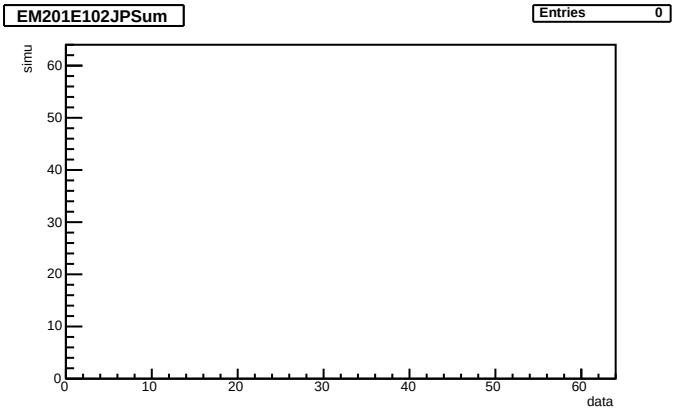

EM201E101Bits
Entries 3080
616
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

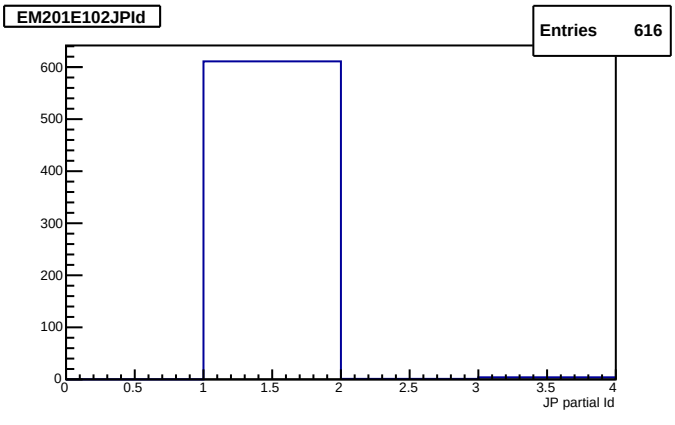

EM201E101Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

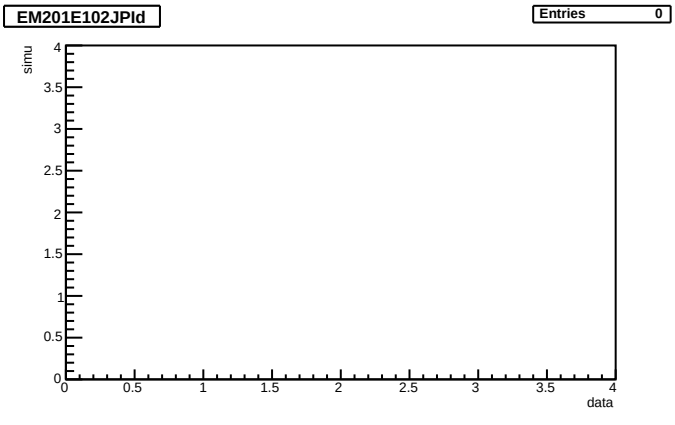

EM201E102JPThBits
Entries 616
JP th bits

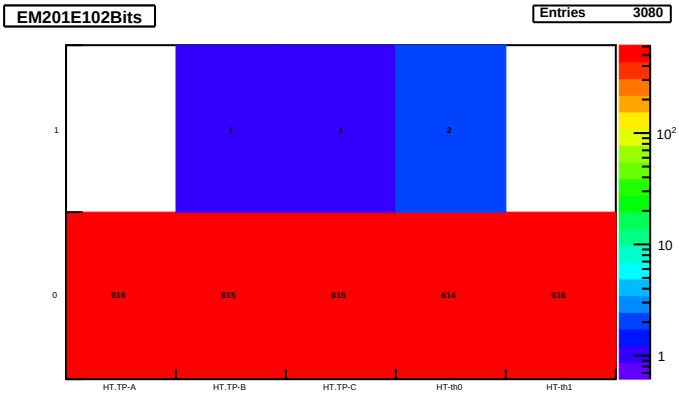

EM201E102JPThBits
Entries 0
simu
data

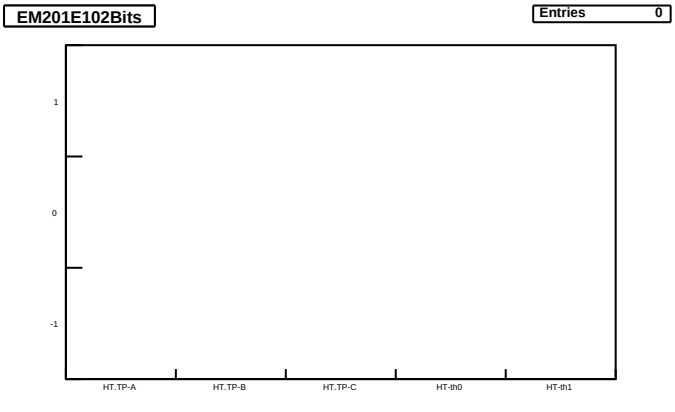

EM201E102JPSum
Entries 616
JP partial sum

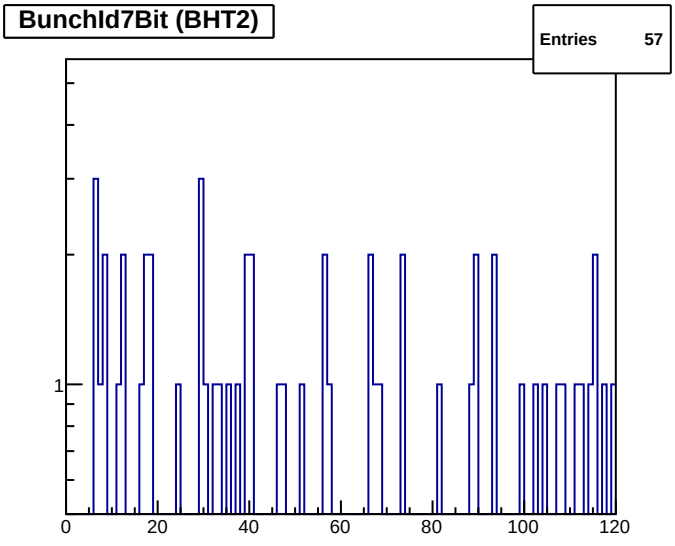

EM201E102JPSum
Entries 0
simu
data

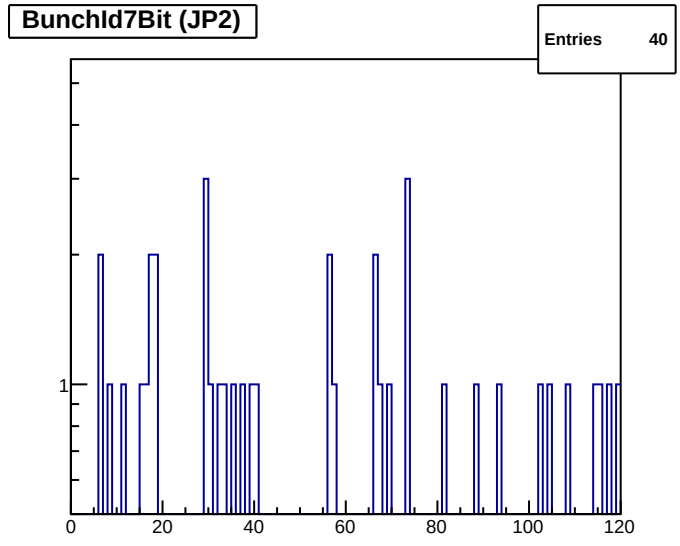

EM201E102JPId
Entries 616
JP partial Id

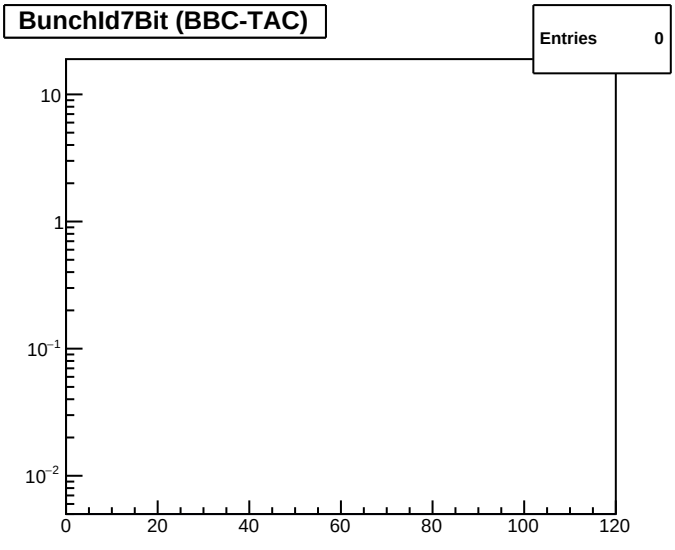

EM201E102JPId
Entries 0
simu
data

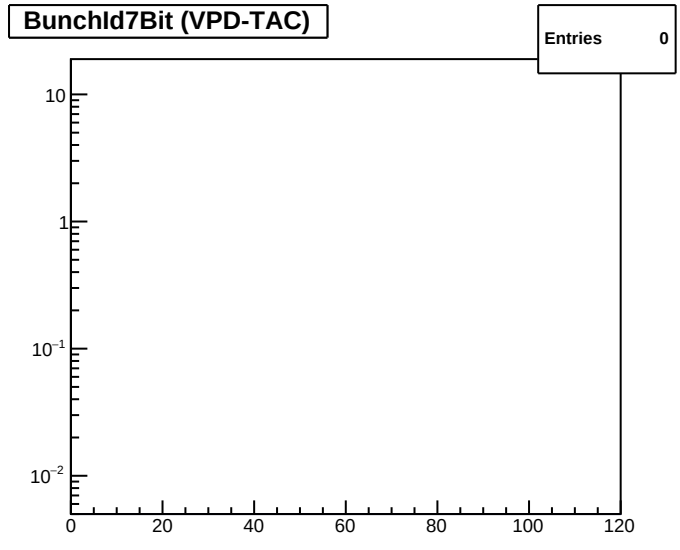

EM201E102Bits
Entries 3080
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

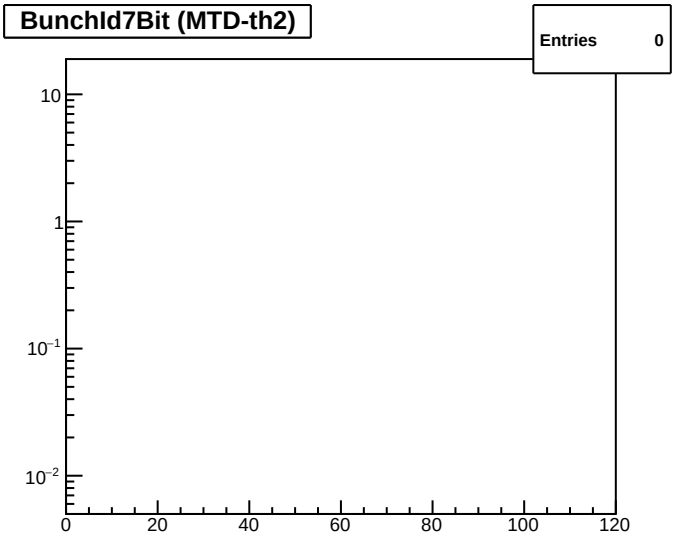

EM201E102Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

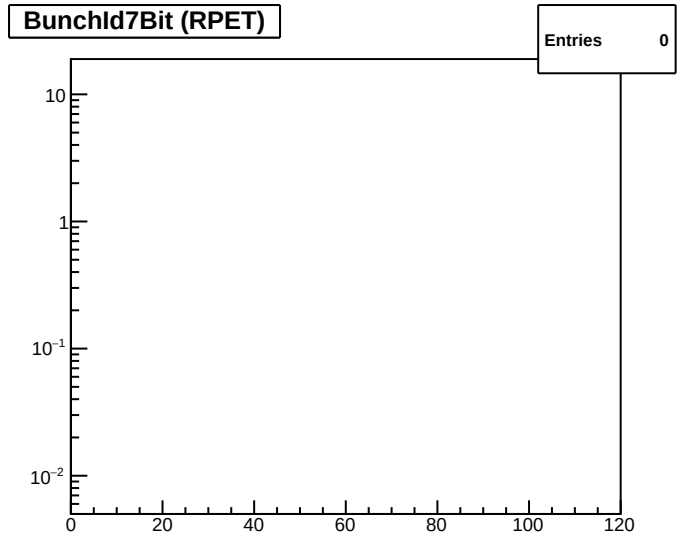

BunchId7Bit (BHT2)
Entries 57
1
0
20
40
60
80
100
120

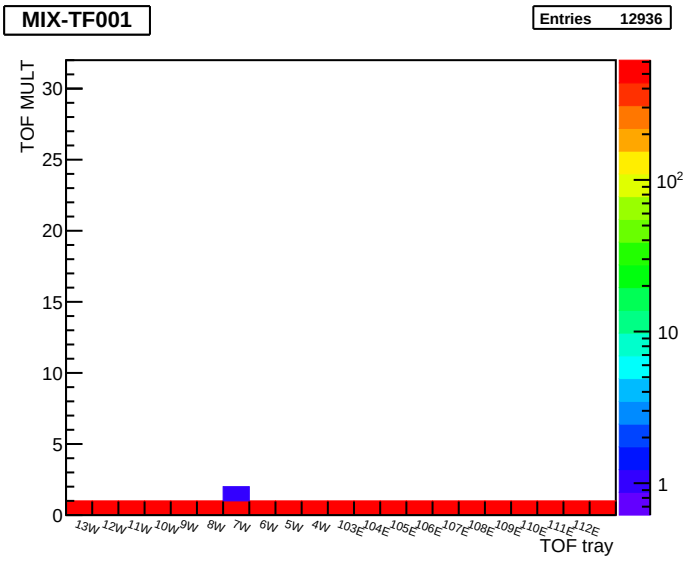

BunchId7Bit (JP2)
Entries 40
1
0
20
40
60
80
100
120

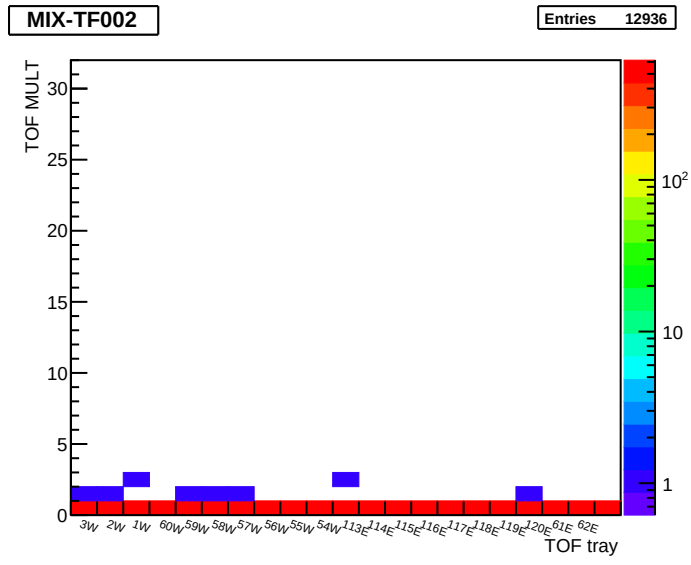

BunchId7Bit (BBC-TAC)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

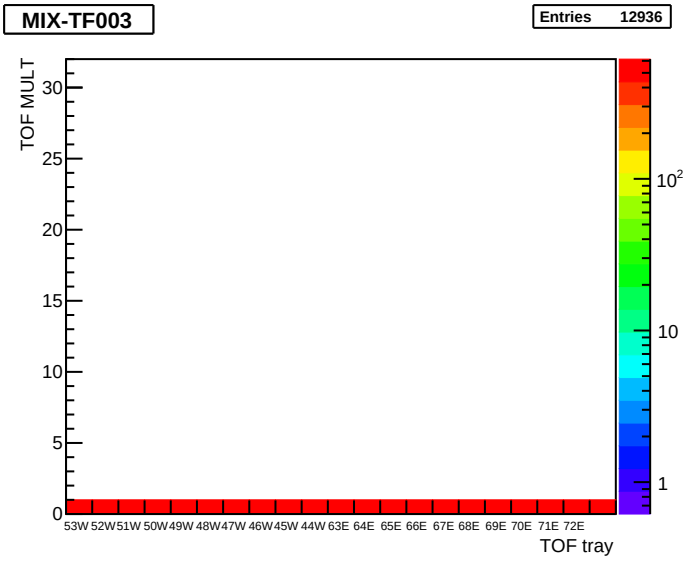

BunchId7Bit (VPD-TAC)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

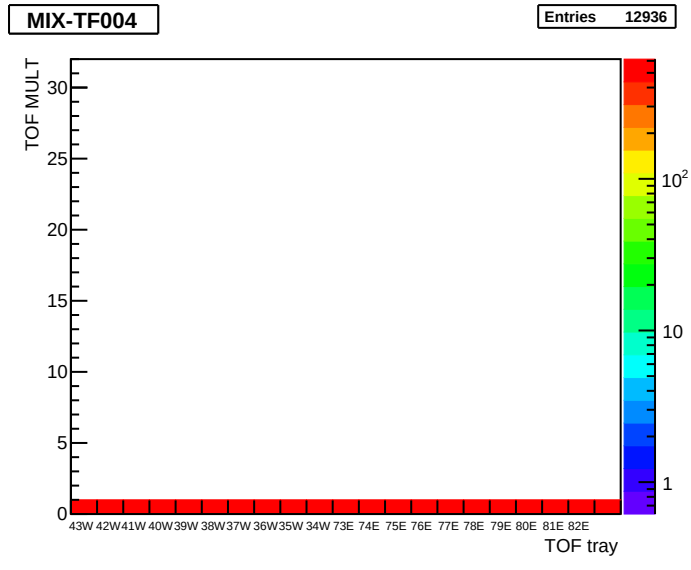

BunchId7Bit (MTD-th2)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

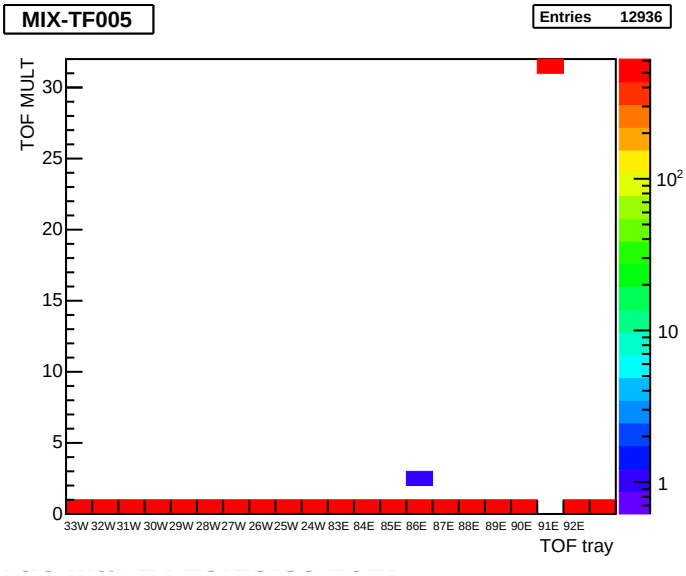

BunchId7Bit (RPET)
Entries 0
10
1
10^{-1}
10^{-2}
0
20
40
60
80
100
120

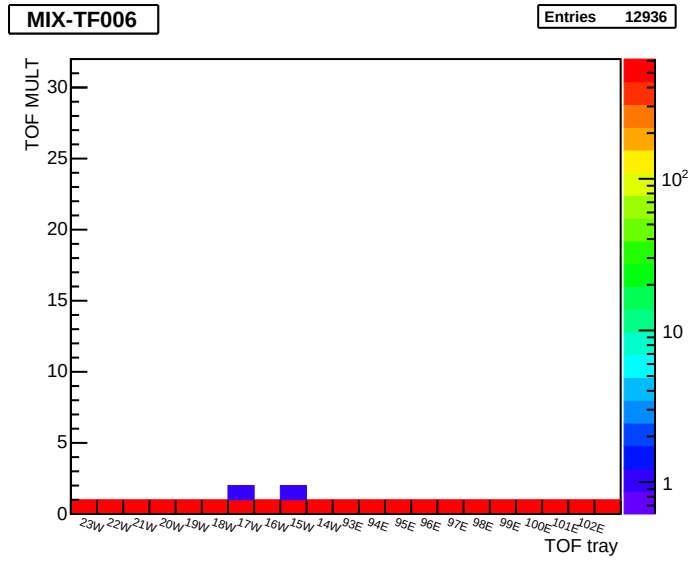

MIX-TF001
Entries 12936
TOF MULT
TOF tray

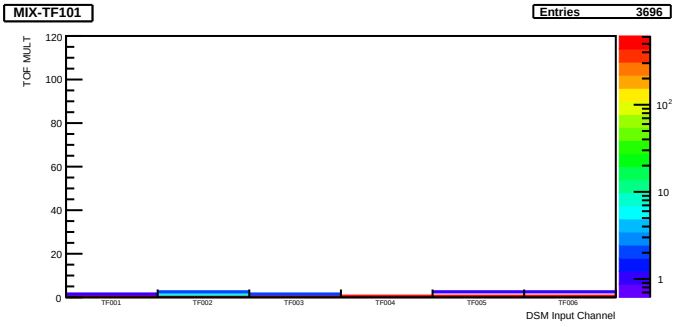

MIX-TF002
Entries 12936
TOF MULT
TOF tray

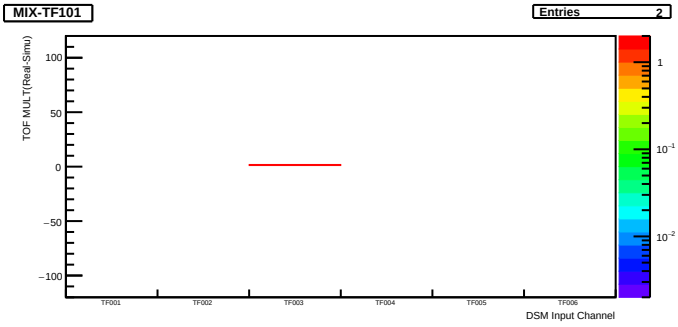

MIX-TF003
Entries 12936
TOF MULT
TOF tray

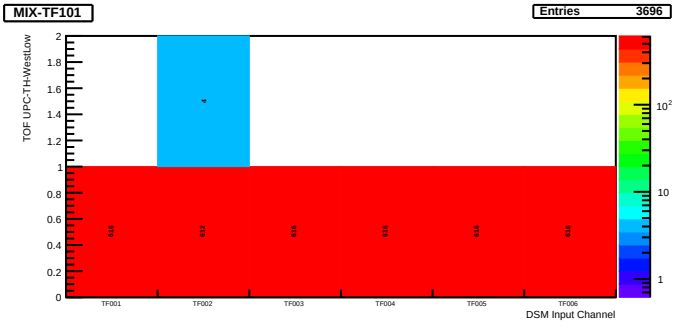

MIX-TF004
Entries 12936
TOF MULT
TOF tray

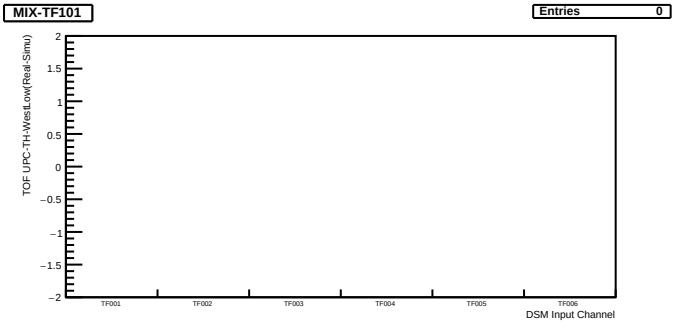

MIX-TF005
Entries 12936
TOF MULT
TOF tray

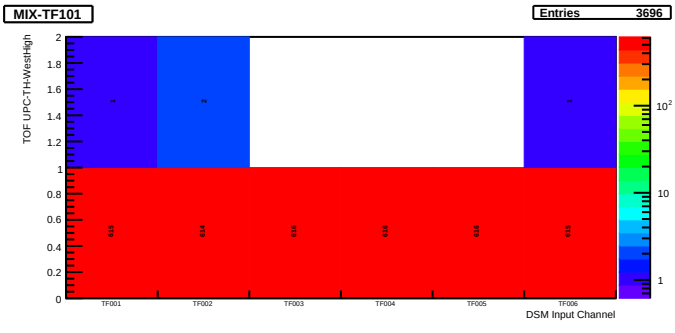

MIX-TF006
Entries 12936
TOF MULT
TOF tray

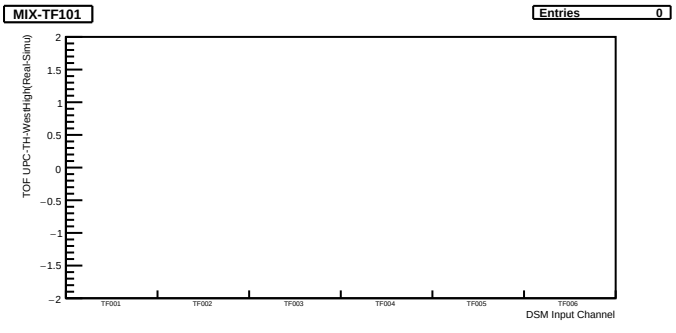

MIX-TF101
Entries 3696
TOF MULT
DSM Input Channel

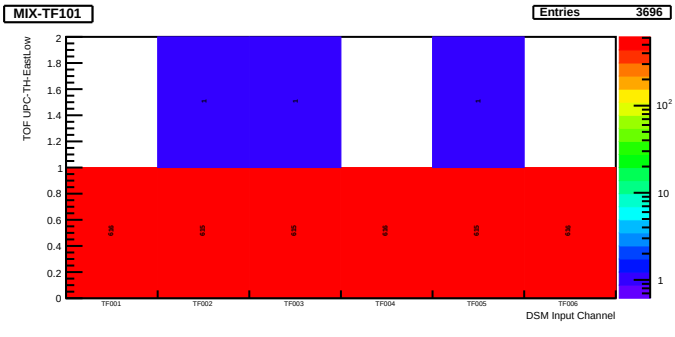

MIX-TF101
Entries 2
TOF MULT(Real-Simu)
DSM Input Channel

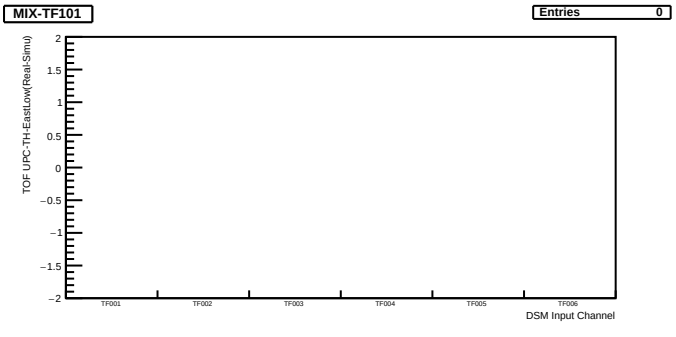

MIX-TF101
Entries 3696
TOF UPC-TH-WestLow
DSM Input Channel

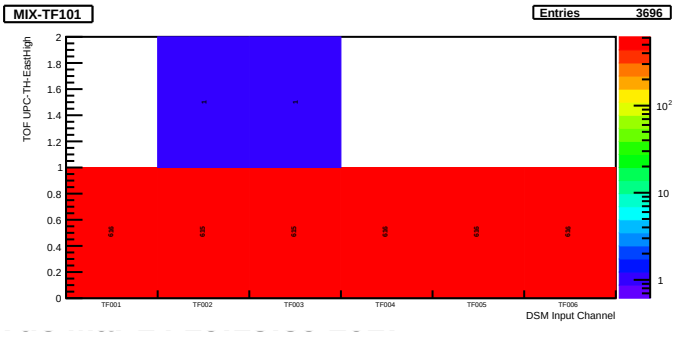

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel

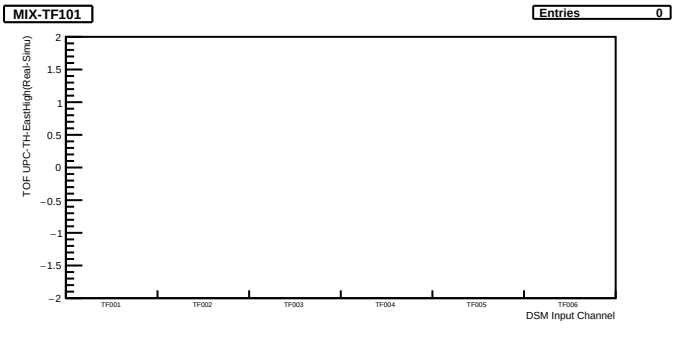

MIX-TF101
Entries 3696
TOF UPC-TH-WestHigh
DSM Input Channel

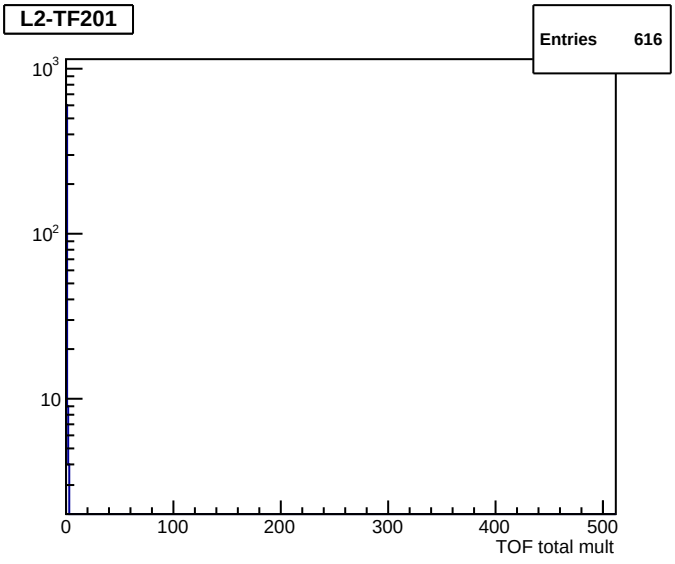

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel

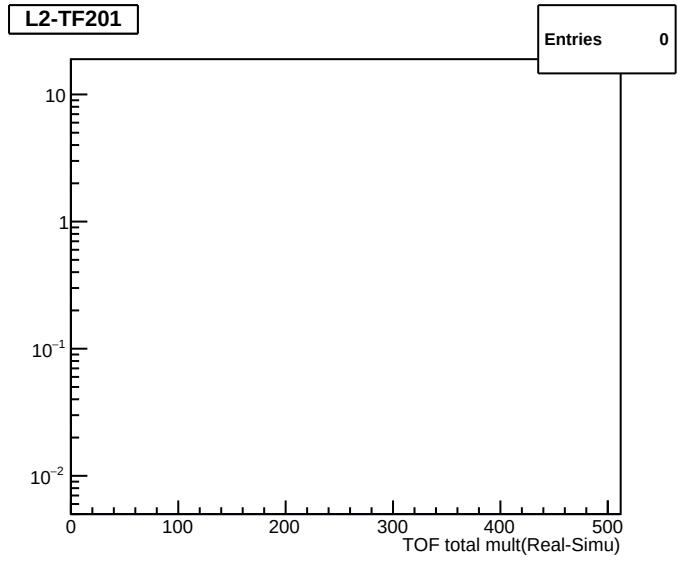

MIX-TF101
Entries 3696
TOF UPC-TH-EastLow
DSM Input Channel

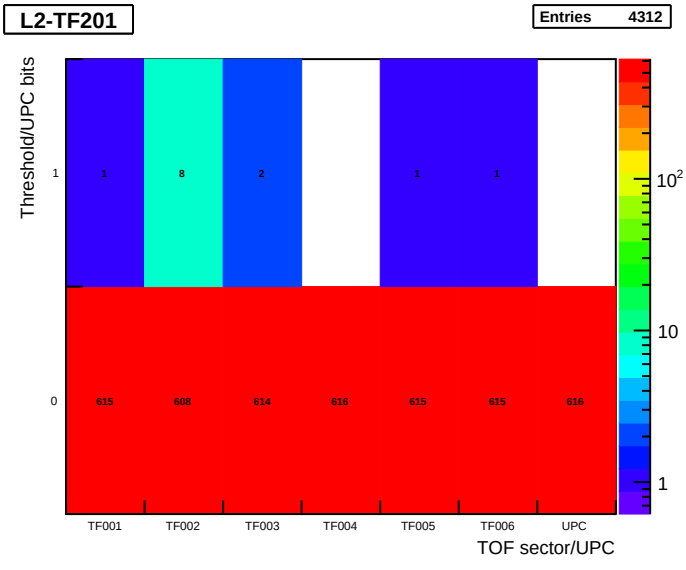

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel

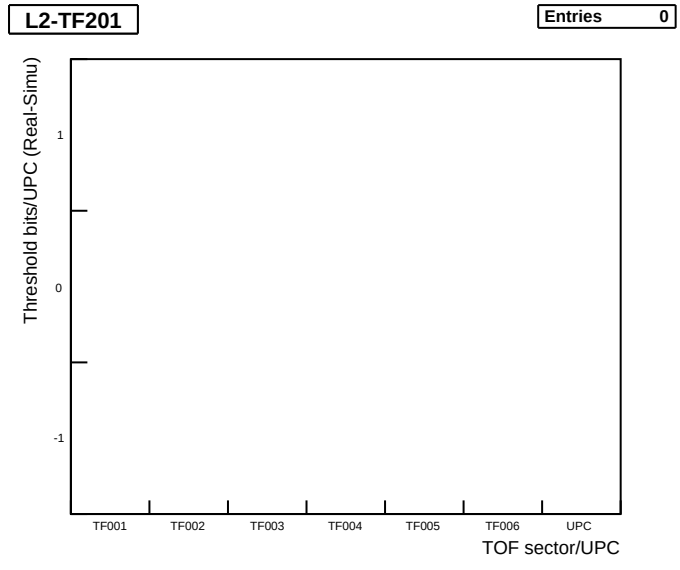

MIX-TF101
Entries 3696
TOF UPC-TH-EastHigh
DSM Input Channel

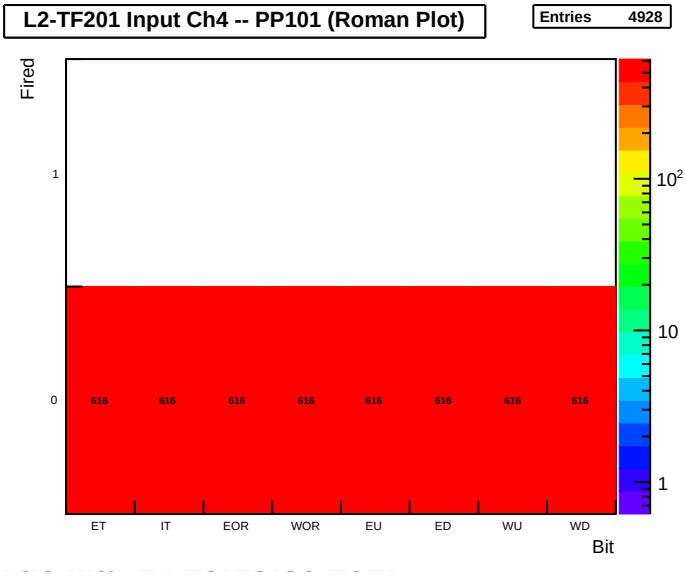

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel

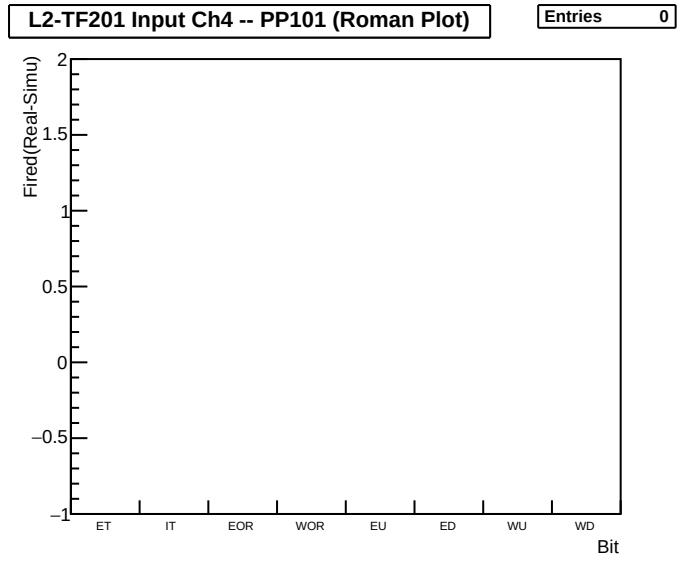

L2-TF201
Entries 616
TOF total mult

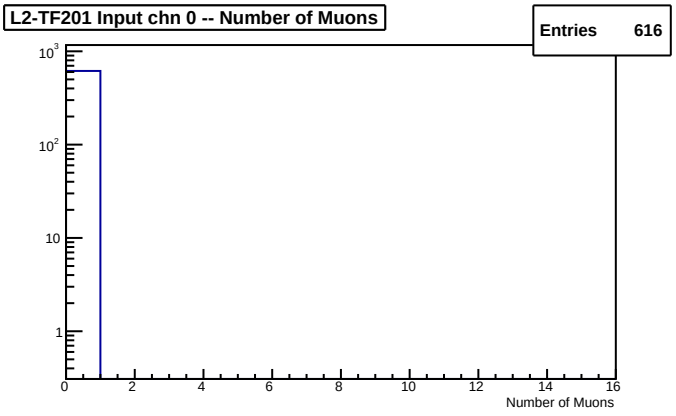

L2-TF201
Entries 0
TOF total mult(Real-Simu)

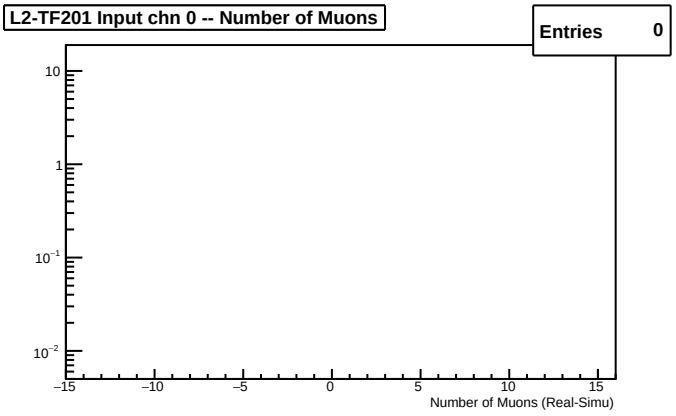

L2-TF201
Entries 4312
Threshold/UPC bits
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

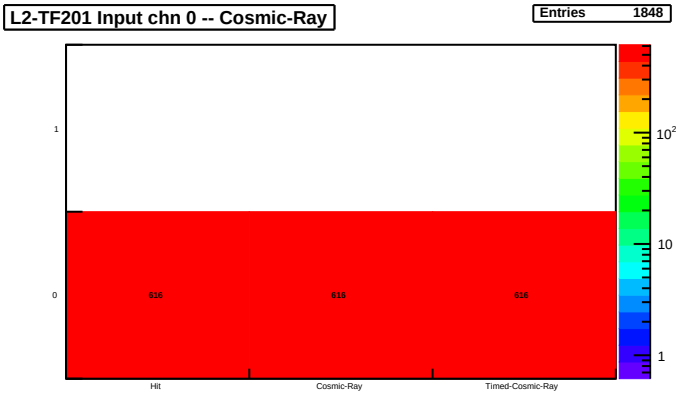

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

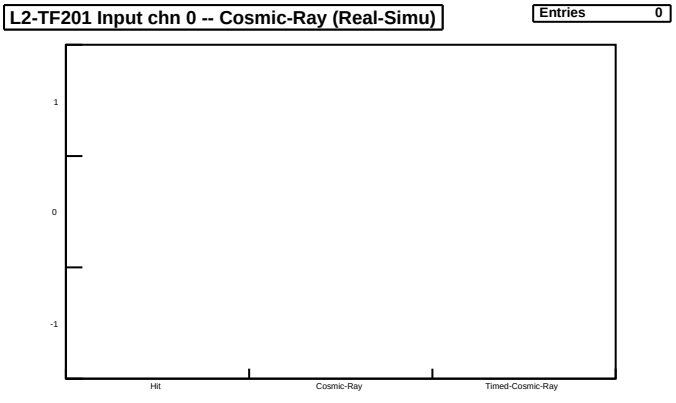

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 4928
Fired
ET IT EOR WOR EU ED WU WD
Bit

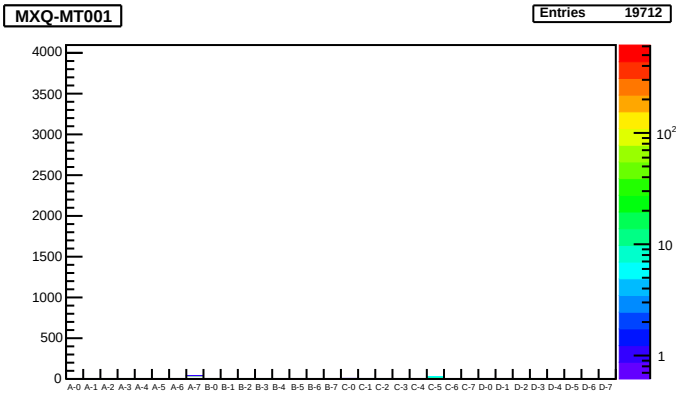

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 0
Fired(Real-Simu)
ET IT EOR WOR EU ED WU WD
Bit

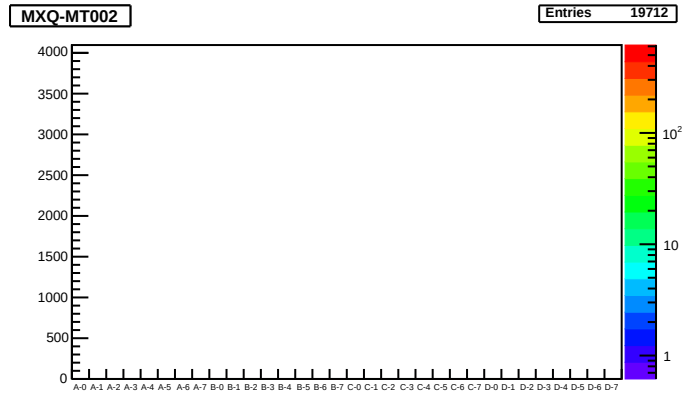

L2-TF201 Input chn 0 -- Number of Muons
Entries 616
Number of Muons

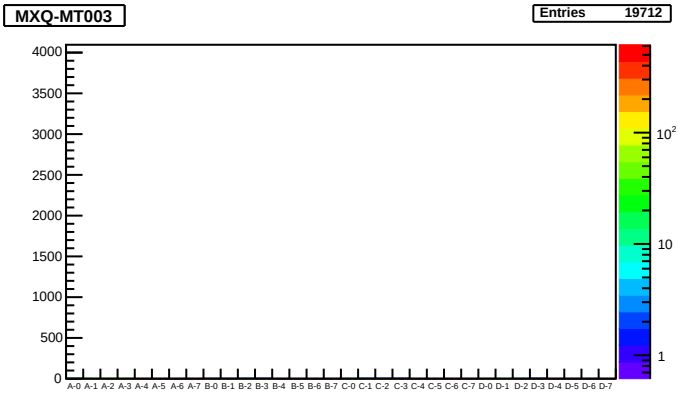

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

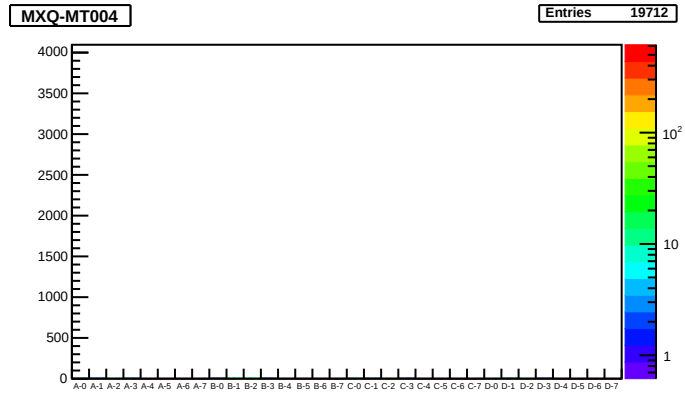

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 1848
616
616
616
Hit
Cosmic-Ray
Timed-Cosmic-Ray

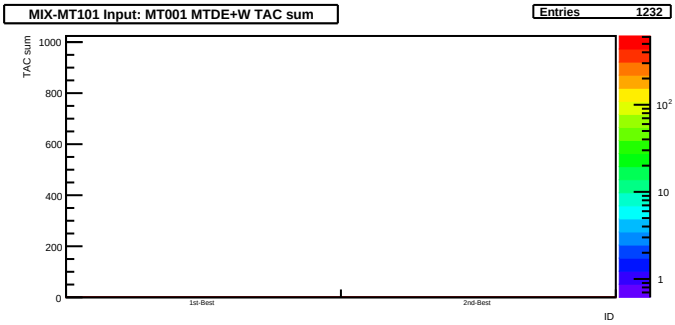

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

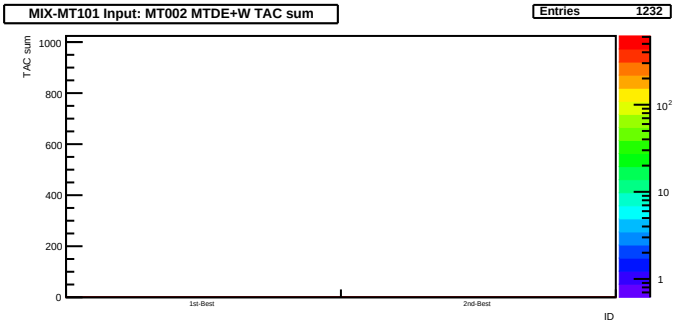

MXQ-MT001
Entries 19712
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^2
10
1

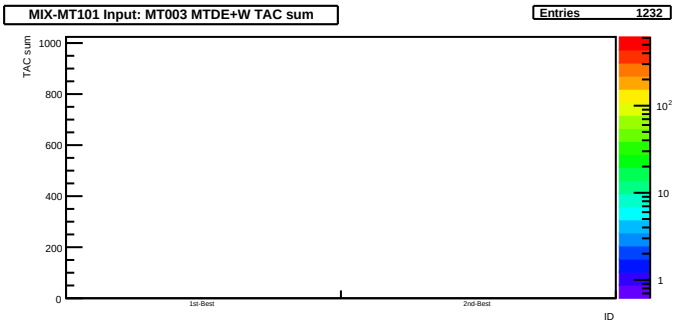

MXQ-MT002
Entries 19712
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^2
10
1

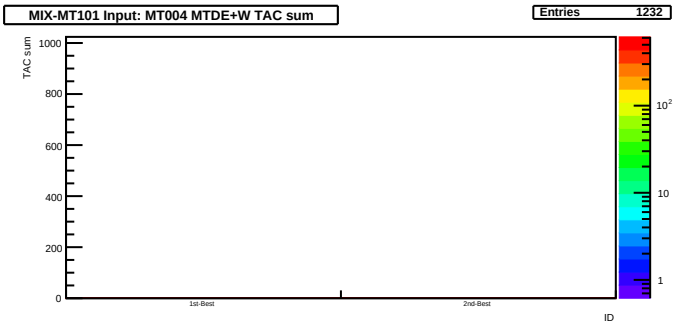

MXQ-MT003
Entries 19712
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^2
10
1

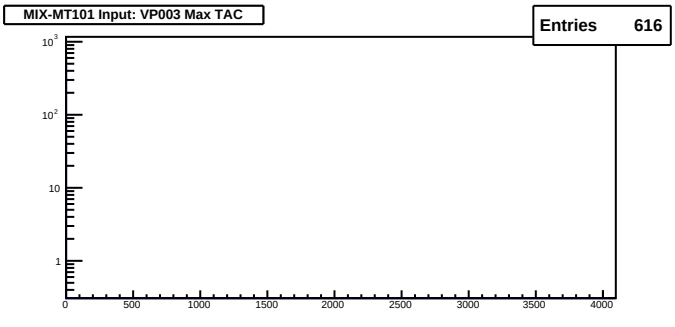

MXQ-MT004
Entries 19712
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10^2
10
1

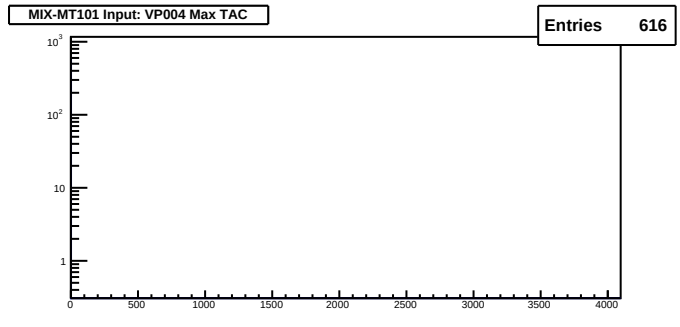

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 1232
TAC sum
1000
800
600
400
200
0
1st-Best
2nd-Best
ID
10^2
10
1

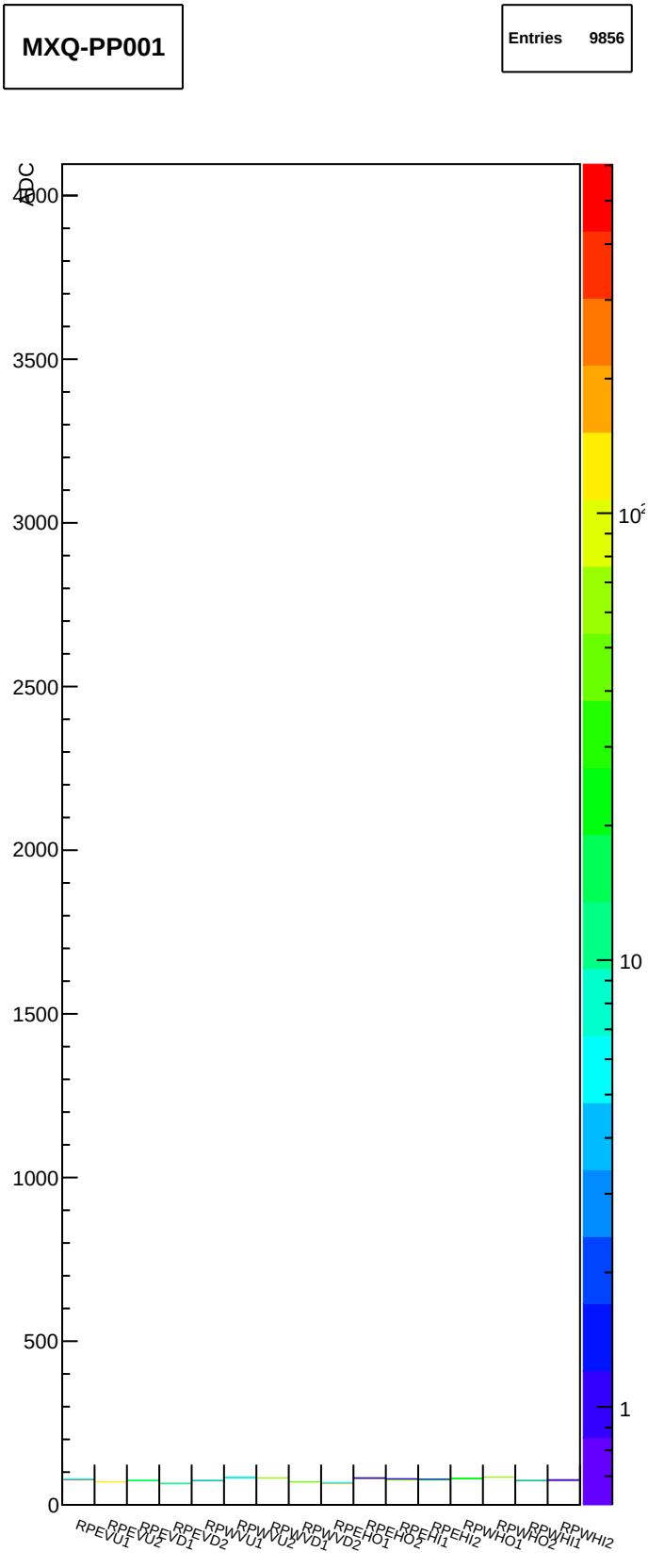

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 1232
TAC sum
1000
800
600
400
200
0
1st-Best
2nd-Best
ID
10^2
10
1

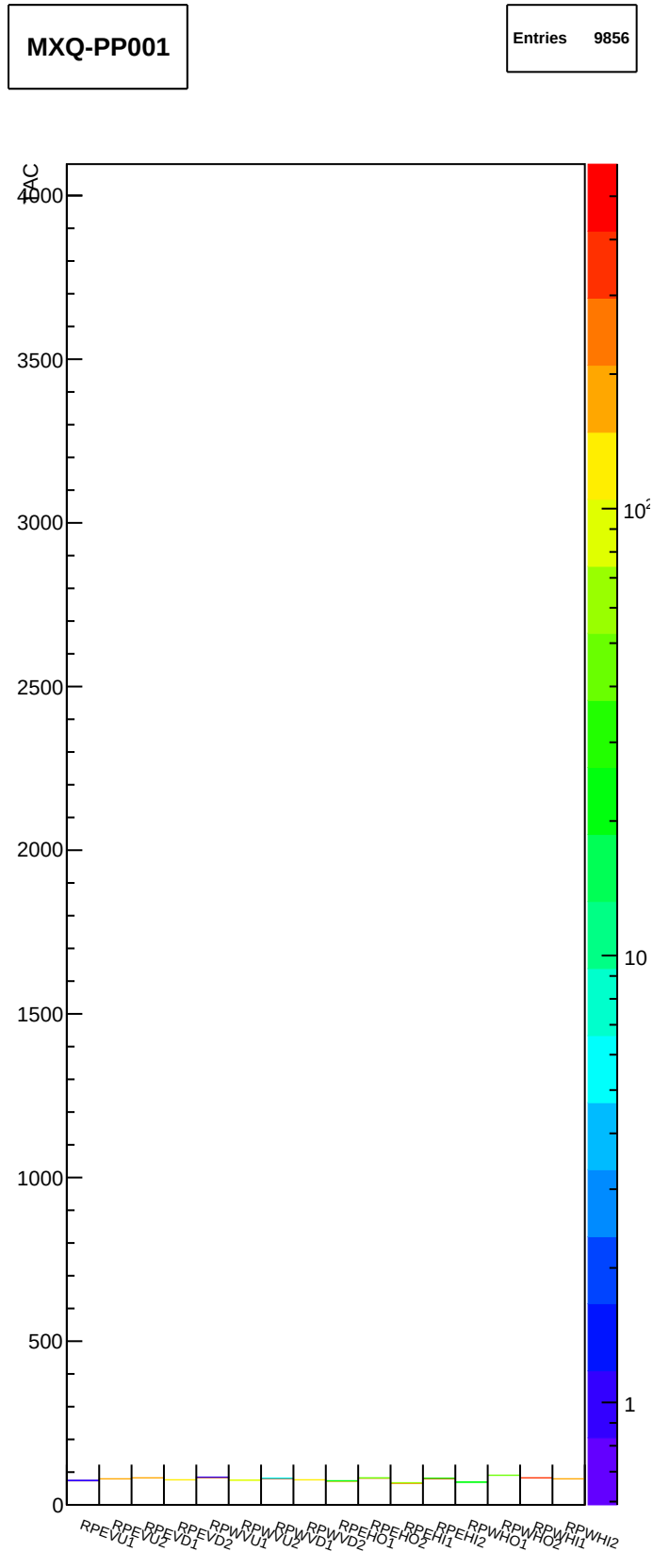

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 1232
TAC sum
1000
800
600
400
200
0
1st-Best
2nd-Best
ID
10^2
10
1

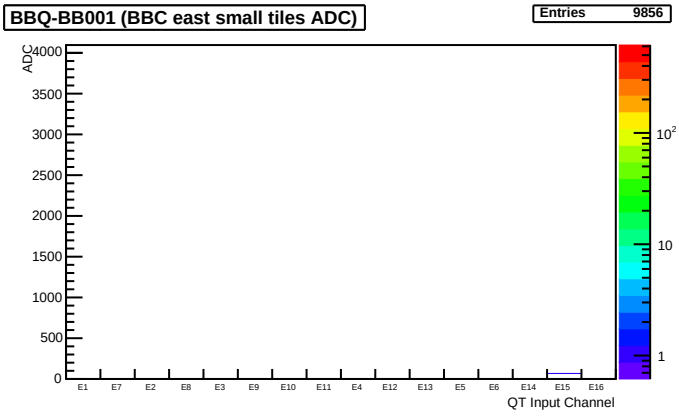

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 1232
TAC sum
1000
800
600
400
200
0
1st-Best
2nd-Best
ID
10^2
10
1

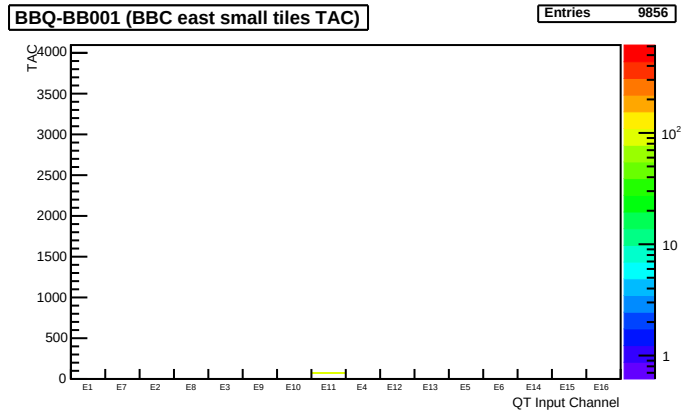

MIX-MT101 Input: VP003 Max TAC
Entries 616
10^3
10^2
10
1
0
500
1000
1500
2000
2500
3000
3500
4000

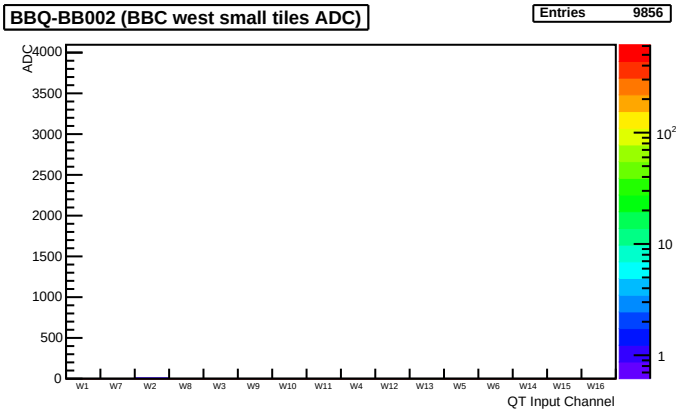

MIX-MT101 Input: VP004 Max TAC
Entries 616
10^3
10^2
10
1
0
500
1000
1500
2000
2500
3000
3500
4000

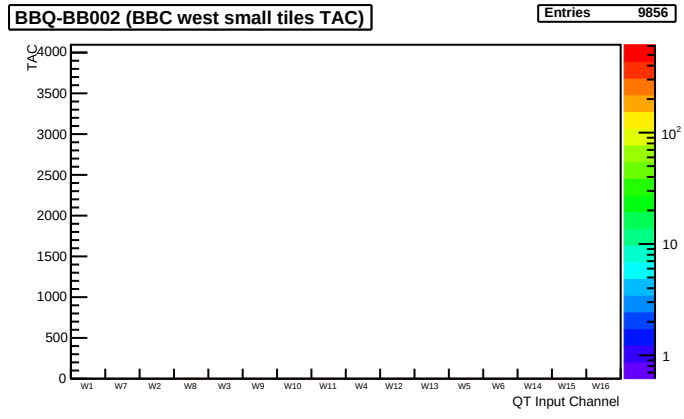

MXQ-PP001
Entries 9856
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10²
10
1

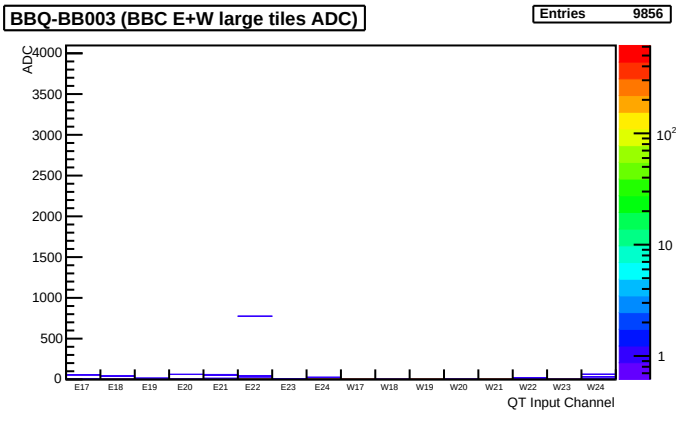

MXQ-PP001
Entries 9856
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10²
10
1

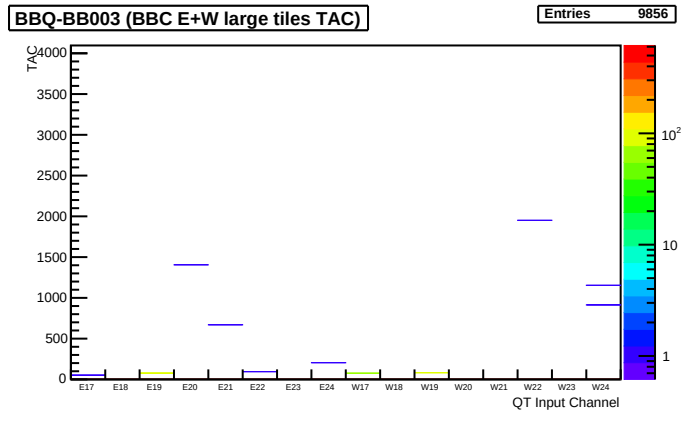

BBQ-BB001 (BBC east small tiles ADC)
Entries 9856
ADC
QT Input Channel

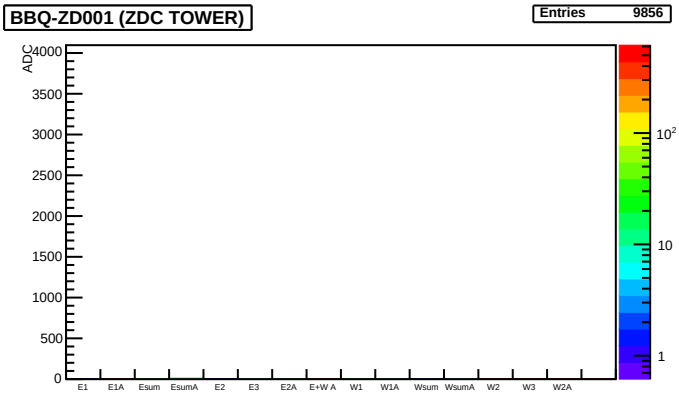

BBQ-BB001 (BBC east small tiles TAC)
Entries 9856
TAC
QT Input Channel

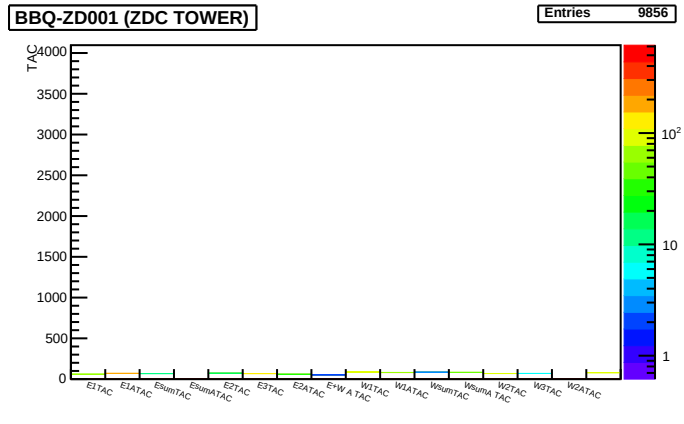

BBQ-BB002 (BBC west small tiles ADC)
Entries 9856
ADC
QT Input Channel

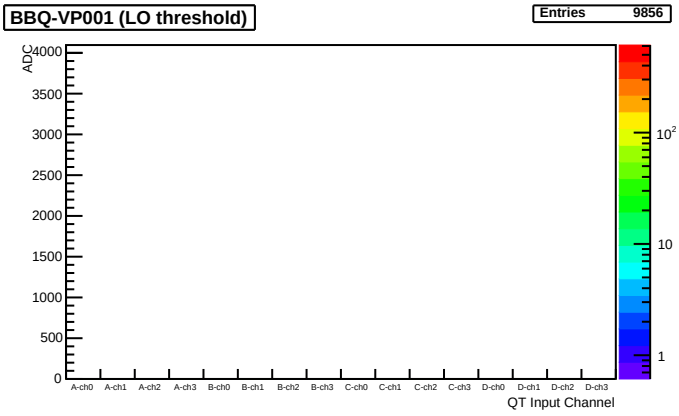

BBQ-BB002 (BBC west small tiles TAC)
Entries 9856
TAC
QT Input Channel

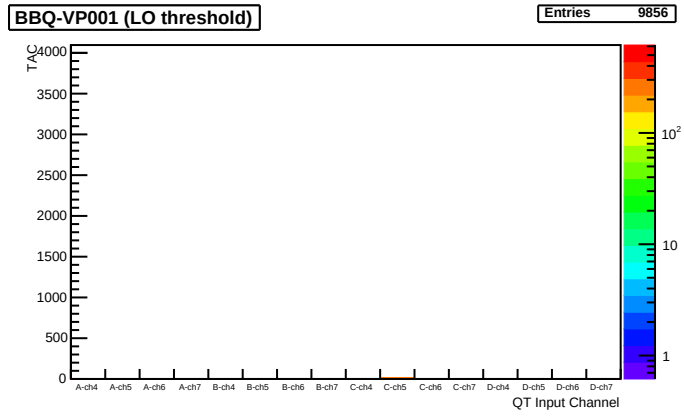

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 9856
ADC
QT Input Channel

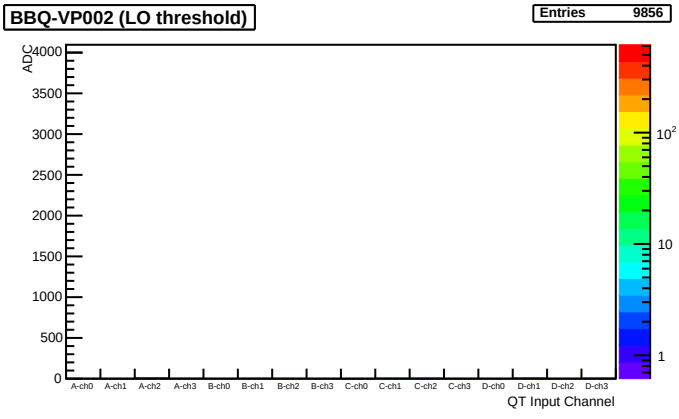

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 9856
TAC
QT Input Channel

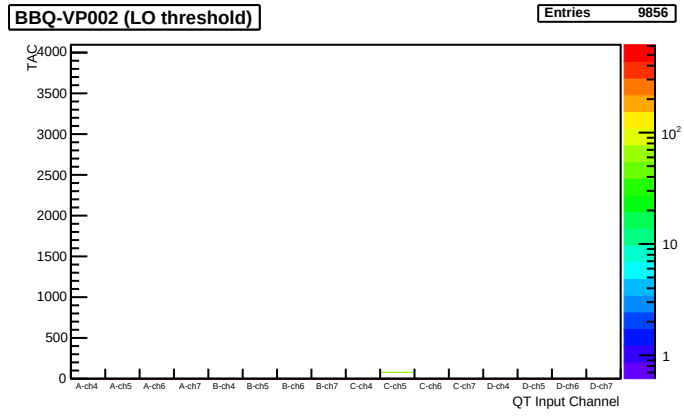

BBQ-ZD001 (ZDC TOWER)
Entries 9856
ADC

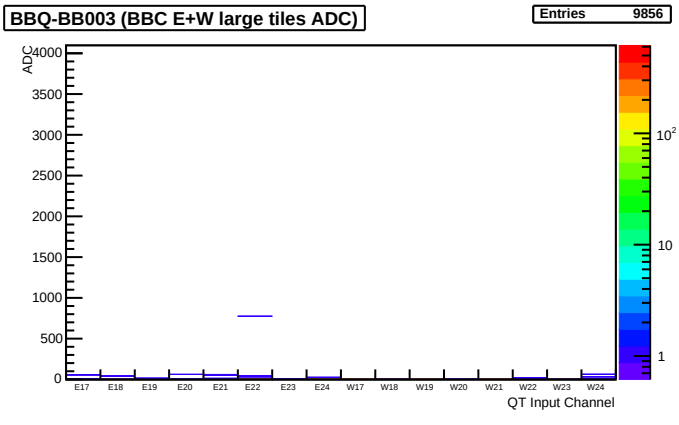

BBQ-ZD001 (ZDC TOWER)
Entries 9856
TAC

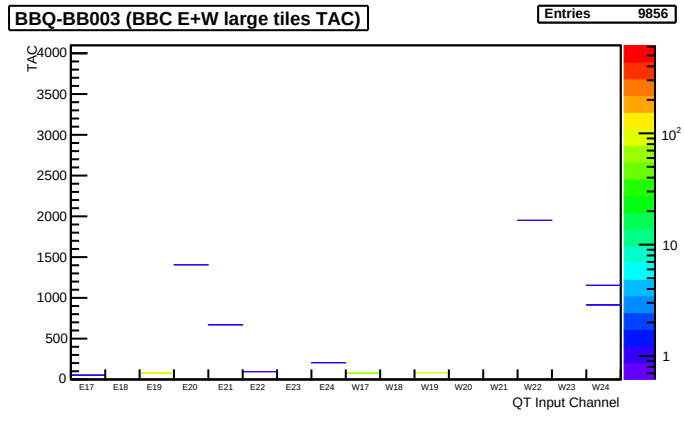

BBQ-VP001 (LO threshold)
Entries 9856
ADC
QT Input Channel

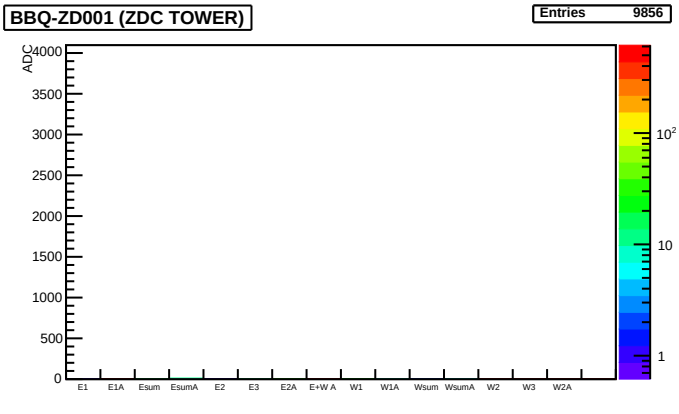

BBQ-VP001 (LO threshold)
Entries 9856
TAC
QT Input Channel

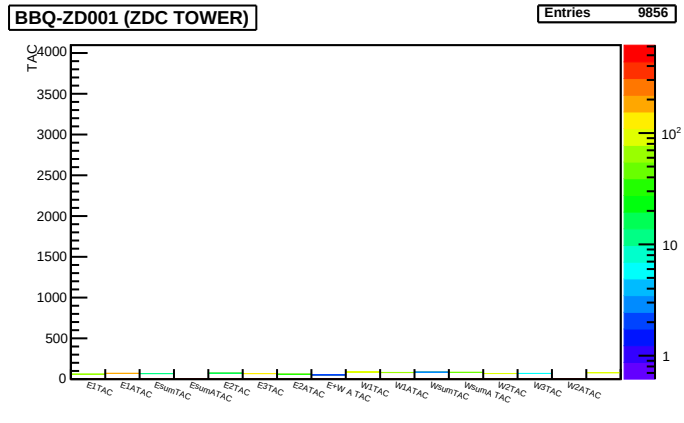

BBQ-VP002 (LO threshold)
Entries 9856
ADC
QT Input Channel

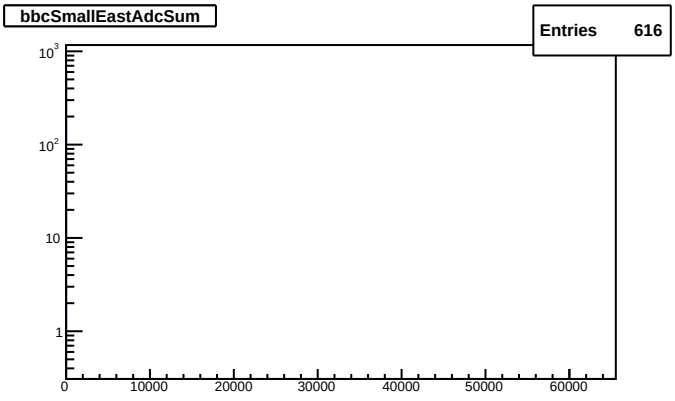

BBQ-VP002 (LO threshold)
Entries 9856
TAC
QT Input Channel

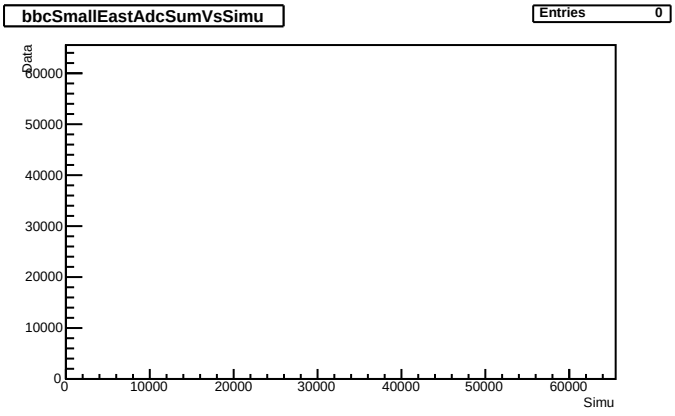

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 9856
ADC
QT Input Channel

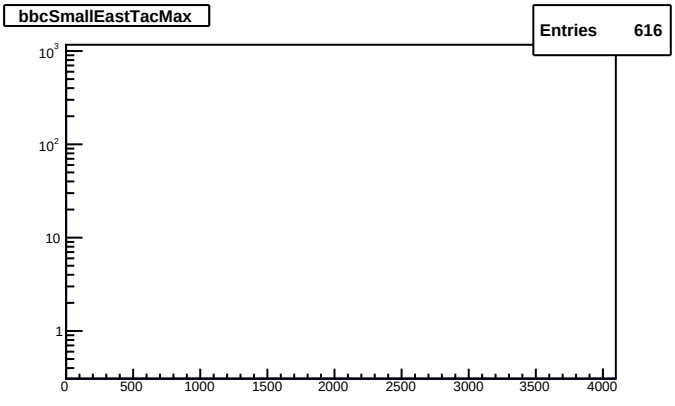

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 9856
TAC
QT Input Channel

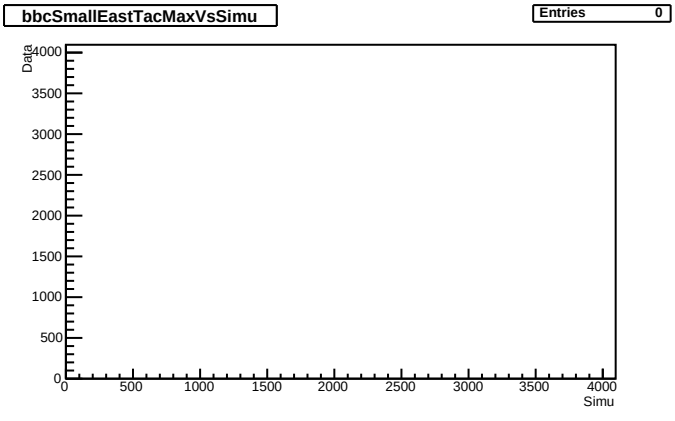

BBQ-ZD001 (ZDC TOWER)
Entries 9856
ADC

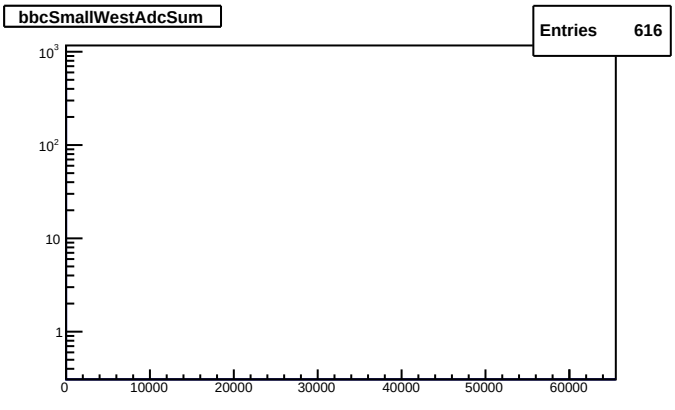

BBQ-ZD001 (ZDC TOWER)
Entries 9856
TAC

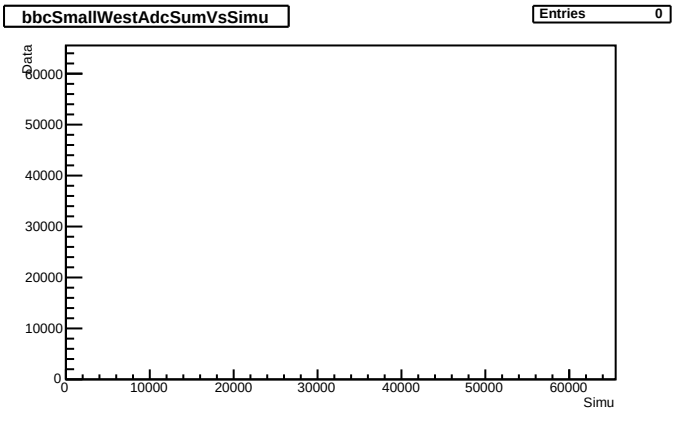

bbcSmallEastAdcSum
Entries 616

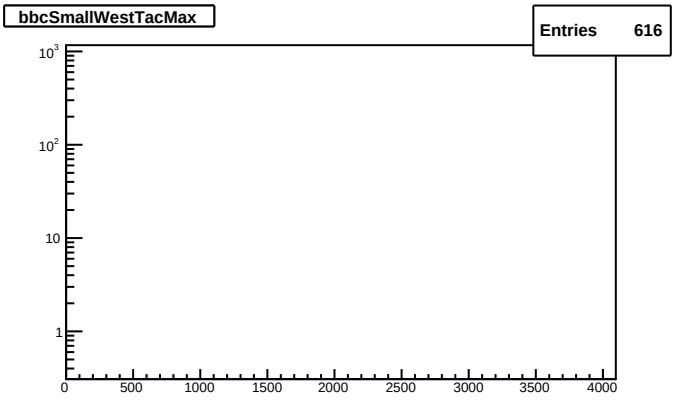

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

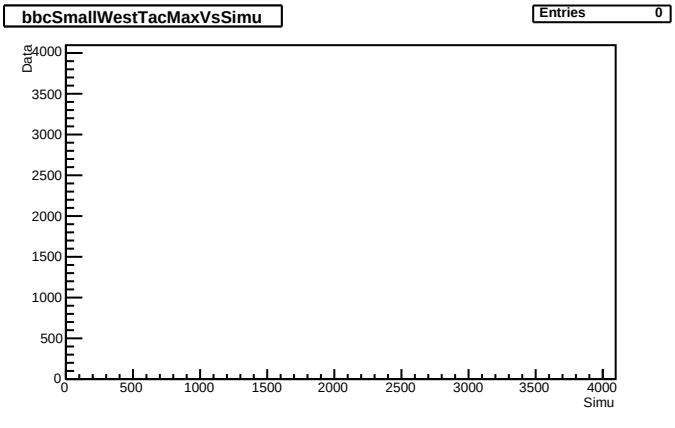

bbcSmallEastTacMax
Entries 616

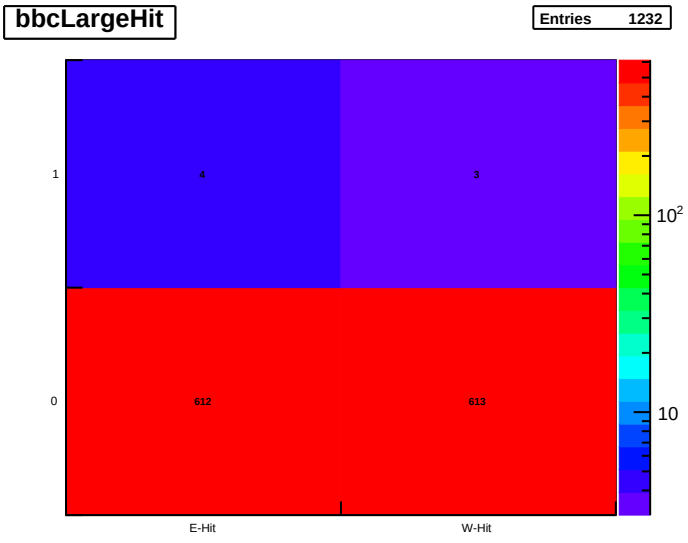

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

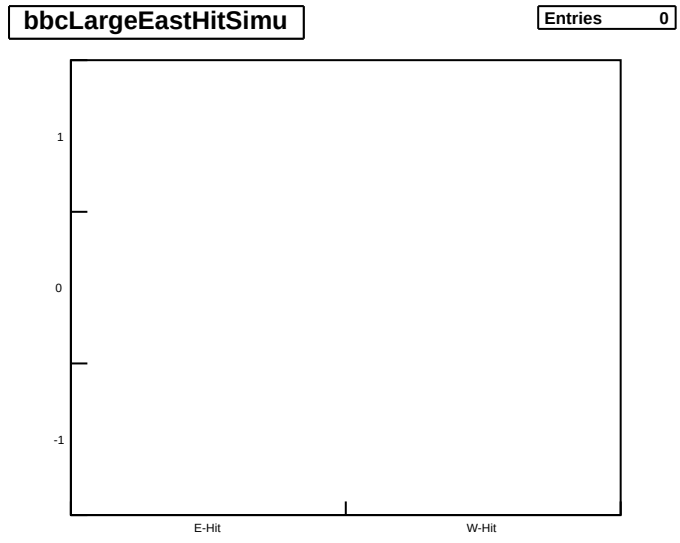

bbcSmallWestAdcSum
Entries 616

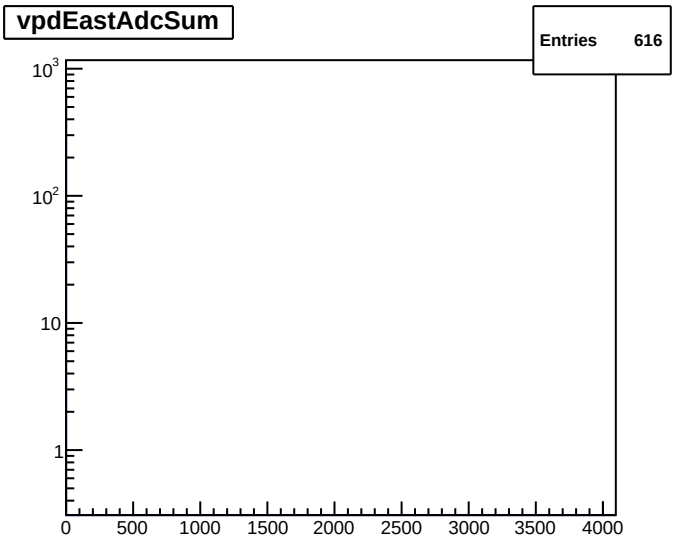

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

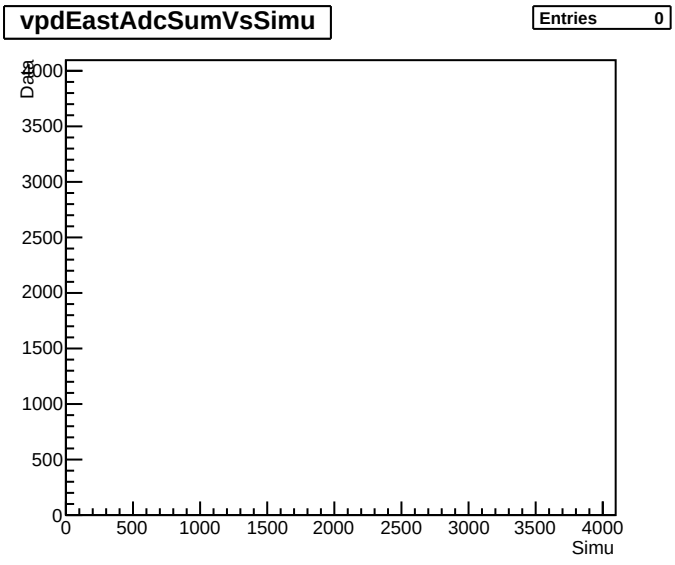

bbcSmallWestTacMax
Entries 616

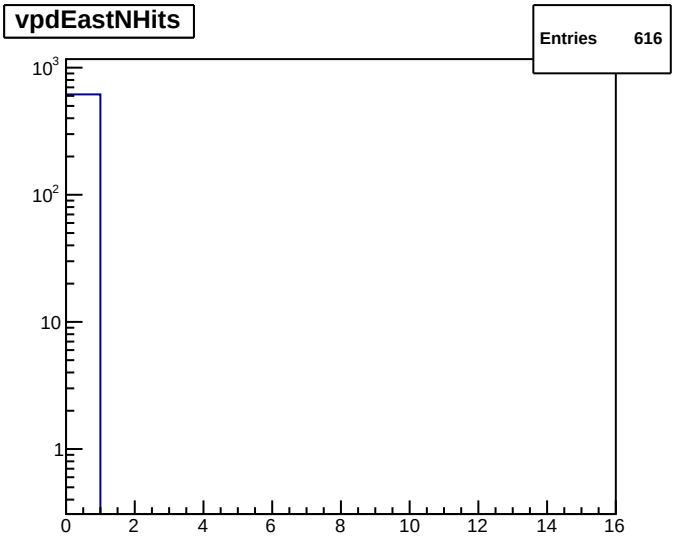

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

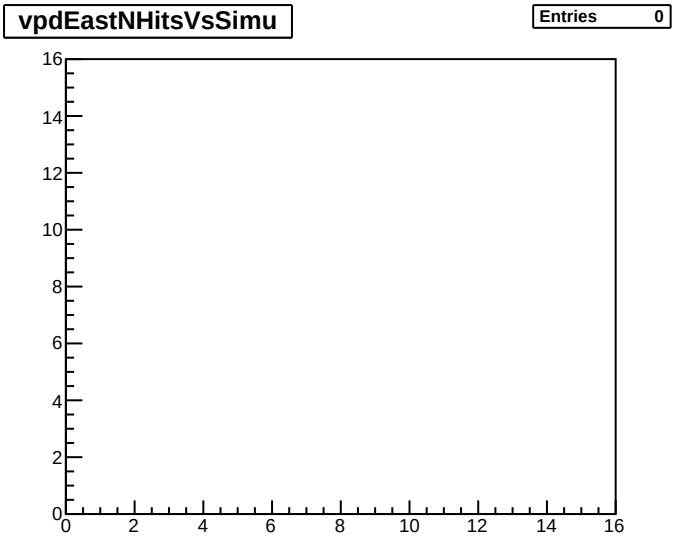

bbcLargeHit
Entries 1232
1
0
4
3
612
613
E-Hit
W-Hit
10^2
10

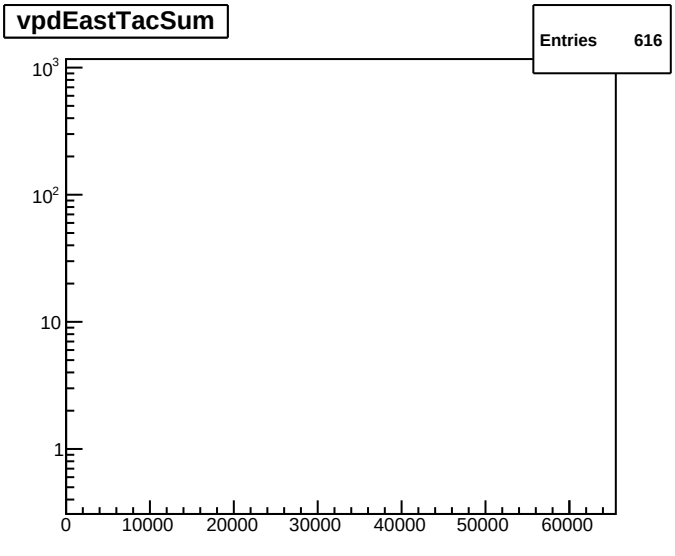

bbcLargeEastHitSimu
Entries 0
1
0
-1
E-Hit
W-Hit

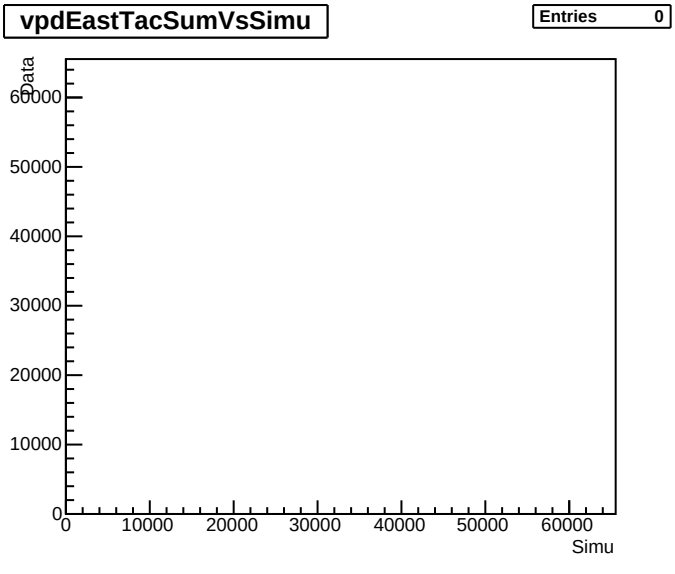

vpdEastAdcSum
Entries 616
10^3
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

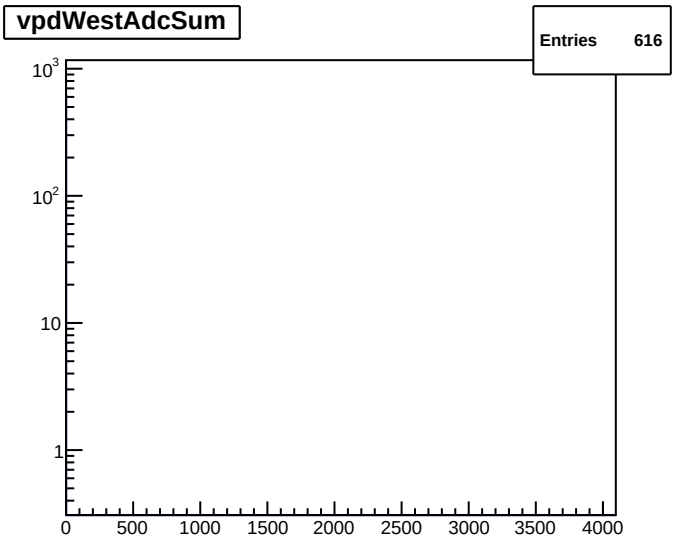

vpdEastAdcSumVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

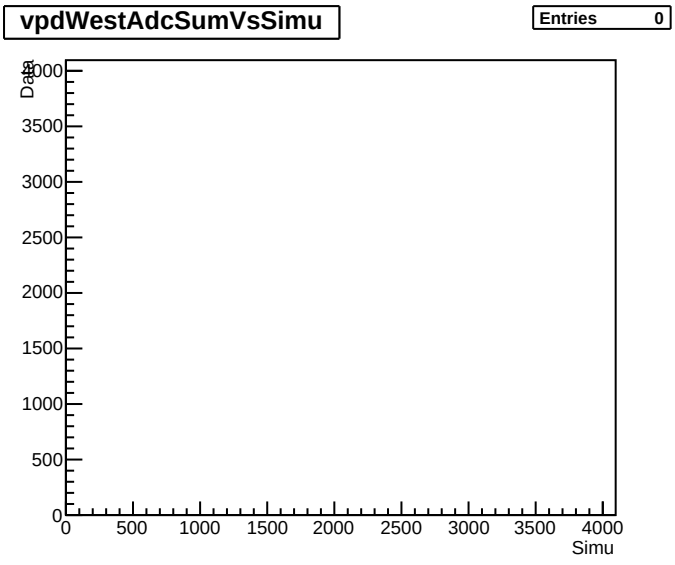

vpdEastNHits
Entries 616
10^3
10^2
10
1
0 2 4 6 8 10 12 14 16

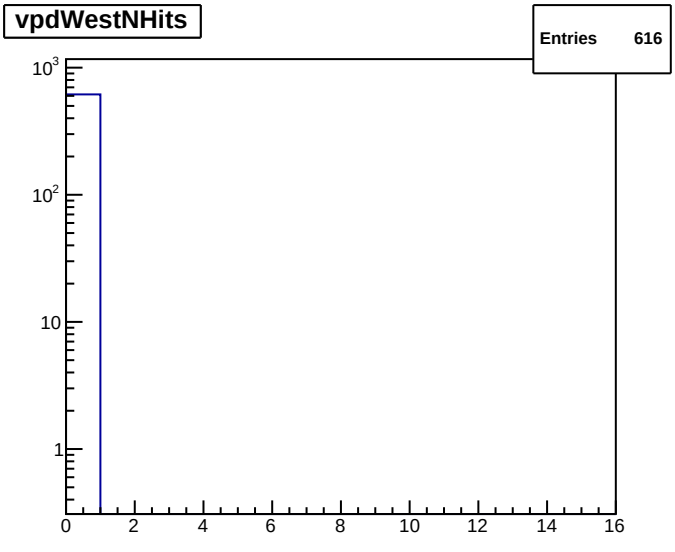

vpdEastNHitsVsSimu
Entries 0
16
14
12
10
8
6
4
2
0
0 2 4 6 8 10 12 14 16

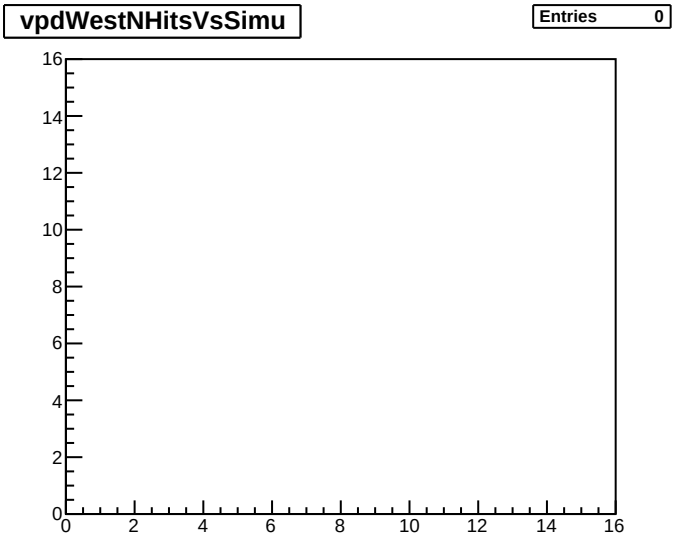

vpdEastTacSum
Entries 616
10^3
10^2
10
1
0 10000 20000 30000 40000 50000 60000

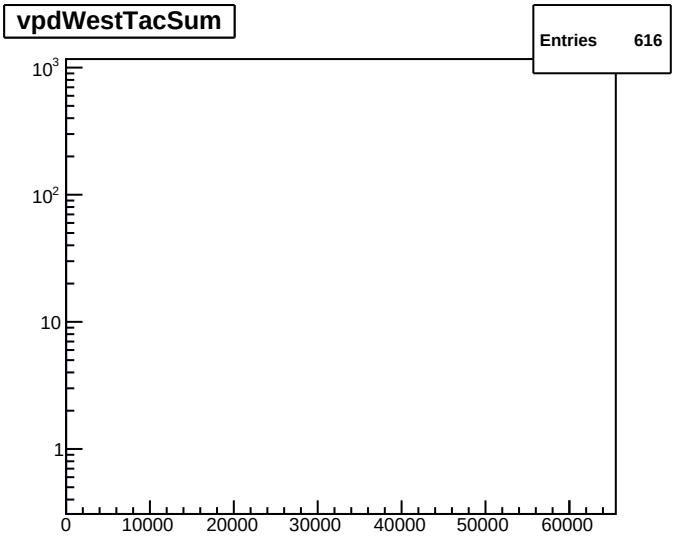

vpdEastTacSumVsSimu
Entries 0
Data
60000
50000
40000
30000
20000
10000
0
0 10000 20000 30000 40000 50000 60000
Simu

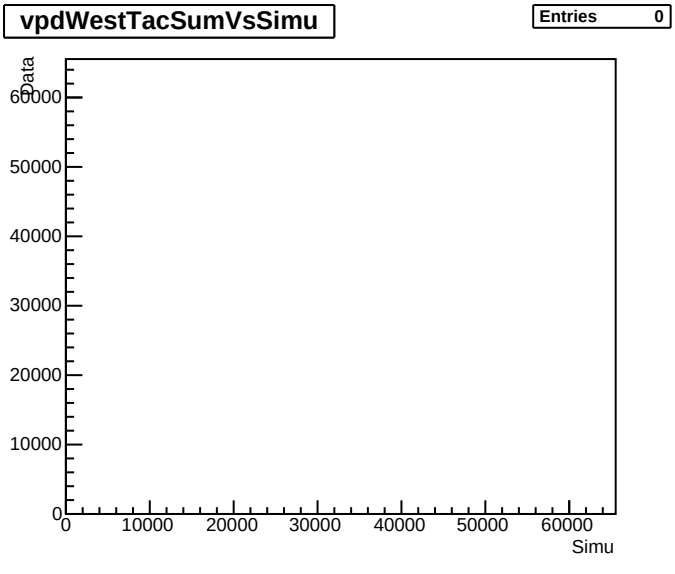

vpdWestAdcSum
Entries 616
10^3
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

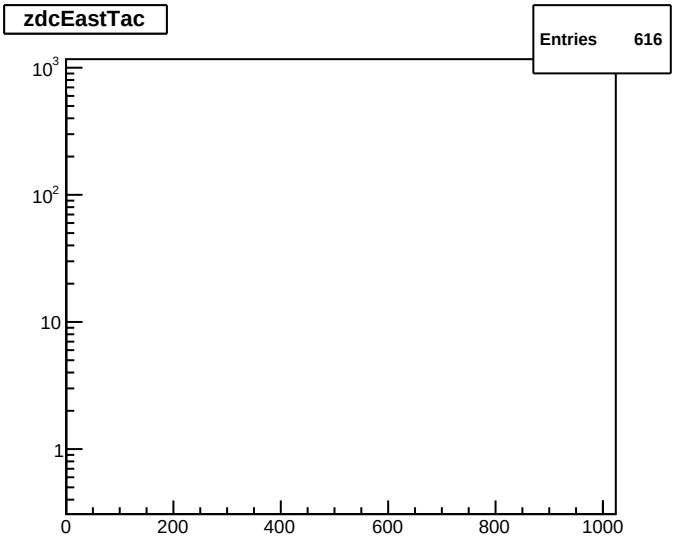

vpdWestAdcSumVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

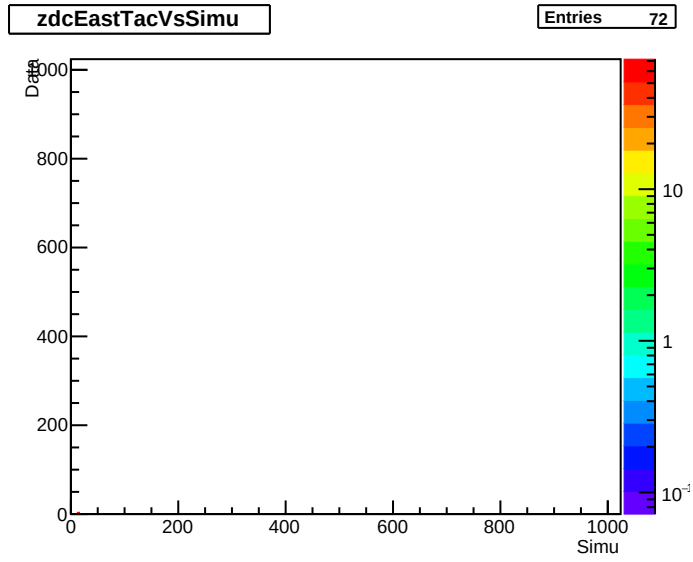

vpdWestNHits
Entries 616
10^3
10^2
10
1
0 2 4 6 8 10 12 14 16

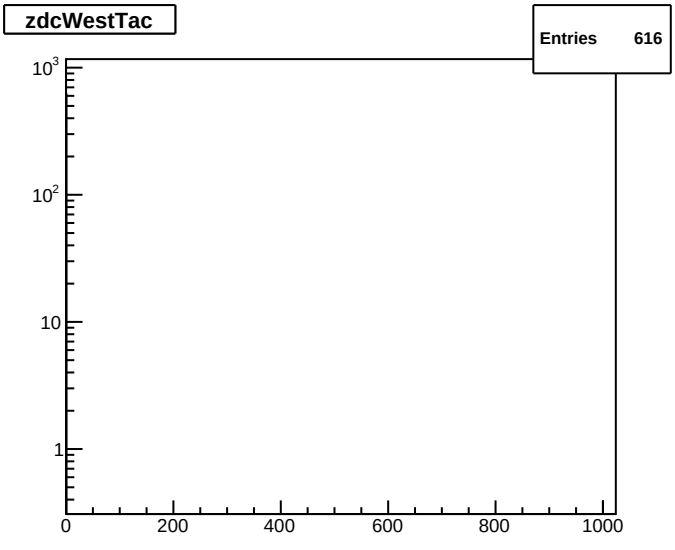

vpdWestNHitsVsSimu
Entries 0
16
14
12
10
8
6
4
2
0
0 2 4 6 8 10 12 14 16

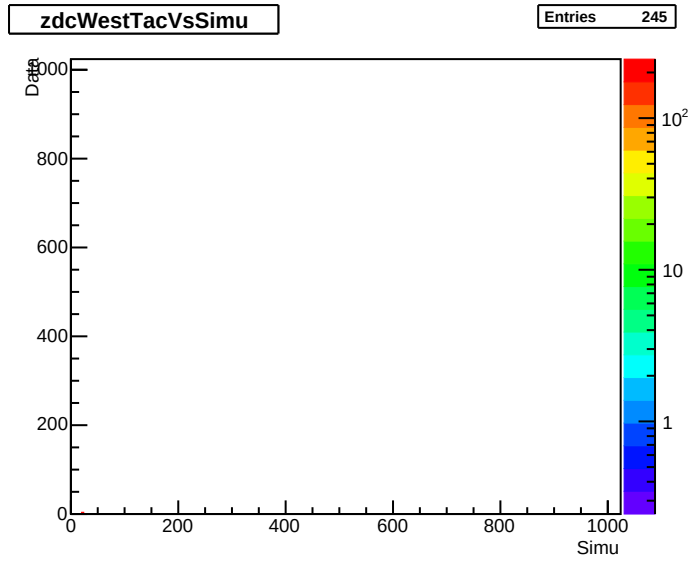

vpdWestTacSum
Entries 616
10^3
10^2
10
1
0 10000 20000 30000 40000 50000 60000

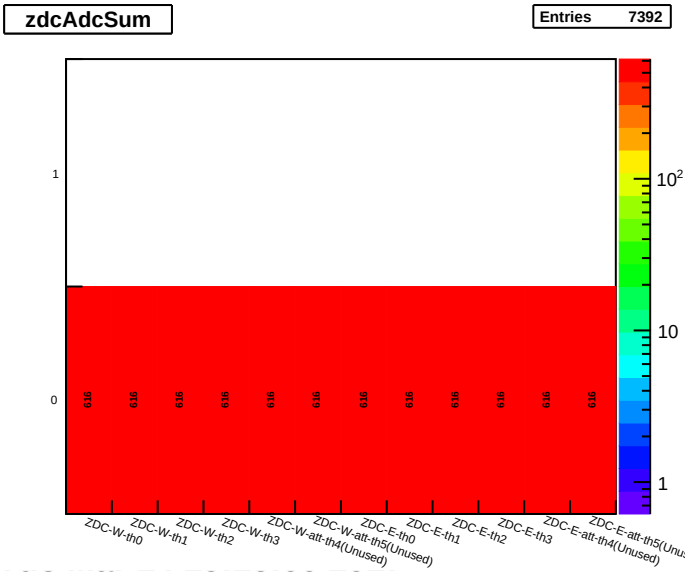

vpdWestTacSumVsSimu
Entries 0
Data
60000
50000
40000
30000
20000
10000
0
0 10000 20000 30000 40000 50000 60000
Simu

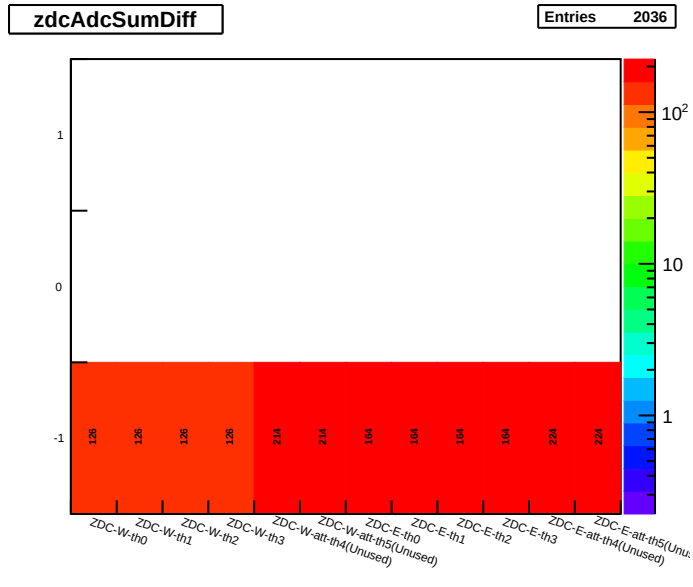

zdcEastTac
Entries 616

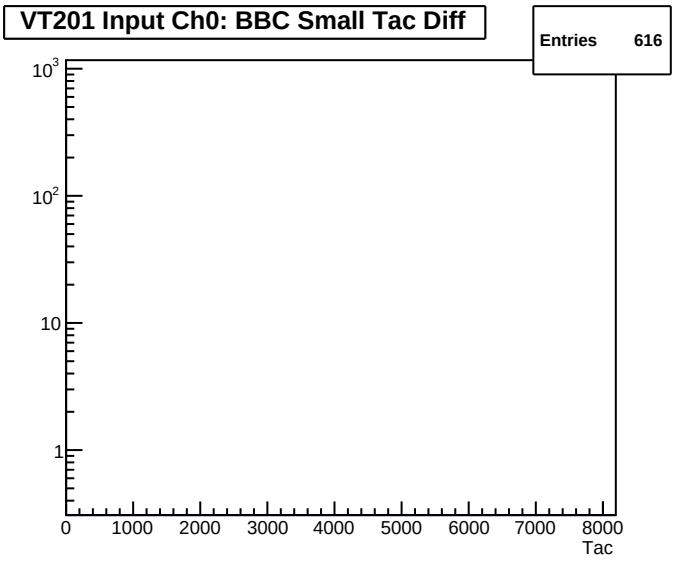

zdcEastTacVsSimu
Entries 72
Data
Simu

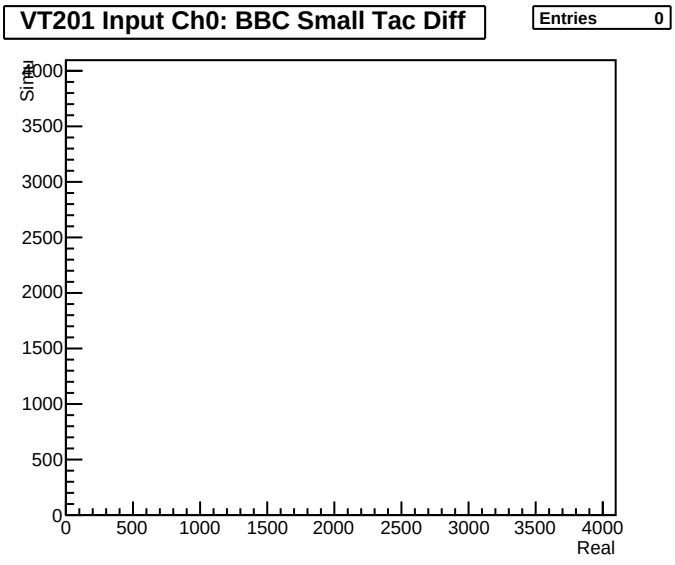

zdcWestTac
Entries 616

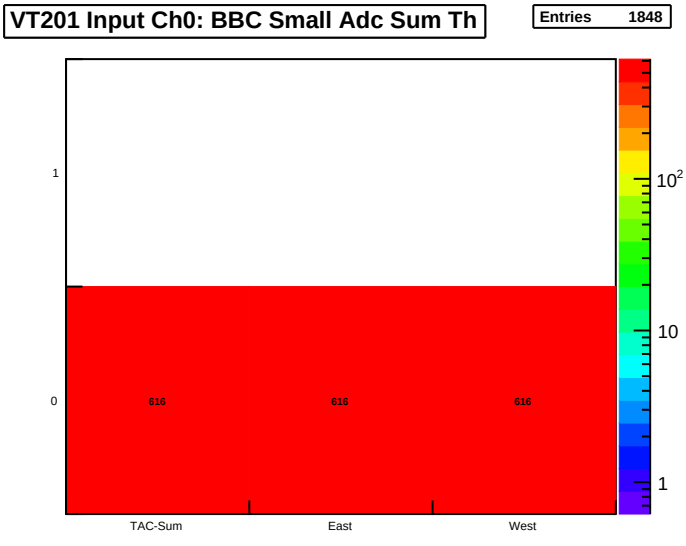

zdcWestTacVsSimu
Entries 245
Data
Simu

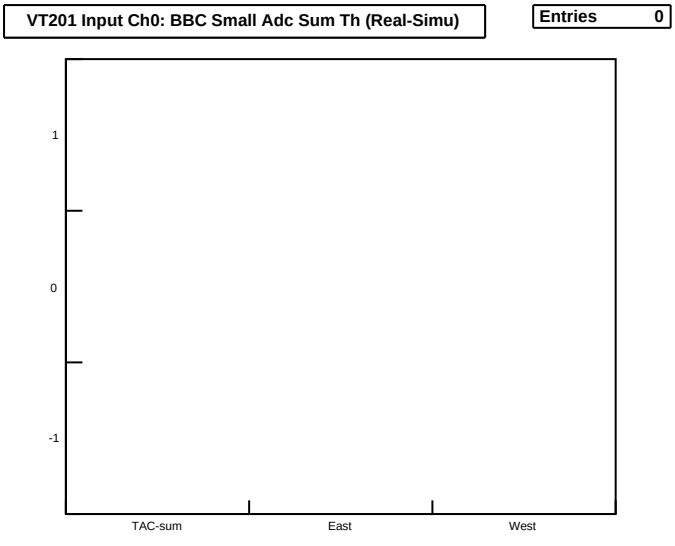

zdcAdcSum
Entries 7392
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

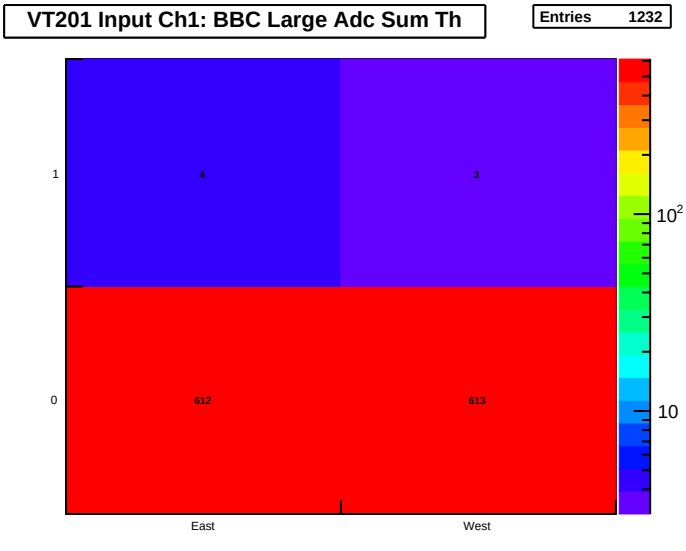

zdcAdcSumDiff
Entries 2036
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

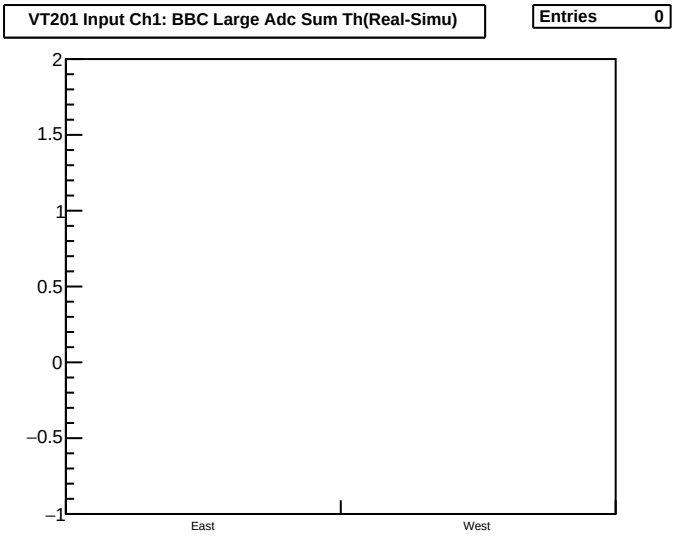

VT201 Input Ch0: BBC Small Tac Diff
Entries 616
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

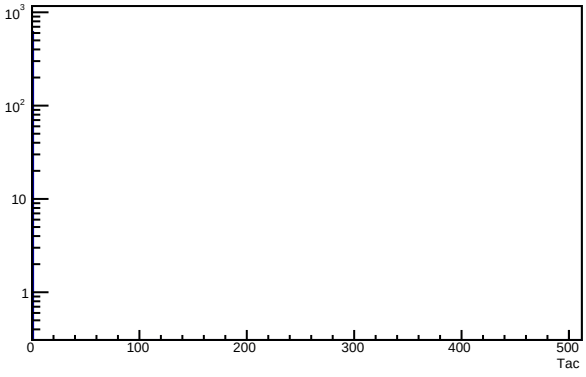

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 1848
616
616
616
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

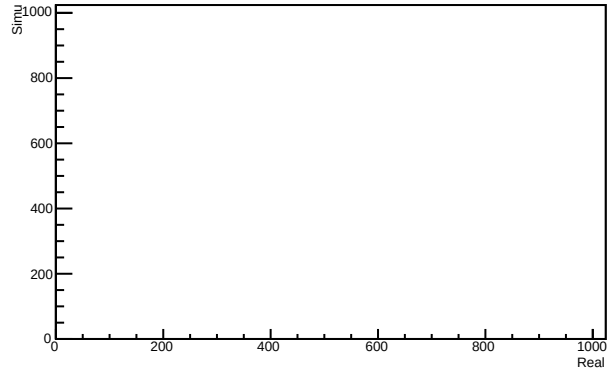

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 1232
4
3
612
613
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

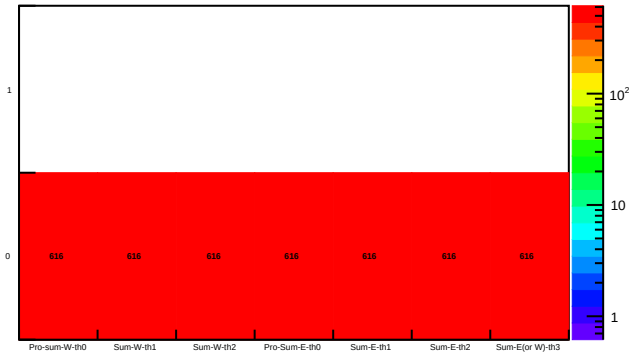
VT201 Input Ch2: ZDC Tac Diff



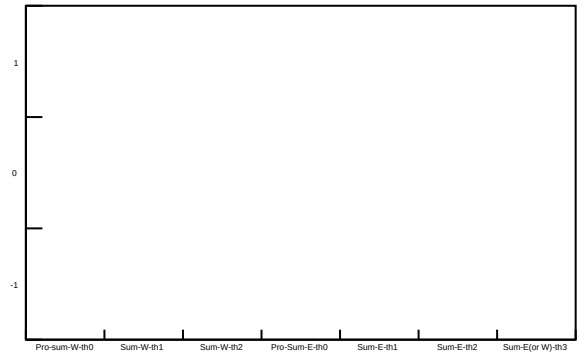
VT201 Input Ch2: ZDC Tac Diff



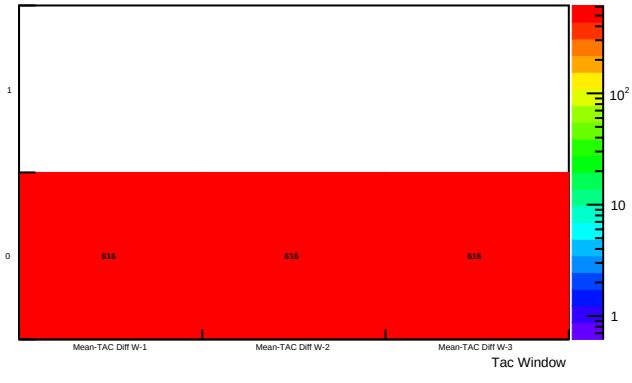
VT201 Input Ch2: ZDC Adc Sum Th



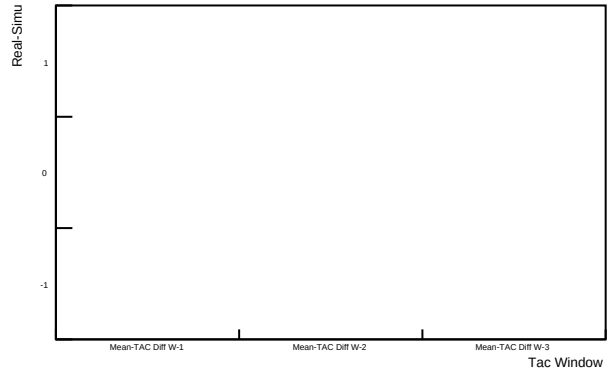
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



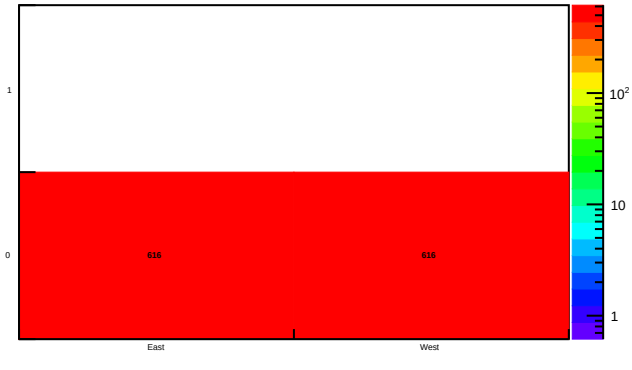
VT201 Input Ch4: VPD Tac Diff



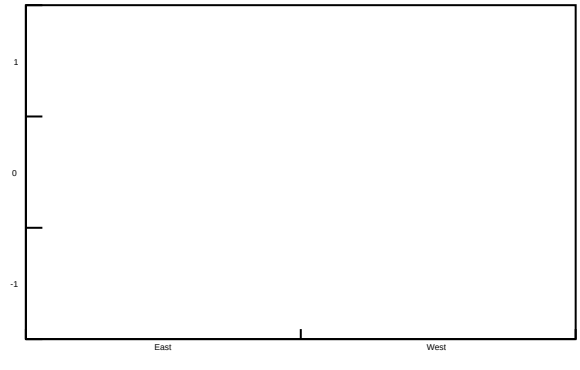
VT201 Input Ch4: VPD Tac Diff

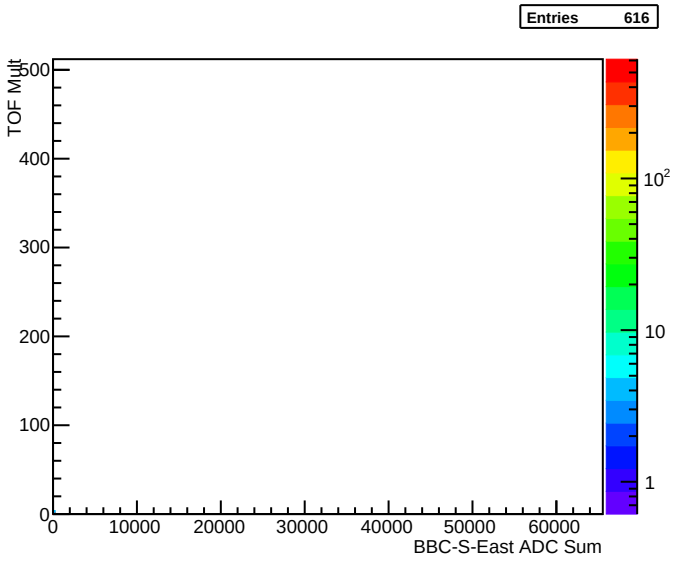


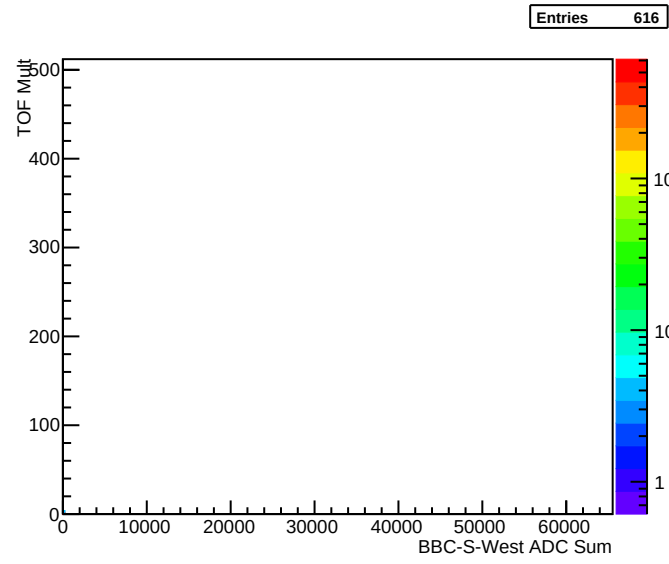
VT201 Input Ch4: VPD Adc Sum Th

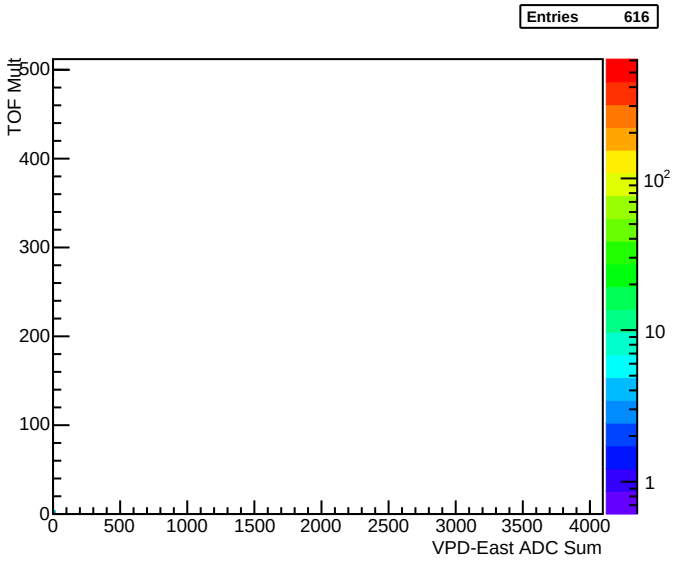


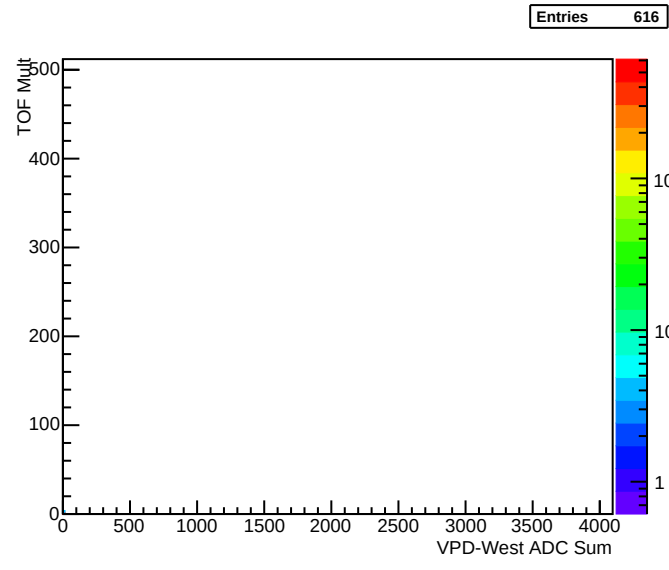
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

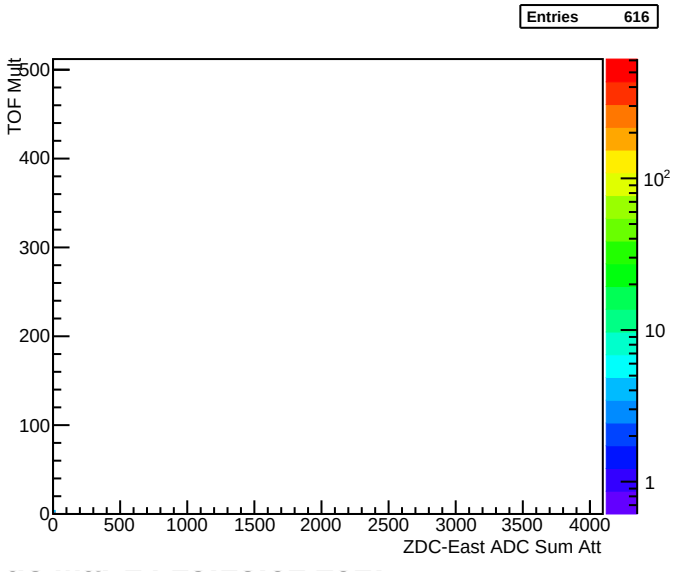


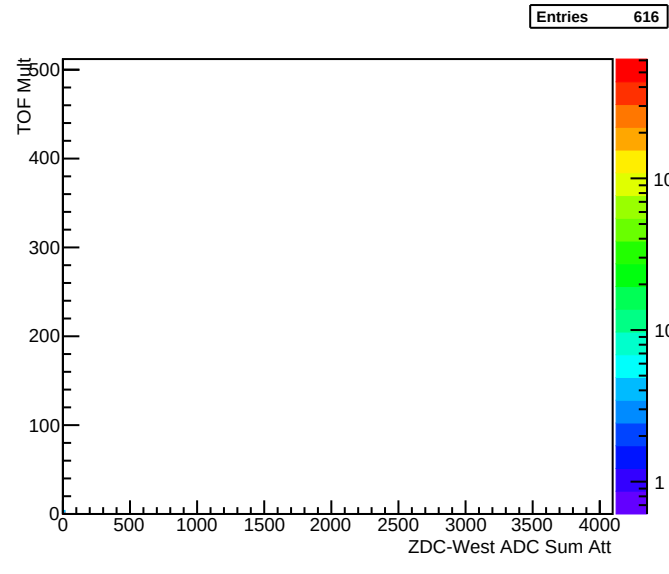

Entries 616
TOF Mult
BBC-S-East ADC Sum

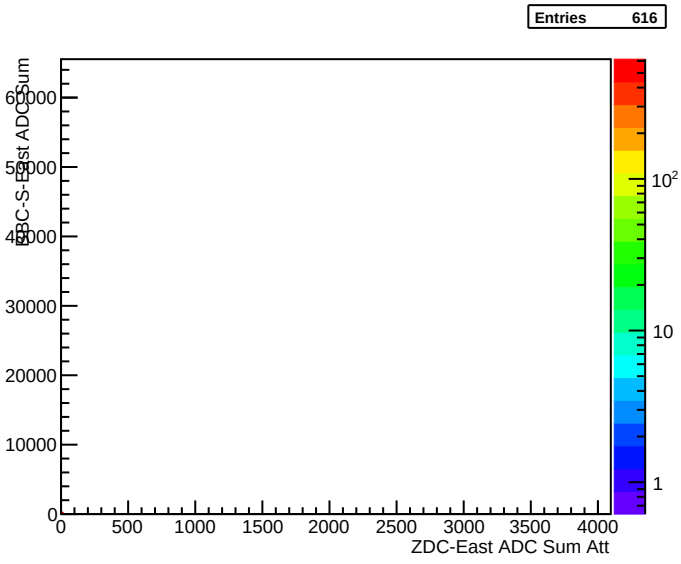

Entries 616
TOF Mult
BBC-S-West ADC Sum

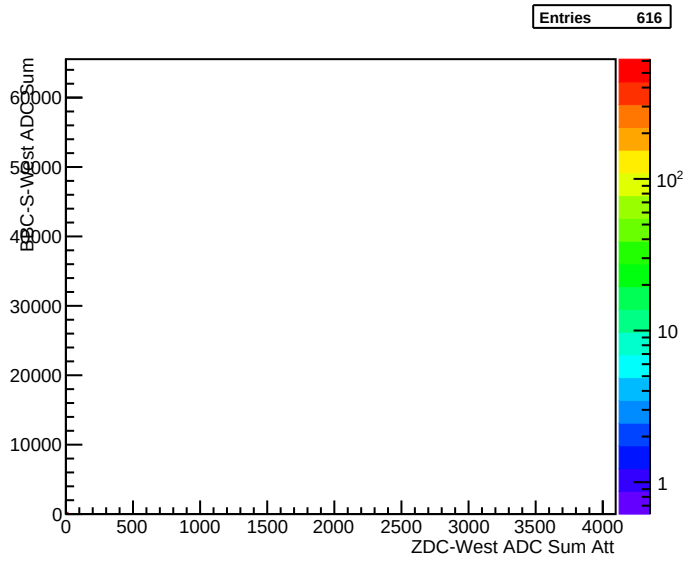

Entries 616
TOF Mult
VPD-East ADC Sum

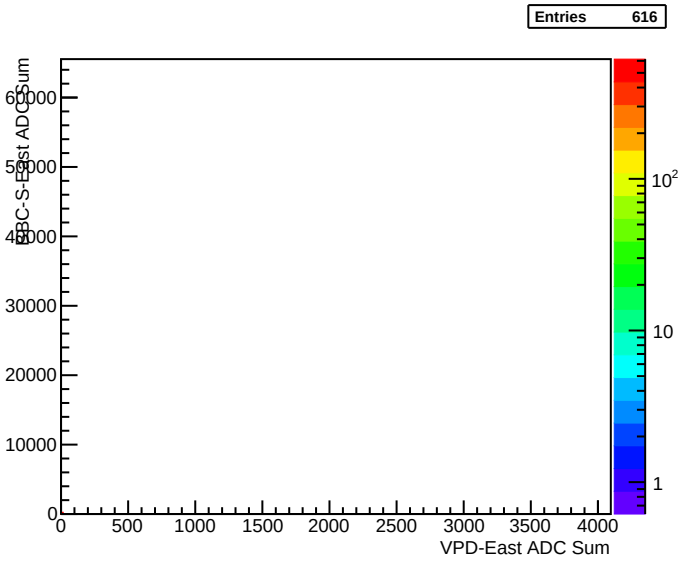

Entries 616
TOF Mult
VPD-West ADC Sum

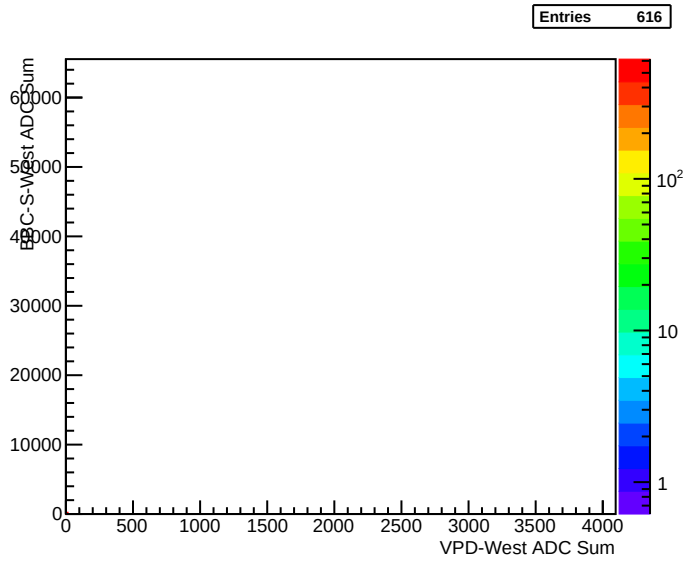

Entries 616
TOF Mult
ZDC-East ADC Sum Att

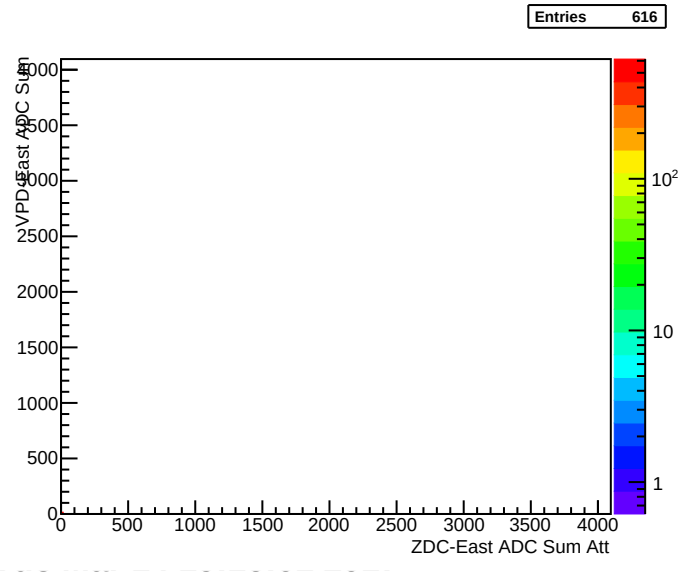

Entries 616
TOF Mult
ZDC-West ADC Sum Att

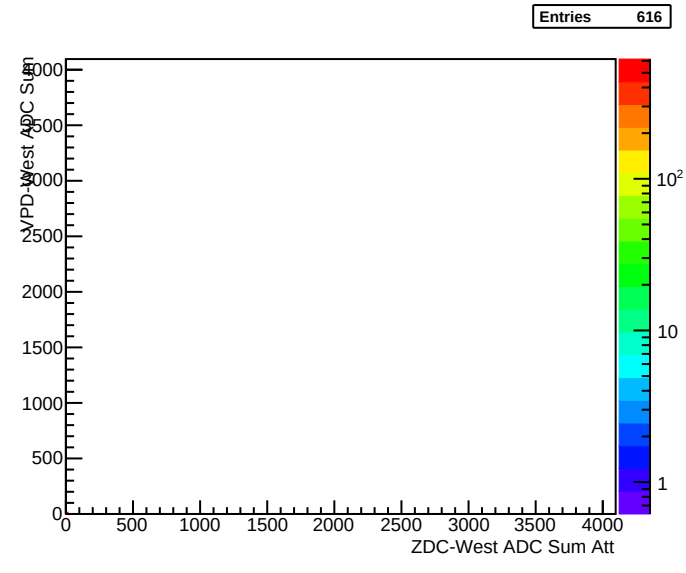

Entries 616
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

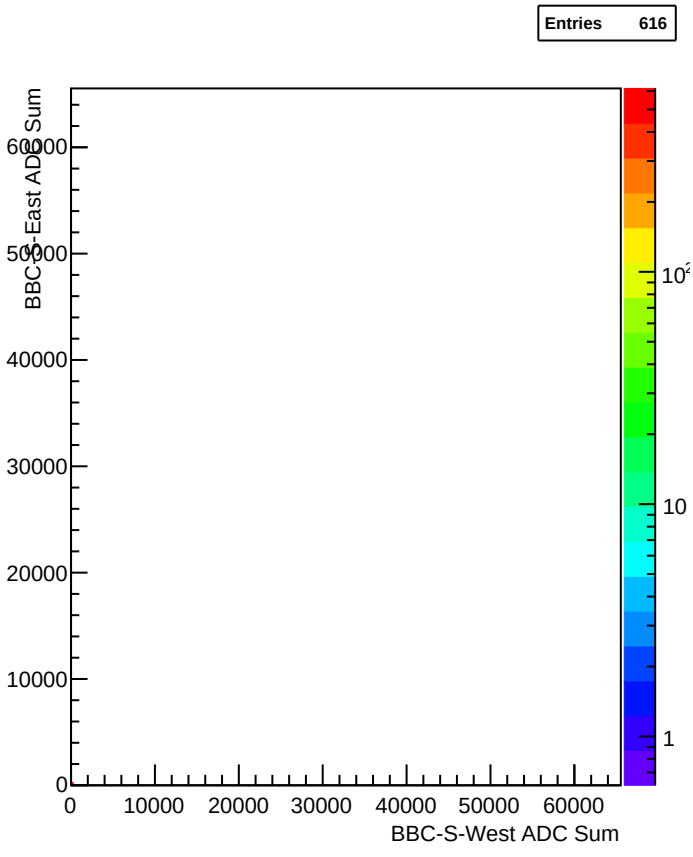

Entries 616
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

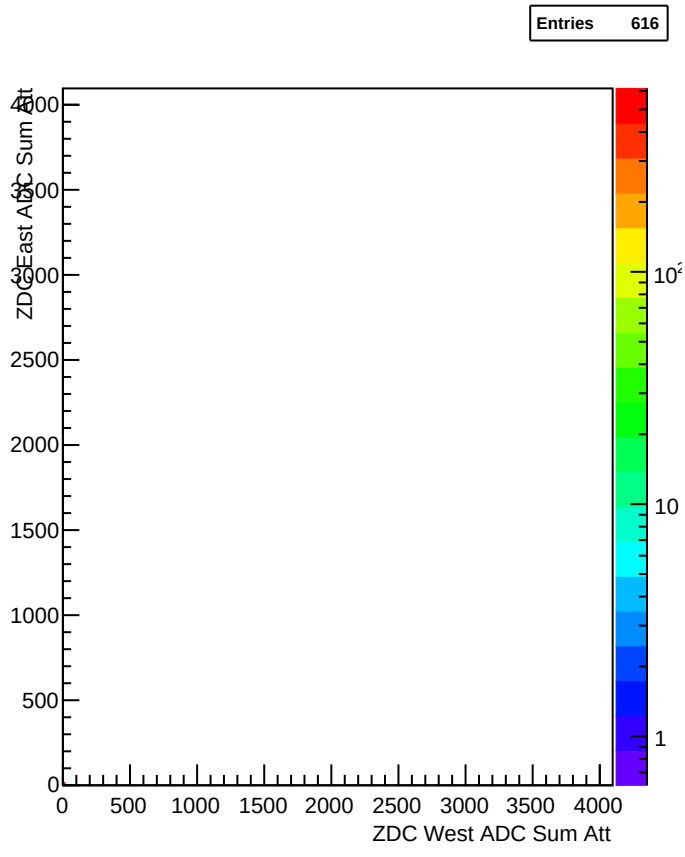

Entries 616
BBC-S-East ADC Sum
VPD-East ADC Sum

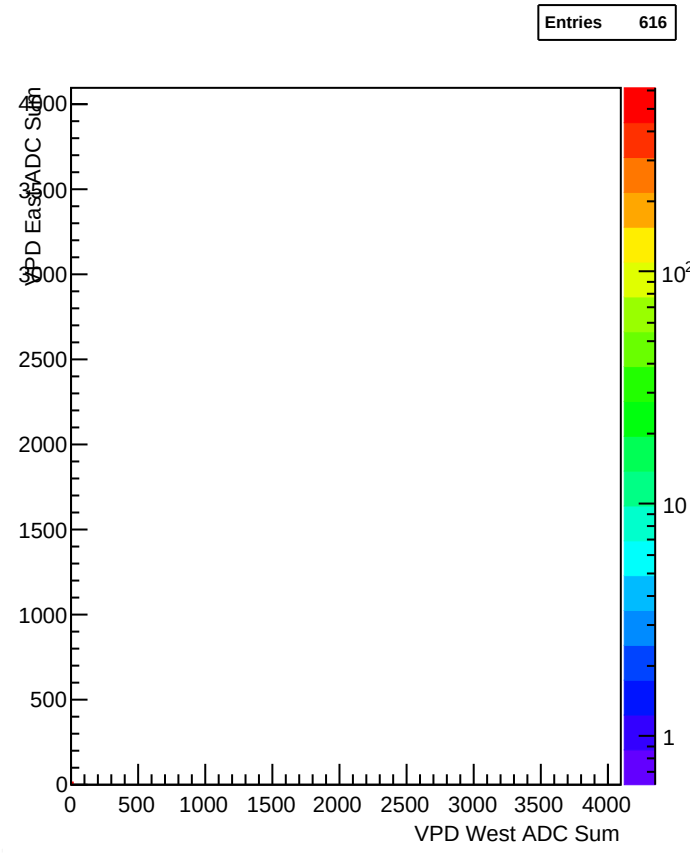

Entries 616
BBC-S-West ADC Sum
VPD-West ADC Sum

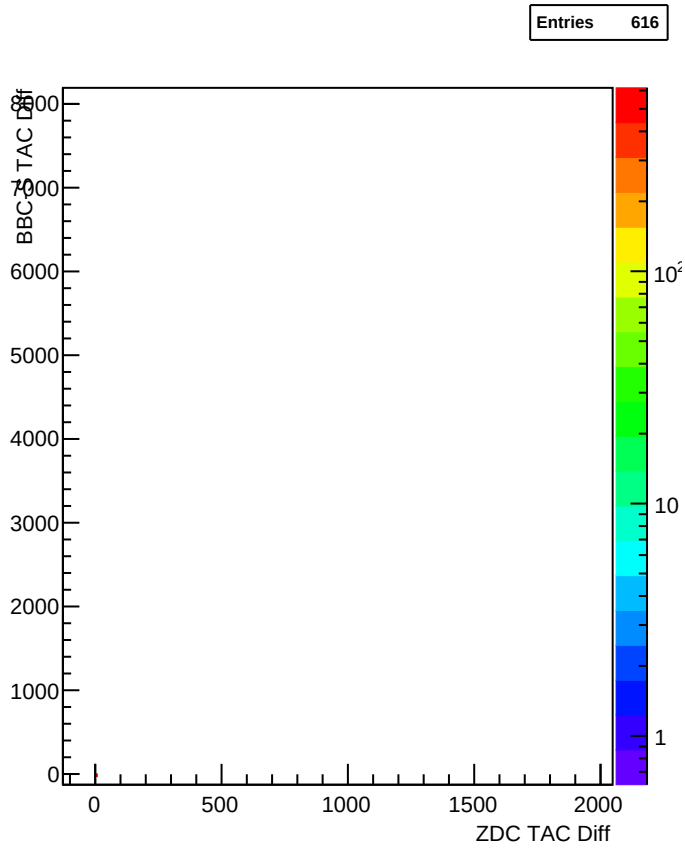

Entries 616
VPD-East ADC Sum
ZDC-East ADC Sum Att

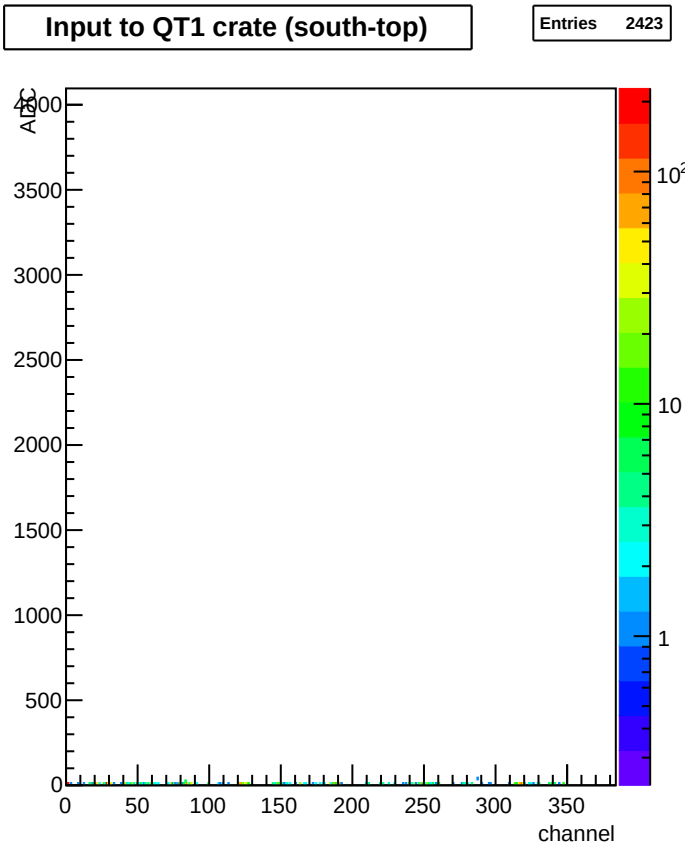

Entries 616
VPD-West ADC Sum
ZDC-West ADC Sum Att

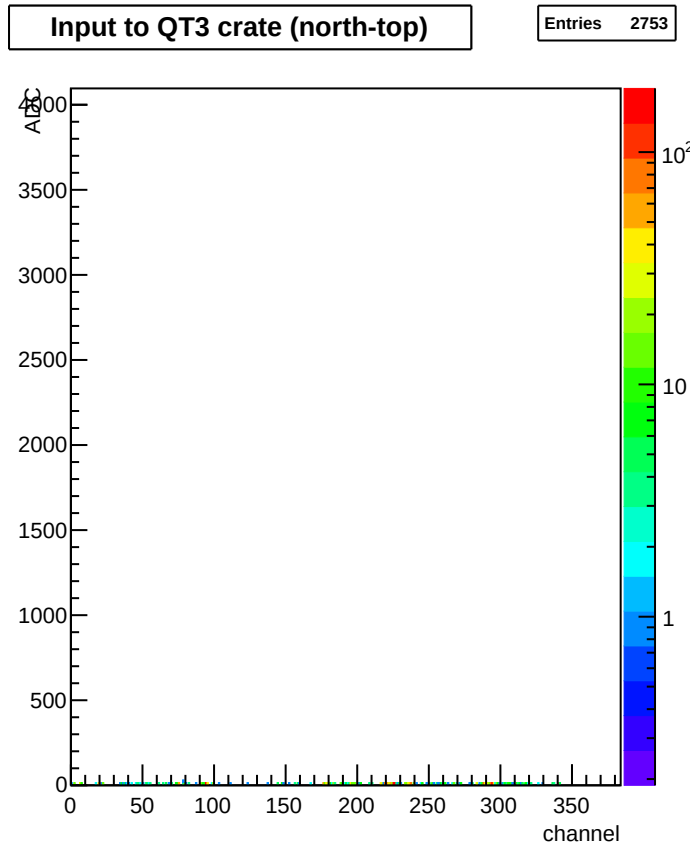

Entries 616
BBC-S-East ADC Sum
BBC-S-West ADC Sum
0
10000
20000
30000
40000
50000
60000
1
10
10²

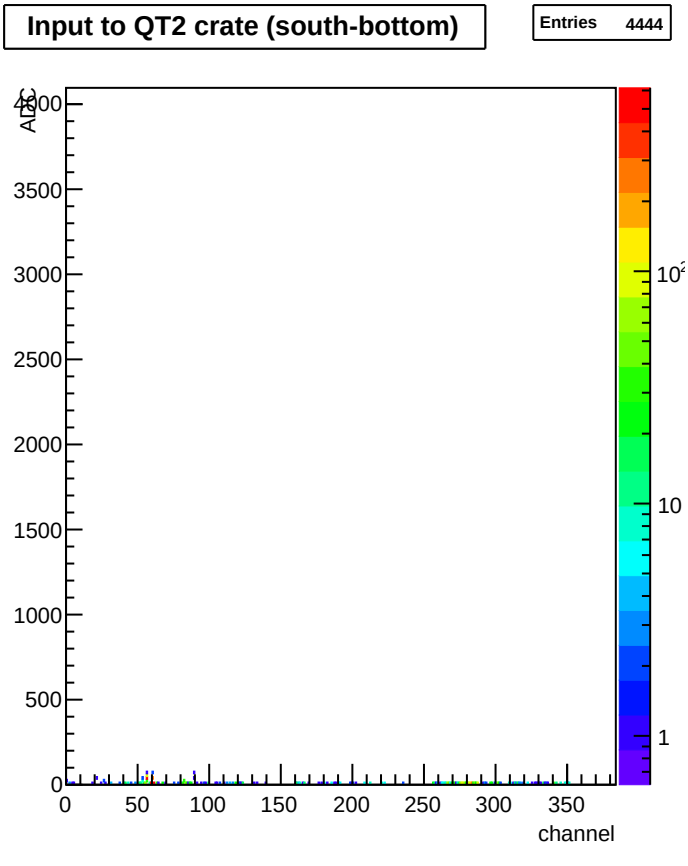

Entries 616
ZDC East ADC Sum Att
ZDC West ADC Sum Att
0
500
1000
1500
2000
2500
3000
3500
4000
1
10
10²

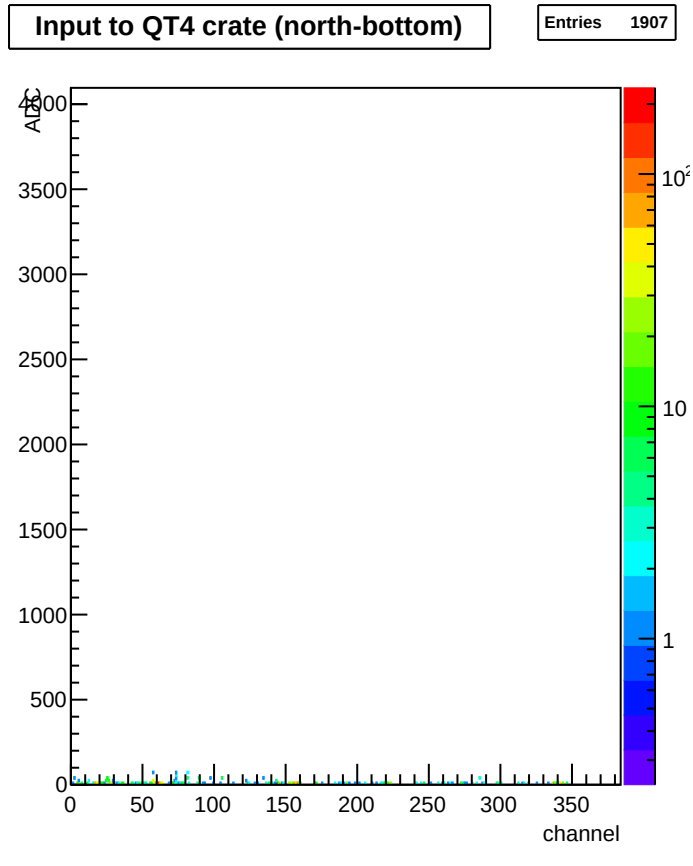

Entries 616
VPD East ADC Sum
VPD West ADC Sum
0
500
1000
1500
2000
2500
3000
3500
4000
1
10
10²

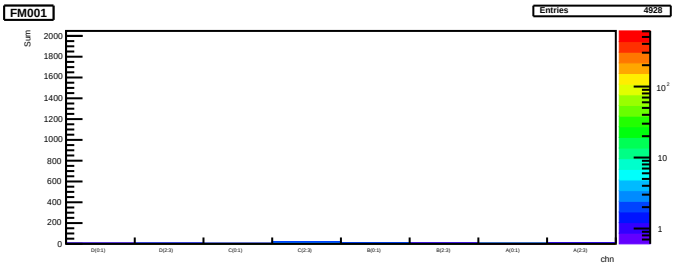

Entries 616
BBC-S TAC Diff
ZDC TAC Diff
0
1000
2000
3000
4000
5000
6000
7000
8000
1
10
10²

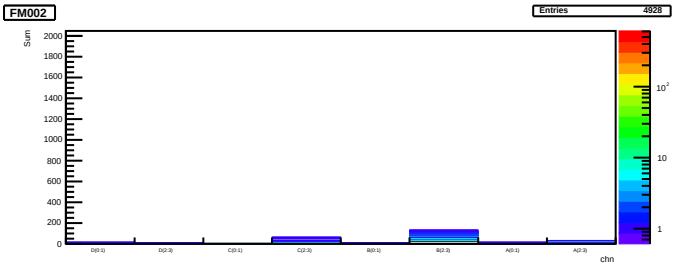

Input to QT1 crate (south-top)
Entries 2423
ADC
channel

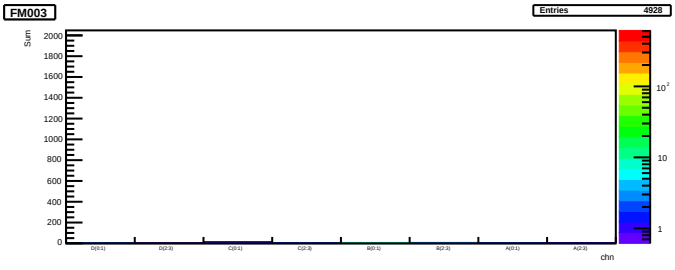

Input to QT3 crate (north-top)
Entries 2753
ADC
channel

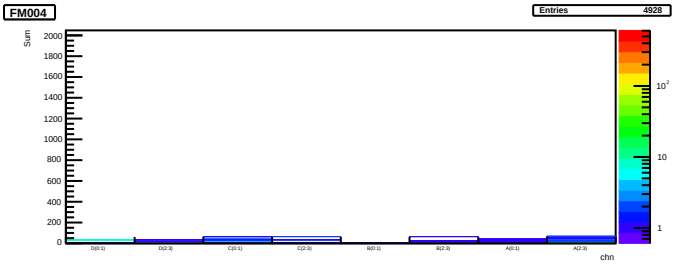

Input to QT2 crate (south-bottom)
Entries 4444
ADC
channel

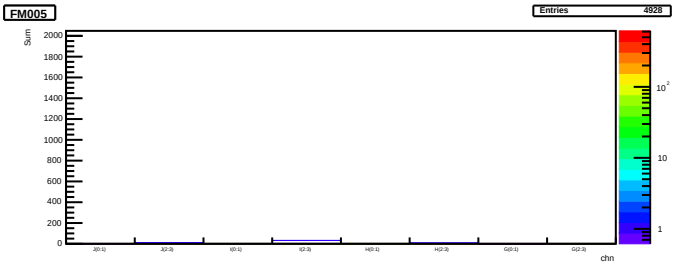

Input to QT4 crate (north-bottom)
Entries 1907
ADC
channel

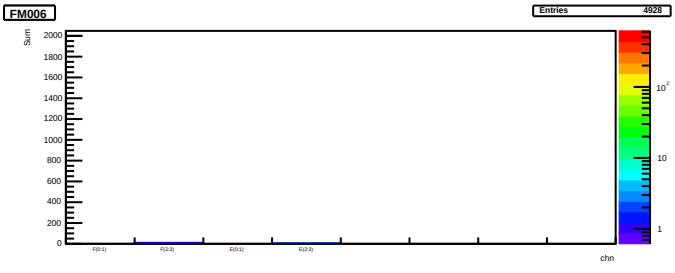

FM001
Entries 4928
Sum
chn

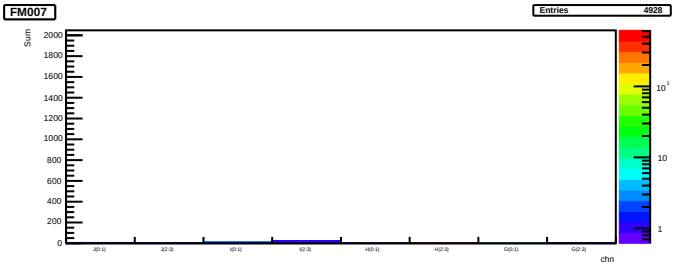

FM002
Entries 4928
Sum
chn

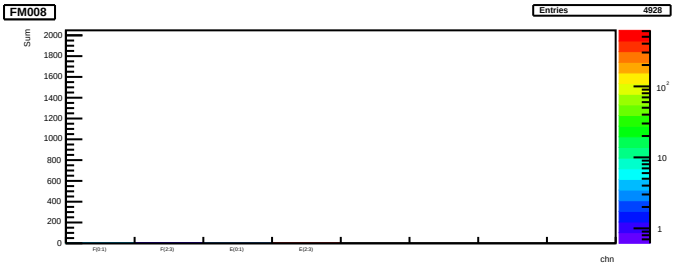

FM003
Entries 4928
Sum
chn

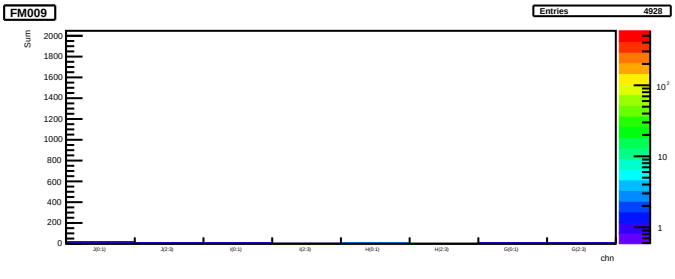

FM004
Entries 4928
Sum
chn

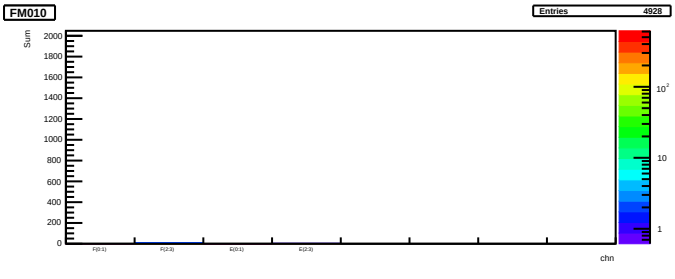

FM005
Entries 4928
Sum
chn

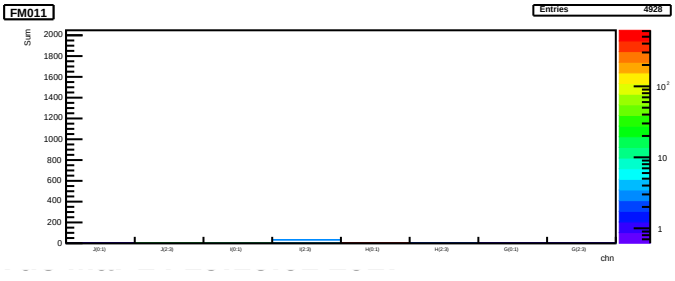

FM006
Entries 4928
Sum
chn

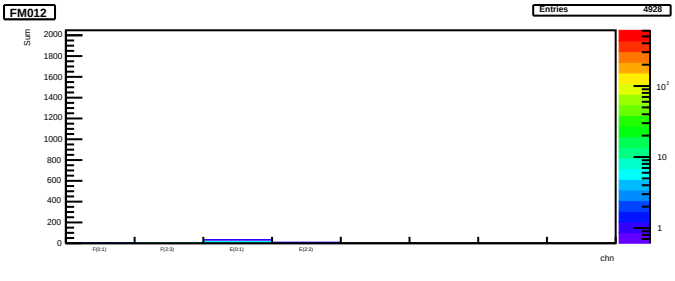

FM007
Entries 4928
Sum
chn

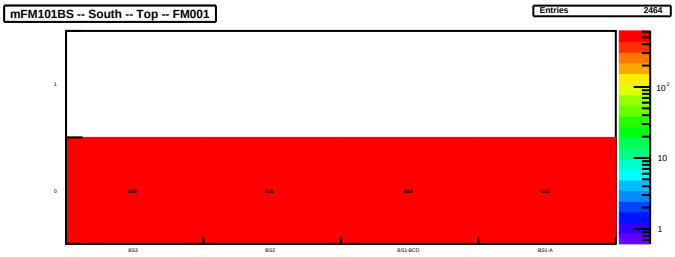

FM008
Entries 4928
Sum
chn

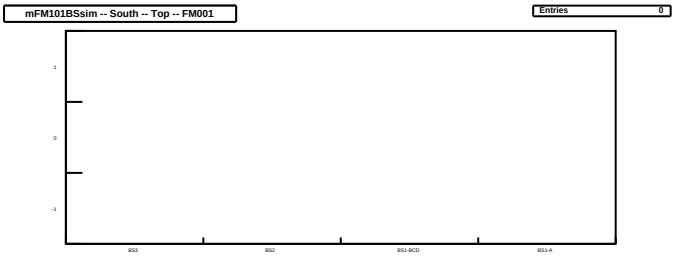

FM009
Entries 4928
Sum
chn

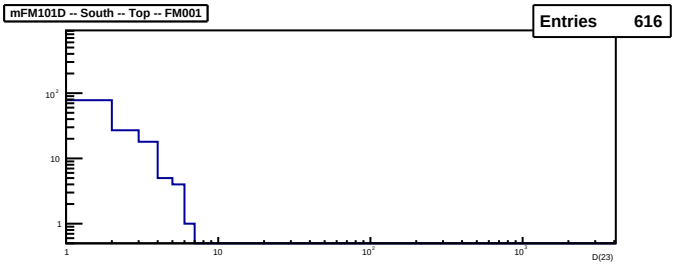

FM010
Entries 4928
Sum
chn

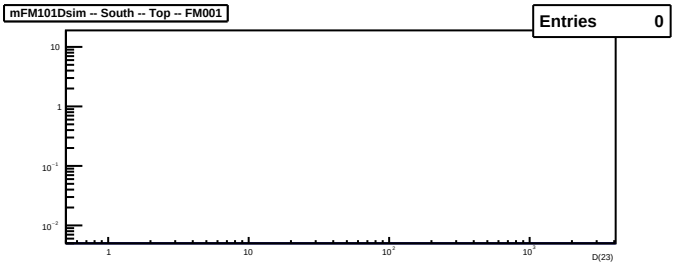

FM011
Entries 4928
Sum
chn

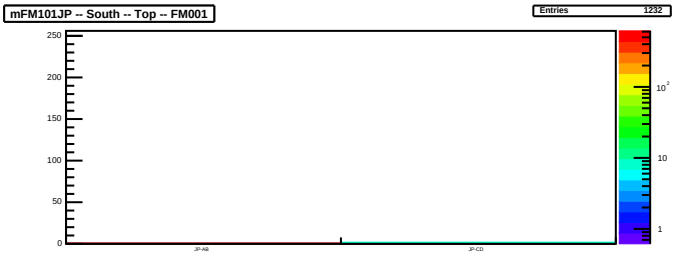

FM012
Entries 4928
Sum
chn


mFM101BS -- South -- Top -- FM001
Entries 2464
616
616
616
616
BS3
BS2
BS1-BCD
BS1-A

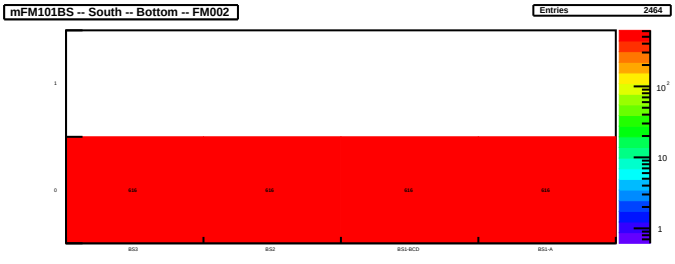

mFM101BSsim -- South -- Top -- FM001
Entries 0
BS3
BS2
BS1-BCD
BS1-A

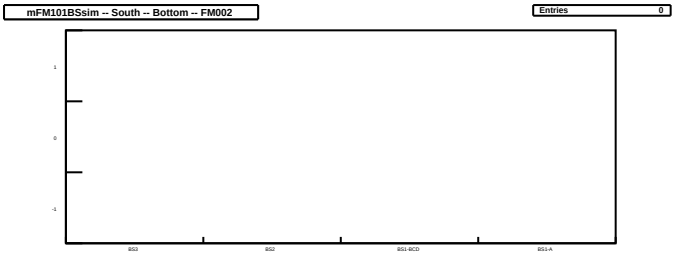

mFM101D -- South -- Top -- FM001
Entries 616
D(23)

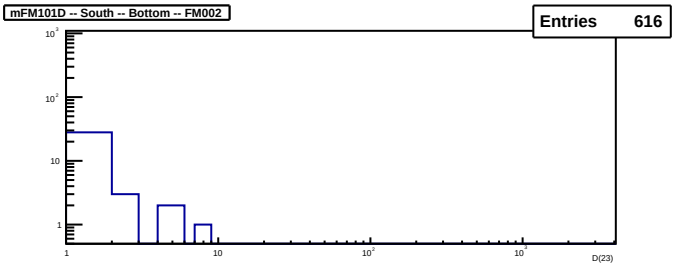

mFM101Dsim -- South -- Top -- FM001
Entries 0
D(23)

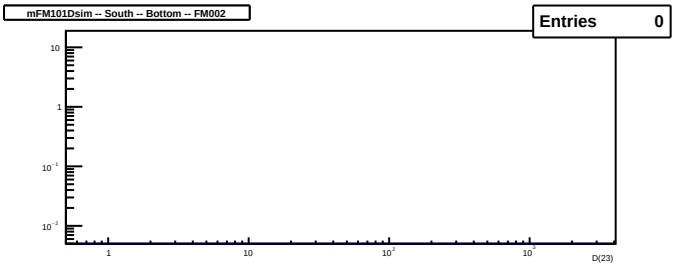

mFM101JP -- South -- Top -- FM001
Entries 1232
JP-AB
JP-CD

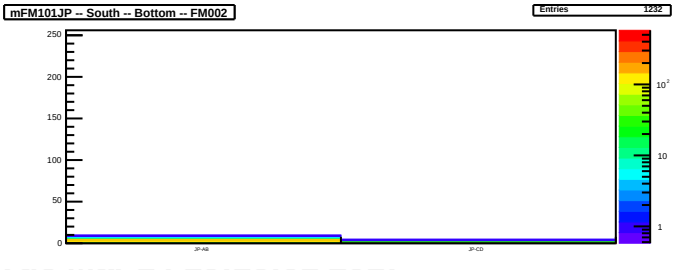
mFM101JPsim -- South -- Top -- FM001
Entries 0
JP-AB
JP-CD

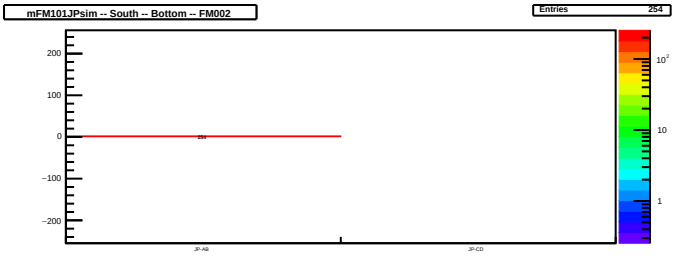

mFM101BS -- South -- Bottom -- FM002
Entries 2464
616
616
616
616
BS3
BS2
BS1-BCD
BS1-A

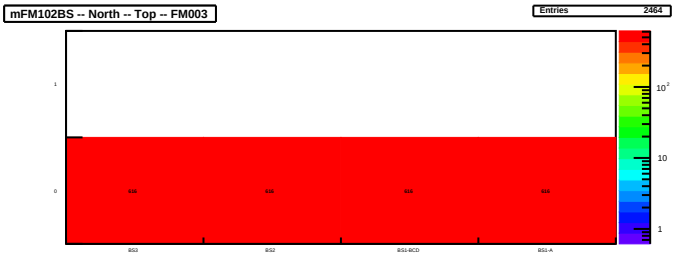

mFM101BSsim -- South -- Bottom -- FM002
Entries 0
BS3
BS2
BS1-BCD
BS1-A


mFM101D -- South -- Bottom -- FM002
Entries 616
D(23)

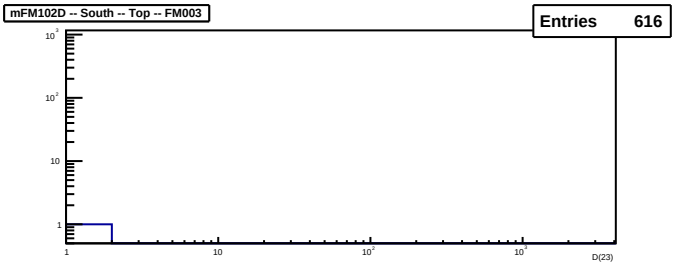

mFM101Dsim -- South -- Bottom -- FM002
Entries 0
D(23)

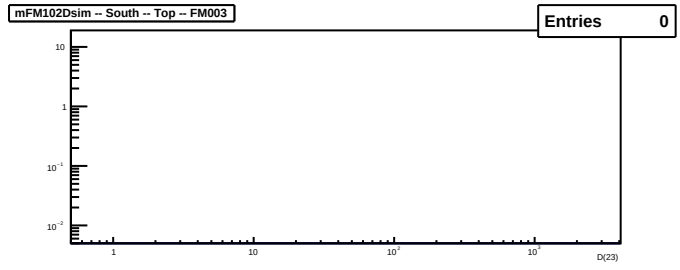

mFM101JP -- South -- Bottom -- FM002
Entries 1232
JP-AB
JP-CD


mFM101JPsim -- South -- Bottom -- FM002
Entries 254
254
JP-AB
JP-CD


mFM102BS -- North -- Top -- FM003
Entries 2464
616
616
616
616
BS3
BS2
BS1-BCD
BS1-A

mFM102BSsim -- North -- Top -- FM003
Entries 0
BS3
BS2
BS1-BCD
BS1-A


mFM102D -- South -- Top -- FM003
Entries 616
D(23)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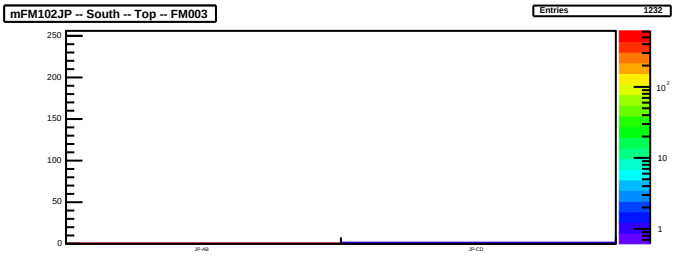
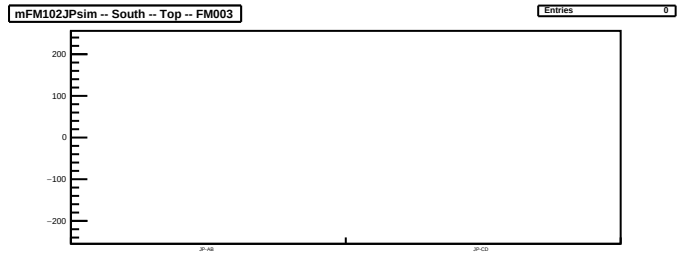
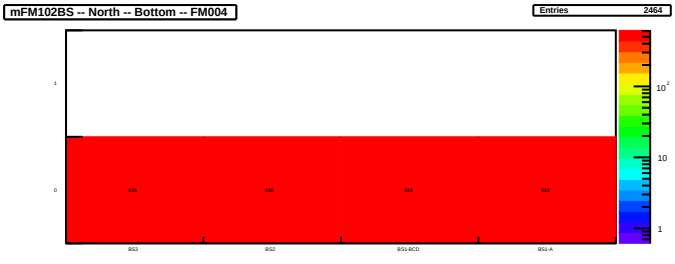
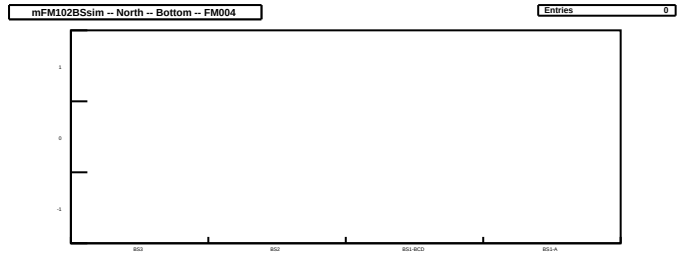
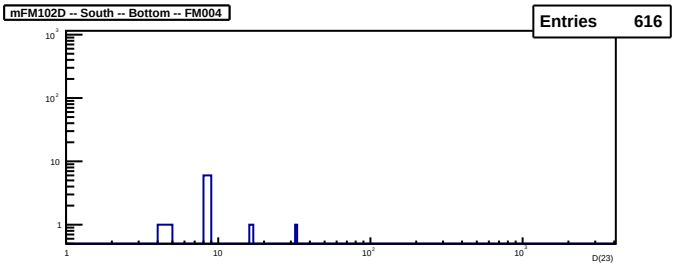
Entries 0
D(23)

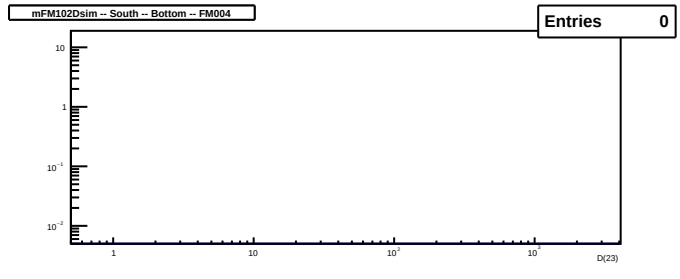

mFM102JP -- South -- Top -- FM003
Entries 1232
JP-AB
JP-CD

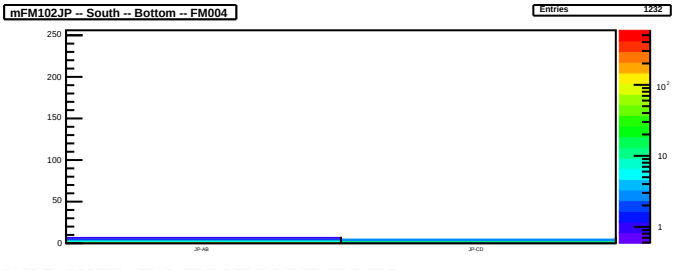

mFM102JPsim -- South -- Top -- FM003
Entries 0
JP-AB
JP-CD

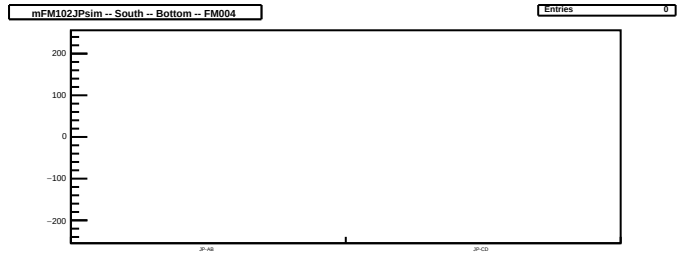

mFM102BS -- North -- Bottom -- FM004
Entries 2464
616
616
616
616
BS3
BS2
BS1-BCD
BS1-A

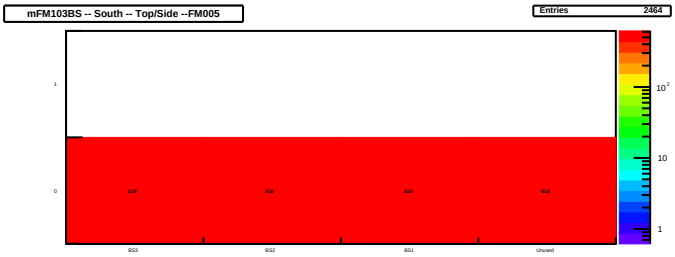

mFM102BSsim -- North -- Bottom -- FM004
Entries 0
BS3
BS2
BS1-BCD
BS1-A

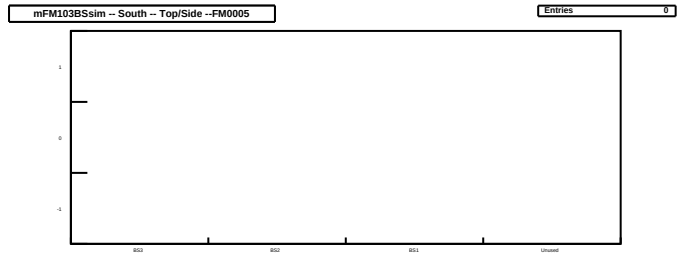

mFM102D -- South -- Bottom -- FM004
Entries 616
D(23)

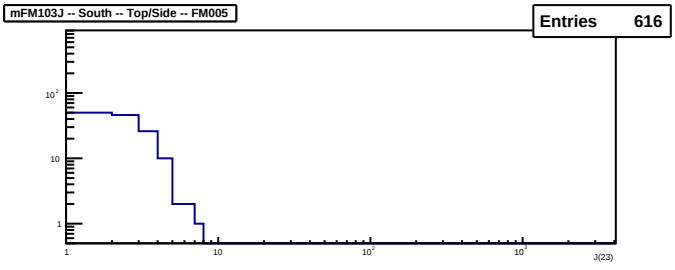

mFM102Dsim -- South -- Bottom -- FM004
Entries 0
D(23)

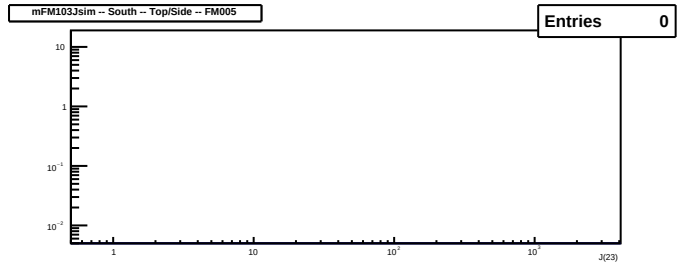

mFM102JP -- South -- Bottom -- FM004
Entries 1232
JP-AB
JP-CD

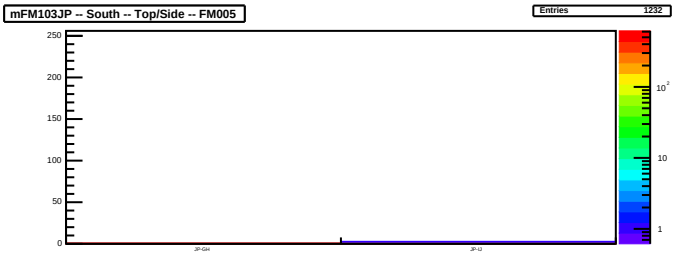

mFM102JPsim -- South -- Bottom -- FM004
Entries 0
JP-AB
JP-CD


mFM103BS -- South -- Top/Side --FM005
Entries 2464
616
616
616
616
BS3
BS2
BS1
Unused

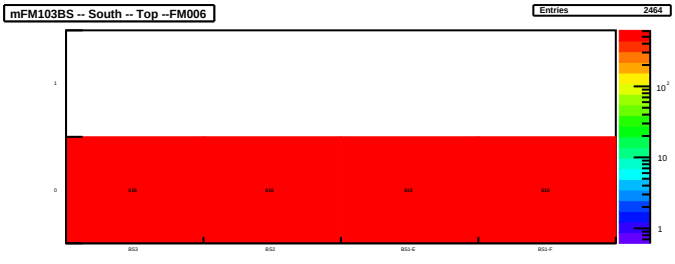

mFM103BSsim -- South -- Top/Side --FM0005
Entries 0
BS3
BS2
BS1
Unused

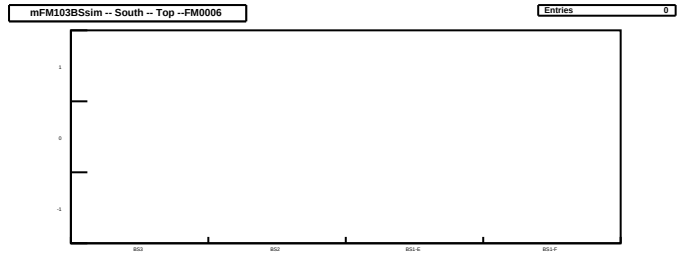

mFM103J -- South -- Top/Side -- FM005
Entries 616
J(23)

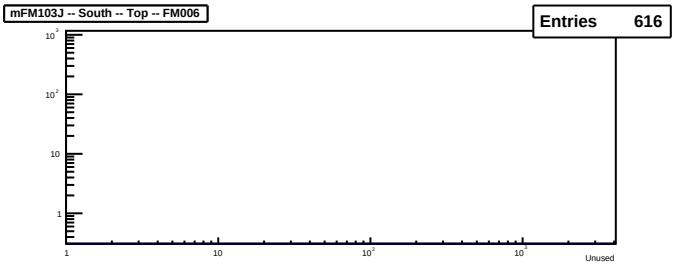

mFM103Jsim -- South -- Top/Side -- FM005
Entries 0
J(23)

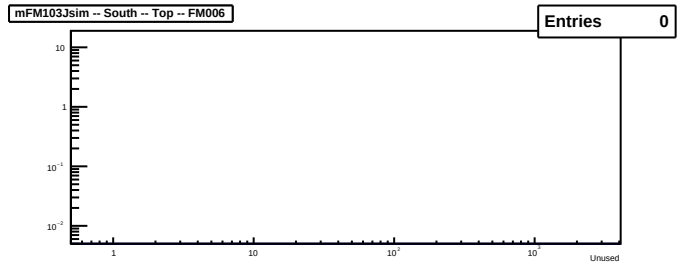

mFM103JP -- South -- Top/Side -- FM005
Entries 1232
JP-GH
JP-IJ

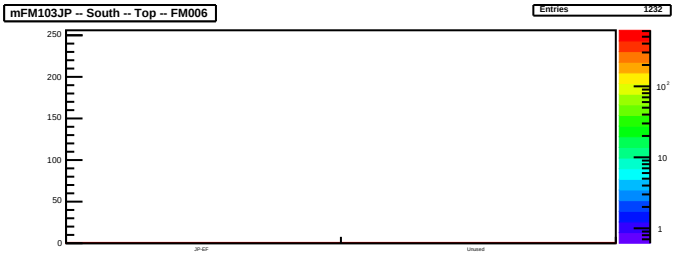
mFM103JPsim -- South -- Top/Side -- FM005
Entries 0
JP-GH
JP-IJ

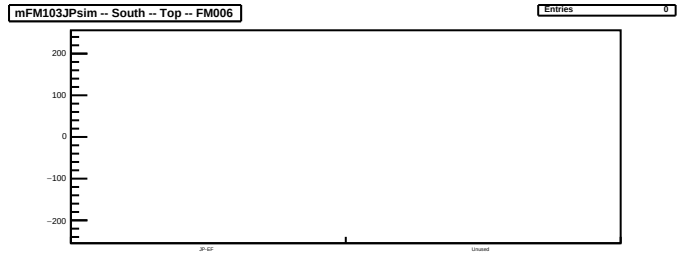

mFM103BS -- South -- Top --FM006
Entries 2464
616
616
616
616
BS3
BS2
BS1-E
BS1-F

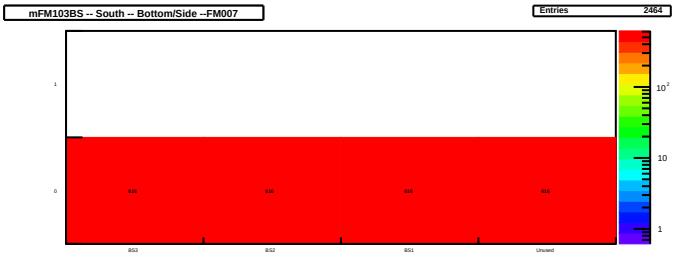

mFM103BSsim -- South -- Top --FM0006
Entries 0
BS3
BS2
BS1-E
BS1-F

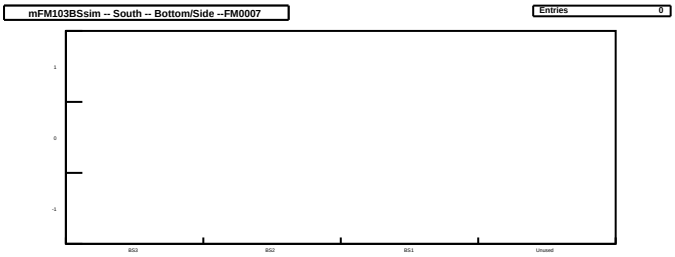

mFM103J -- South -- Top -- FM006
Entries 616
Unused

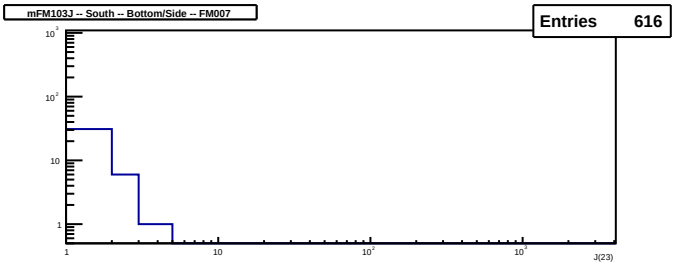

mFM103Jsim -- South -- Top -- FM006
Entries 0
Unused

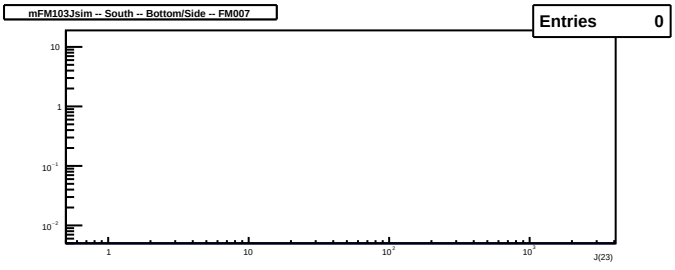

mFM103JP -- South -- Top -- FM006
Entries 1232
JP-EF
Unused

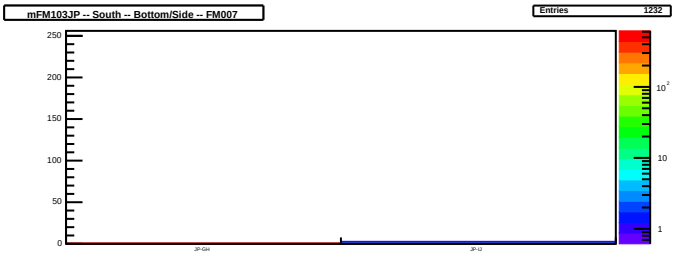

mFM103JPsim -- South -- Top -- FM006
Entries 0
JP-EF
Unused

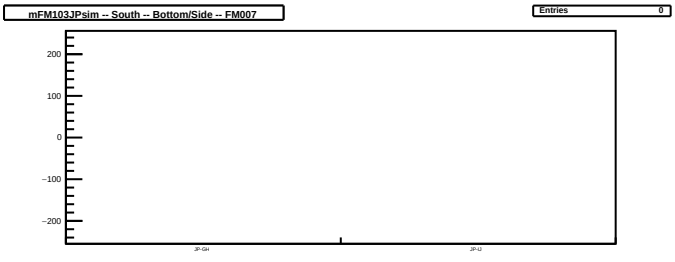

mFM103BS -- South -- Bottom/Side --FM007
Entries 2464
616
616
616
616
BS3
BS2
BS1
Unused

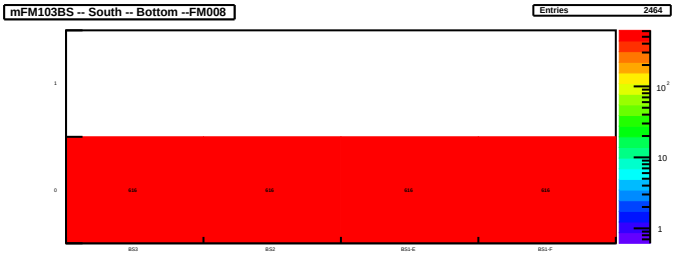

mFM103BSsim -- South -- Bottom/Side --FM0007
Entries 0
BS3
BS2
BS1
Unused

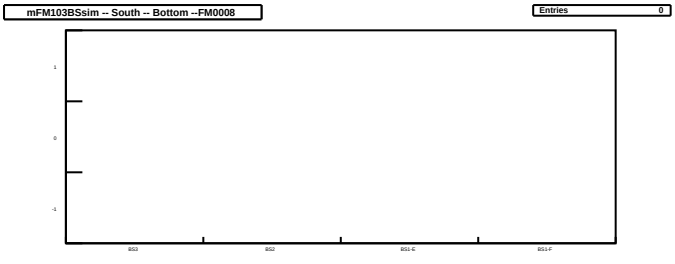

mFM103J -- South -- Bottom/Side -- FM007
Entries 616
J(23)

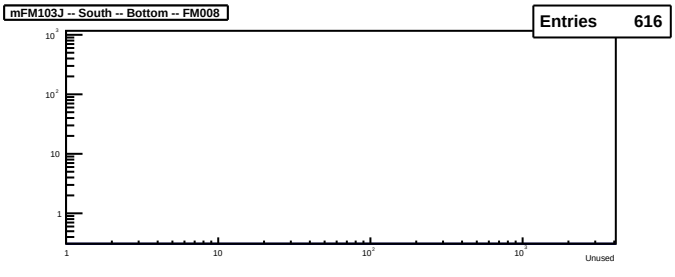

mFM103Jsim -- South -- Bottom/Side -- FM007
Entries 0
J(23)

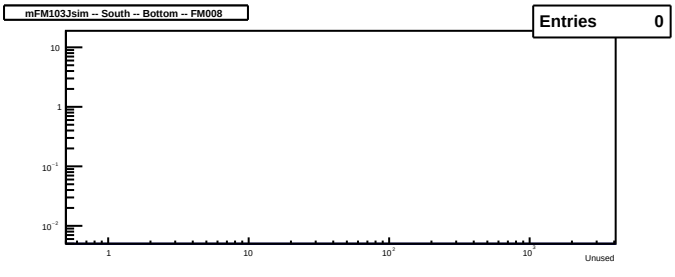

mFM103JP -- South -- Bottom/Side -- FM007
Entries 1232
JP-GH
JP-U


mFM103JPsim -- South -- Bottom/Side -- FM007
Entries 0
JP-GH
JP-U

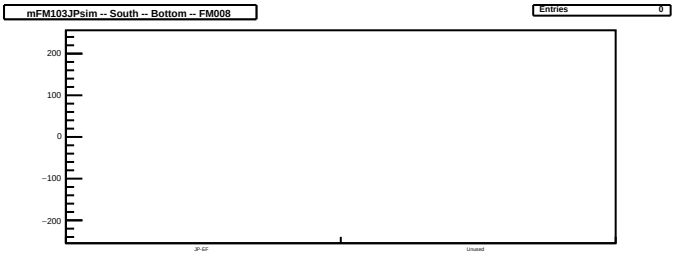

mFM103BS -- South -- Bottom --FM008
Entries 2464
616
616
616
616
BS3
BS2
BS1-E
BS1-F

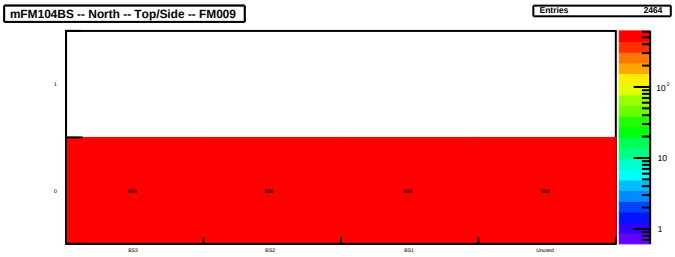

mFM103BSsim -- South -- Bottom --FM0008
Entries 0
BS3
BS2
BS1-E
BS1-F

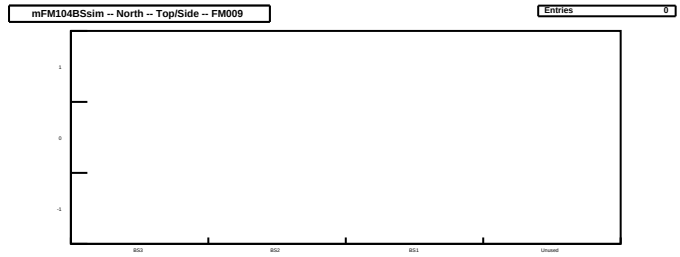

mFM103J -- South -- Bottom -- FM008
Entries 616
Unused

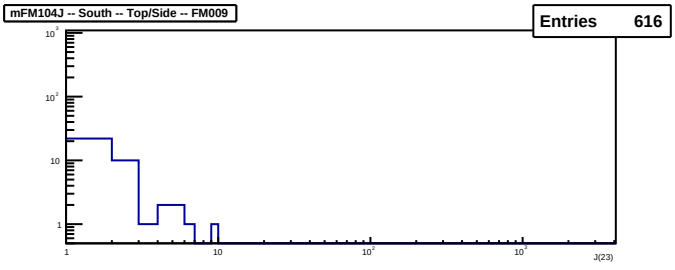

mFM103Jsim -- South -- Bottom -- FM008
Entries 0
Unused

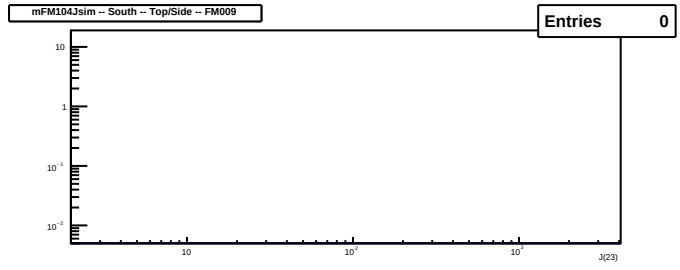
mFM103JP -- South -- Bottom -- FM008
Entries 1232
JP-EF
Unused

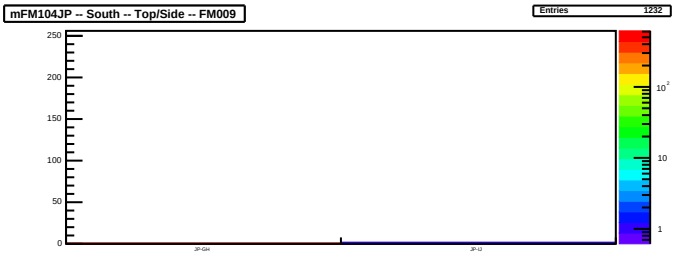

mFM103JPsim -- South -- Bottom -- FM008
Entries 0
JP-EF
Unused


mFM104BS -- North -- Top/Side -- FM009
Entries 2464
616
616
616
616
BS3
BS2
BS1
Unused

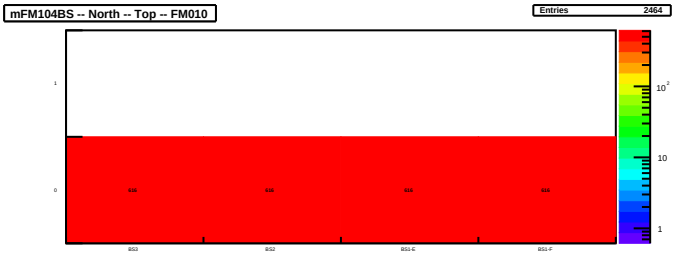

mFM104BSsim -- North -- Top/Side -- FM009
Entries 0
BS3
BS2
BS1
Unused

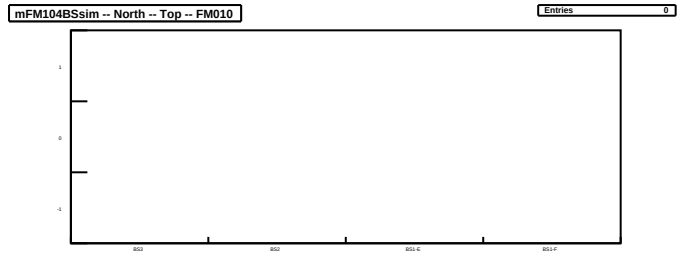

mFM104J -- South -- Top/Side -- FM009
Entries 616
J(23)

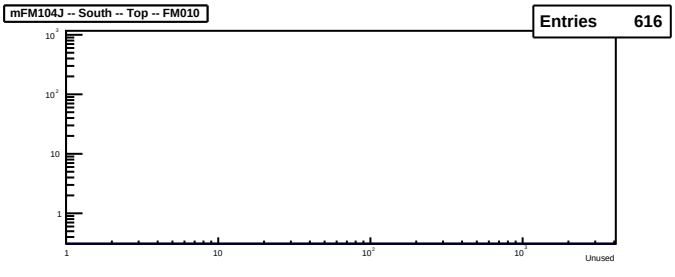

mFM104Jsim -- South -- Top/Side -- FM009
Entries 0
J(23)

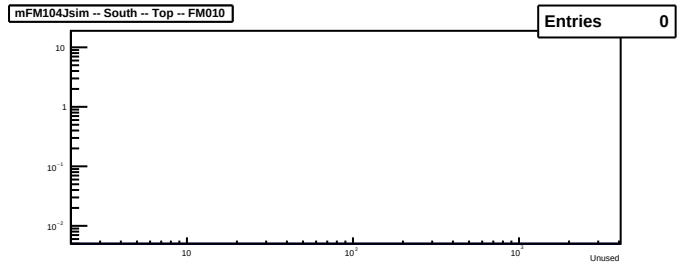

mFM104JP -- South -- Top/Side -- FM009
Entries 1232
JP-GH
JP-IJ

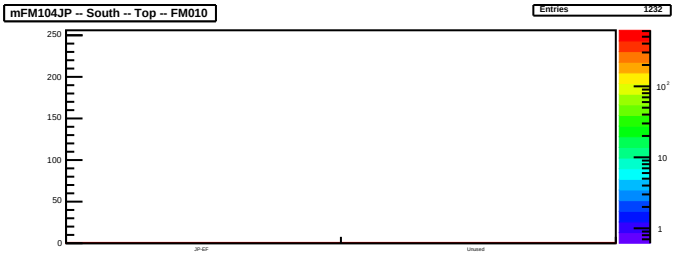
mFM104JPsim -- South -- Top/Side -- FM009
Entries 0
JP-GH
JP-IJ


mFM104BS -- North -- Top -- FM010
Entries 2464
616
616
616
616
BS3
BS2
BS1-E
BS1-F

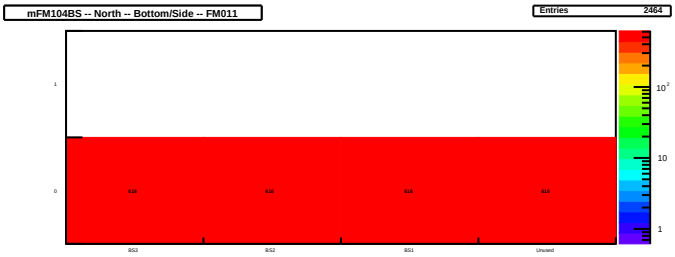

mFM104BSsim -- North -- Top -- FM010
Entries 0
BS3
BS2
BS1-E
BS1-F

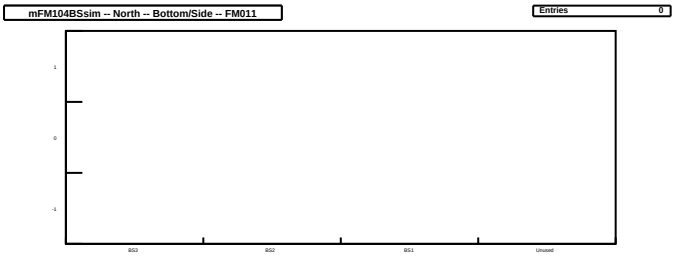

mFM104J -- South -- Top -- FM010
Entries 616
Unused

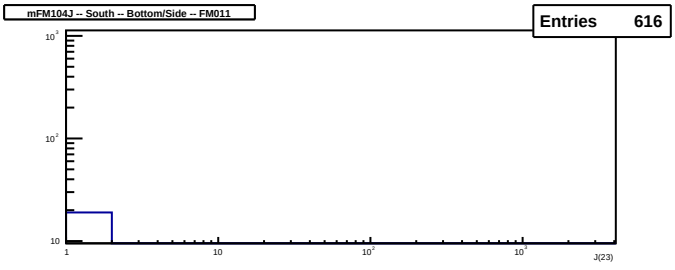

mFM104Jsim -- South -- Top -- FM010
Entries 0
Unused

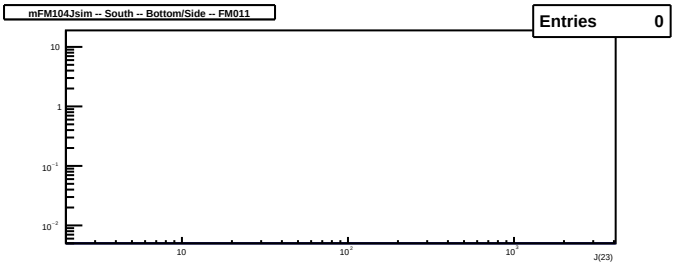

mFM104JP -- South -- Top -- FM010
Entries 1232
JP-EF
Unused

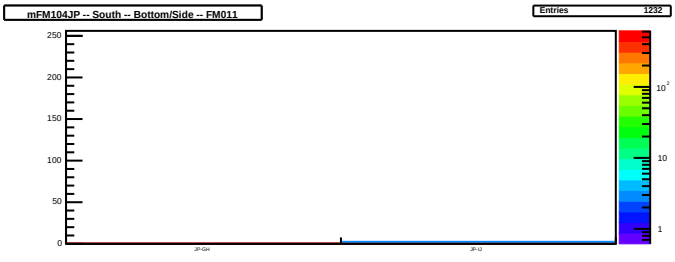
mFM104JPsim -- South -- Top -- FM010
Entries 0
JP-EF
Unused

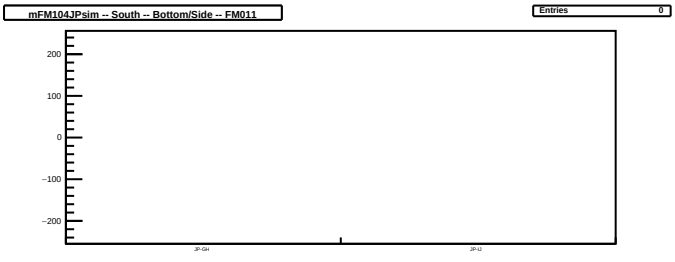

mFM104BS -- North -- Bottom/Side -- FM011
Entries 2464
616
616
616
616
BS3
BS2
BS1
Unused

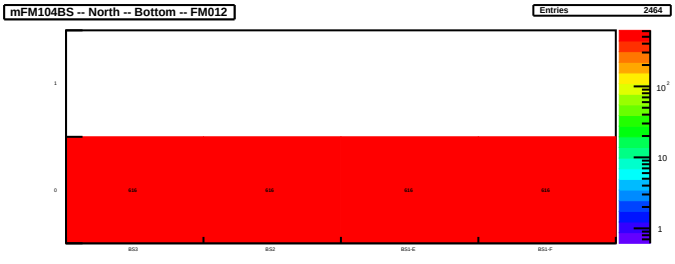

mFM104BSsim -- North -- Bottom/Side -- FM011
Entries 0
BS3
BS2
BS1
Unused

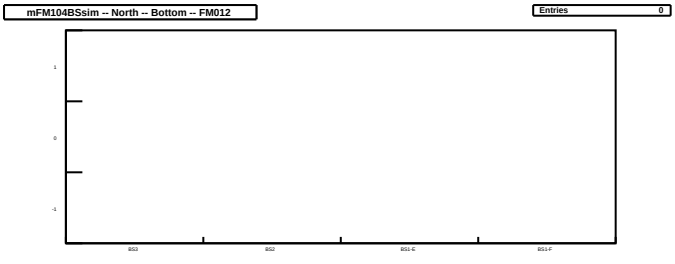

mFM104J -- South -- Bottom/Side -- FM011
Entries 616
J(23)

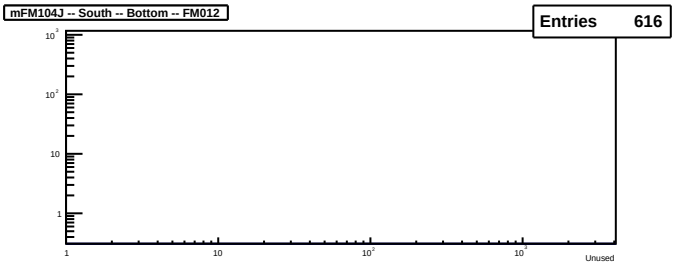

mFM104Jsim -- South -- Bottom/Side -- FM011
Entries 0
J(23)

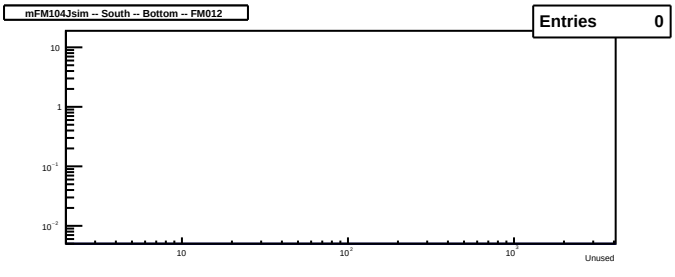

mFM104JP -- South -- Bottom/Side -- FM011
Entries 1232
JP-GH
JP-U

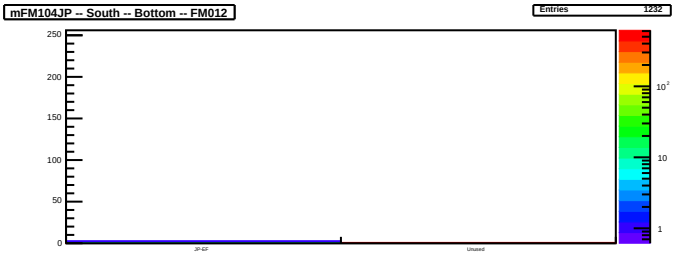

mFM104JPsim -- South -- Bottom/Side -- FM011
Entries 0
JP-GH
JP-U


mFM104BS -- North -- Bottom -- FM012
Entries 2464
616
616
616
616
BS3
BS2
BS1-E
BS1-F

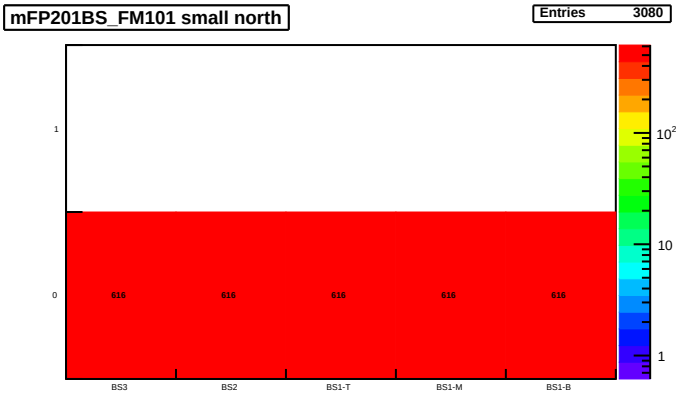

mFM104BSsim -- North -- Bottom -- FM012
Entries 0
BS3
BS2
BS1-E
BS1-F

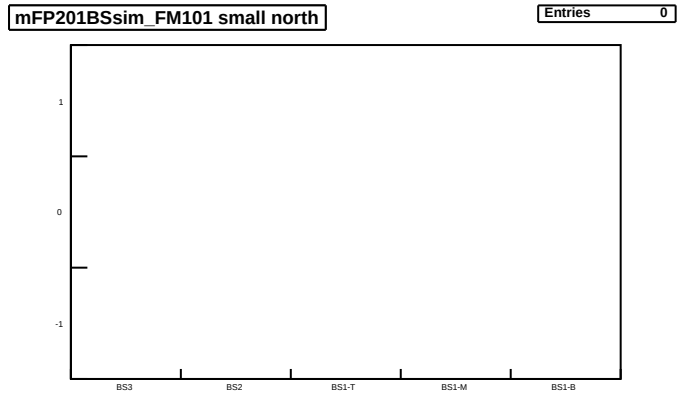

mFM104J -- South -- Bottom -- FM012
Entries 616
Unused

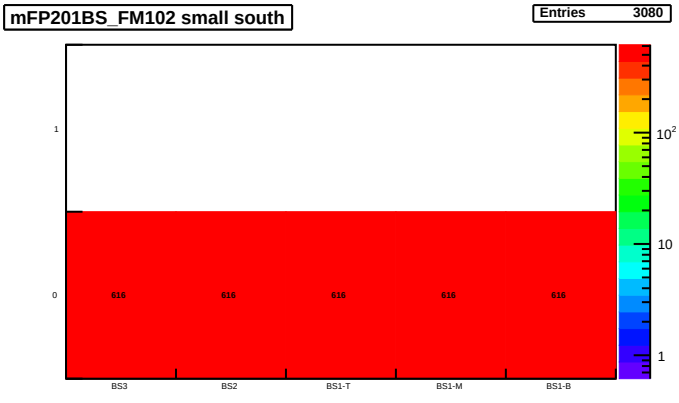

mFM104Jsim -- South -- Bottom -- FM012
Entries 0
Unused

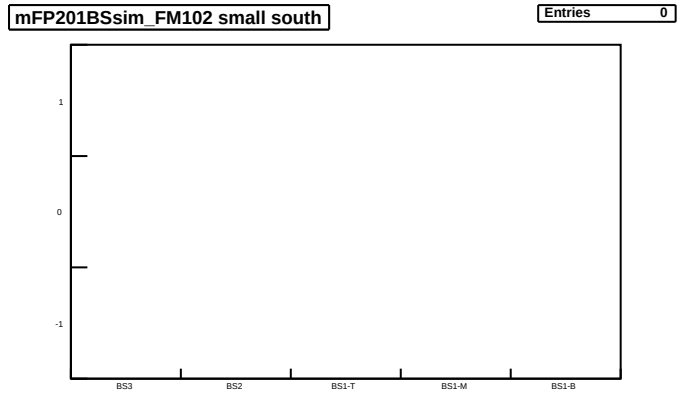

mFM104JP -- South -- Bottom -- FM012
Entries 1232
JP-EF
Unused

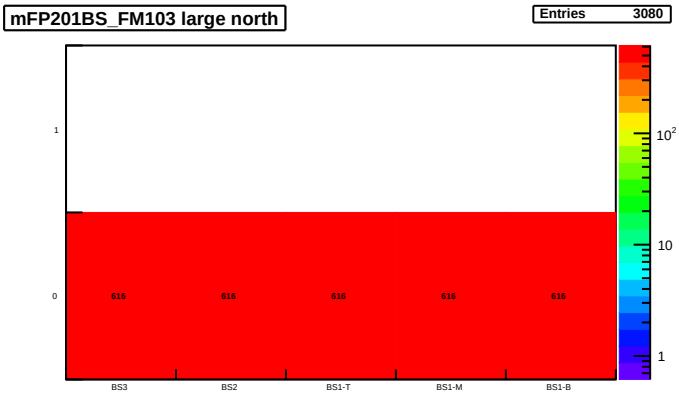
mFM104JPsim -- South -- Bottom -- FM012
Entries 0
JP-EF
Unused

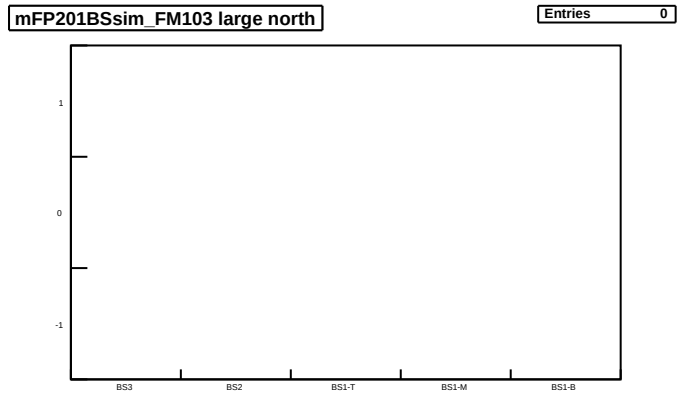

mFP201BS_FM101 small north
Entries 3080
616
BS3
BS2
BS1-T
BS1-M
BS1-B

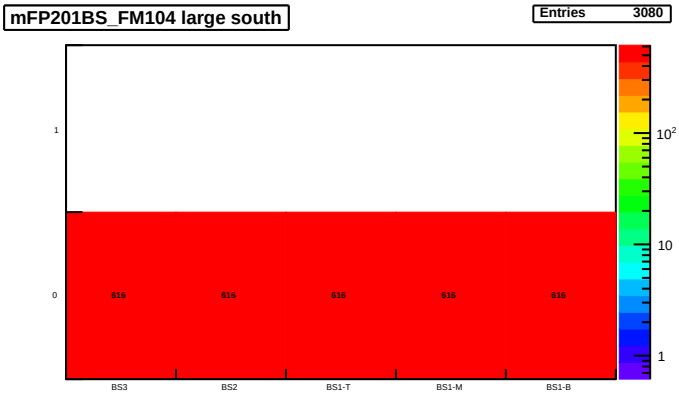

mFP201BSsim_FM101 small north
Entries 0
BS3
BS2
BS1-T
BS1-M
BS1-B

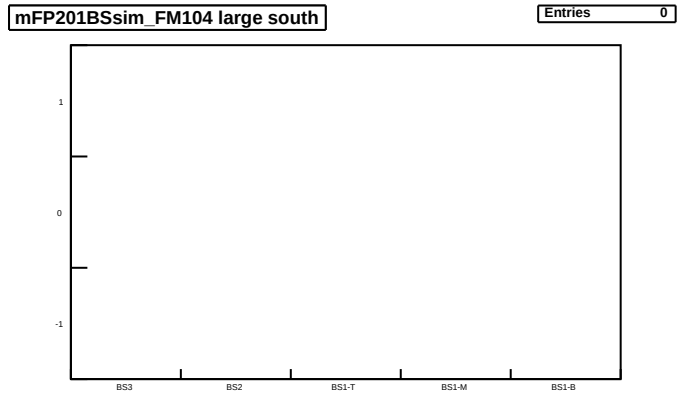

mFP201BS_FM102 small south
Entries 3080
616
BS3
BS2
BS1-T
BS1-M
BS1-B

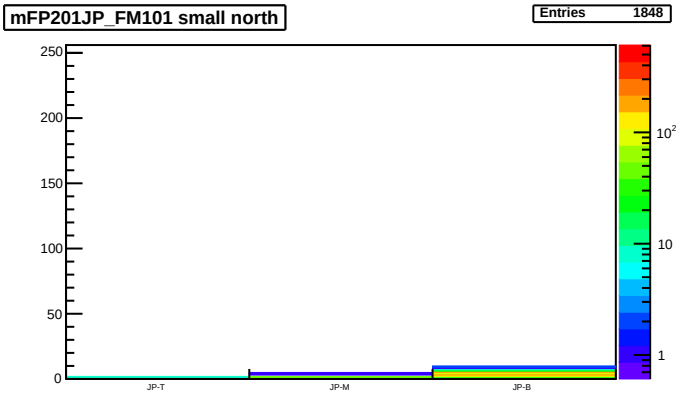

mFP201BSsim_FM102 small south
Entries 0
BS3
BS2
BS1-T
BS1-M
BS1-B

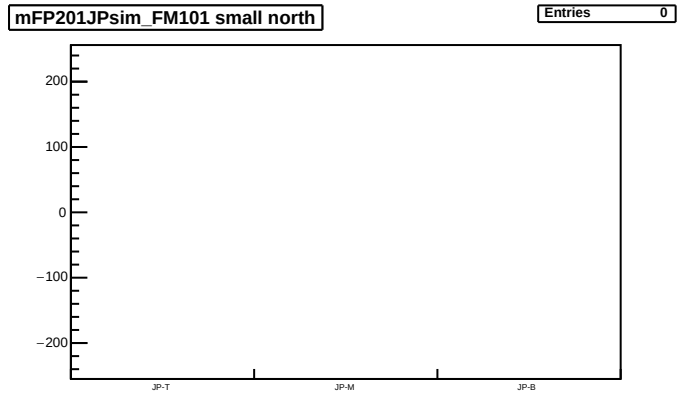

mFP201BS_FM103 large north
Entries 3080
616
BS3
BS2
BS1-T
BS1-M
BS1-B

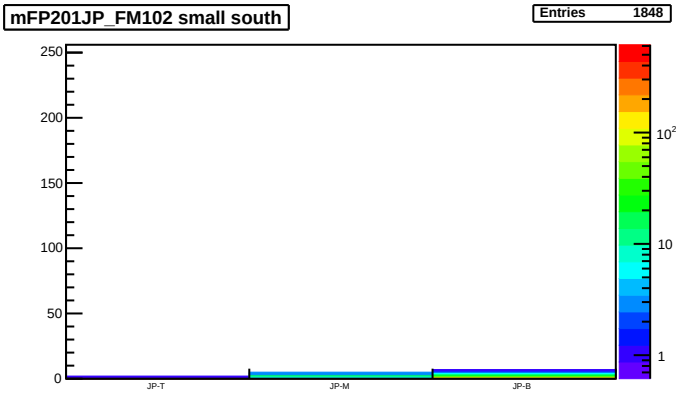

mFP201BSsim_FM103 large north
Entries 0
BS3
BS2
BS1-T
BS1-M
BS1-B

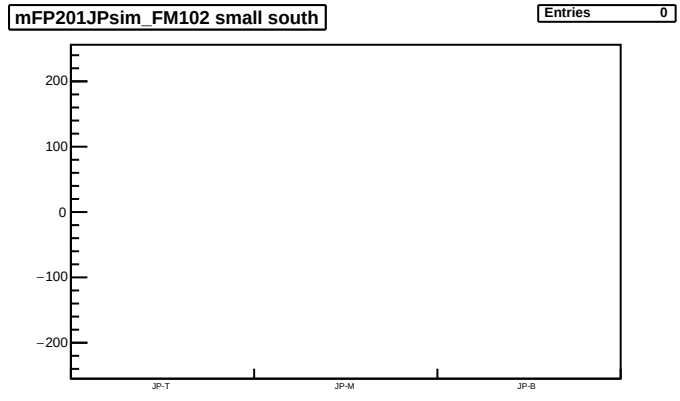

mFP201BS_FM104 large south
Entries 3080
616
BS3
BS2
BS1-T
BS1-M
BS1-B

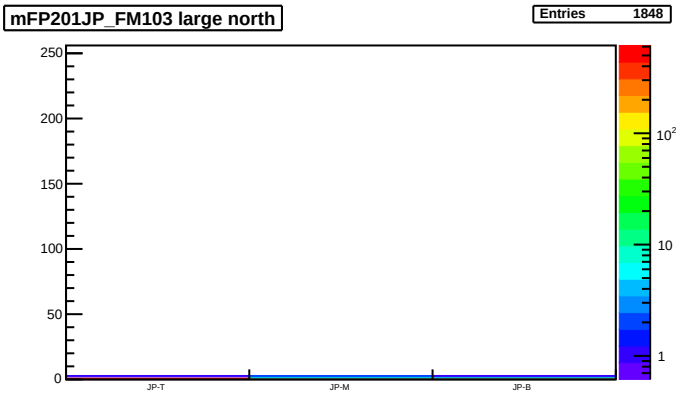

mFP201BSsim_FM104 large south
Entries 0
BS3
BS2
BS1-T
BS1-M
BS1-B

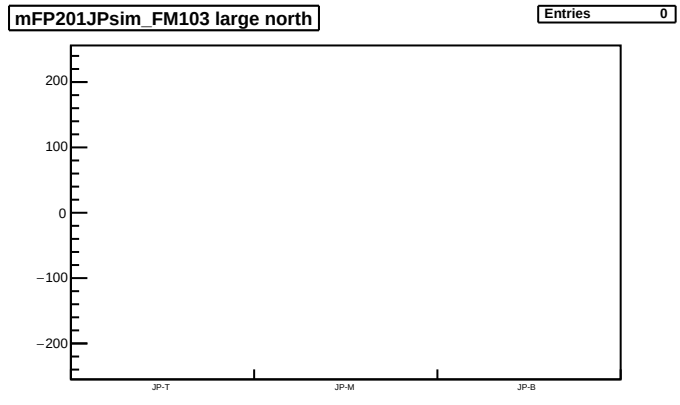

mFP201JP_FM101 small north
Entries 1848
250
200
150
100
50
0
JP-T
JP-M
JP-B
10^2
10
1

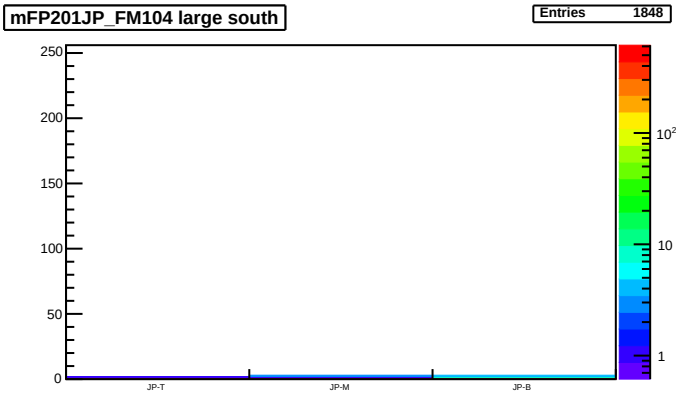

mFP201JPsim_FM101 small north
Entries 0
200
100
0
−100
−200
JP-T
JP-M
JP-B

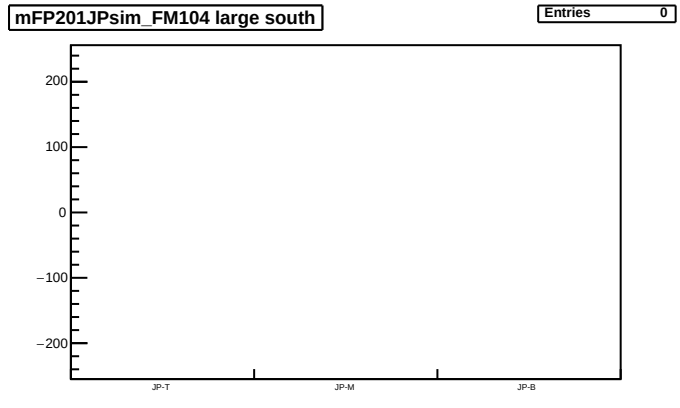

mFP201JP_FM102 small south
Entries 1848
250
200
150
100
50
0
JP-T
JP-M
JP-B
10^2
10
1

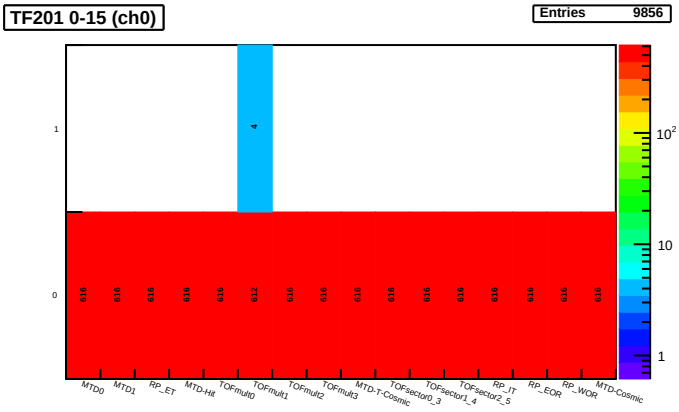

mFP201JPsim_FM102 small south
Entries 0
200
100
0
−100
−200
JP-T
JP-M
JP-B

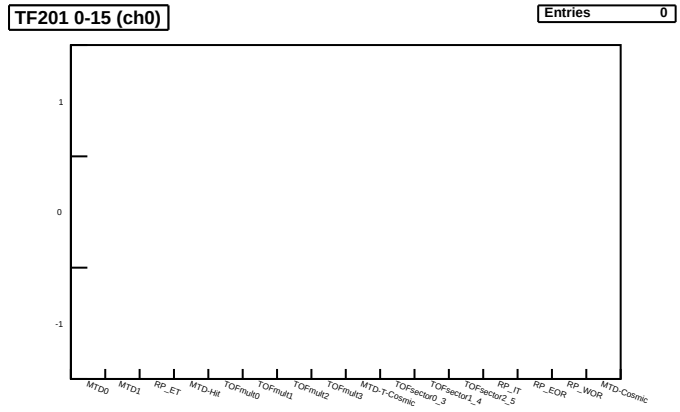

mFP201JP_FM103 large north
Entries 1848
250
200
150
100
50
0
JP-T
JP-M
JP-B
10^2
10
1

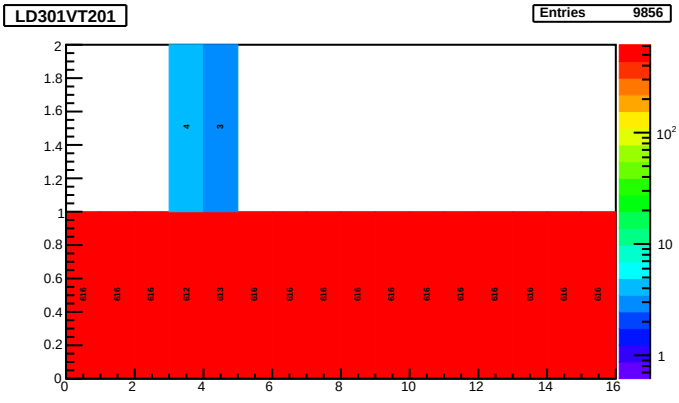

mFP201JPsim_FM103 large north
Entries 0
200
100
0
−100
−200
JP-T
JP-M
JP-B

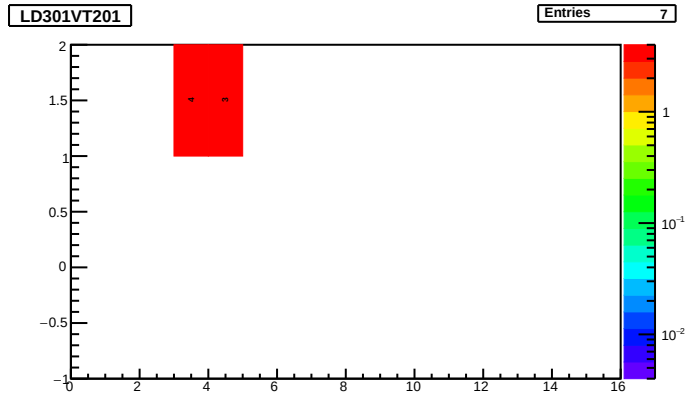

mFP201JP_FM104 large south
Entries 1848
250
200
150
100
50
0
JP-T
JP-M
JP-B
10^2
10
1

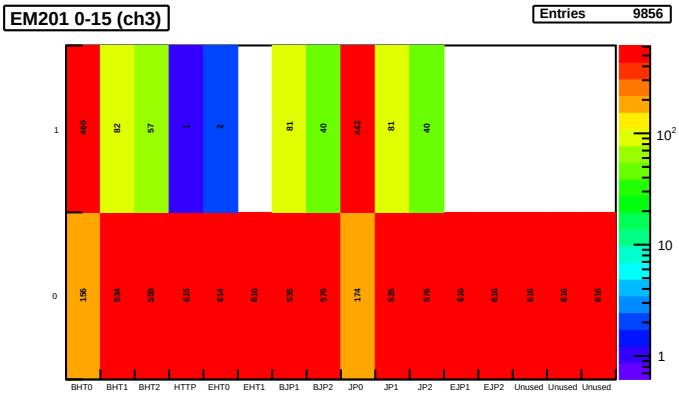

mFP201JPsim_FM104 large south
Entries 0
200
100
0
−100
−200
JP-T
JP-M
JP-B

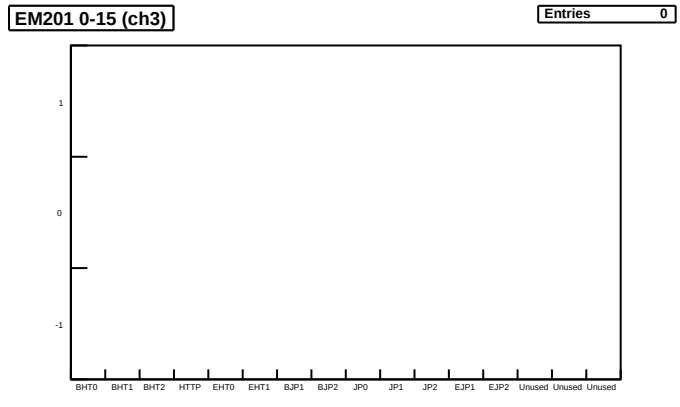

TF201 0-15 (ch0)
Entries 9856
MTD0 MTD1 RP_ET MTD-Hit TOFmult0 TOFmult1 TOFmult2 TOFmult3 MTD-T-Cosmic TOFsector0_3 TOFsector1_4 TOFsector2_5 RP_IT RP_EOR RP_WOR MTD-Cosmic

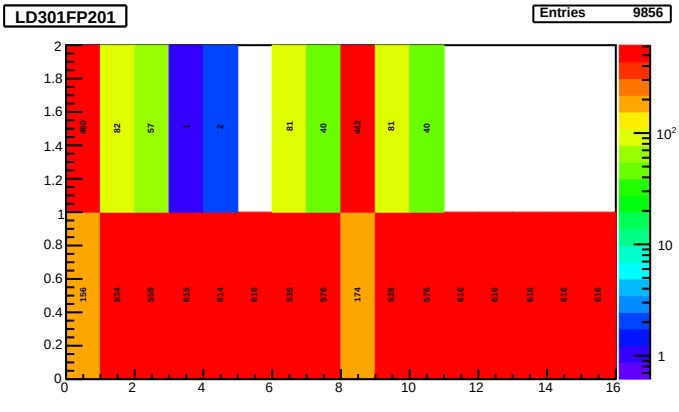

TF201 0-15 (ch0)
Entries 0
MTD0 MTD1 RP_ET MTD-Hit TOFmult0 TOFmult1 TOFmult2 TOFmult3 MTD-T-Cosmic TOFsector0_3 TOFsector1_4 TOFsector2_5 RP_IT RP_EOR RP_WOR MTD-Cosmic

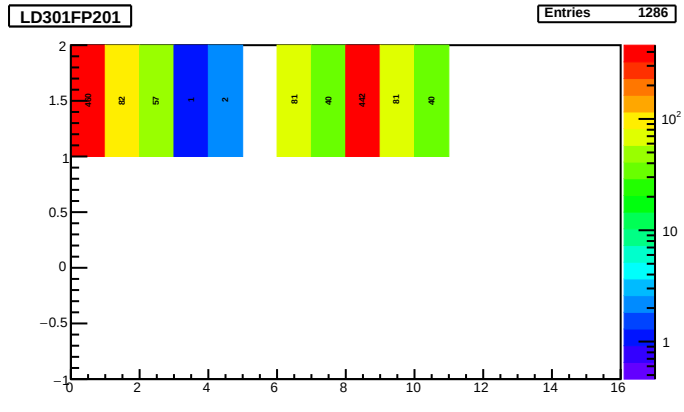

LD301VT201
Entries 9856


LD301VT201
Entries 7


EM201 0-15 (ch3)
Entries 9856
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


LD301FP201
Entries 9856


LD301FP201
Entries 1286