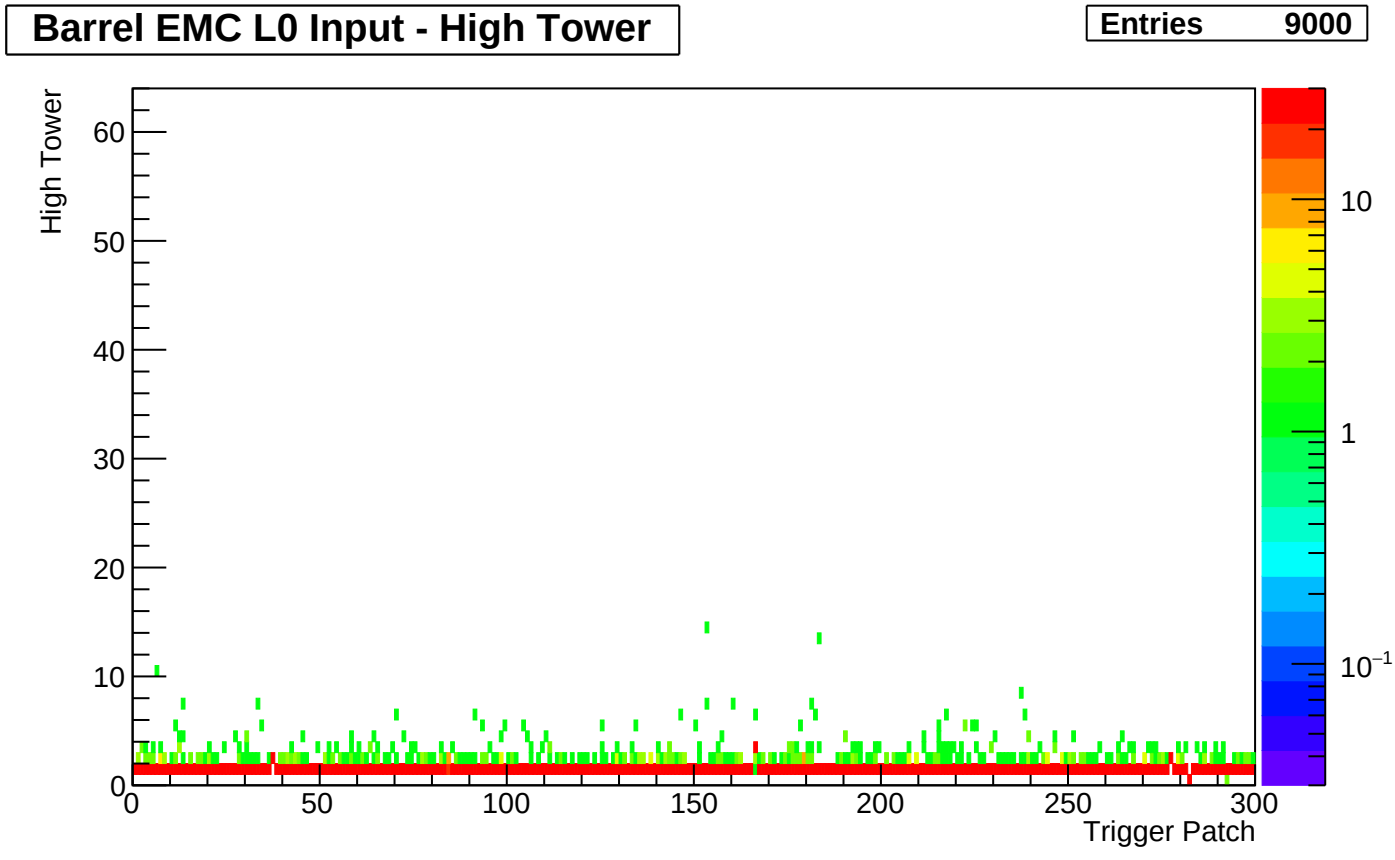
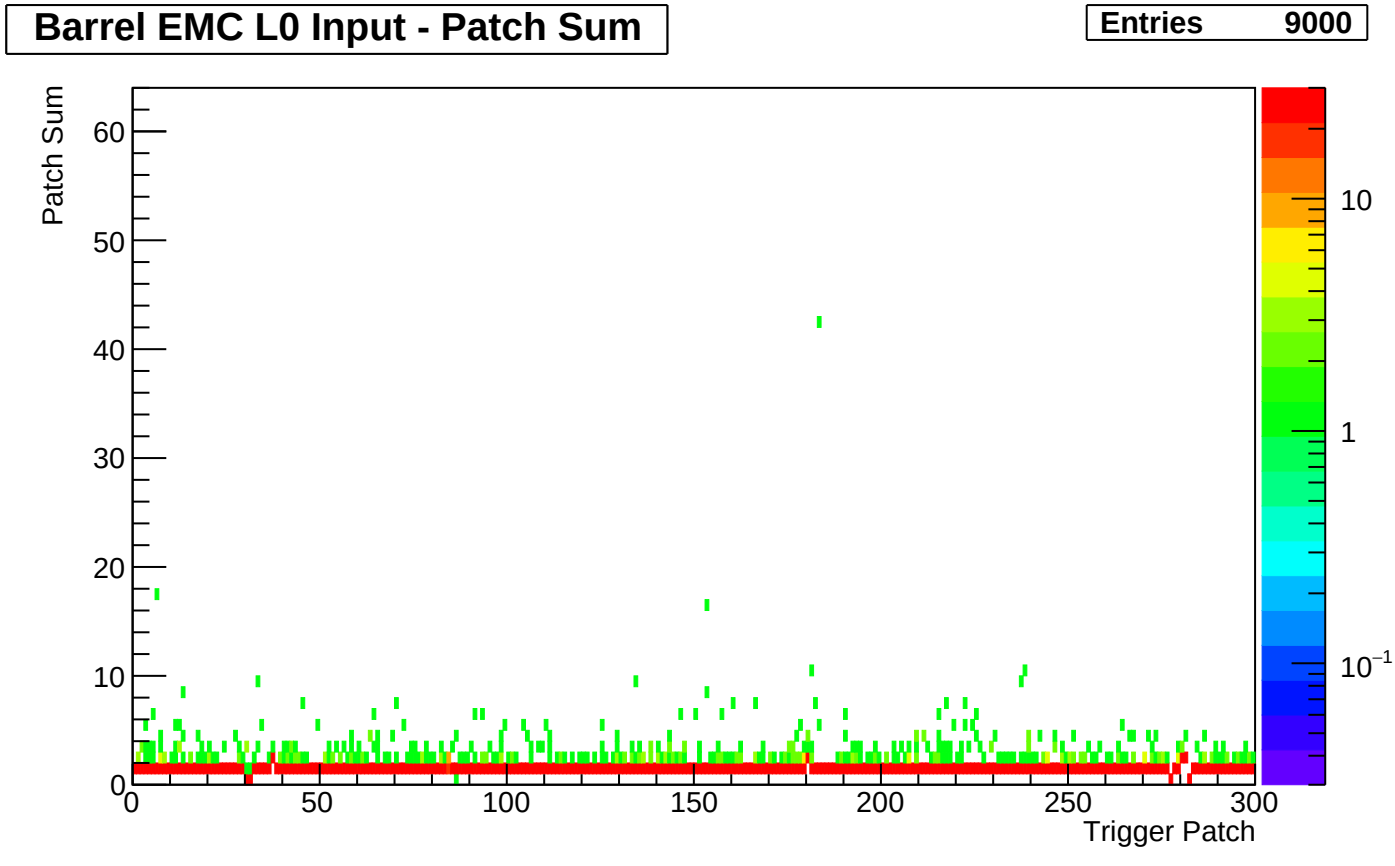
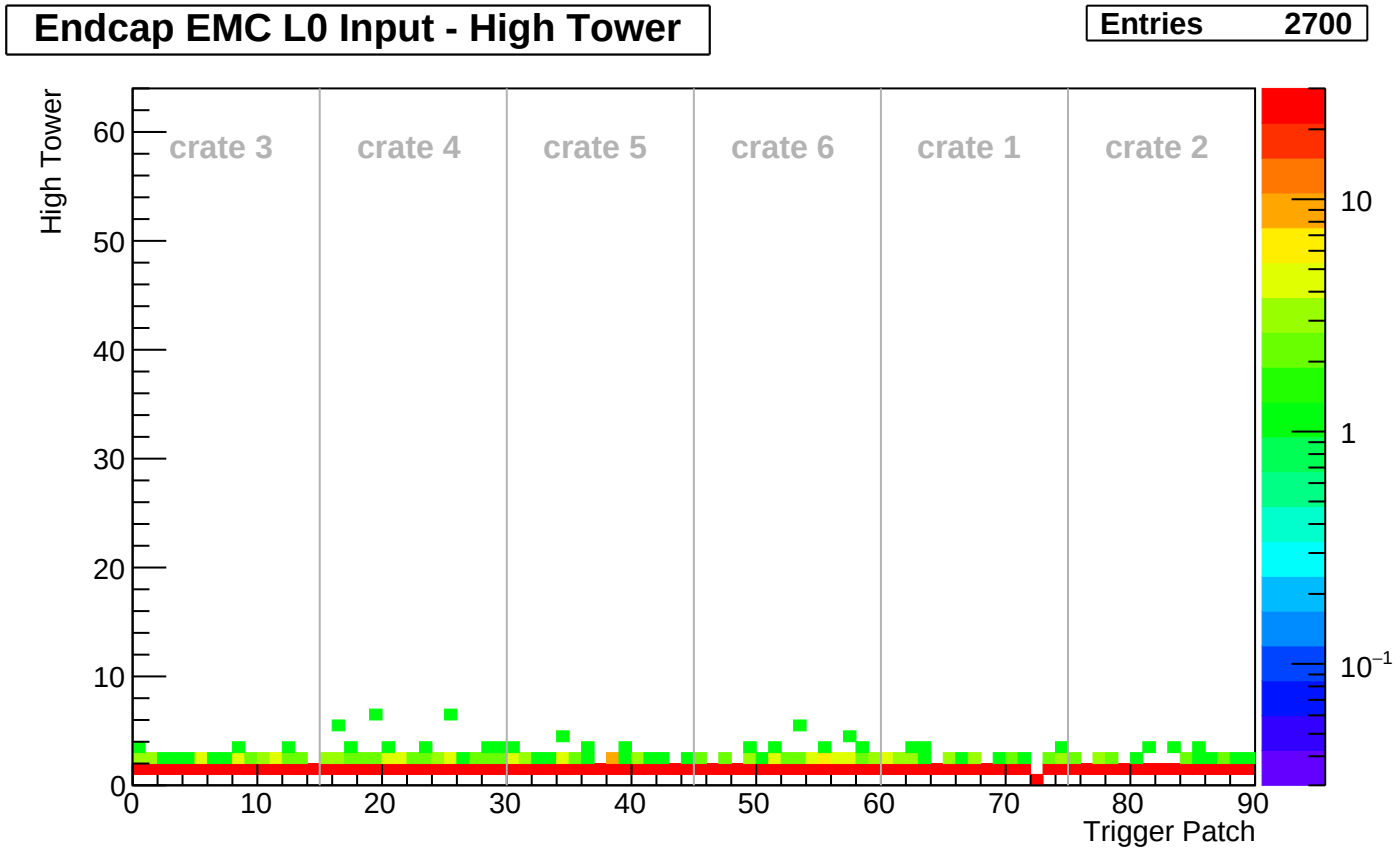
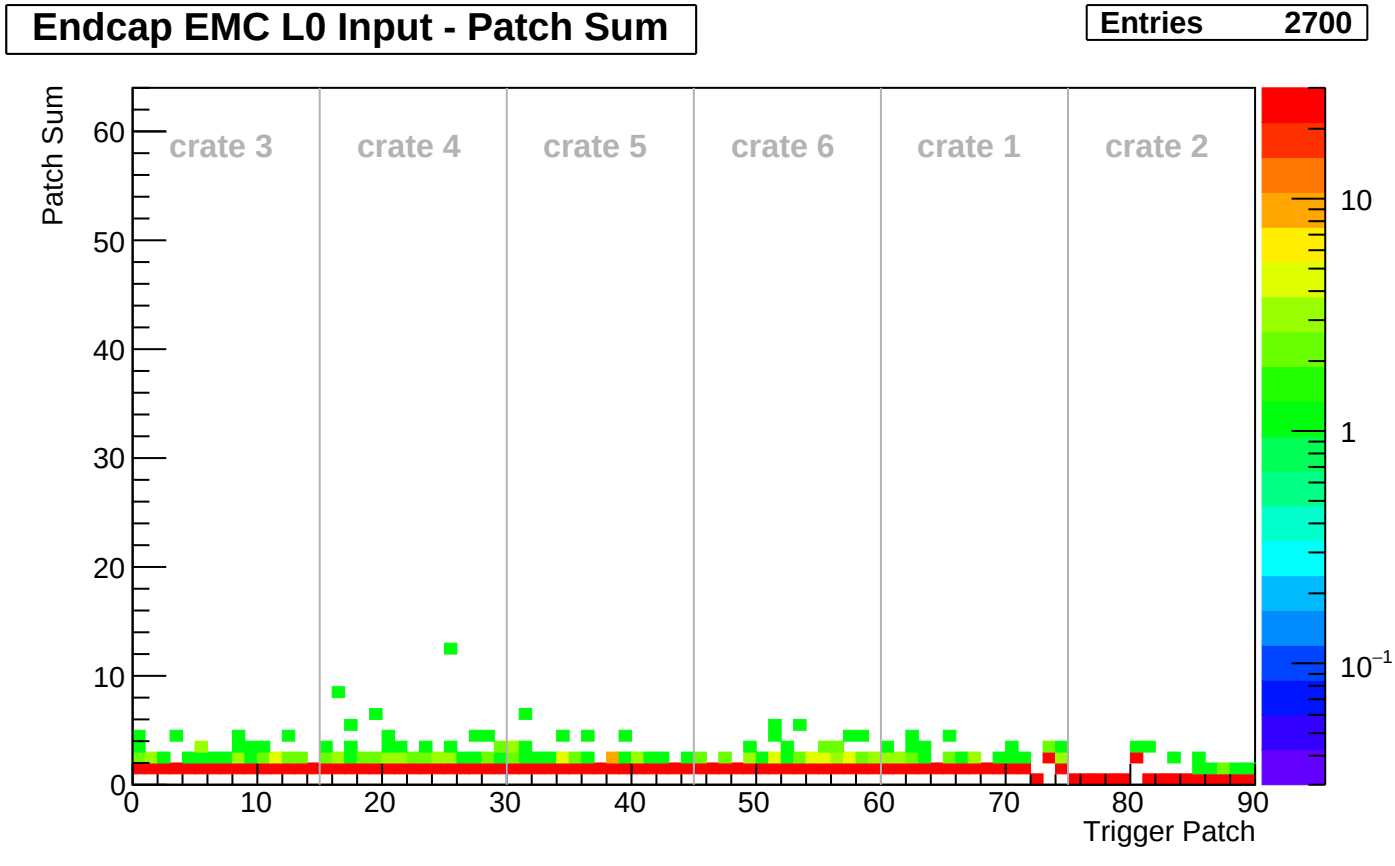
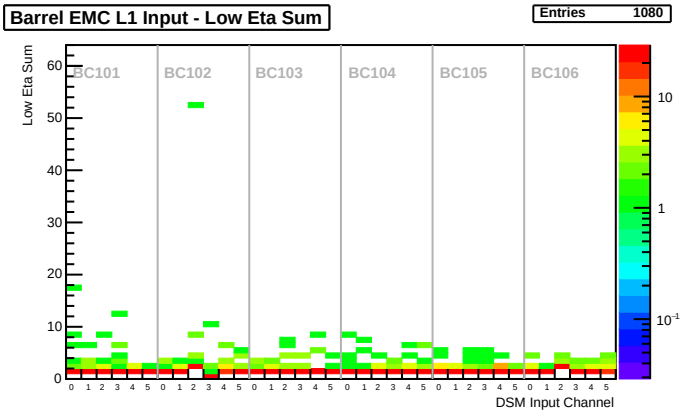
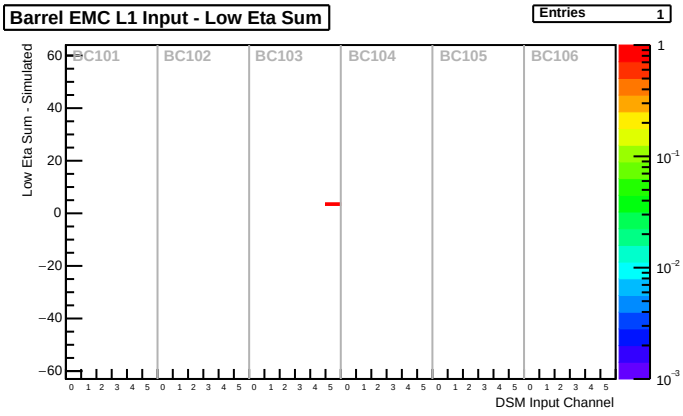

Barrel EMC L0 Input - High Tower
Entries 9000
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
10
1
10^{-1}

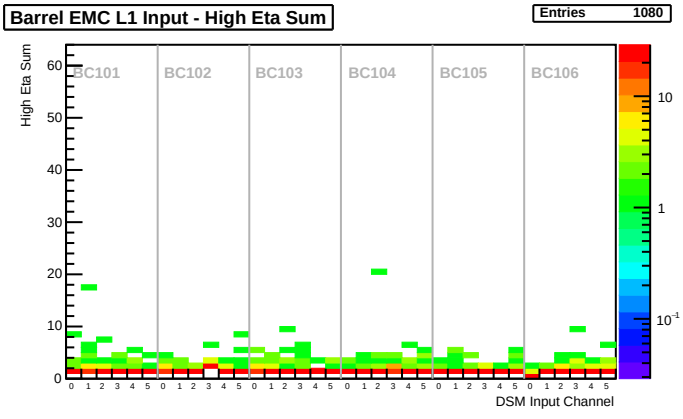

Barrel EMC L0 Input - Patch Sum
Entries 9000
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
10
1
10^{-1}

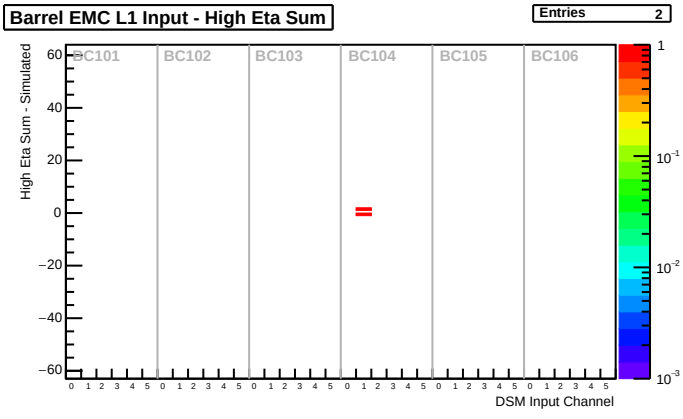

Endcap EMC L0 Input - High Tower
Entries 2700
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10^{-1}

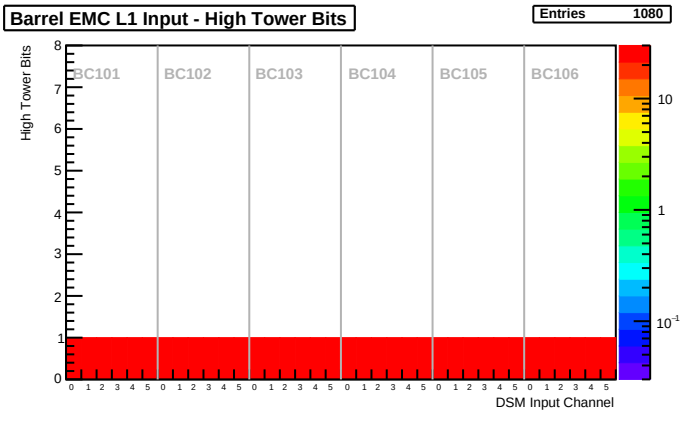

Endcap EMC L0 Input - Patch Sum
Entries 2700
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10^{-1}

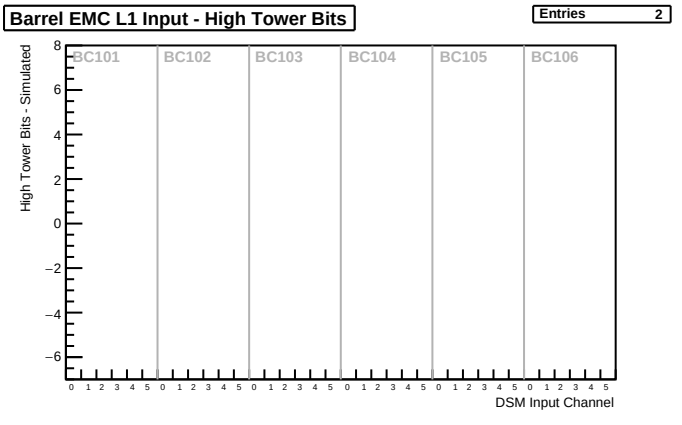

Barrel EMC L1 Input - Low Eta Sum
Entries 1080
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

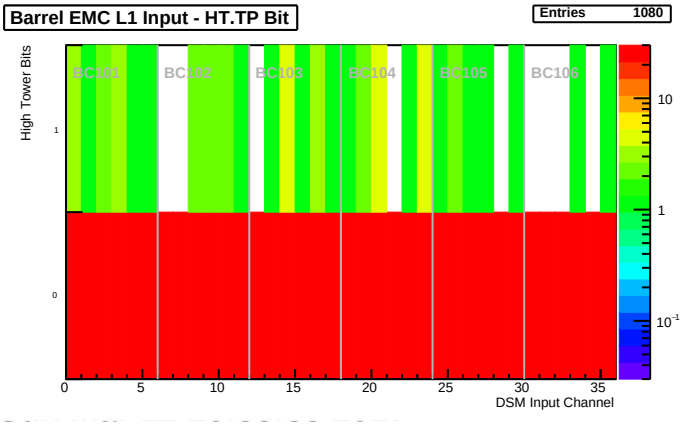

Barrel EMC L1 Input - Low Eta Sum
Entries 1
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

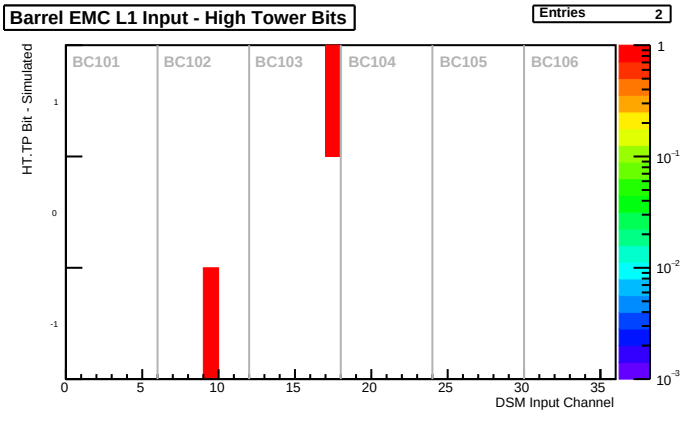

Barrel EMC L1 Input - High Eta Sum
Entries 1080
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

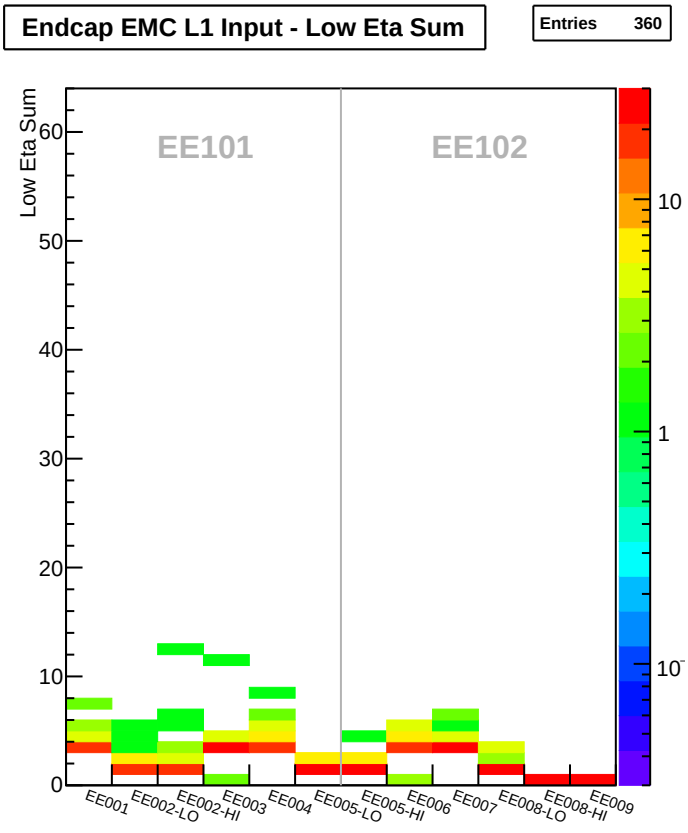

Barrel EMC L1 Input - High Eta Sum
Entries 2
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

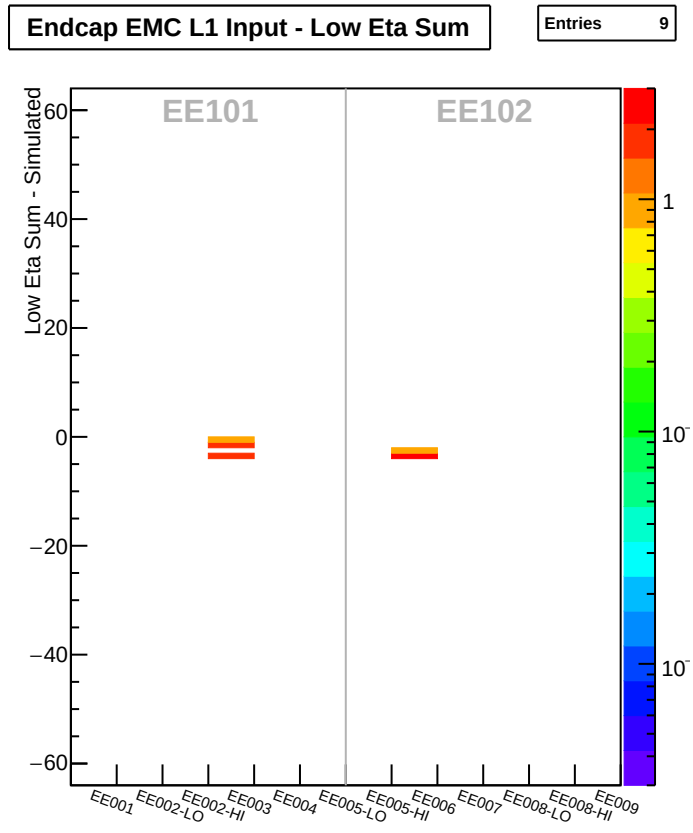

Barrel EMC L1 Input - High Tower Bits
Entries 1080
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

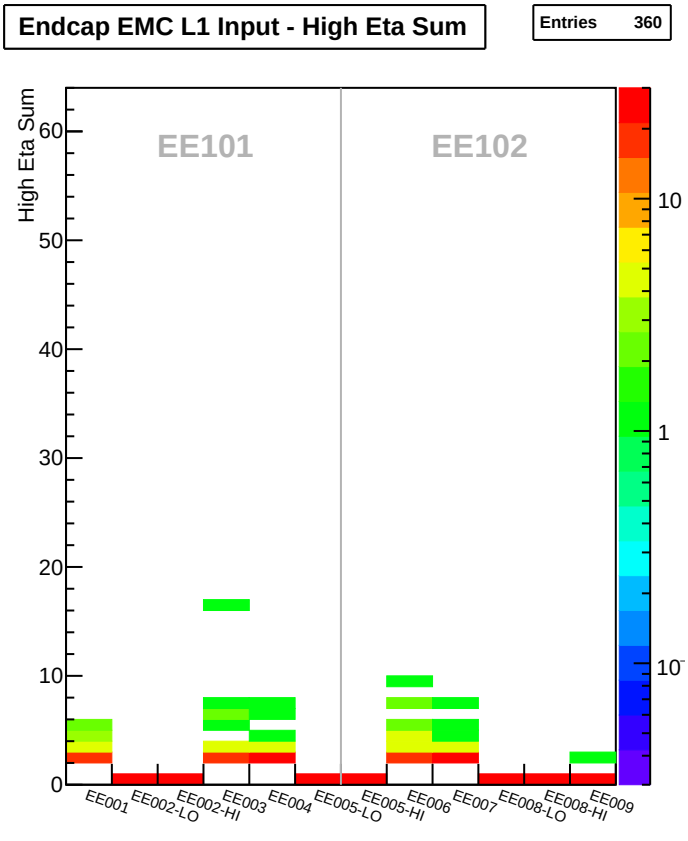

Barrel EMC L1 Input - High Tower Bits
Entries 2
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

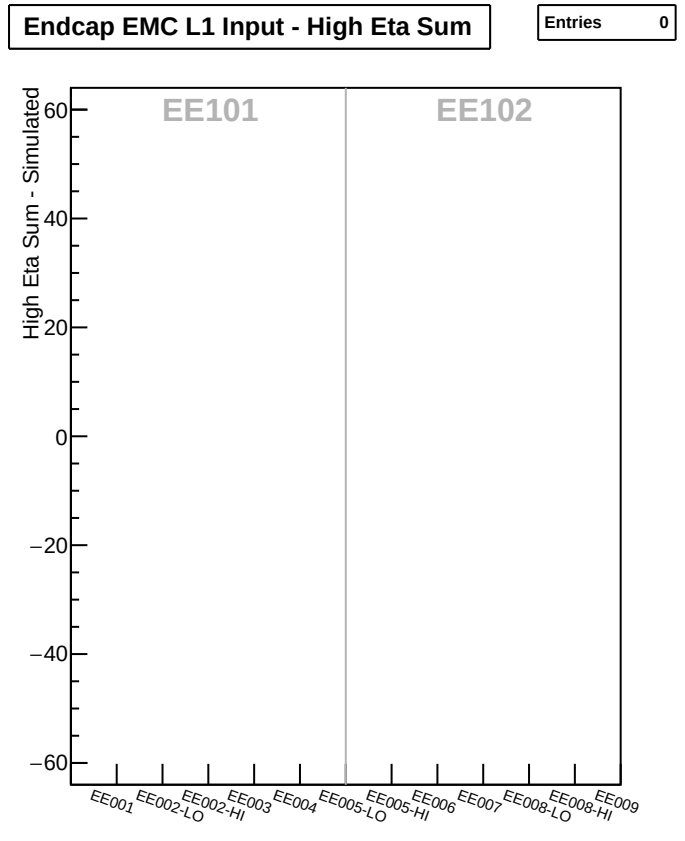

Barrel EMC L1 Input - HT.TP Bit
Entries 1080
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

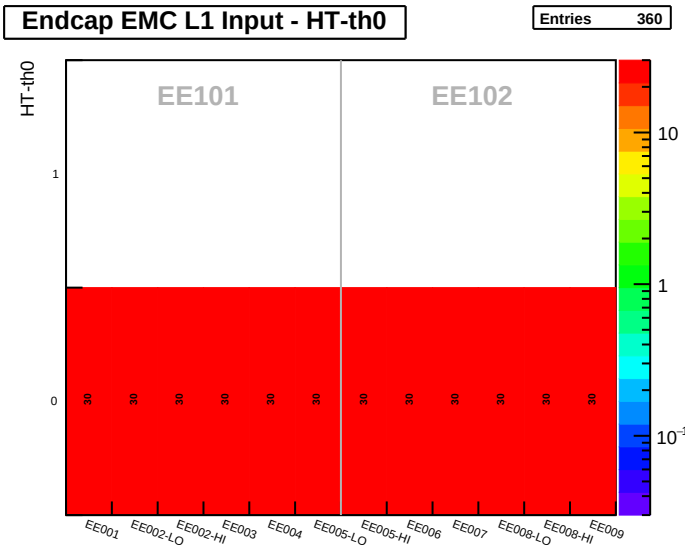

Barrel EMC L1 Input - High Tower Bits
Entries 2
HT.TP Bit - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

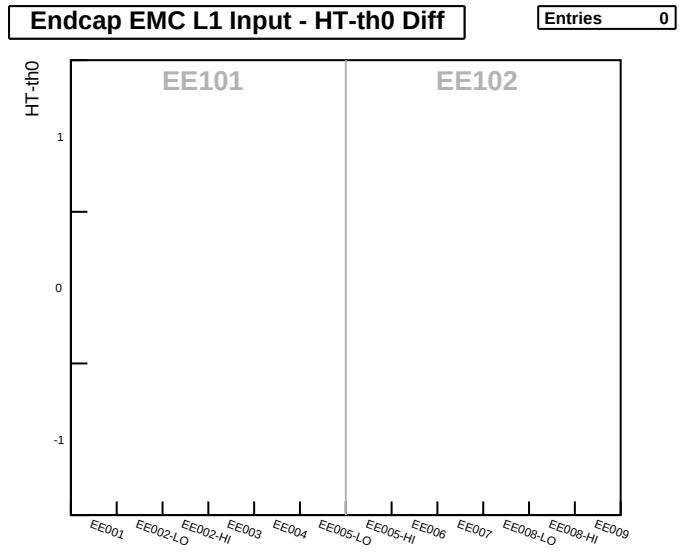

Endcap EMC L1 Input - Low Eta Sum
Entries 360
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

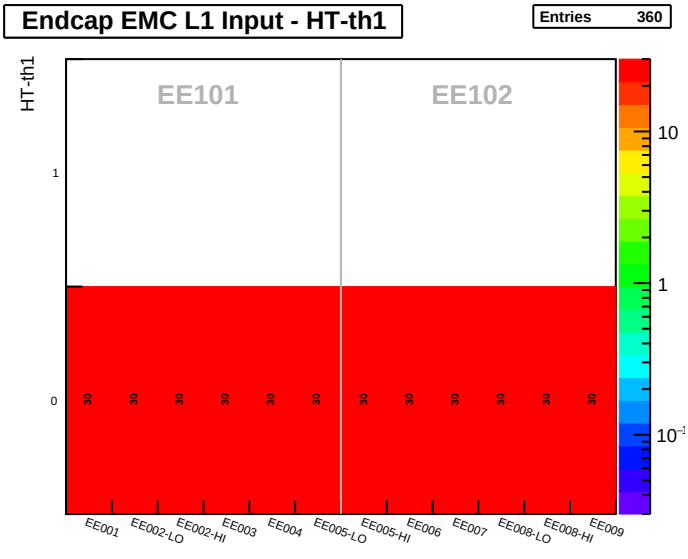

Endcap EMC L1 Input - Low Eta Sum
Entries 9
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

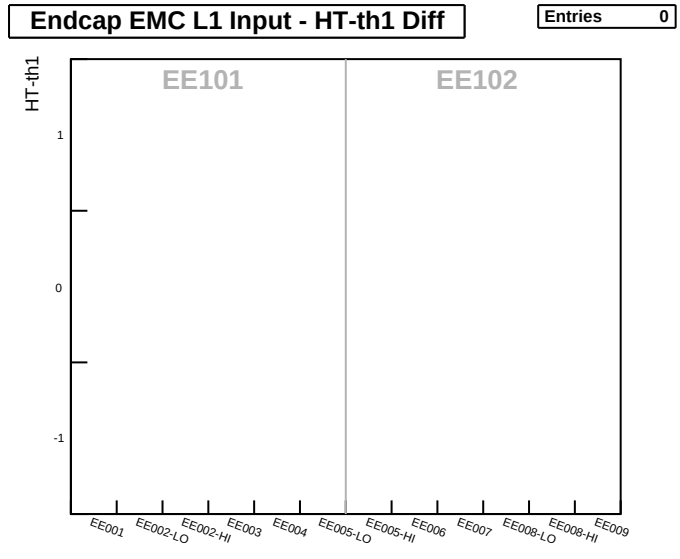

Endcap EMC L1 Input - High Eta Sum
Entries 360
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

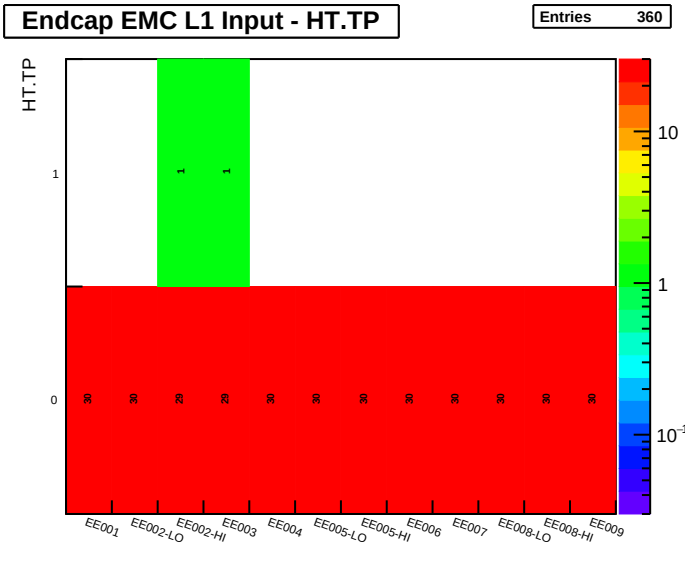

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

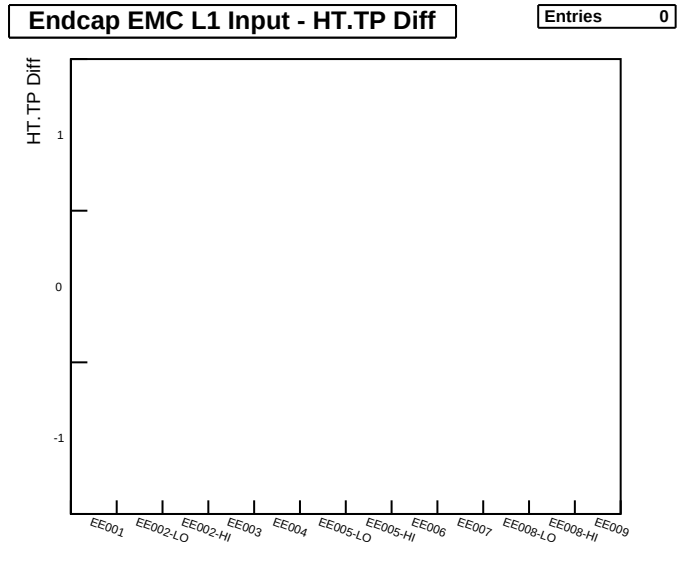

Endcap EMC L1 Input - HT-th0
Entries 360
HT-th0
EE101
EE102
1
0
30
10
1
10^{-1}
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

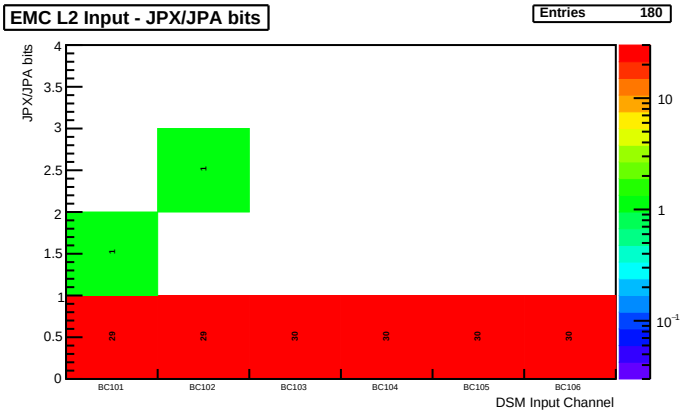

Endcap EMC L1 Input - HT-th0 Diff
Entries 0
HT-th0
EE101
EE102
1
0
-1
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

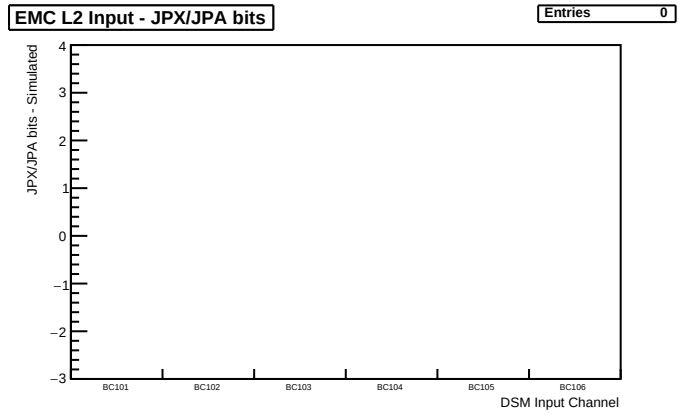

Endcap EMC L1 Input - HT-th1
Entries 360
HT-th1
EE101
EE102
1
0
30
10
1
10^{-1}
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

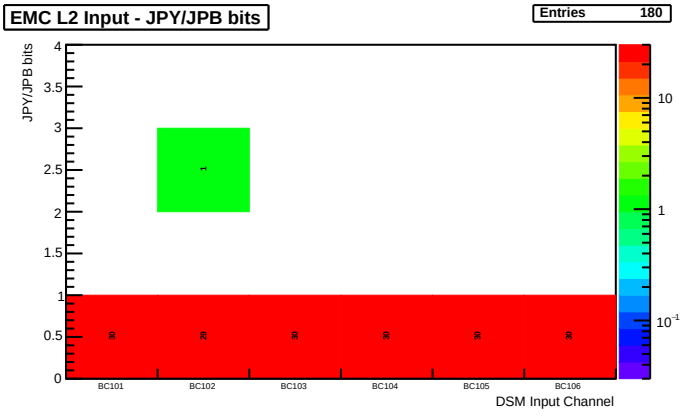

Endcap EMC L1 Input - HT-th1 Diff
Entries 0
HT-th1
EE101
EE102
1
0
-1
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

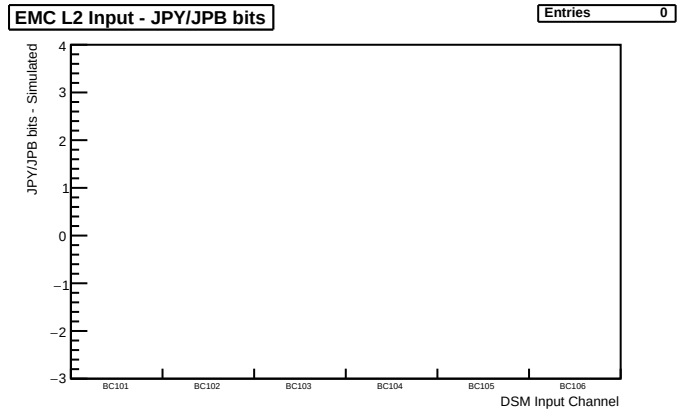

Endcap EMC L1 Input - HT.TP
Entries 360
HT.TP
1
0
1
1
30
29
10
1
10^{-1}
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

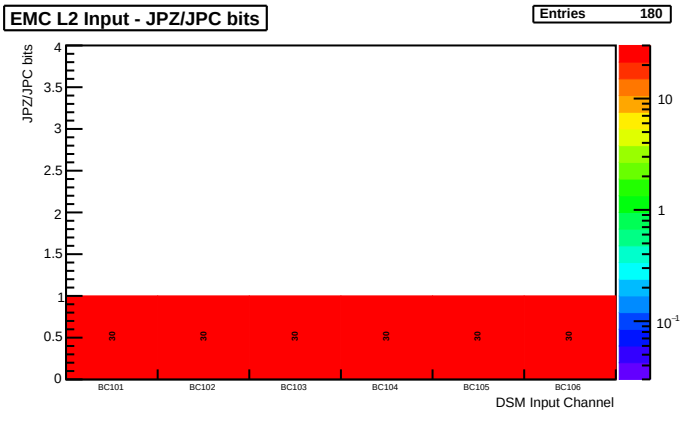

Endcap EMC L1 Input - HT.TP Diff
Entries 0
HT.TP Diff
1
0
-1
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

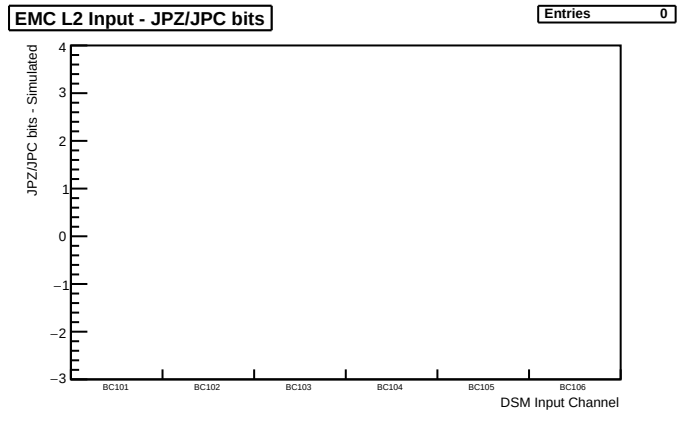

EMC L2 Input - JPX/JPA bits
Entries 180
JPX/JPA bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

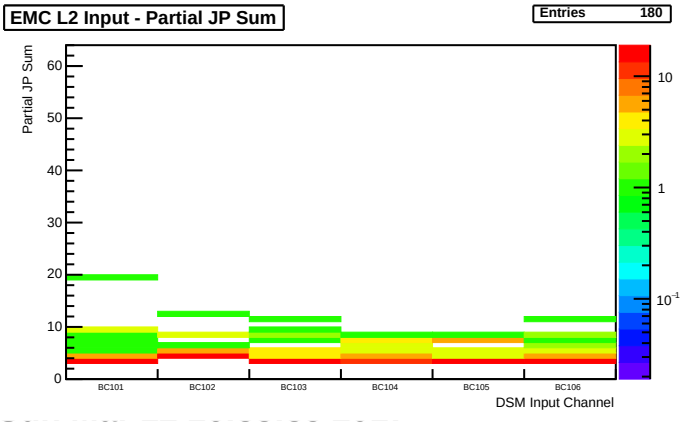

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

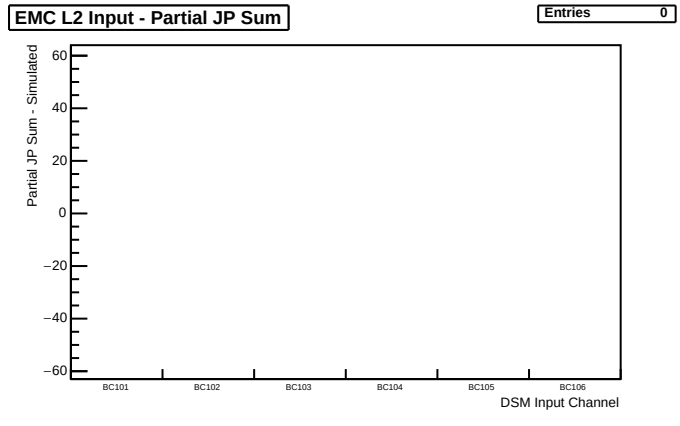

EMC L2 Input - JPY/JPB bits
Entries 180
JPY/JPB bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

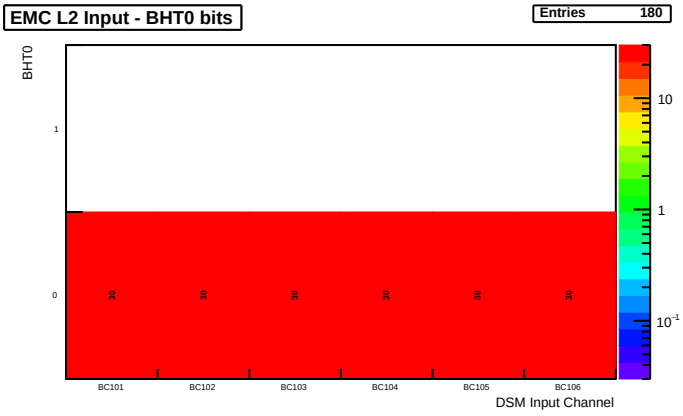

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

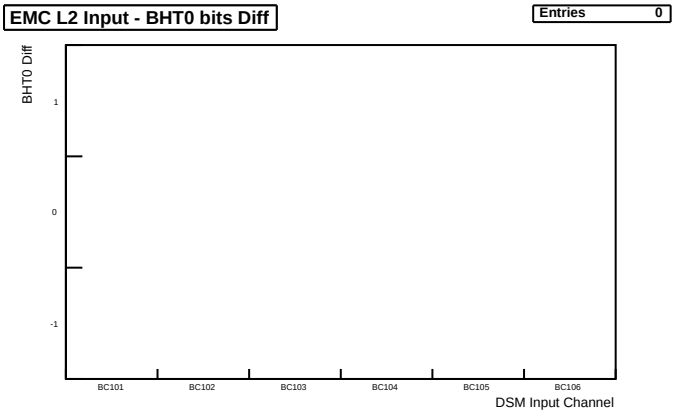

EMC L2 Input - JPZ/JPC bits
Entries 180
JPZ/JPC bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

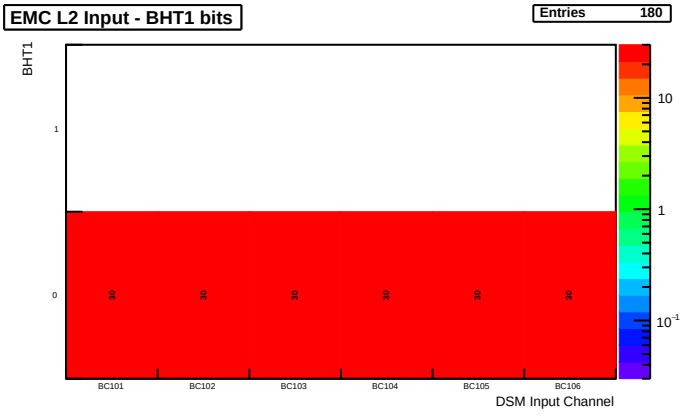

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

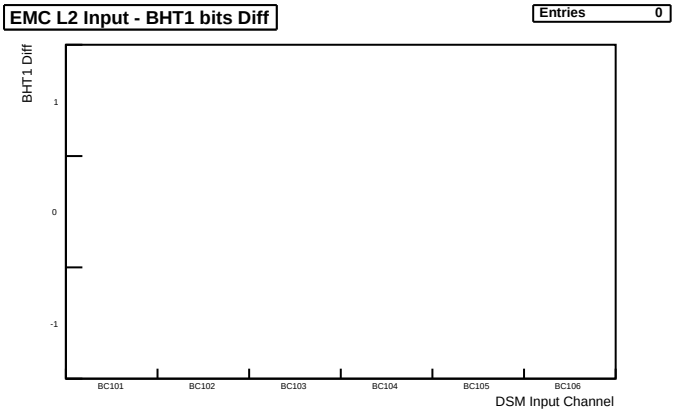

EMC L2 Input - Partial JP Sum
Entries 180
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

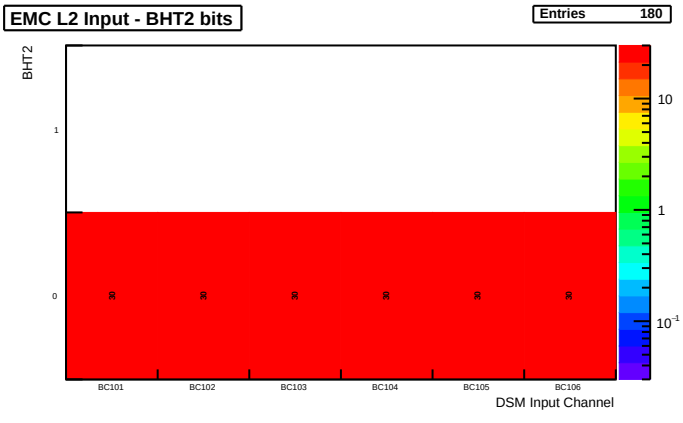

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

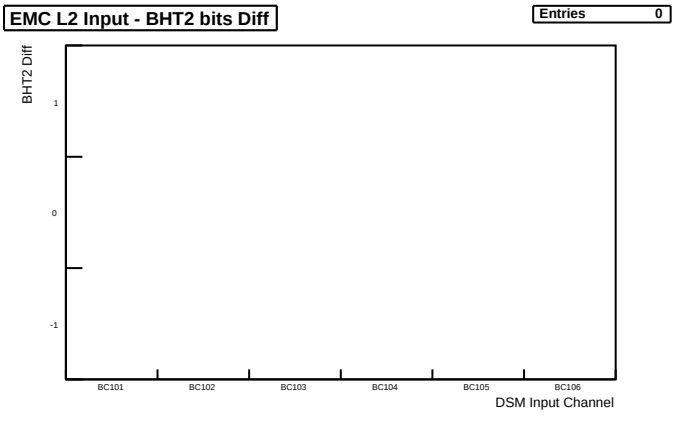

EMC L2 Input - BHT0 bits
Entries 180
BHT0
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

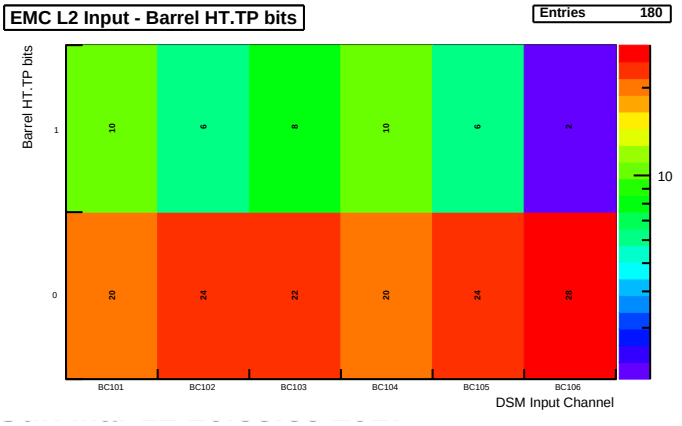

EMC L2 Input - BHT0 bits Diff
Entries 0
BHT0 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

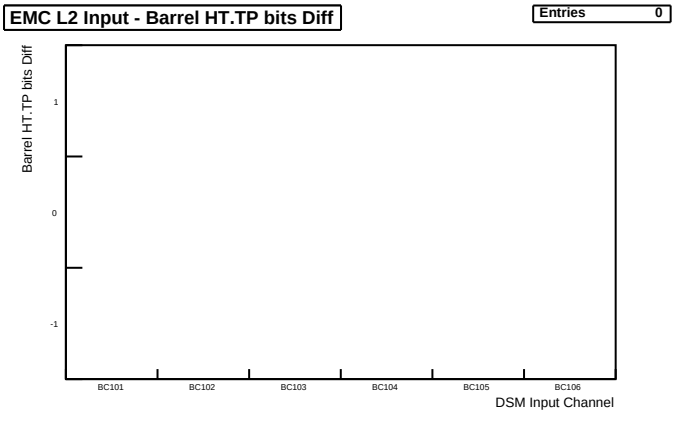

EMC L2 Input - BHT1 bits
Entries 180
BHT1
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

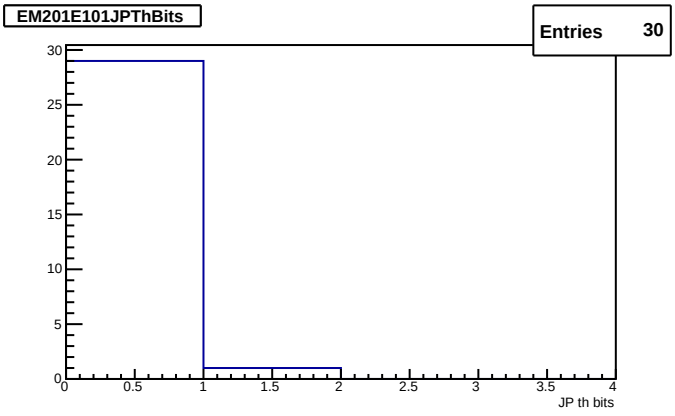

EMC L2 Input - BHT1 bits Diff
Entries 0
BHT1 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

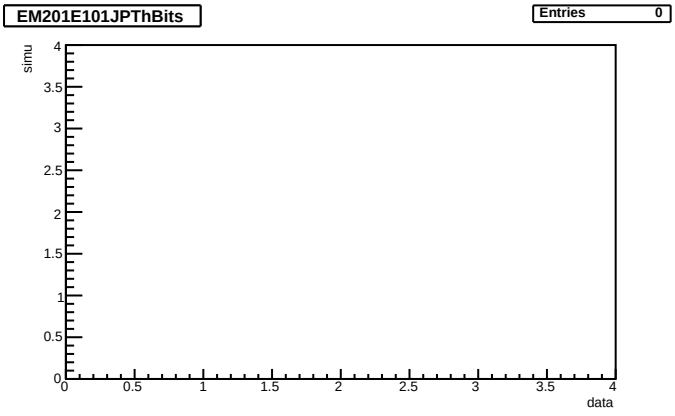

EMC L2 Input - BHT2 bits
Entries 180
BHT2
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

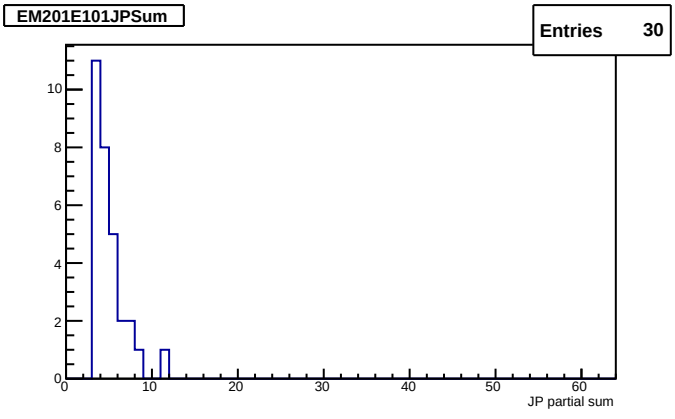

EMC L2 Input - BHT2 bits Diff
Entries 0
BHT2 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

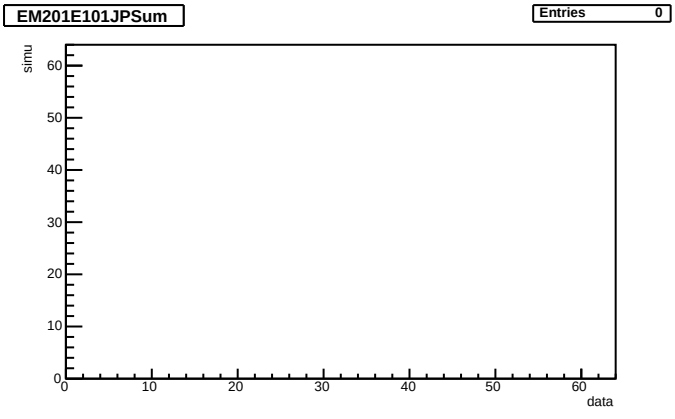

EMC L2 Input - Barrel HT.TP bits
Entries 180
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

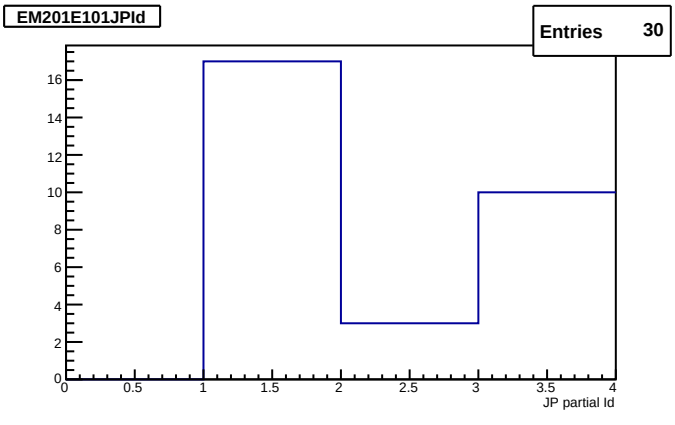

EMC L2 Input - Barrel HT.TP bits Diff
Entries 0
Barrel HT.TP bits Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

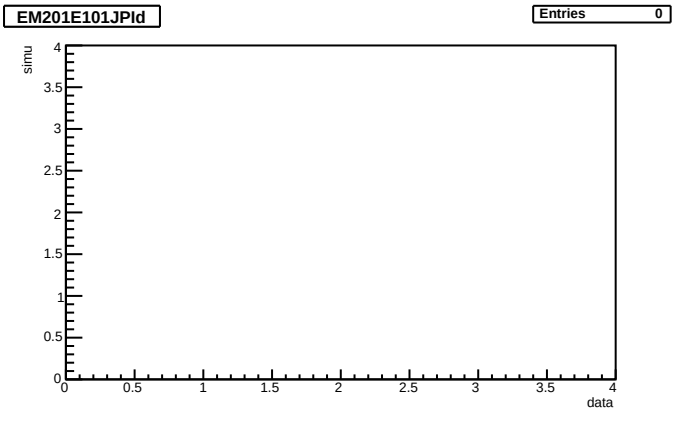

EM201E101JPThBits
Entries 30
JP th bits

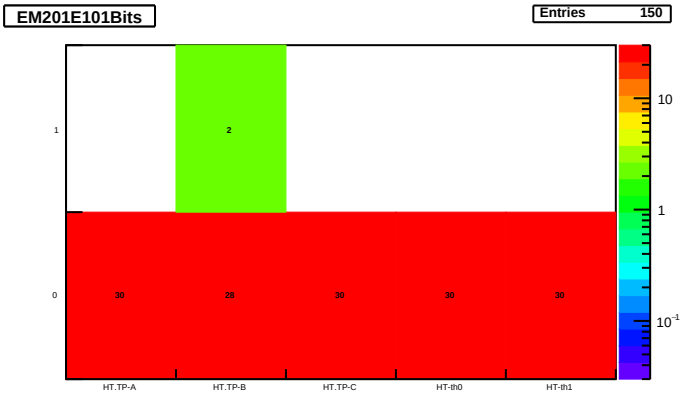

EM201E101JPThBits
Entries 0
simu
data

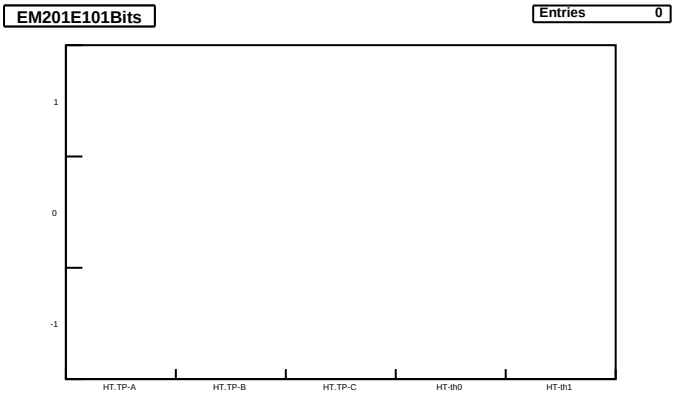

EM201E101JPSum
Entries 30
JP partial sum

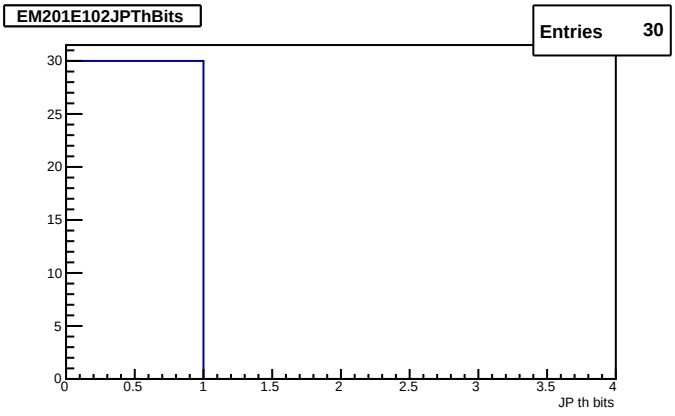

EM201E101JPSum
Entries 0
simu
data

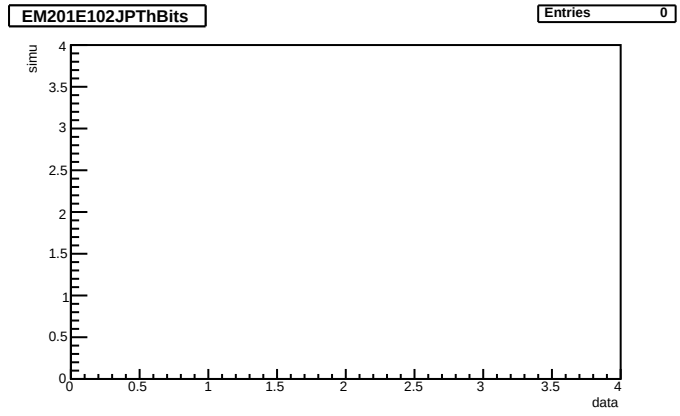

EM201E101JPId
Entries 30
JP partial Id

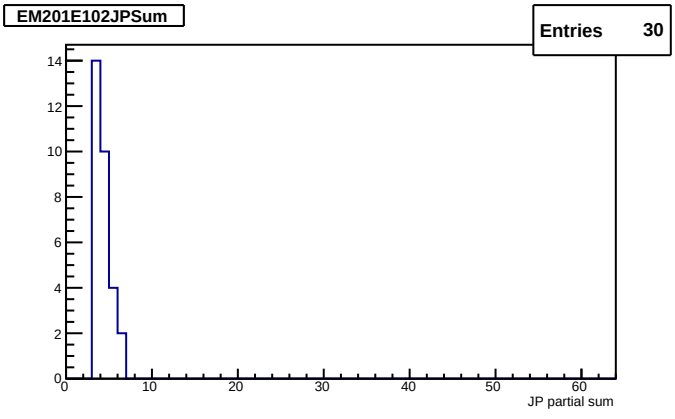

EM201E101JPId
Entries 0
simu
data

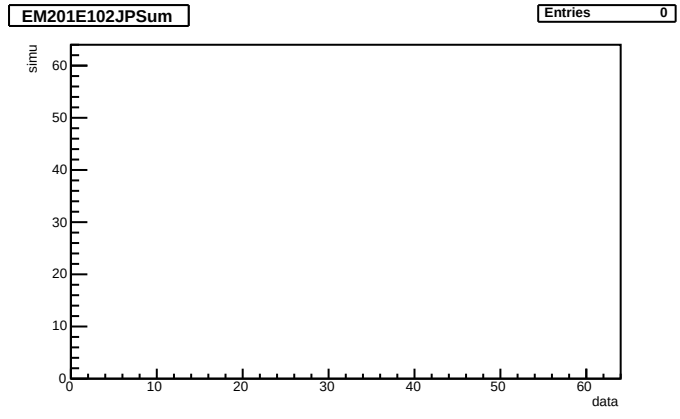

EM201E101Bits
Entries 150
2
30
28
30
30
30
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

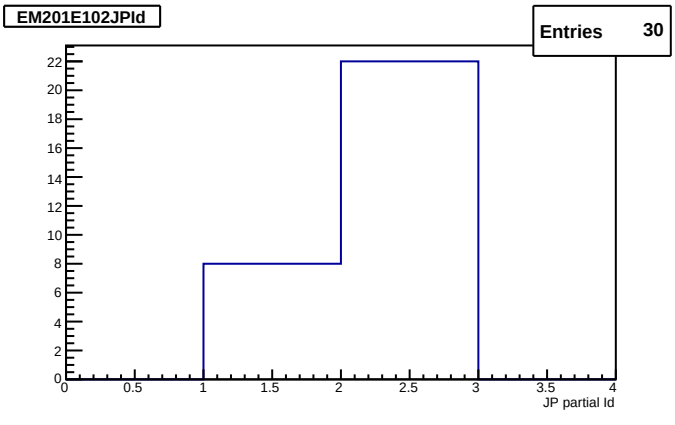

EM201E101Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

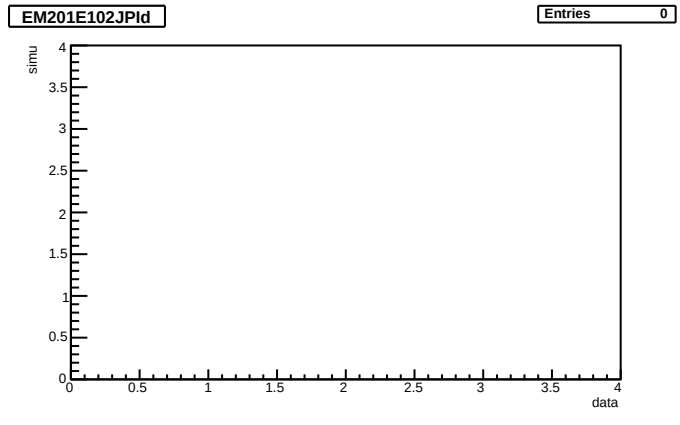

EM201E102JPThBits
Entries 30
JP th bits

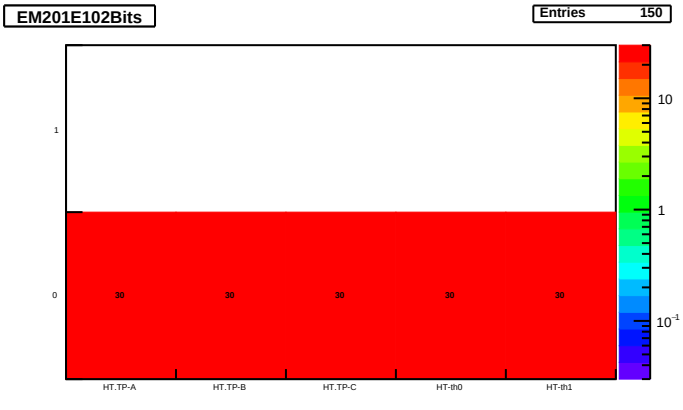

EM201E102JPThBits
Entries 0
simu
data

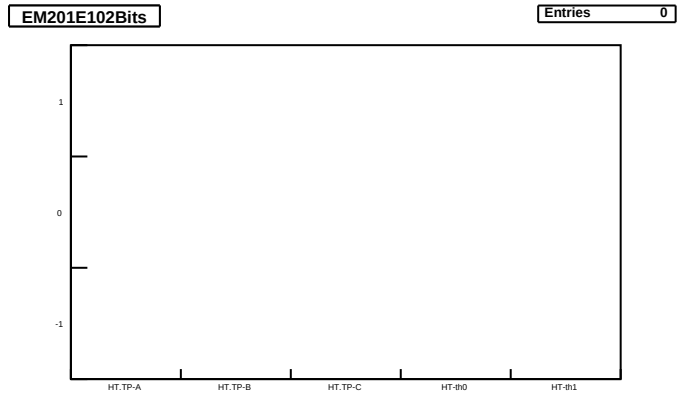

EM201E102JPSum
Entries 30
JP partial sum

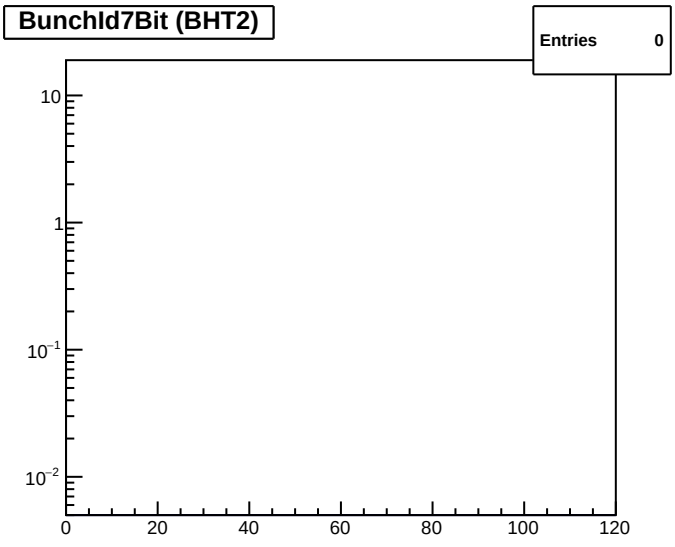

EM201E102JPSum
Entries 0
simu
data

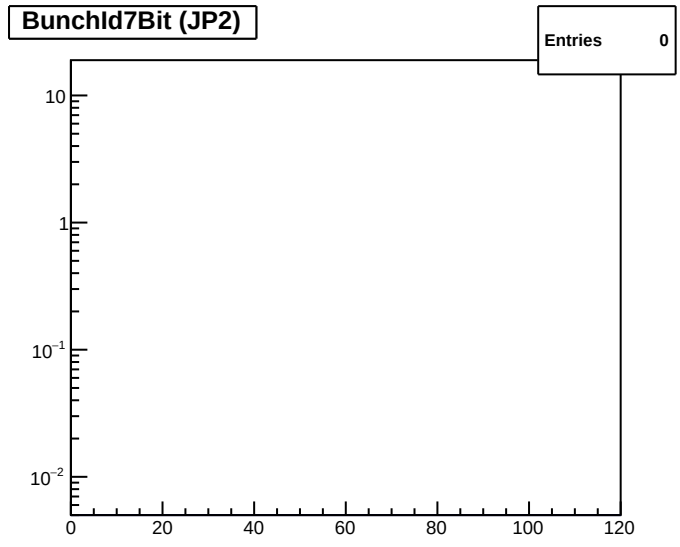

EM201E102JPId
Entries 30
JP partial Id

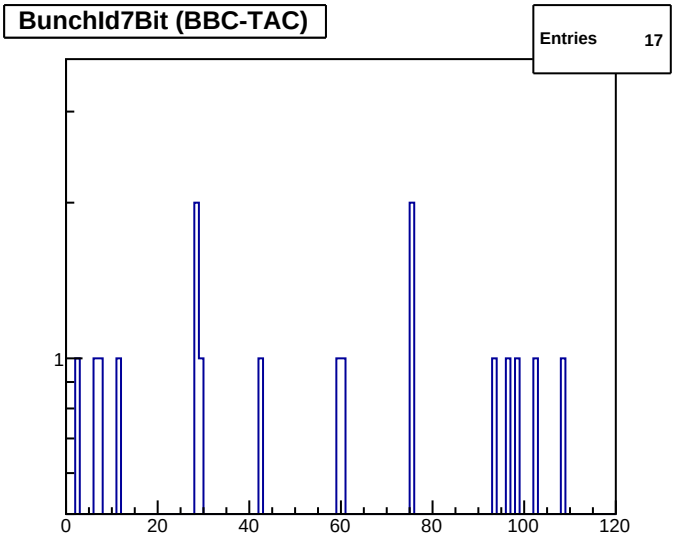

EM201E102JPId
Entries 0
simu
data

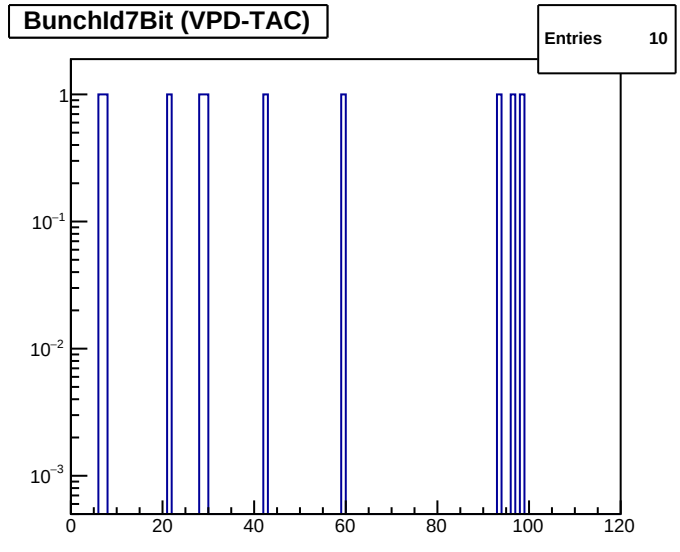

EM201E102Bits
Entries 150
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

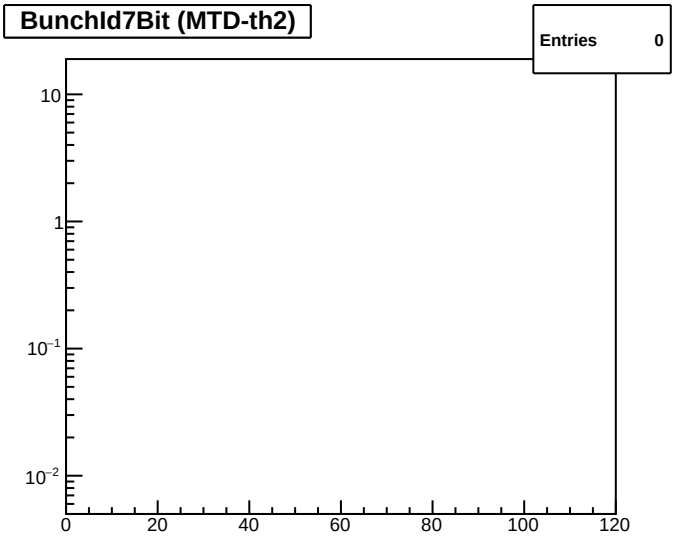

EM201E102Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

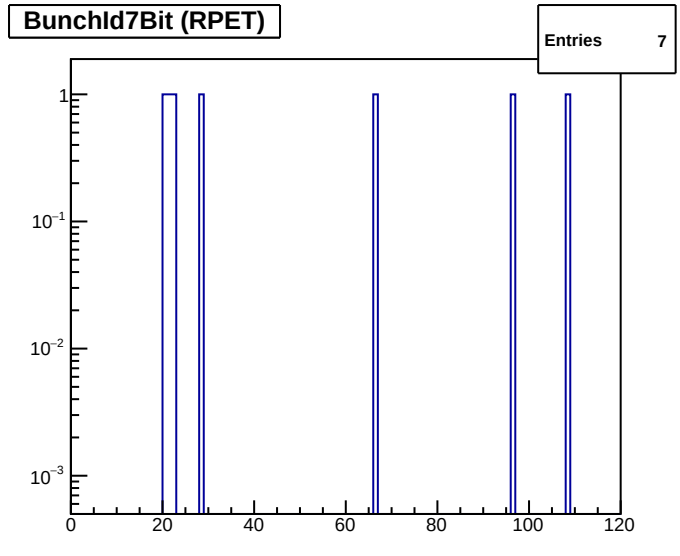

BunchId7Bit (BHT2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

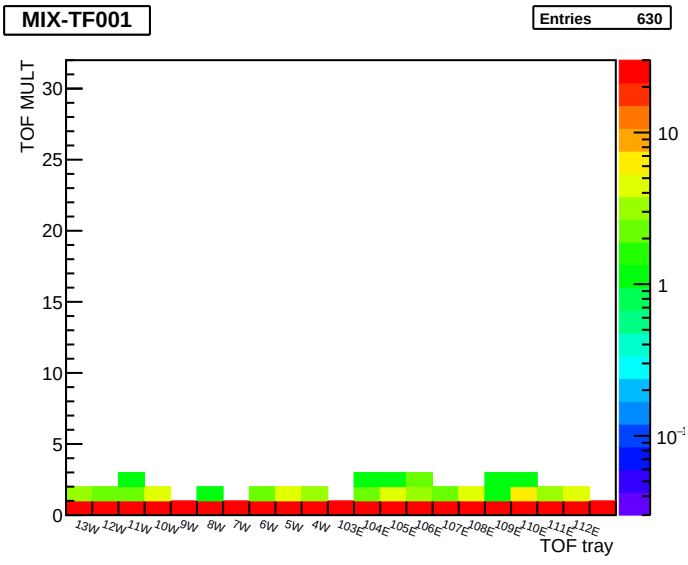

BunchId7Bit (JP2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

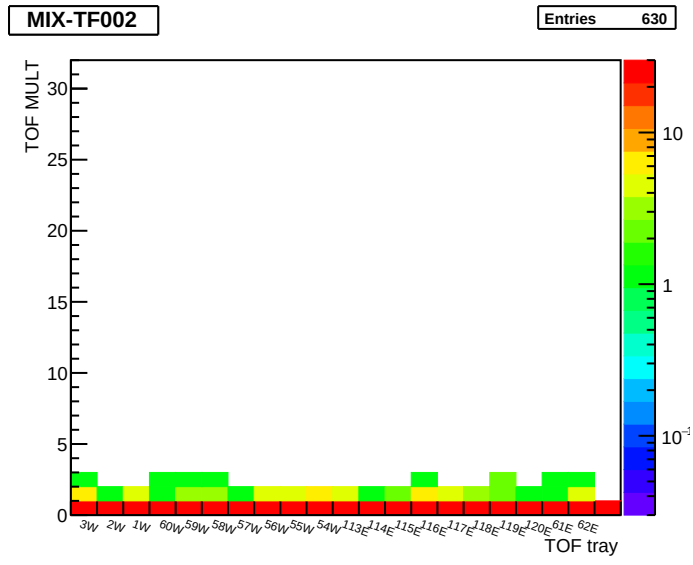

BunchId7Bit (BBC-TAC)
Entries 17
1
0 20 40 60 80 100 120

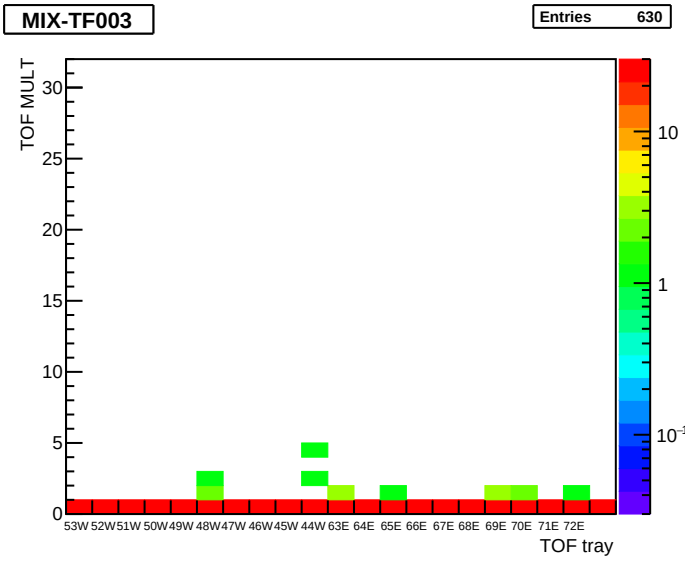

BunchId7Bit (VPD-TAC)
Entries 10
1
10^{-1}
10^{-2}
10^{-3}
0 20 40 60 80 100 120

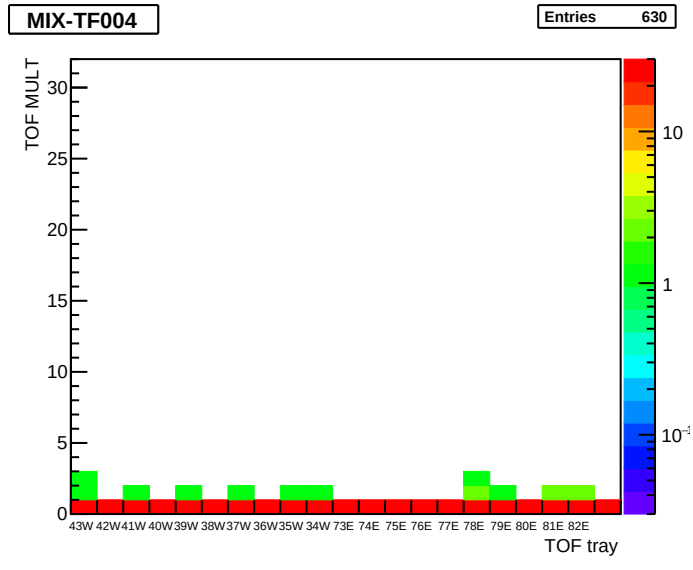

BunchId7Bit (MTD-th2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

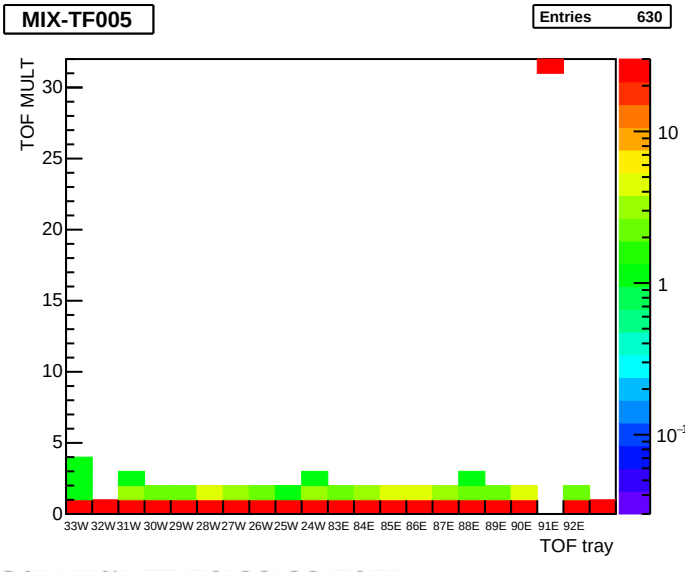

BunchId7Bit (RPET)
Entries 7
1
10^{-1}
10^{-2}
10^{-3}
0 20 40 60 80 100 120

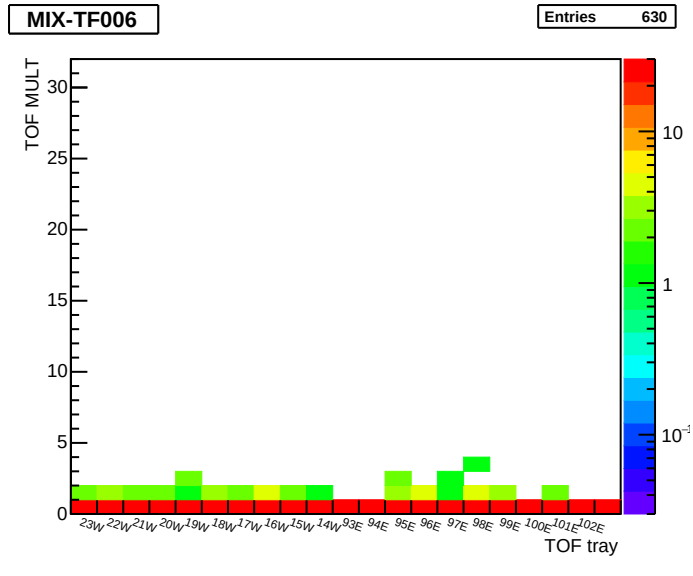

MIX-TF001
Entries 630
TOF MULT
TOF tray

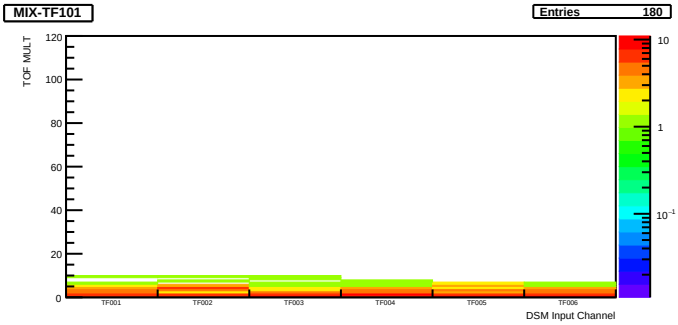

MIX-TF002
Entries 630
TOF MULT
TOF tray

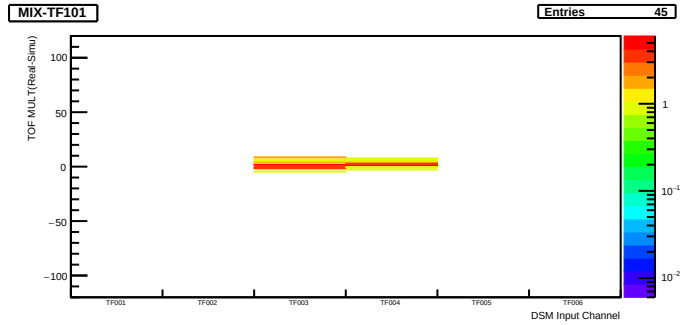

MIX-TF003
Entries 630
TOF MULT
TOF tray

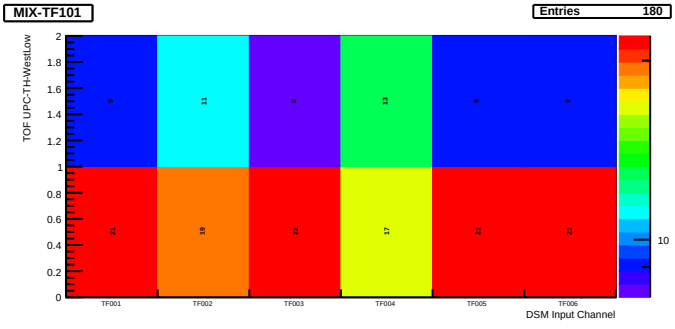

MIX-TF004
Entries 630
TOF MULT
TOF tray

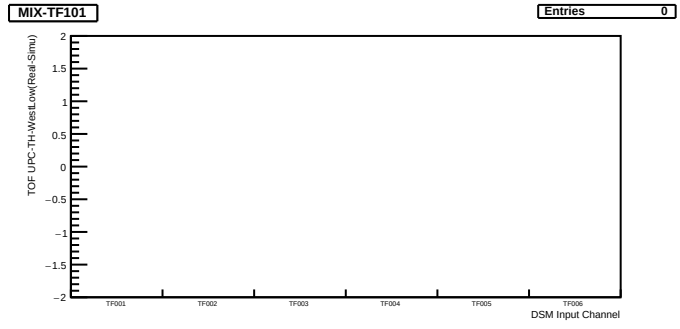

MIX-TF005
Entries 630
TOF MULT
TOF tray

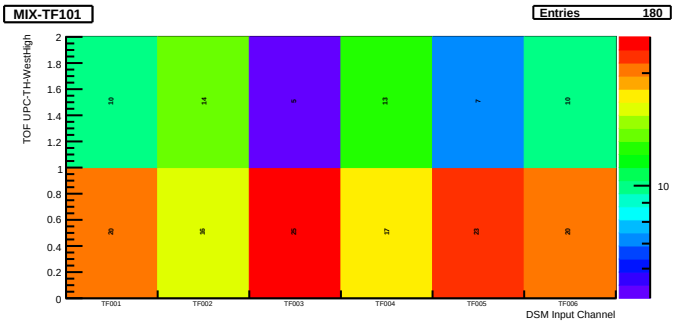

MIX-TF006
Entries 630
TOF MULT
TOF tray

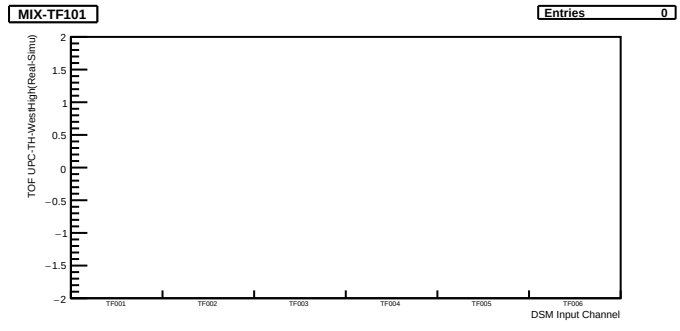

MIX-TF101
Entries 180
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

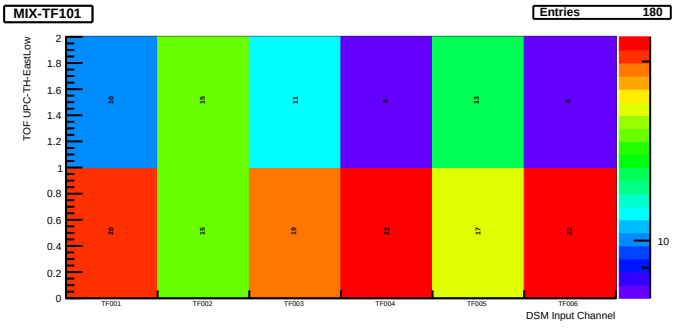

MIX-TF101
Entries 45
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

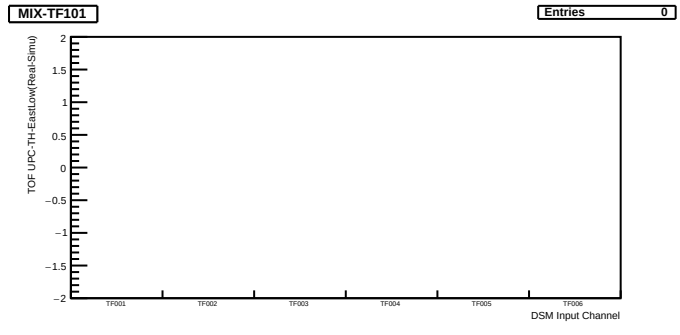

MIX-TF101
Entries 180
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

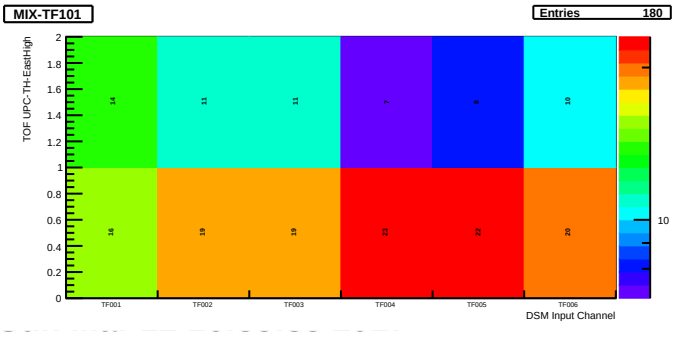

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

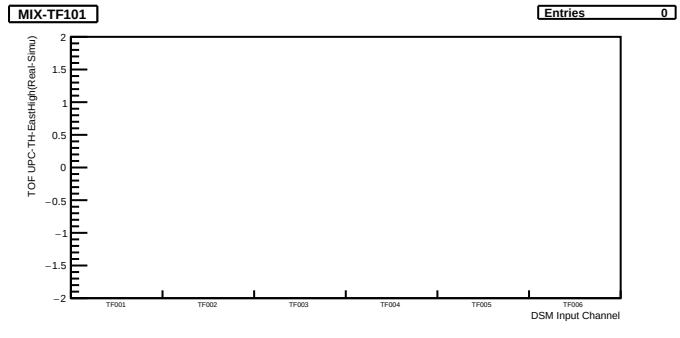

MIX-TF101
Entries 180
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

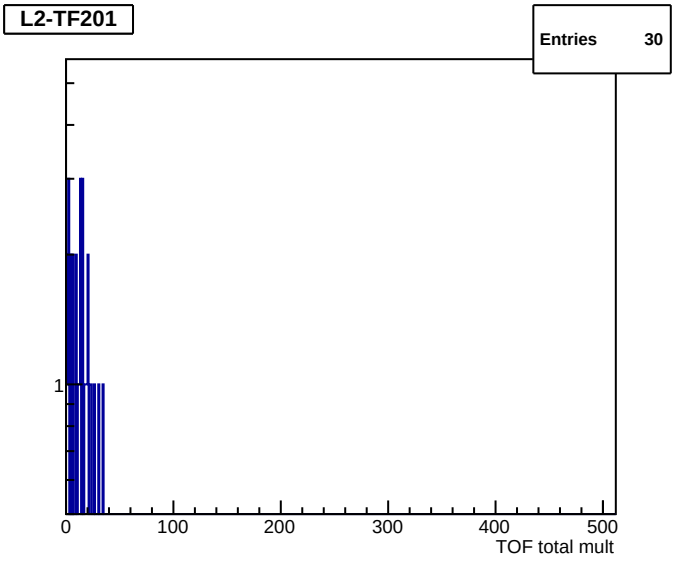

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

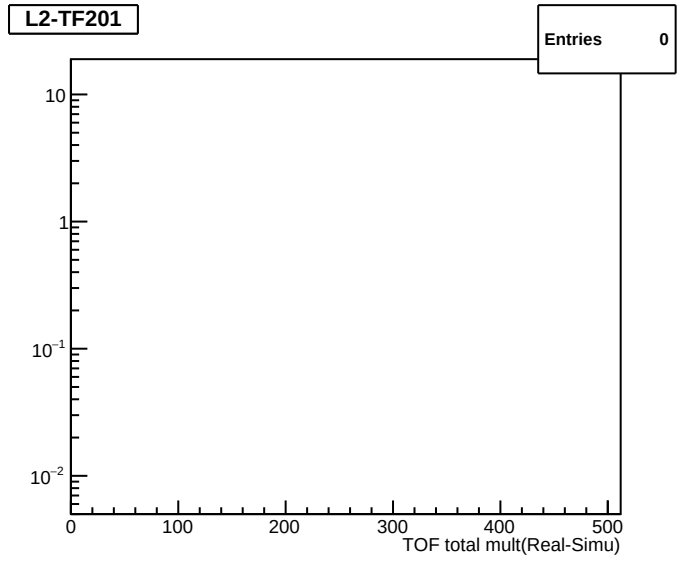

MIX-TF101
Entries 180
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

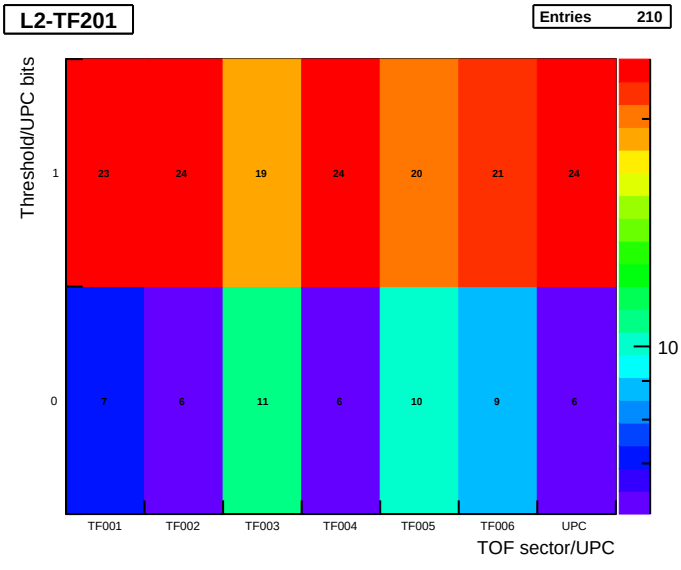

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

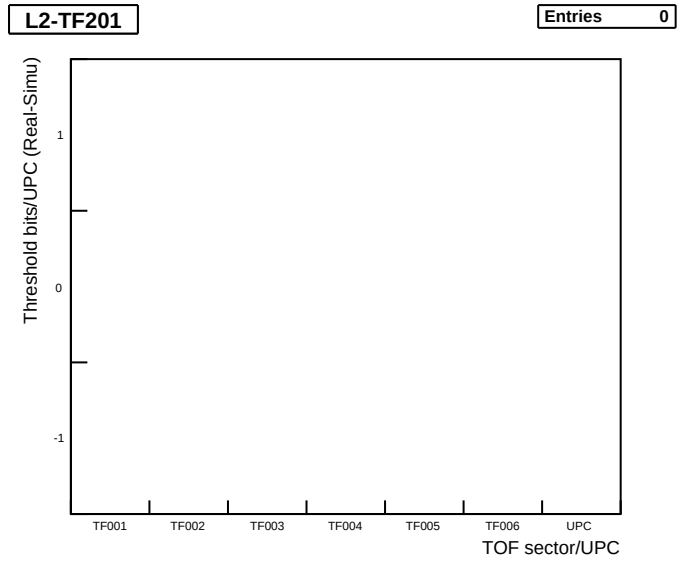

MIX-TF101
Entries 180
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

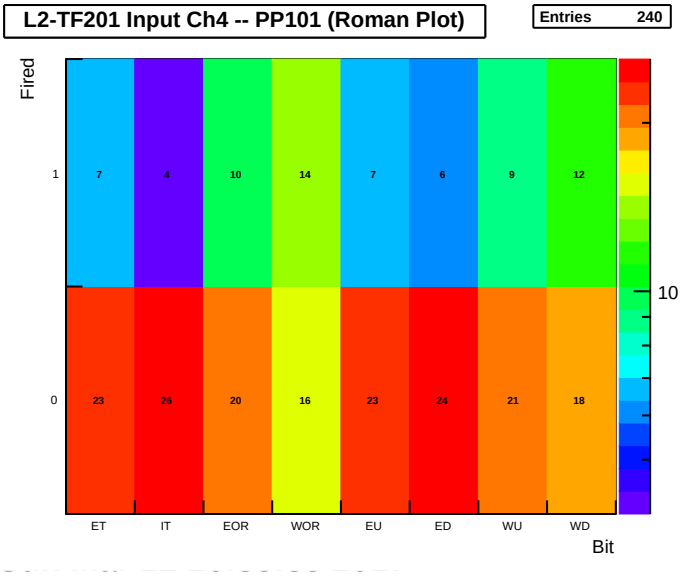

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

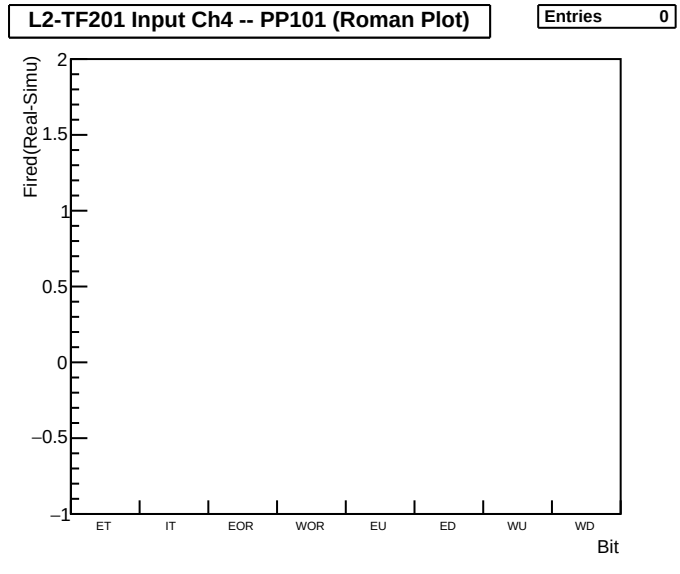

L2-TF201
Entries 30
1
0
100
200
300
400
500
TOF total mult

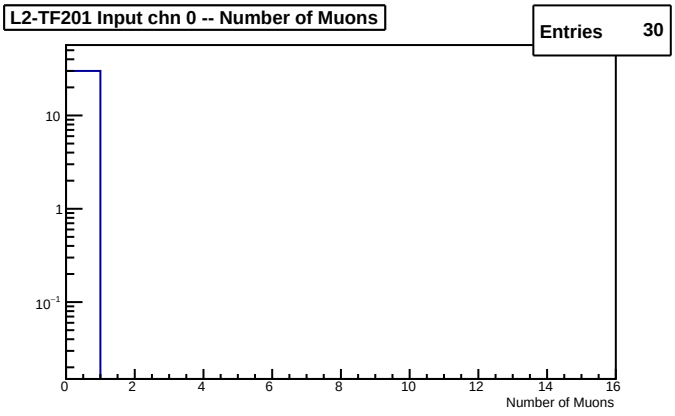

L2-TF201
Entries 0
10
1
10^{-1}
10^{-2}
0
100
200
300
400
500
TOF total mult(Real-Simu)

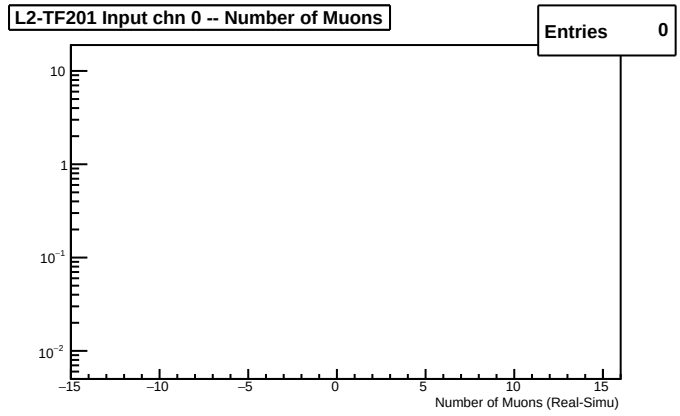

L2-TF201
Entries 210
Threshold/UPC bits
1
0
23 24 19 24 20 21 24
7 6 11 6 10 9 6
10
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

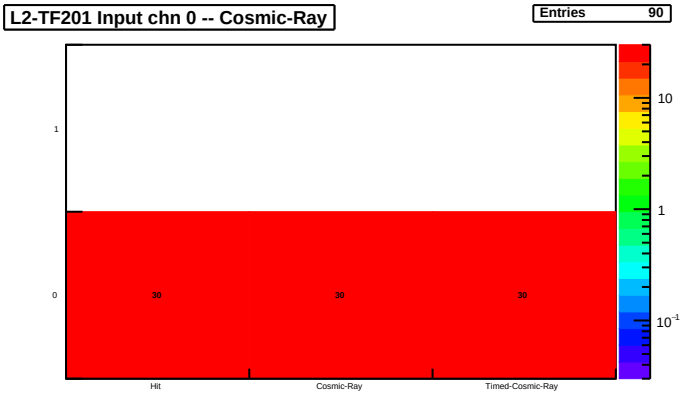

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
1
0
-1
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

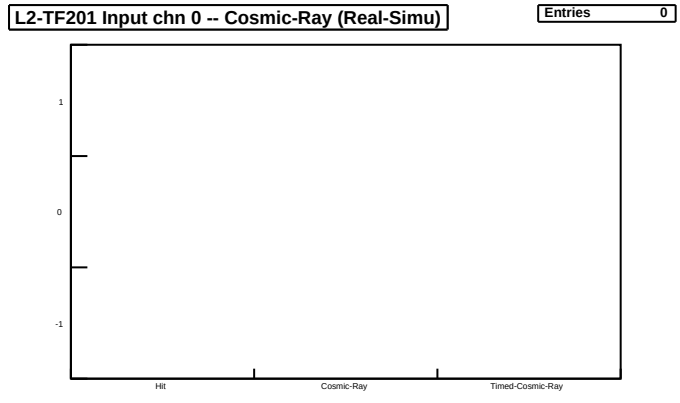

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 240
Fired
1
0
7 4 10 14 7 6 9 12
23 26 20 16 23 24 21 18
10
ET IT EOR WOR EU ED WU WD
Bit

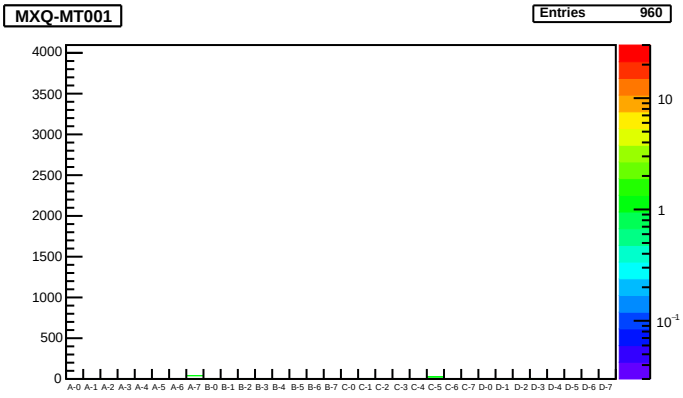

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 0
Fired(Real-Simu)
2
1.5
1
0.5
0
−0.5
−1
ET IT EOR WOR EU ED WU WD
Bit

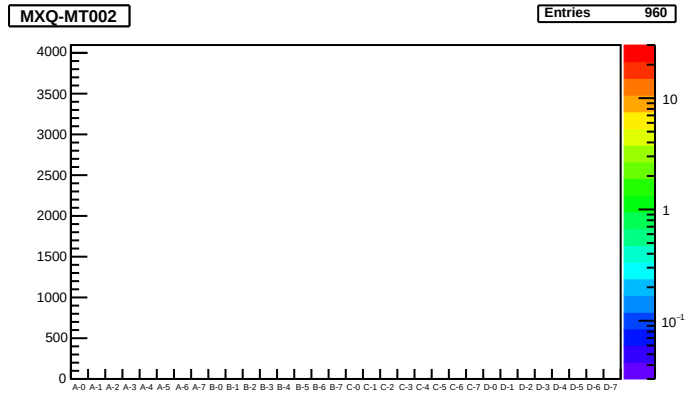

L2-TF201 Input chn 0 -- Number of Muons
Entries 30
Number of Muons

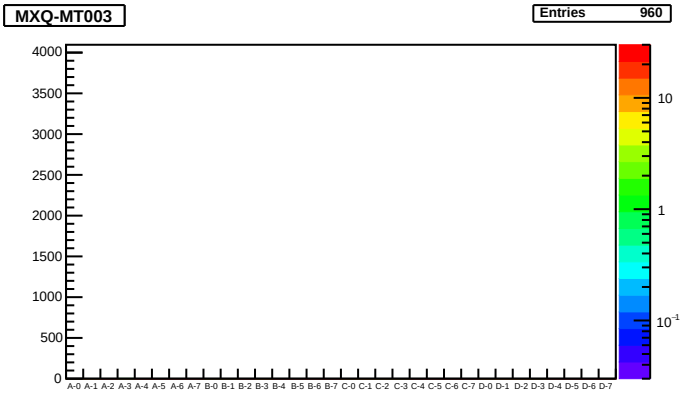

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

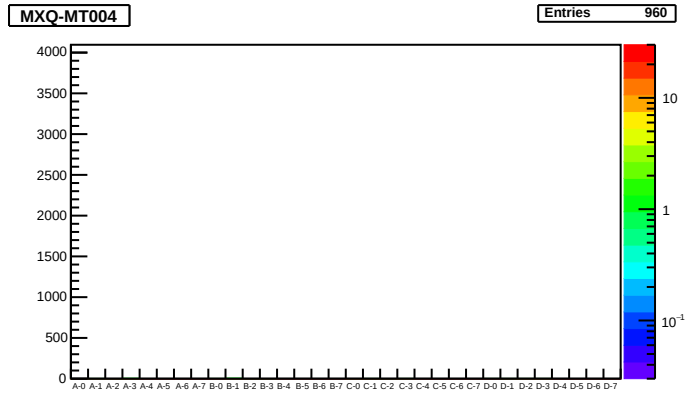

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 90
Hit
Cosmic-Ray
Timed-Cosmic-Ray

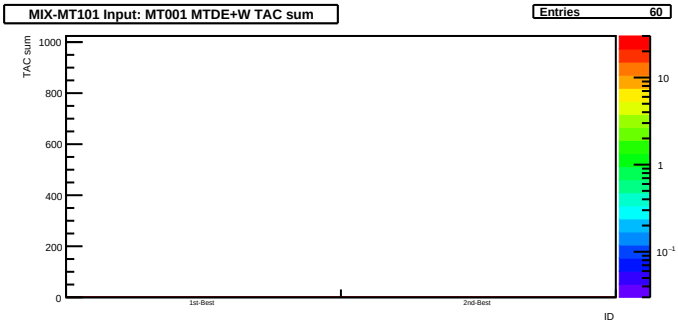

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

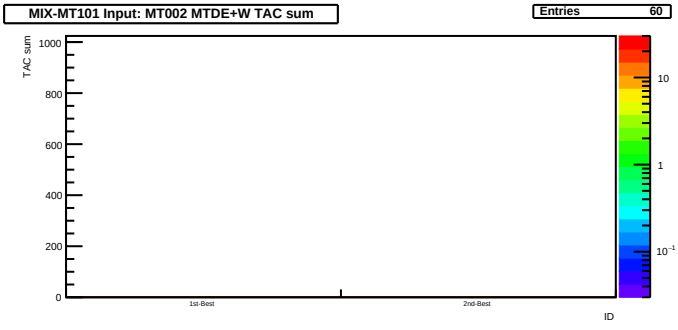

MXQ-MT001
Entries 960
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10
1
10^{-1}

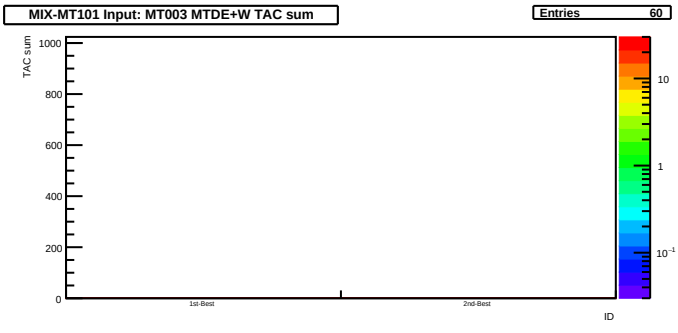

MXQ-MT002
Entries 960
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10
1
10^{-1}

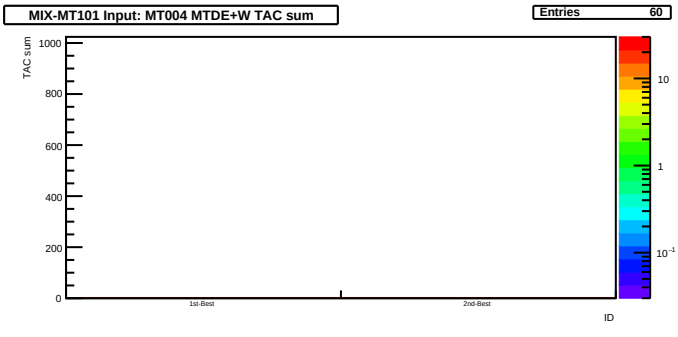

MXQ-MT003
Entries 960
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10
1
10^{-1}

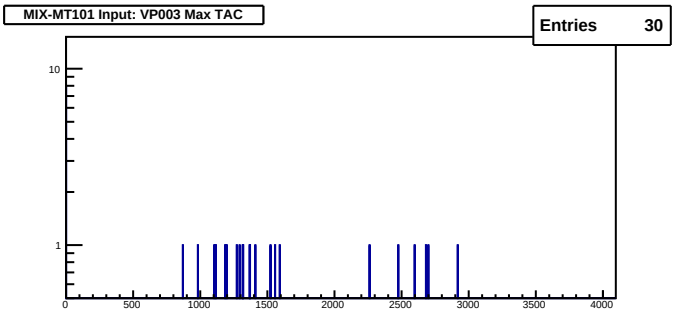

MXQ-MT004
Entries 960
4000
3500
3000
2500
2000
1500
1000
500
0
A-0 A-1 A-2 A-3 A-4 A-5 A-6 A-7 B-0 B-1 B-2 B-3 B-4 B-5 B-6 B-7 C-0 C-1 C-2 C-3 C-4 C-5 C-6 C-7 D-0 D-1 D-2 D-3 D-4 D-5 D-6 D-7
10
1
10^{-1}

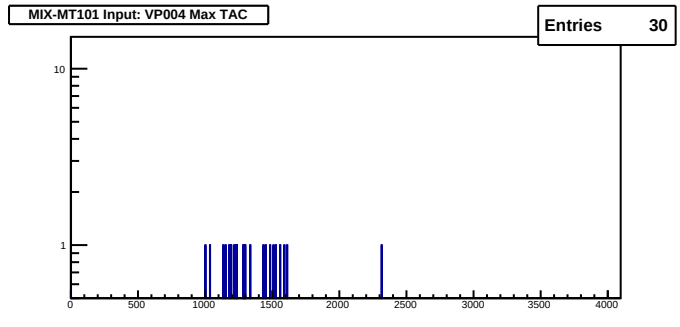

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 60
TAC sum
1000
800
600
400
200
0
1st-Best
2nd-Best
ID
10
1
10^{-1}

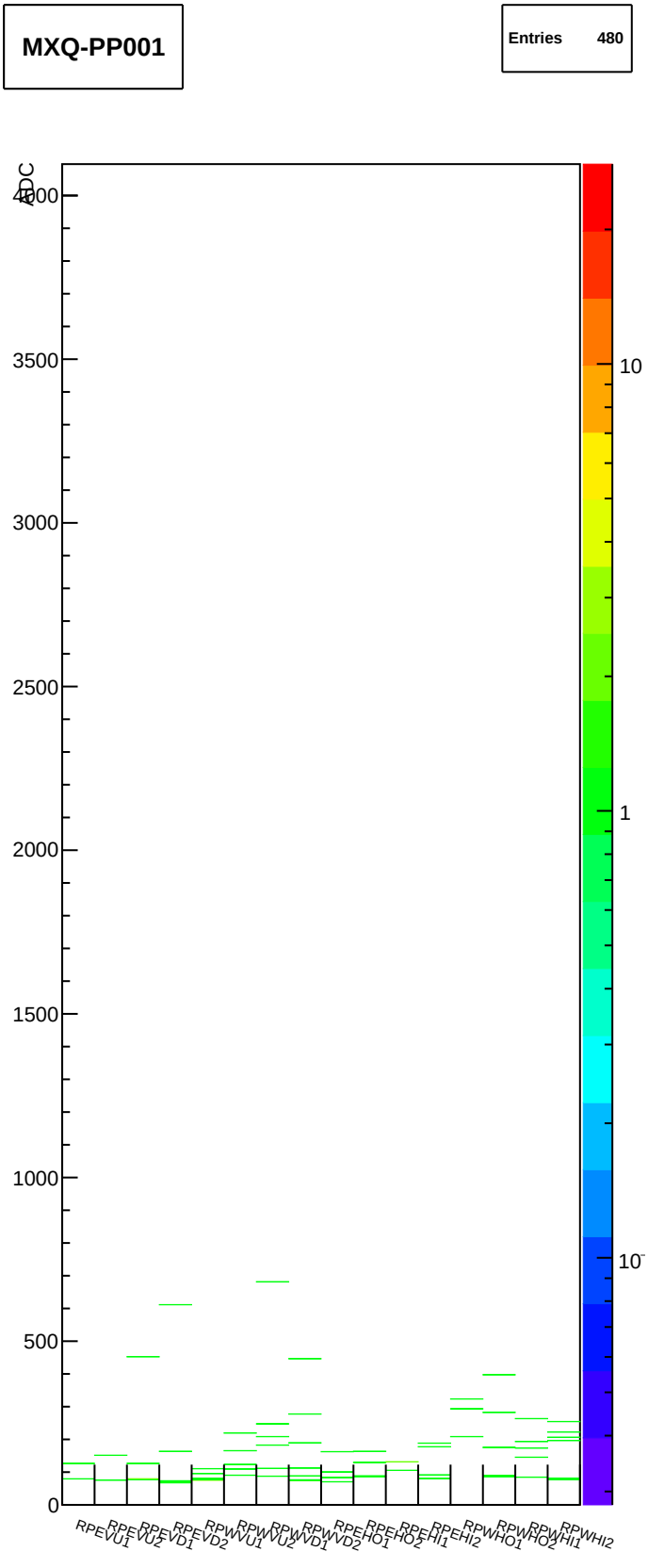

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 60
TAC sum
1000
800
600
400
200
0
1st-Best
2nd-Best
ID
10
1
10^{-1}

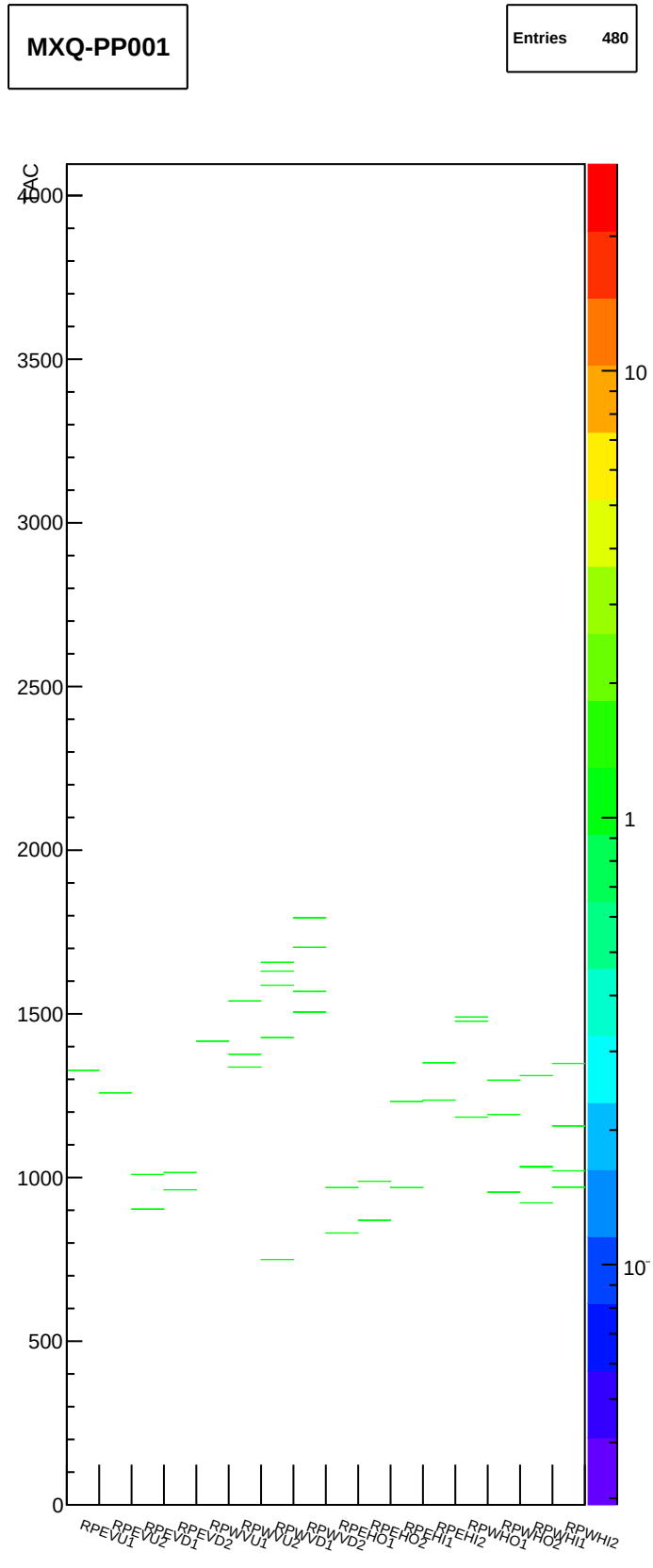

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 60
TAC sum
1000
800
600
400
200
0
1st-Best
2nd-Best
ID
10
1
10^{-1}

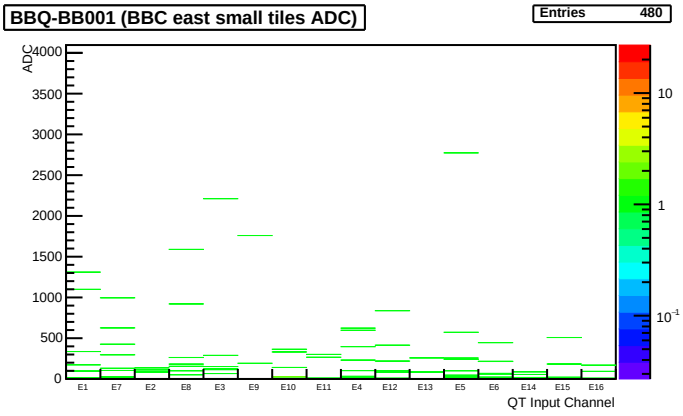

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 60
TAC sum
1000
800
600
400
200
0
1st-Best
2nd-Best
ID
10
1
10^{-1}

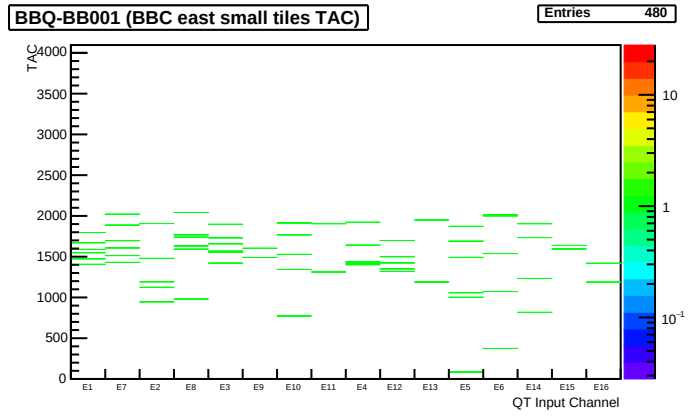

MIX-MT101 Input: VP003 Max TAC
Entries 30
10
1
0
500
1000
1500
2000
2500
3000
3500
4000

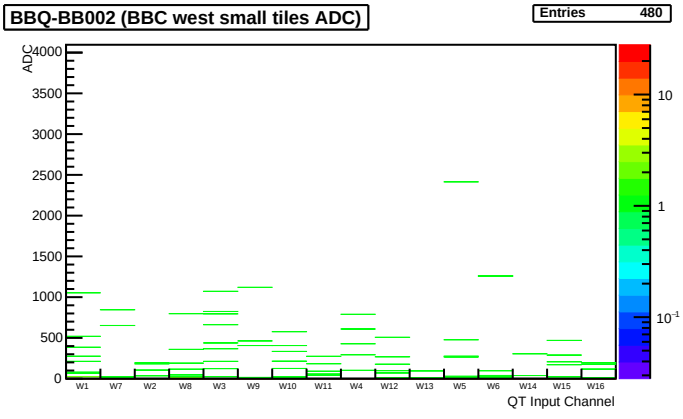

MIX-MT101 Input: VP004 Max TAC
Entries 30
10
1
0
500
1000
1500
2000
2500
3000
3500
4000

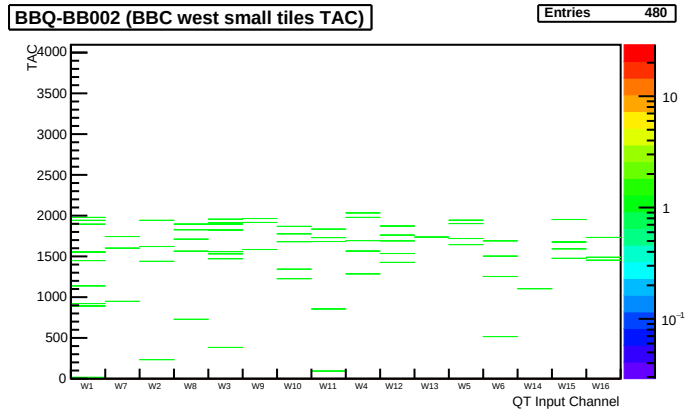

MXQ-PP001
Entries 480
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
10
1
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2

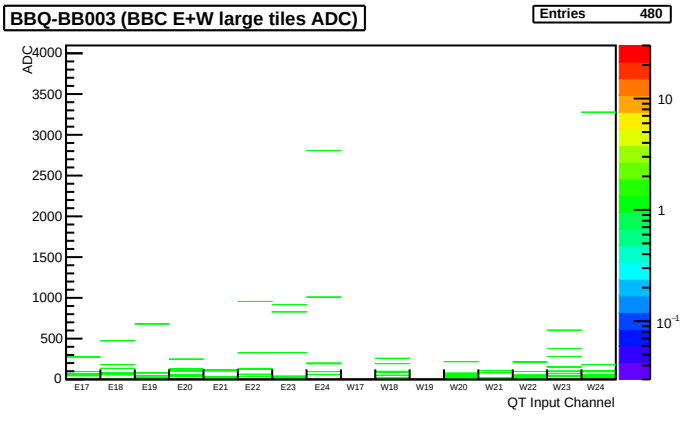

MXQ-PP001
Entries 480
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
10
1
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2

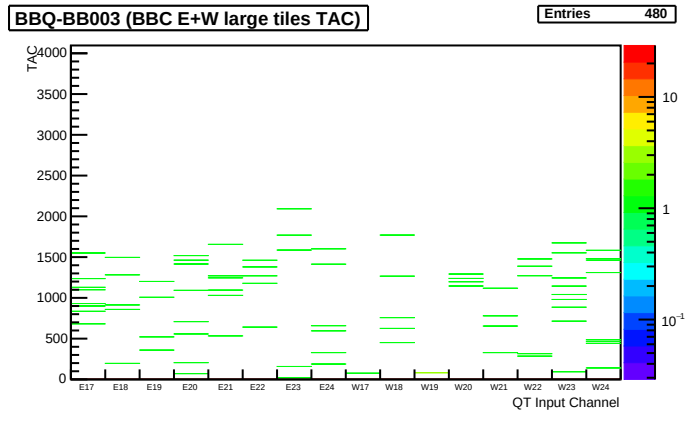

BBQ-BB001 (BBC east small tiles ADC)
Entries 480
ADC
QT Input Channel

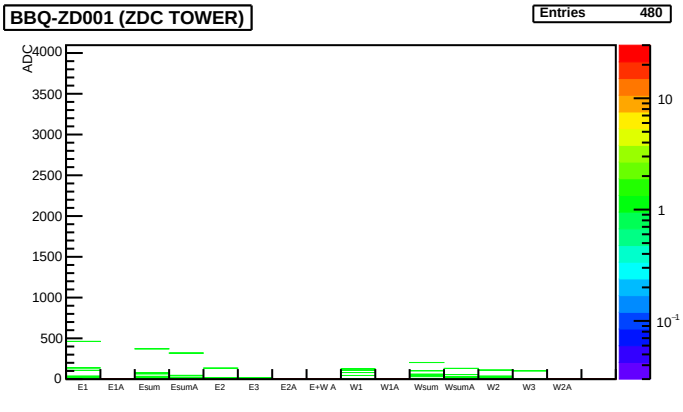

BBQ-BB001 (BBC east small tiles TAC)
Entries 480
TAC
QT Input Channel

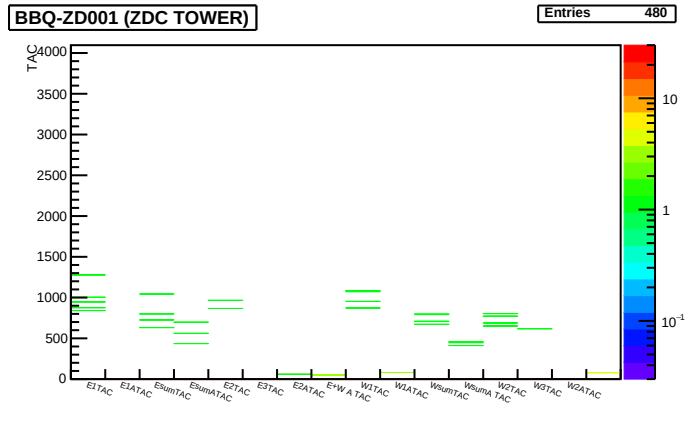

BBQ-BB002 (BBC west small tiles ADC)
Entries 480
ADC
QT Input Channel

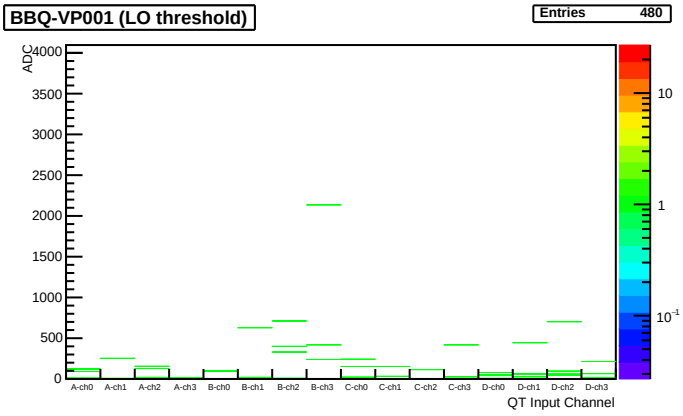

BBQ-BB002 (BBC west small tiles TAC)
Entries 480
TAC
QT Input Channel

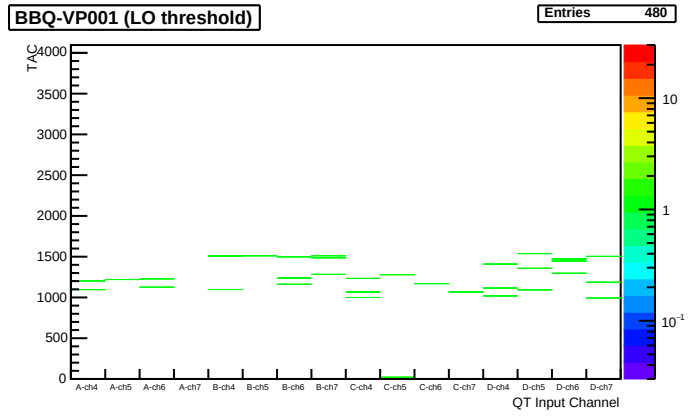

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 480
ADC
QT Input Channel

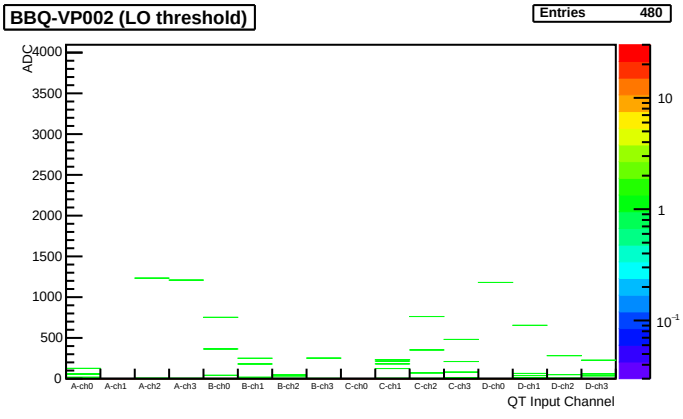

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 480
TAC
QT Input Channel

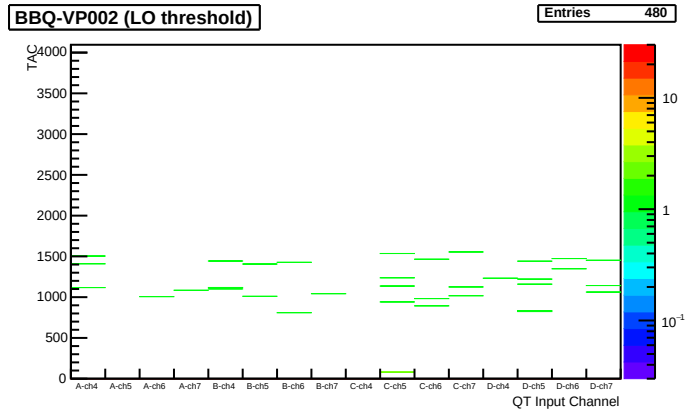

BBQ-ZD001 (ZDC TOWER)
Entries 480
ADC

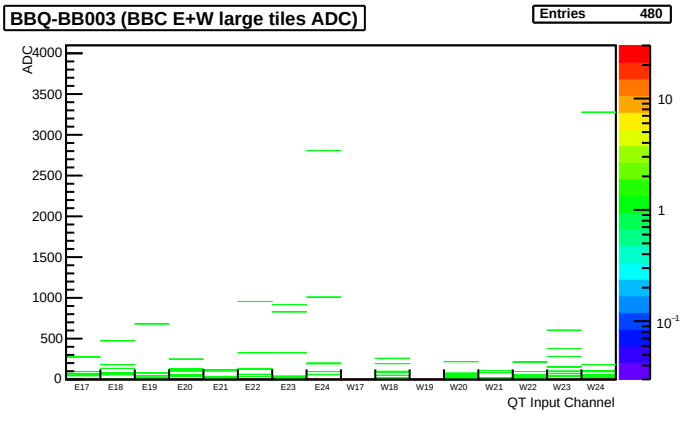

BBQ-ZD001 (ZDC TOWER)
Entries 480
TAC

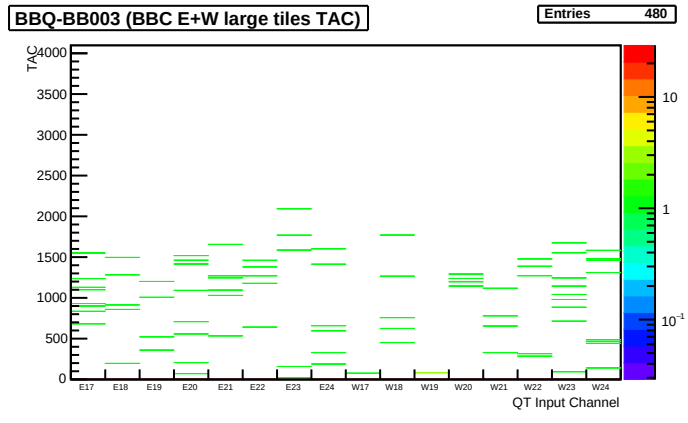

BBQ-VP001 (LO threshold)
Entries 480
ADC
QT Input Channel

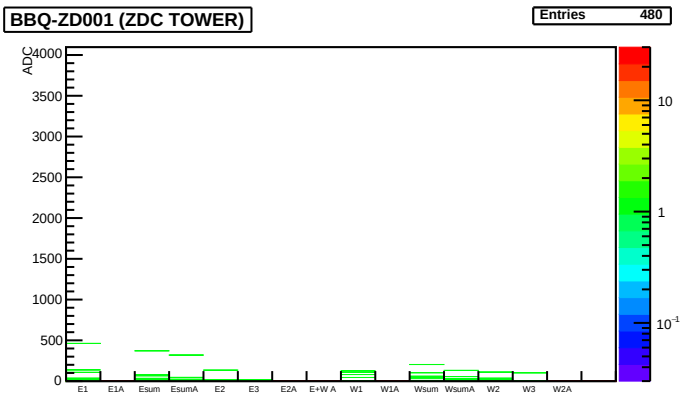

BBQ-VP001 (LO threshold)
Entries 480
TAC
QT Input Channel

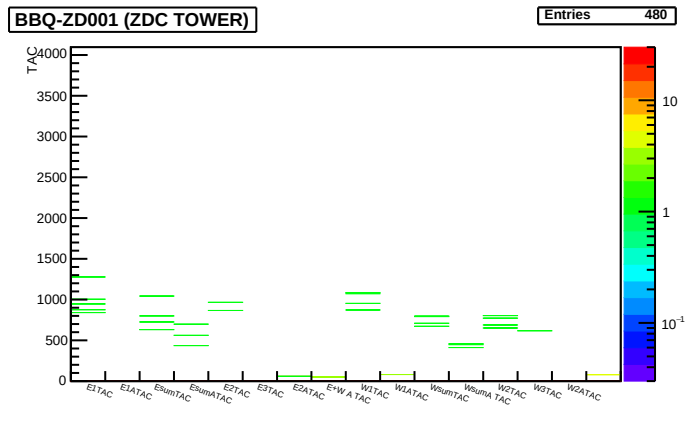

BBQ-VP002 (LO threshold)
Entries 480
ADC
QT Input Channel

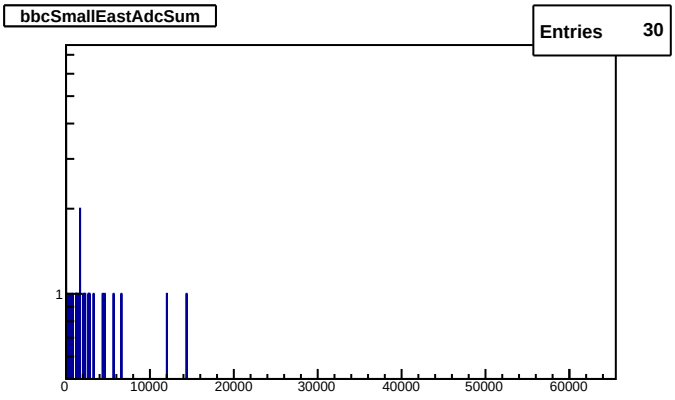

BBQ-VP002 (LO threshold)
Entries 480
TAC
QT Input Channel

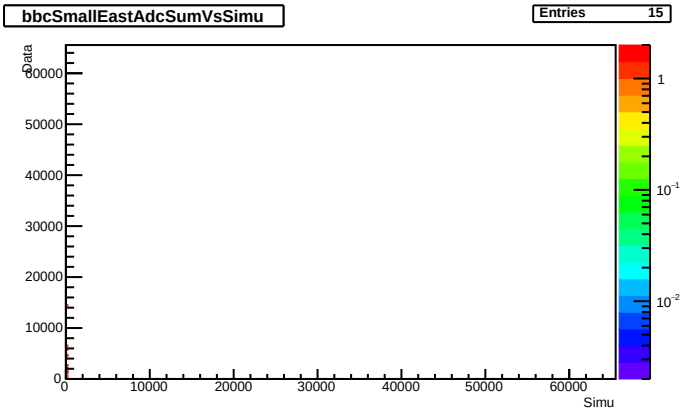

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 480
ADC
QT Input Channel

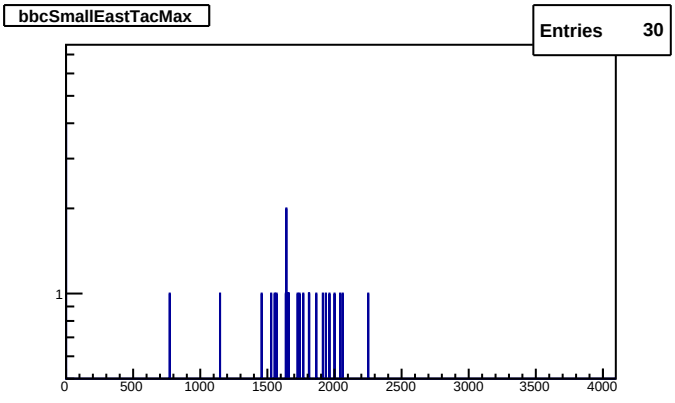

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 480
TAC
QT Input Channel

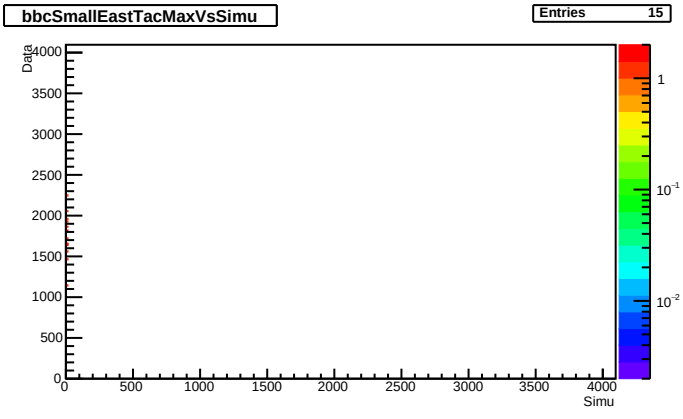

BBQ-ZD001 (ZDC TOWER)
Entries 480
ADC

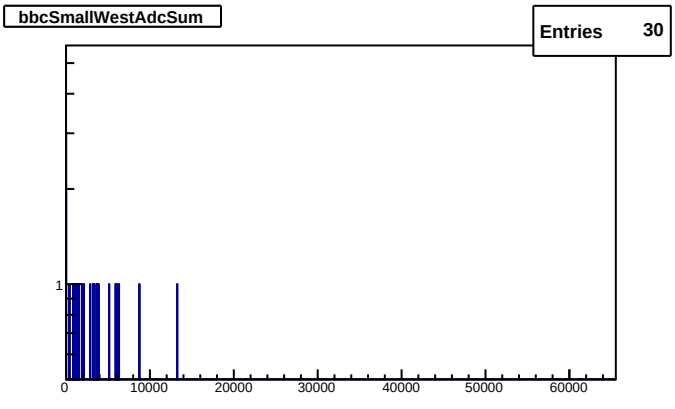

BBQ-ZD001 (ZDC TOWER)
Entries 480
TAC

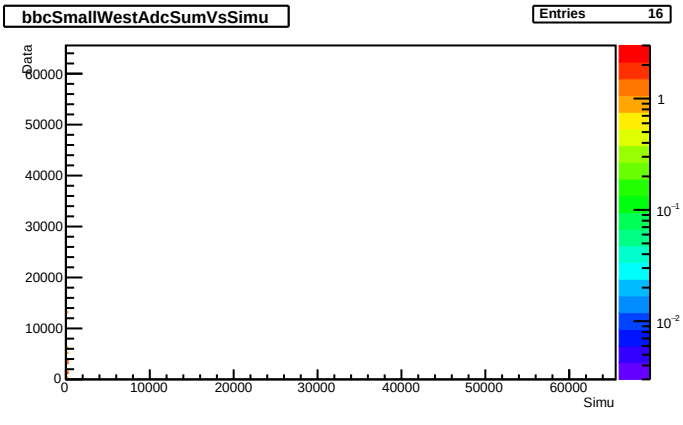

bbcSmallEastAdcSum
Entries 30

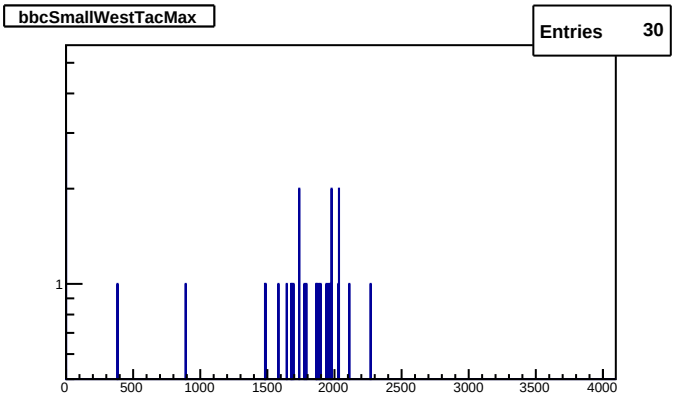

bbcSmallEastAdcSumVsSimu
Entries 15
Data
Simu

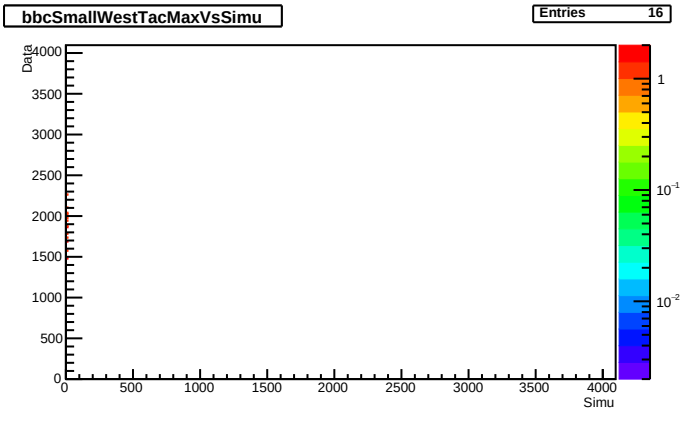

bbcSmallEastTacMax
Entries 30

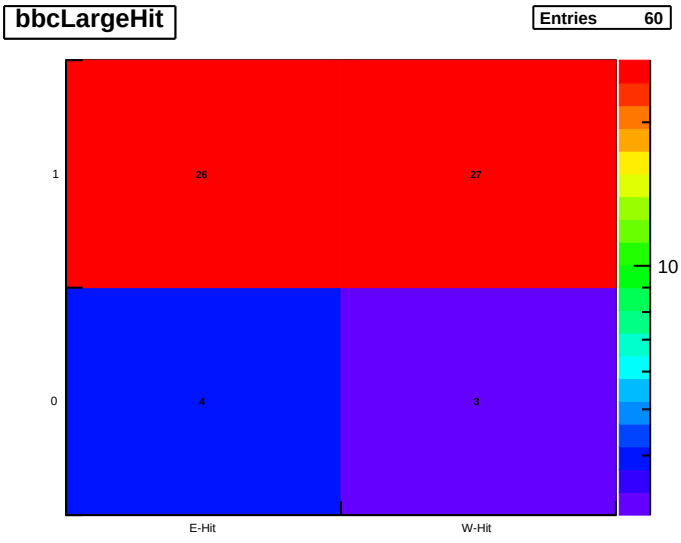

bbcSmallEastTacMaxVsSimu
Entries 15
Data
Simu

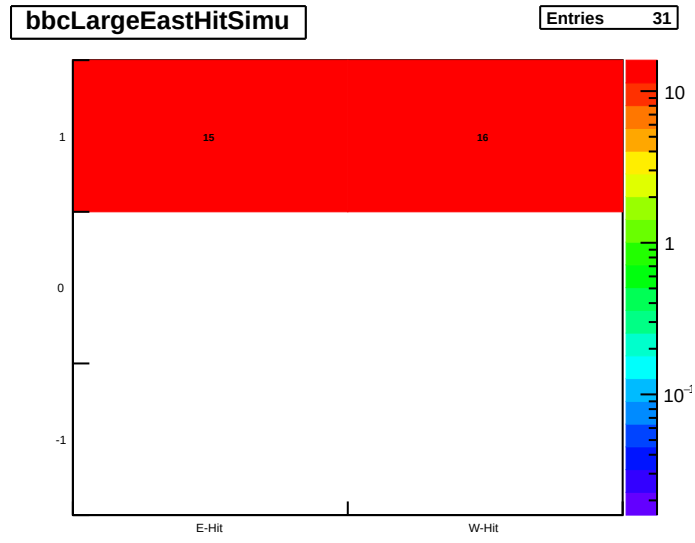

bbcSmallWestAdcSum
Entries 30

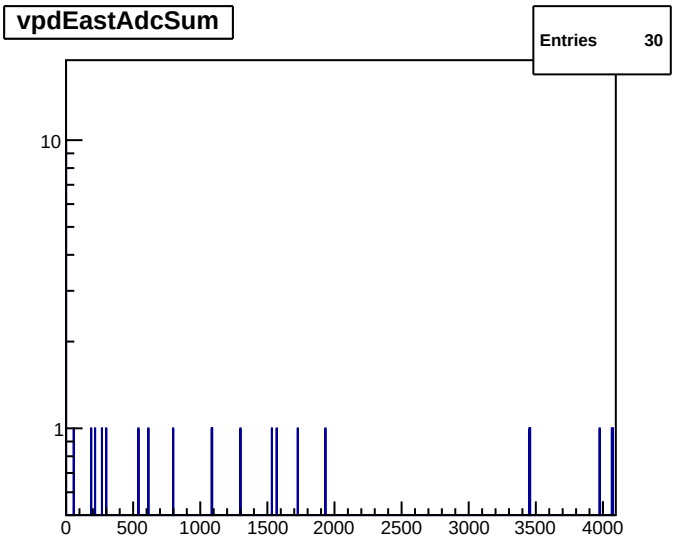

bbcSmallWestAdcSumVsSimu
Entries 16
Data
Simu

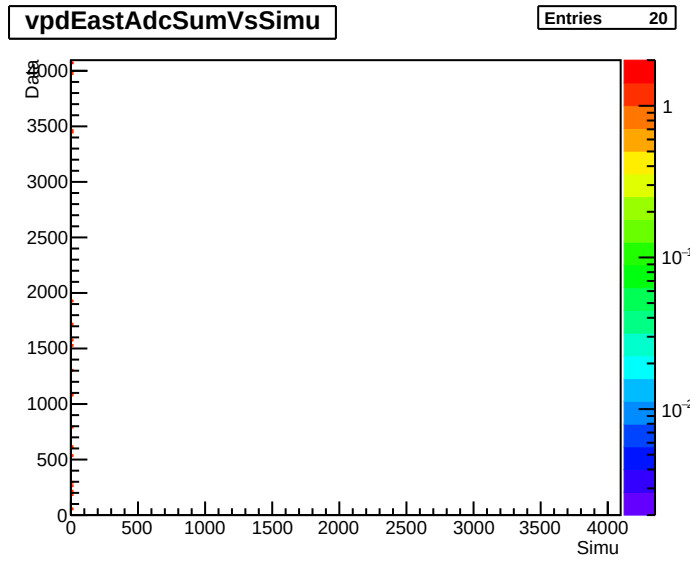

bbcSmallWestTacMax
Entries 30

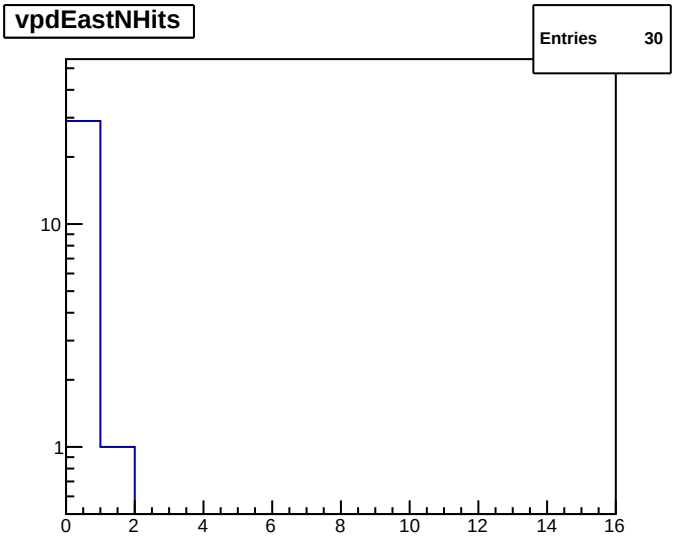

bbcSmallWestTacMaxVsSimu
Entries 16
Data
Simu

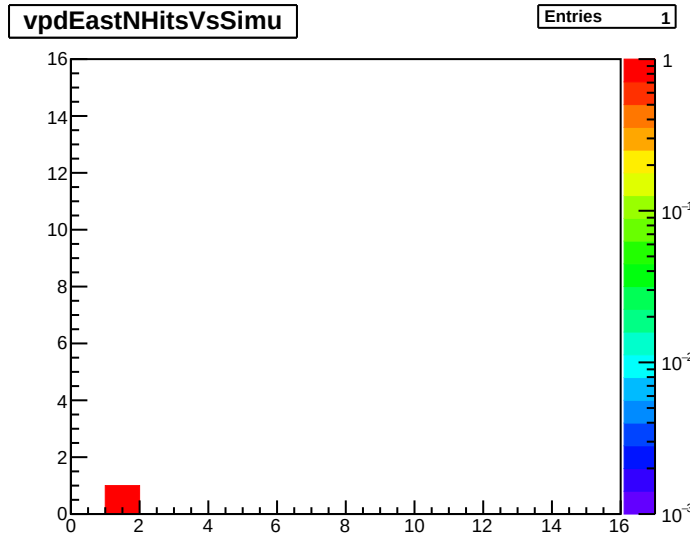

bbcLargeHit
Entries 60
1
0
26
27
4
3
10
E-Hit
W-Hit

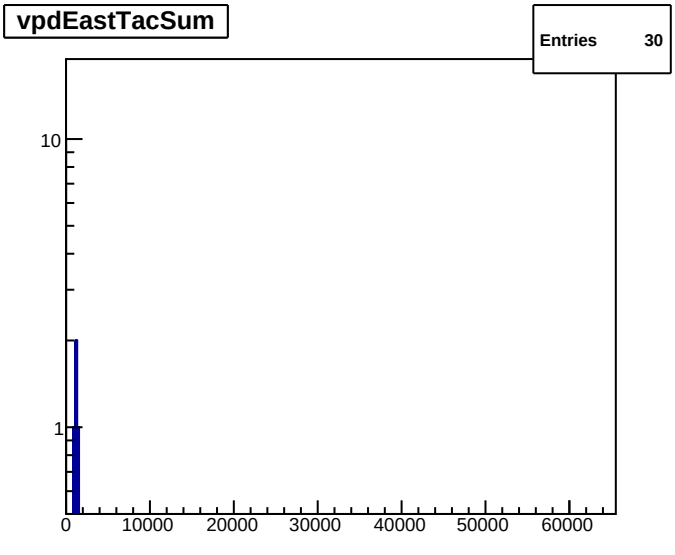

bbcLargeEastHitSimu
Entries 31
1
0
-1
15
16
10
1
10^{-1}
E-Hit
W-Hit

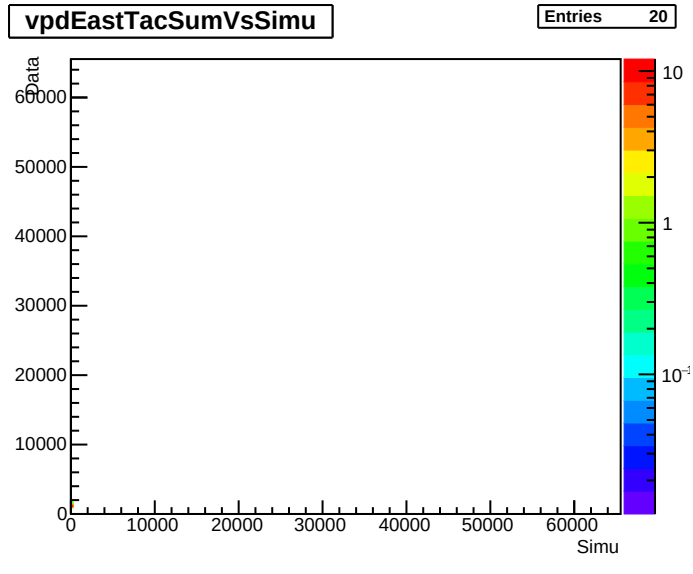

vpdEastAdcSum
Entries 30
10
1
0 500 1000 1500 2000 2500 3000 3500 4000

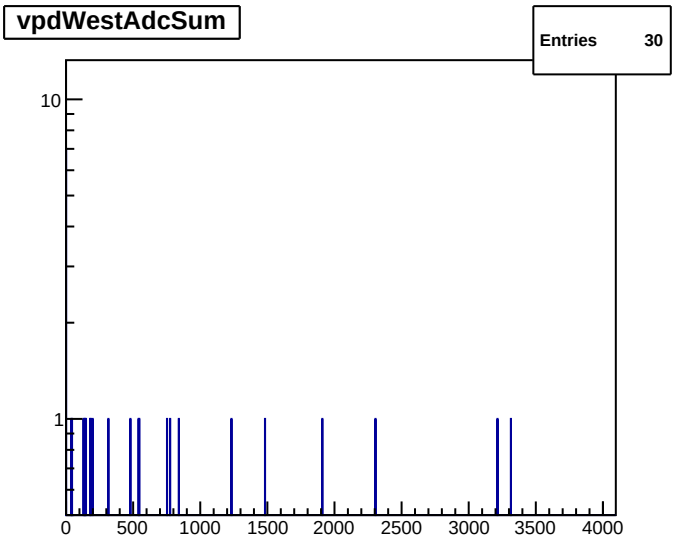

vpdEastAdcSumVsSimu
Entries 20
Data
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu
1

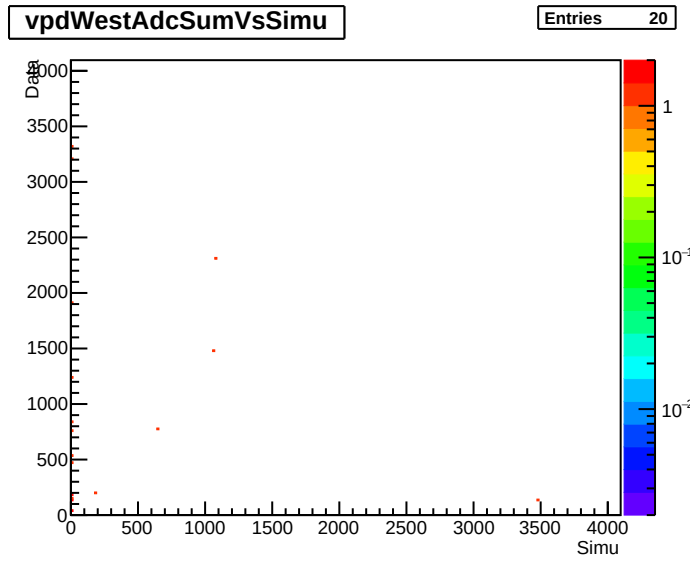

vpdEastNHits
Entries 30
10
1
0 2 4 6 8 10 12 14 16

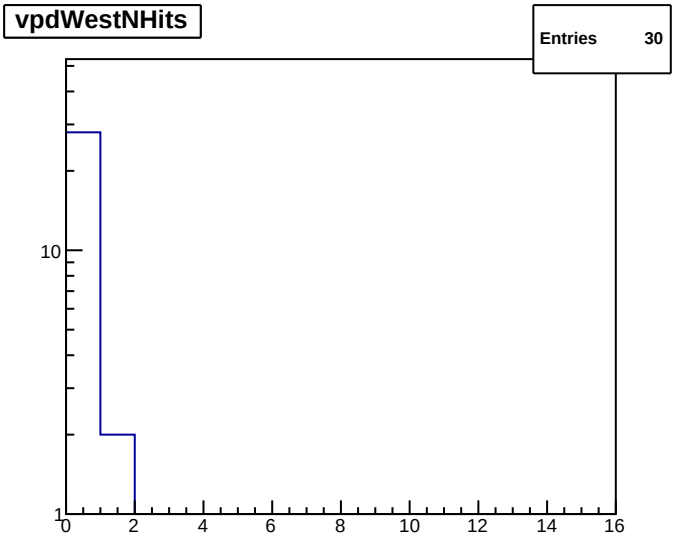

vpdEastNHitsVsSimu
Entries 1
16 14 12 10 8 6 4 2 0
0 2 4 6 8 10 12 14 16
1

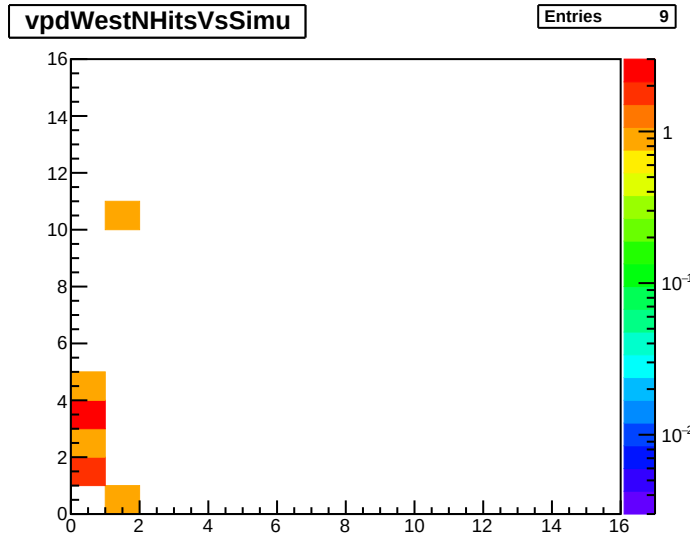

vpdEastTacSum
Entries 30
10
1
0 10000 20000 30000 40000 50000 60000

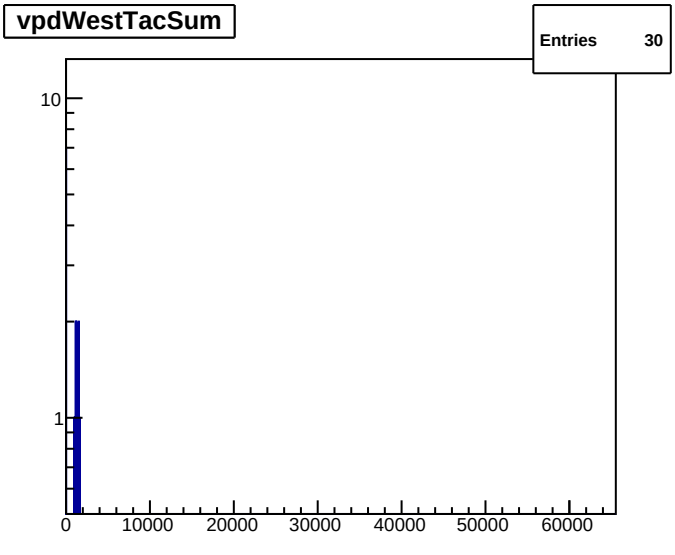

vpdEastTacSumVsSimu
Entries 20
Data
60000 50000 40000 30000 20000 10000 0
0 10000 20000 30000 40000 50000 60000
Simu
10
1

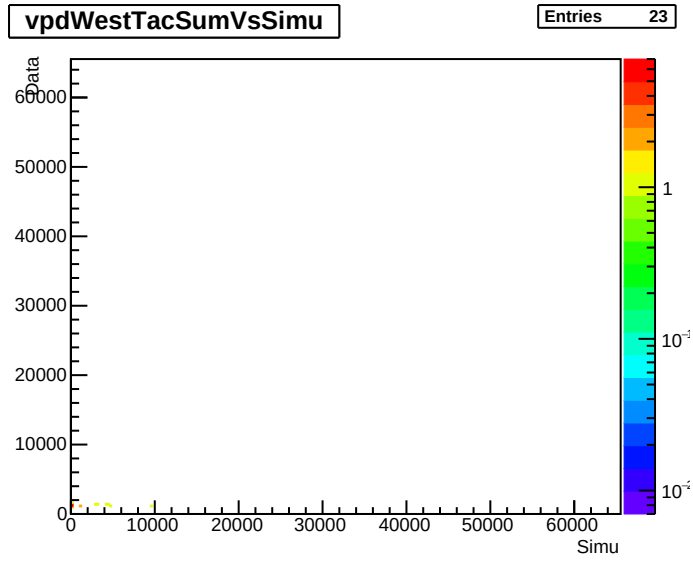

vpdWestAdcSum
Entries 30

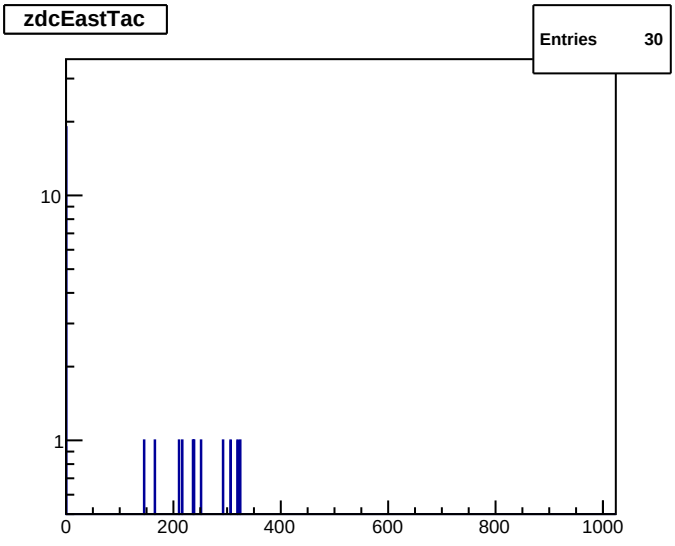

vpdWestAdcSumVsSimu
Entries 20
Data
Simu

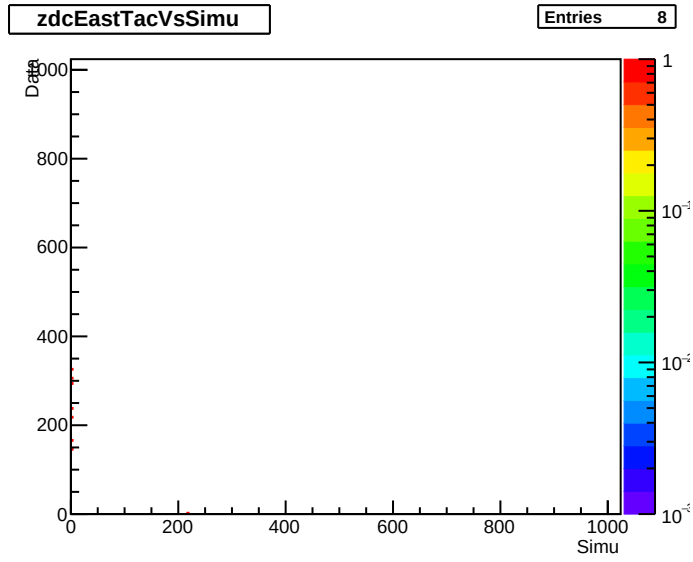

vpdWestNHits
Entries 30

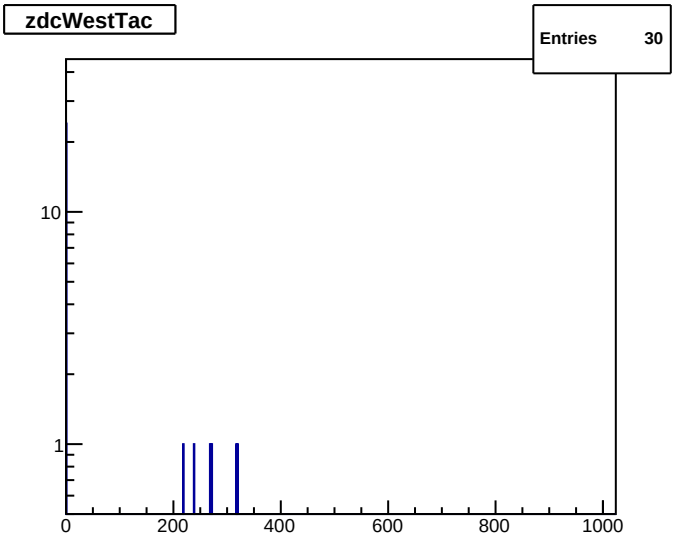

vpdWestNHitsVsSimu
Entries 9

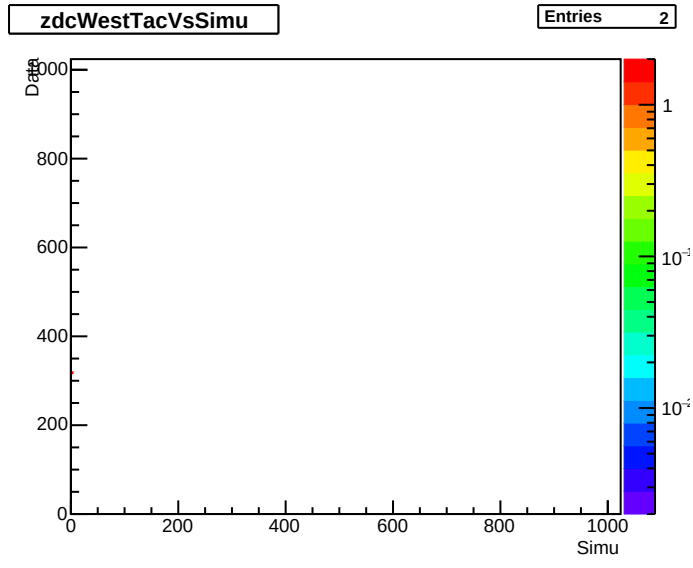

vpdWestTacSum
Entries 30

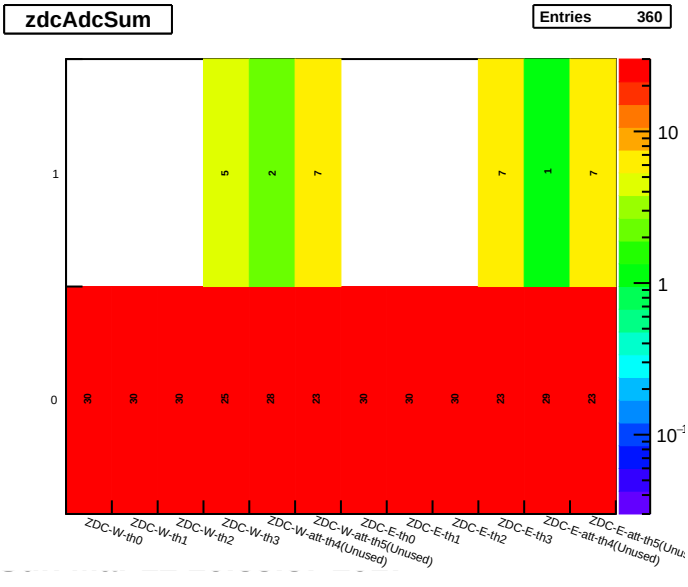

vpdWestTacSumVsSimu
Entries 23
Data
Simu

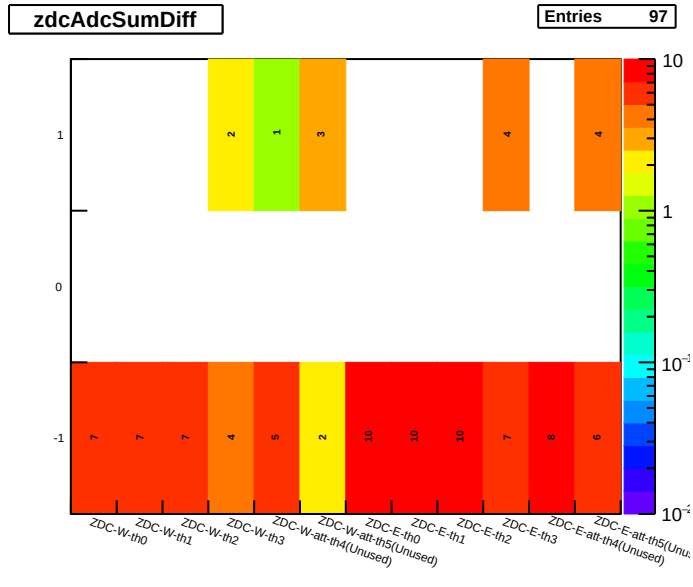

zdcEastTac
Entries 30
10
1
0
200
400
600
800
1000

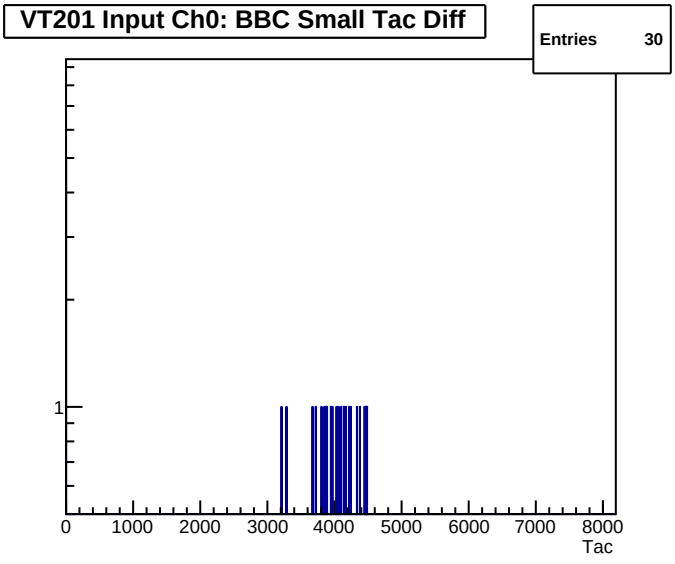

zdcEastTacVsSimu
Entries 8
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu
1

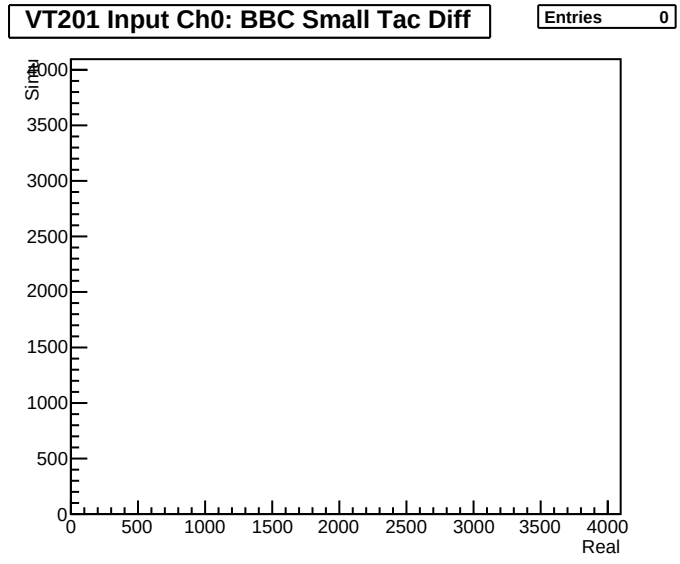

zdcWestTac
Entries 30
10
1
0
200
400
600
800
1000

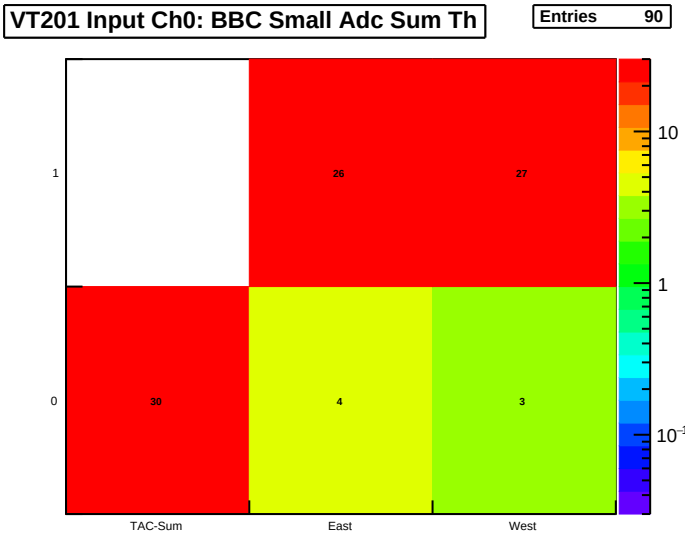

zdcWestTacVsSimu
Entries 2
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu
1

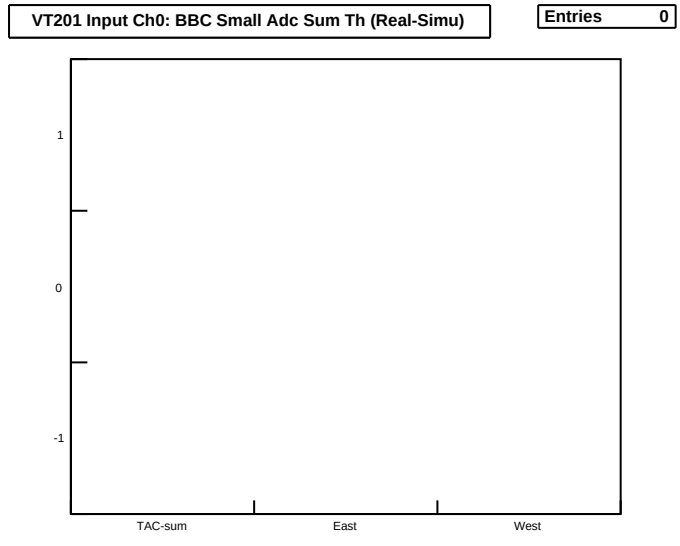

zdcAdcSum
Entries 360
1
0
10
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

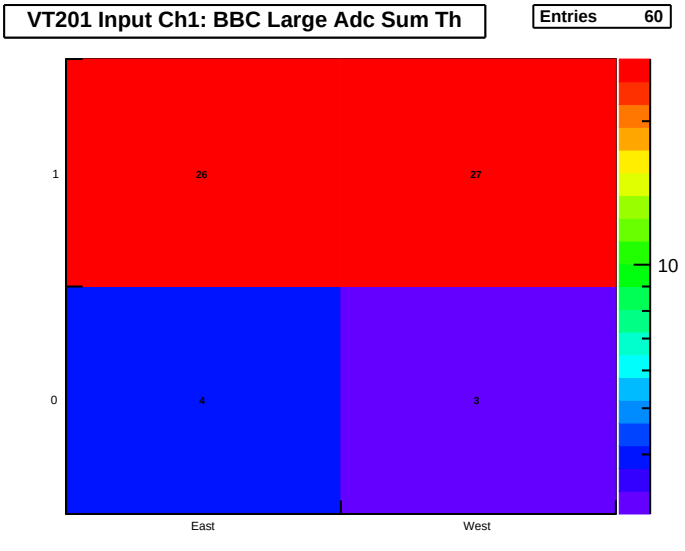

zdcAdcSumDiff
Entries 97
1
0
-1
10
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

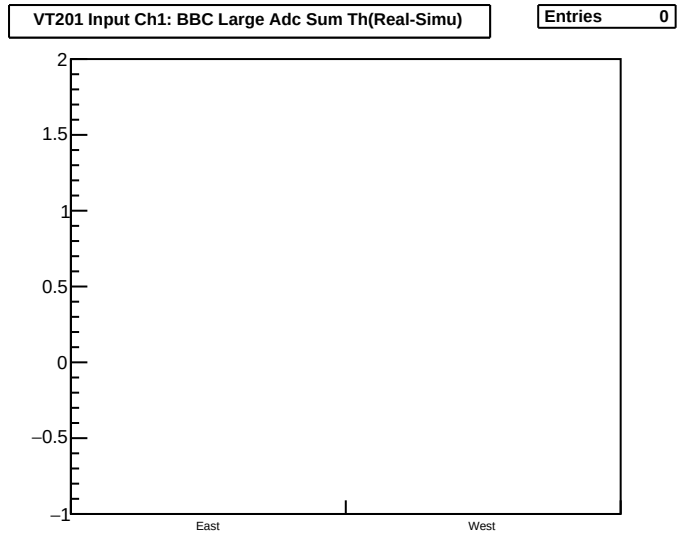

VT201 Input Ch0: BBC Small Tac Diff
Entries 30
1
0 1000 2000 3000 4000 5000 6000 7000 8000
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Real

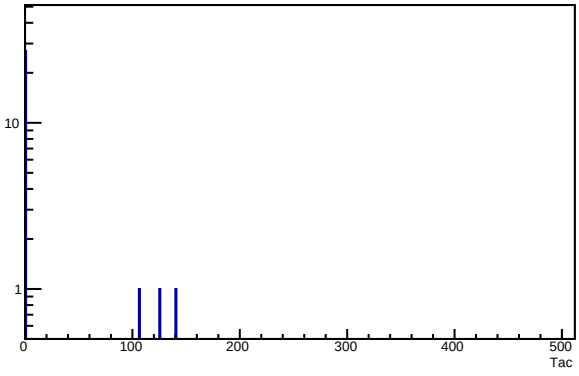

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 90
1
0
26
27
30
4
3
10
1
10^-1
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
1
0
-1
TAC-sum
East
West

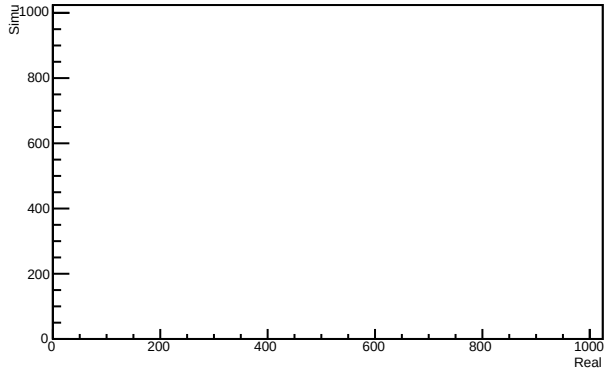

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 60
1
0
26
27
4
3
10
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
2
1.5
1
0.5
0
−0.5
−1
East
West

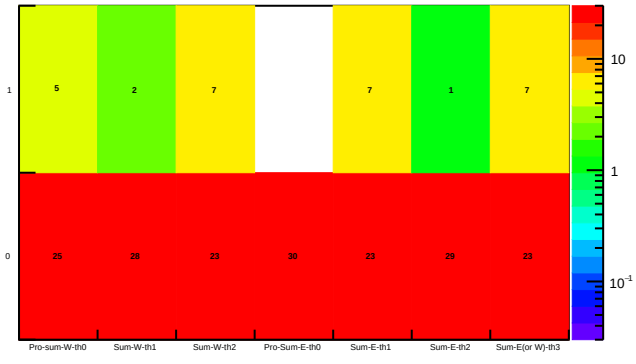
VT201 Input Ch2: ZDC Tac Diff



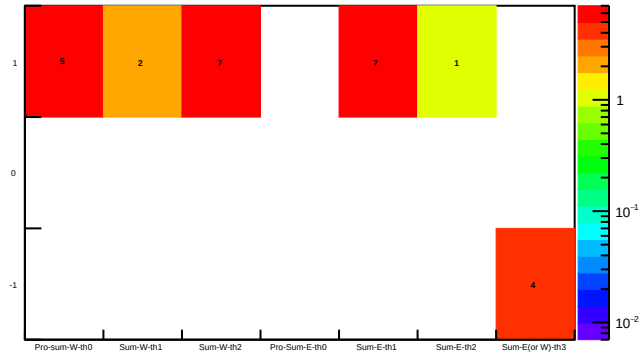
VT201 Input Ch2: ZDC Tac Diff



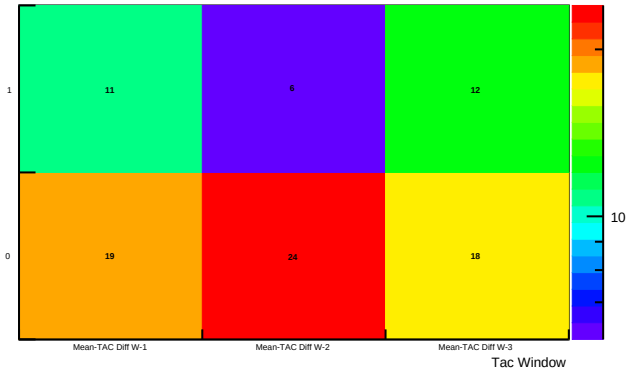
VT201 Input Ch2: ZDC Adc Sum Th



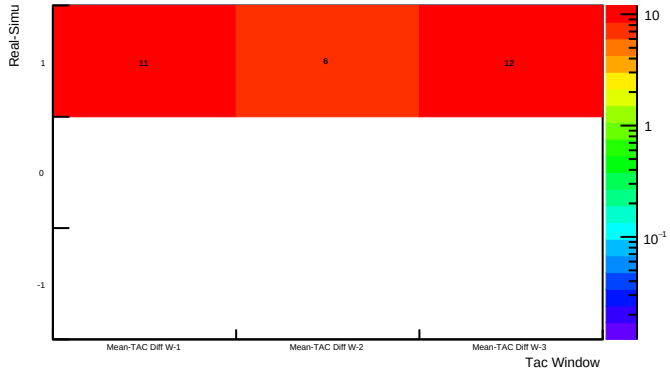
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



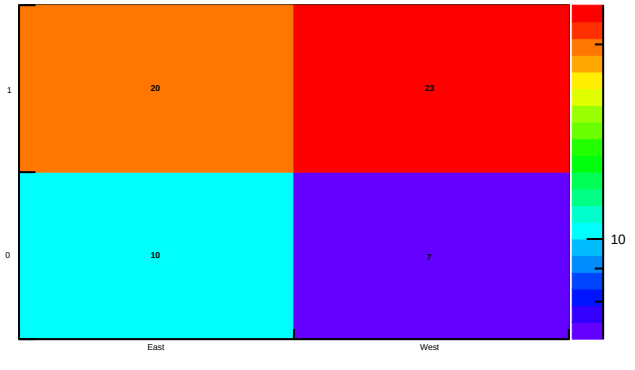
VT201 Input Ch4: VPD Tac Diff



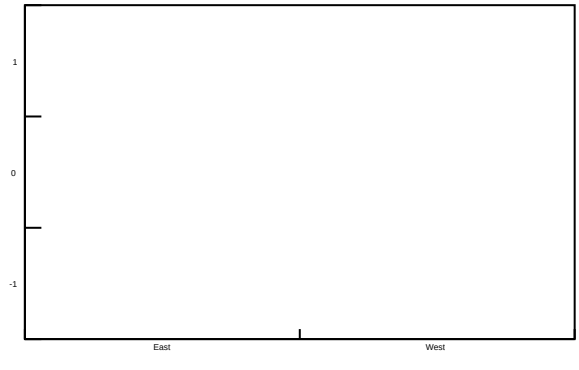
VT201 Input Ch4: VPD Tac Diff

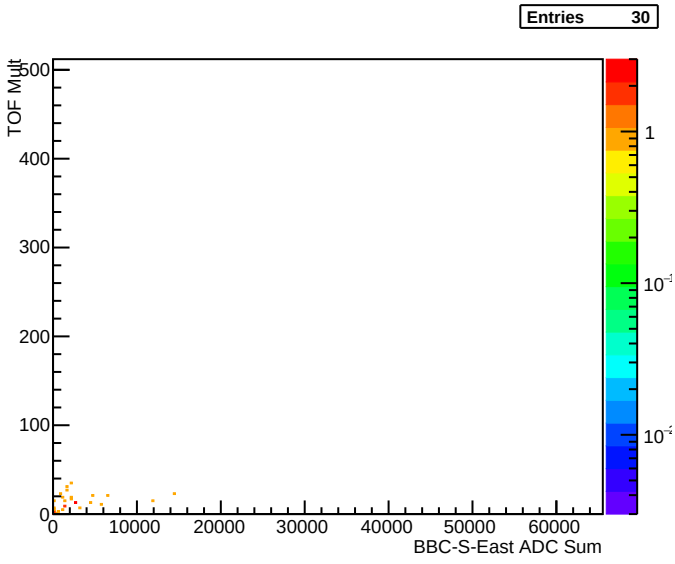


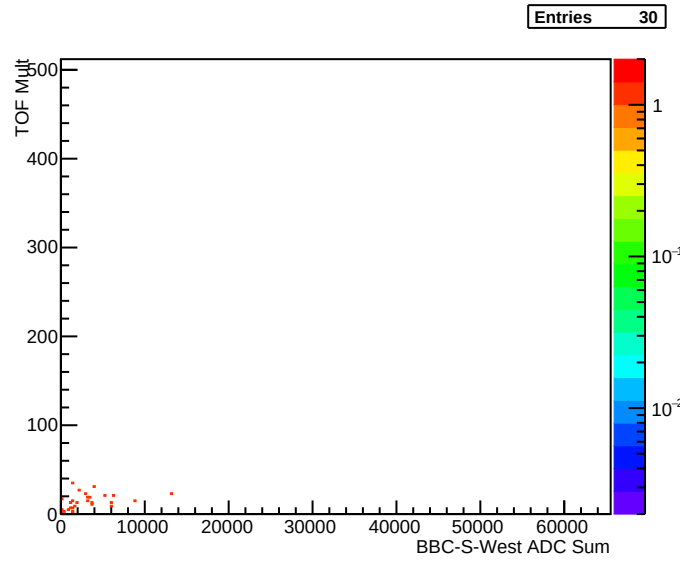
VT201 Input Ch4: VPD Adc Sum Th

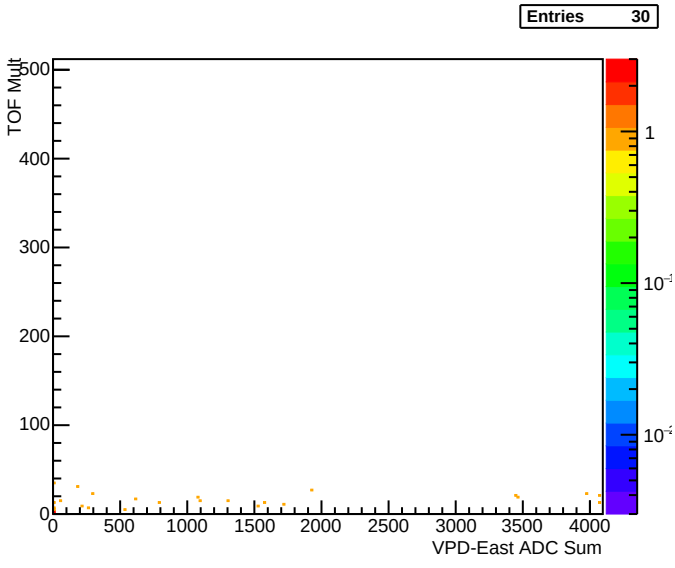


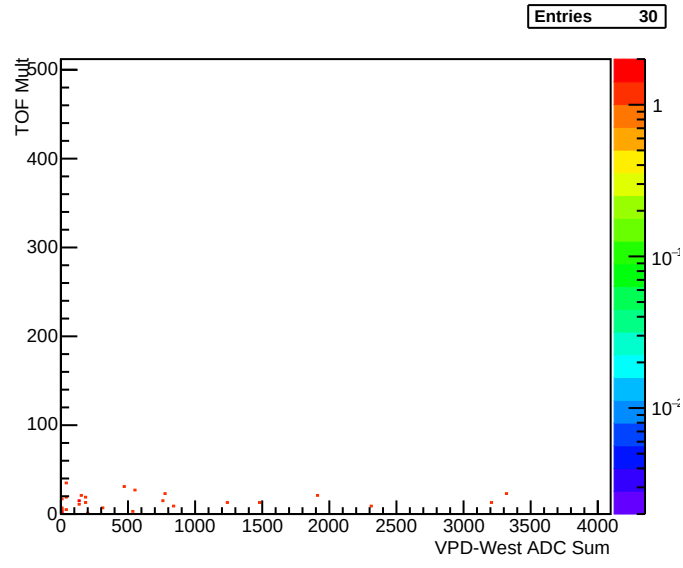
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

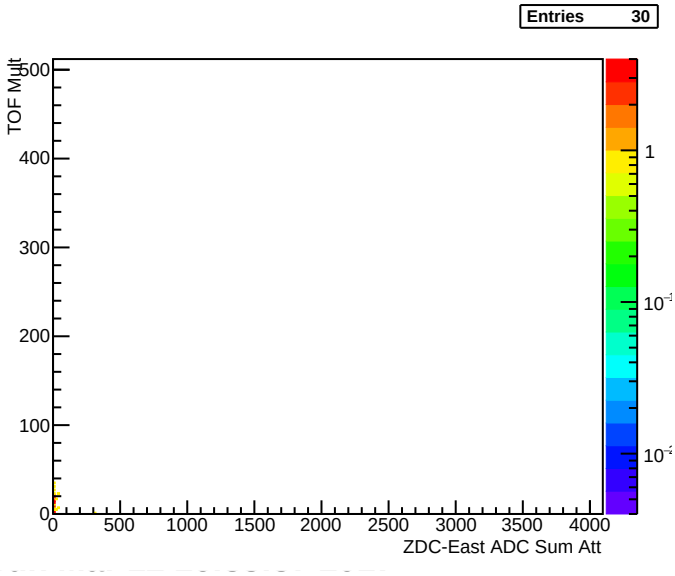


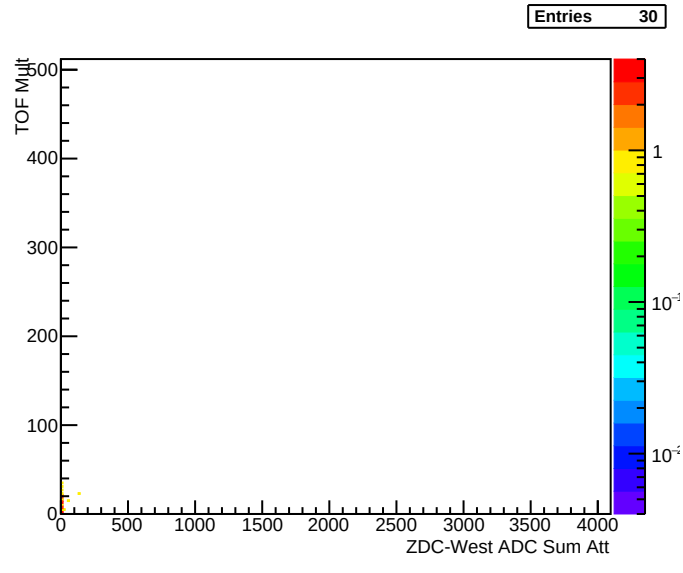

Entries 30
TOF Mult
BBC-S-East ADC Sum

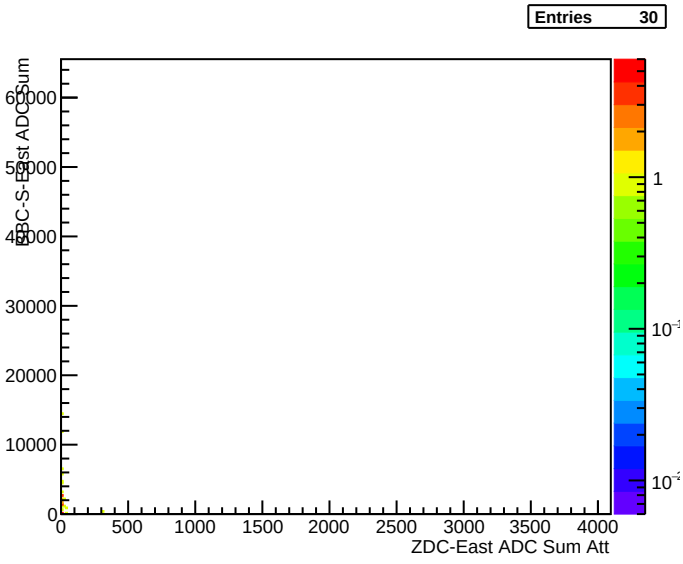

Entries 30
TOF Mult
BBC-S-West ADC Sum

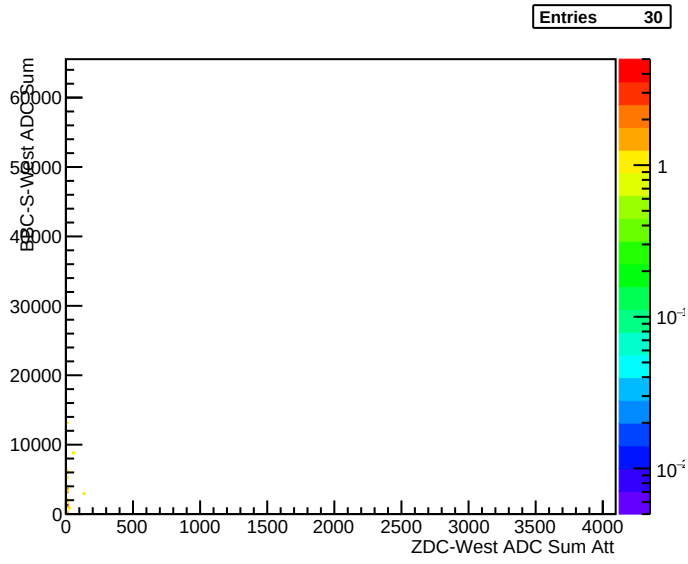

Entries 30
TOF Mult
VPD-East ADC Sum

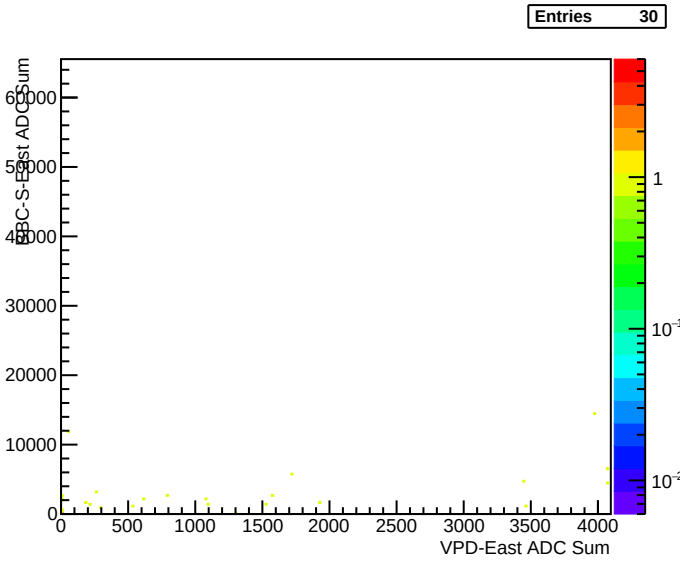

Entries 30
TOF Mult
VPD-West ADC Sum

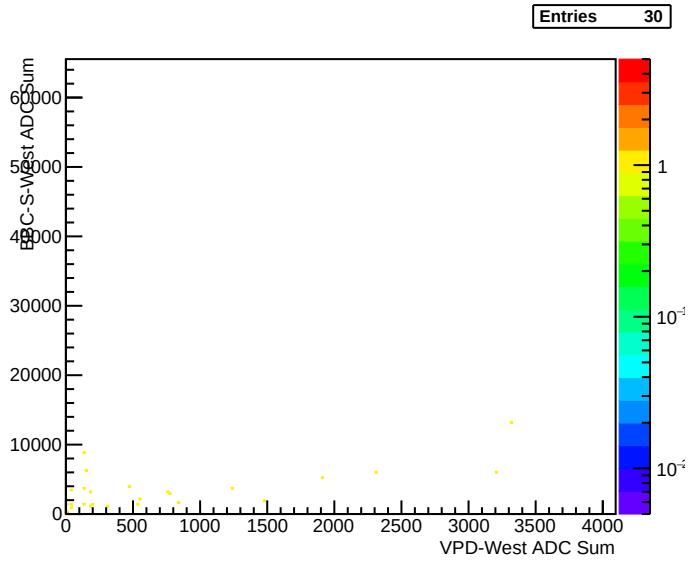

Entries 30
TOF Mult
ZDC-East ADC Sum Att

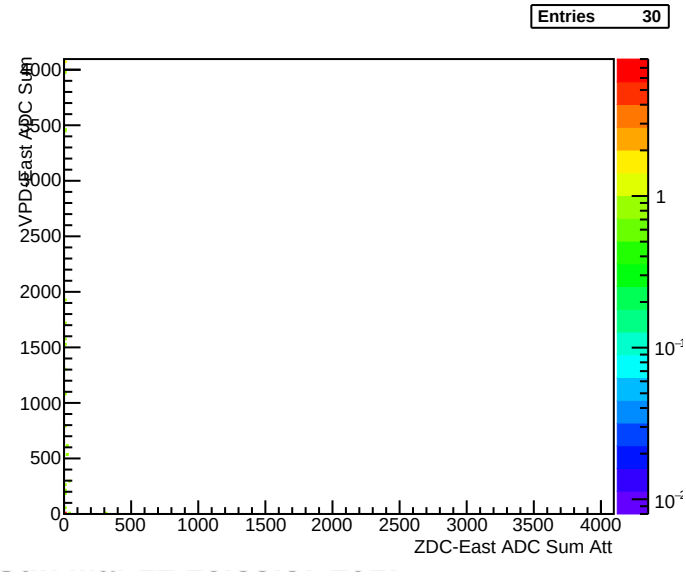

Entries 30
TOF Mult
ZDC-West ADC Sum Att

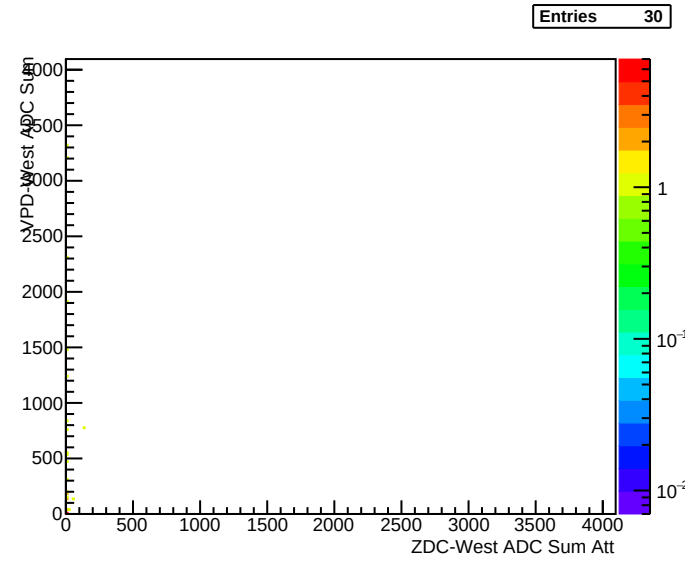

Entries 30
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

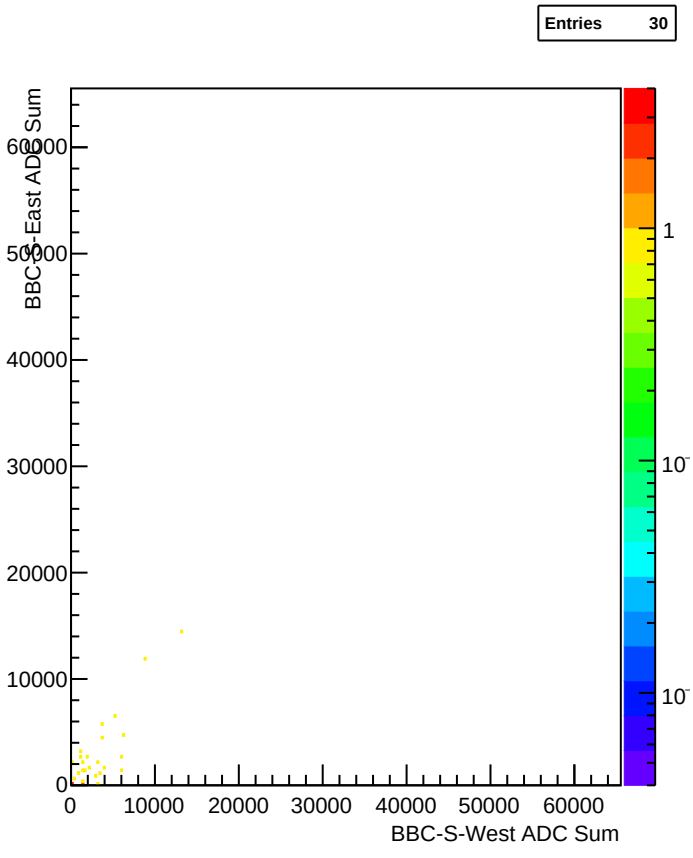

Entries 30
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

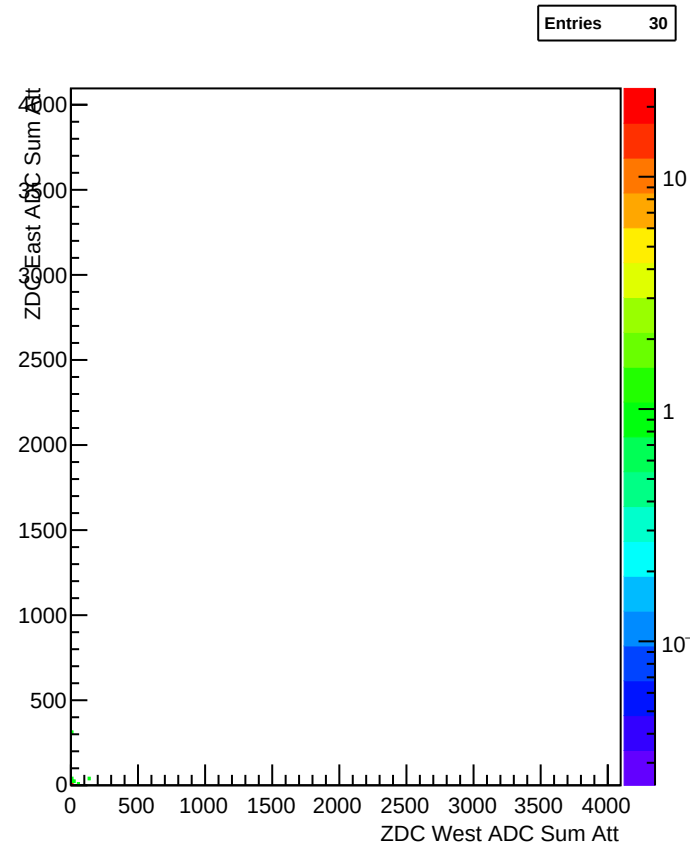

Entries 30
BBC-S-East ADC Sum
VPD-East ADC Sum

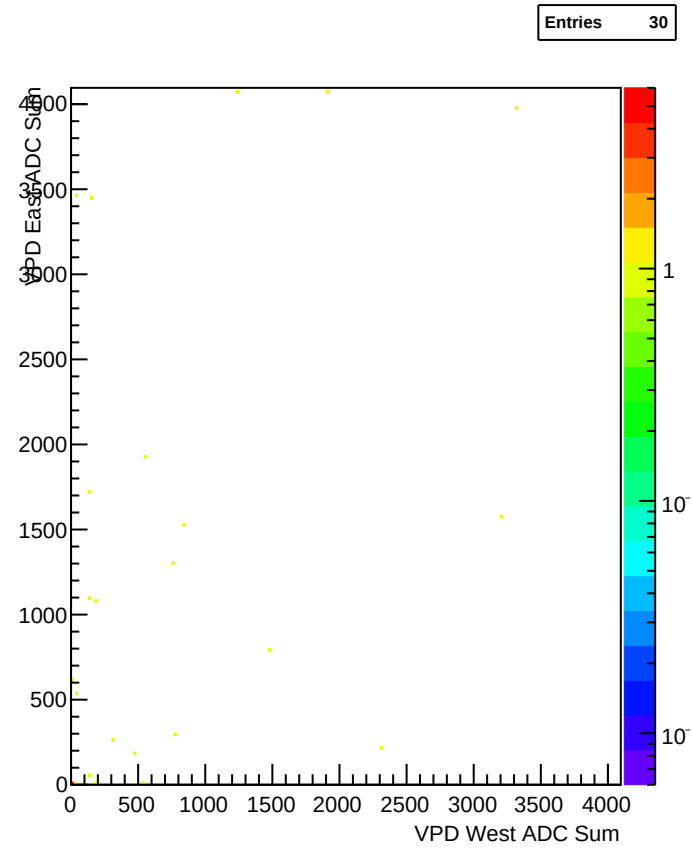

Entries 30
BBC-S-West ADC Sum
VPD-West ADC Sum

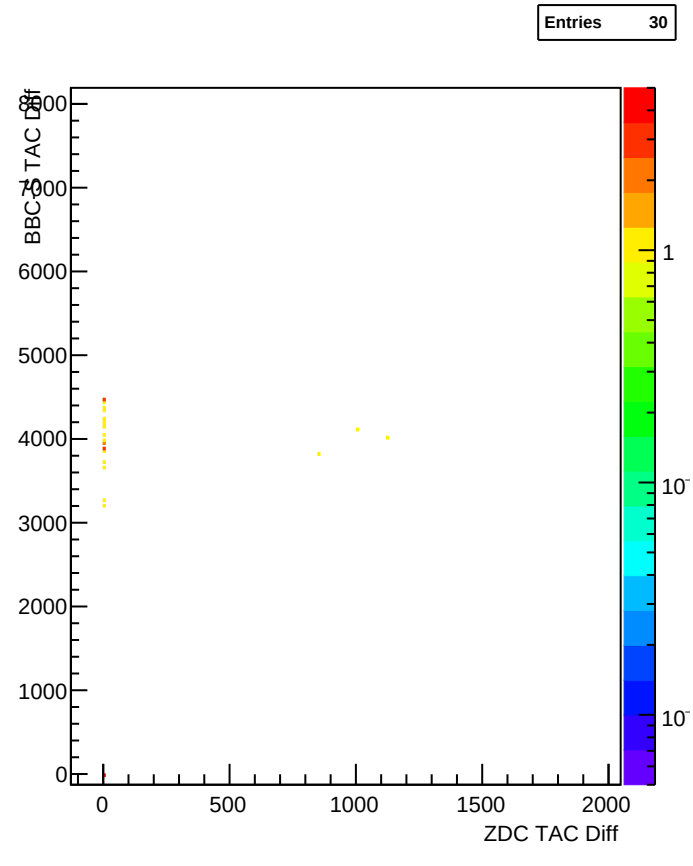

Entries 30
VPD-East ADC Sum
ZDC-East ADC Sum Att

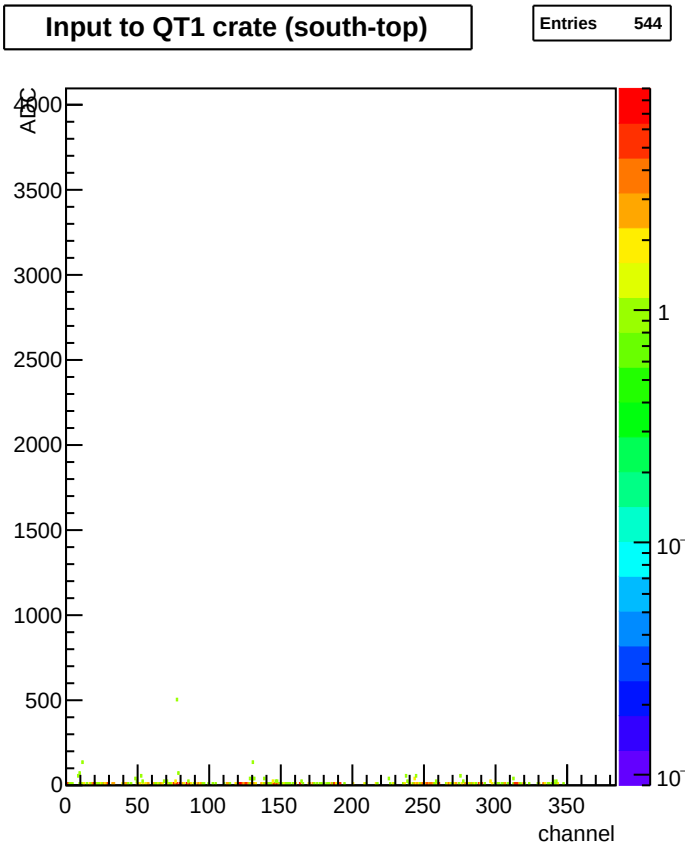

Entries 30
VPD-West ADC Sum
ZDC-West ADC Sum Att

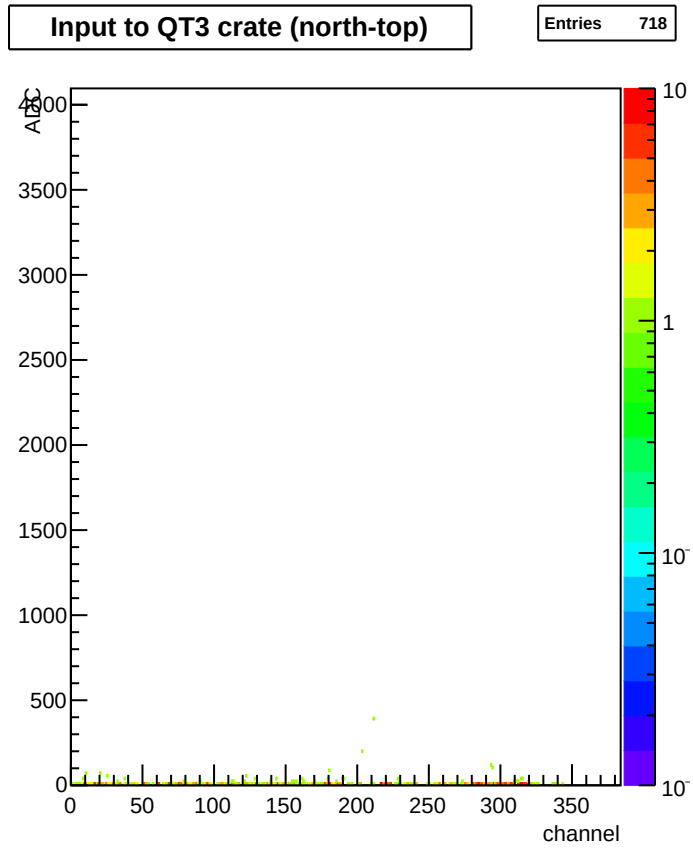

Entries 30
BBC-S-East ADC Sum
BBC-S-West ADC Sum
0
10000
20000
30000
40000
50000
60000
1
10

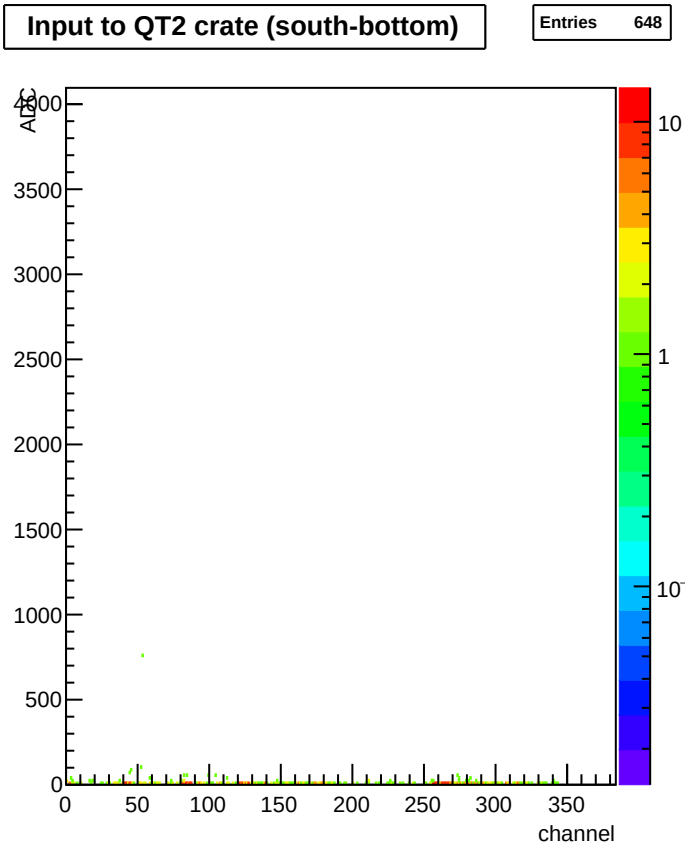

Entries 30
ZDC East ADC Sum Att
ZDC West ADC Sum Att
0
500
1000
1500
2000
2500
3000
3500
4000
1
10

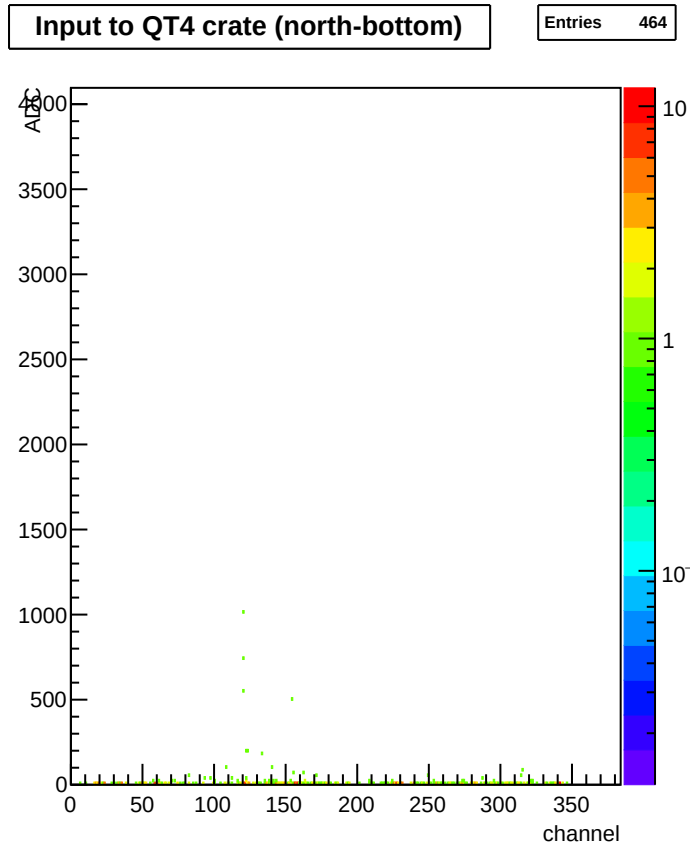

Entries 30
VPD East ADC Sum
VPD West ADC Sum
0
500
1000
1500
2000
2500
3000
3500
4000
1
10

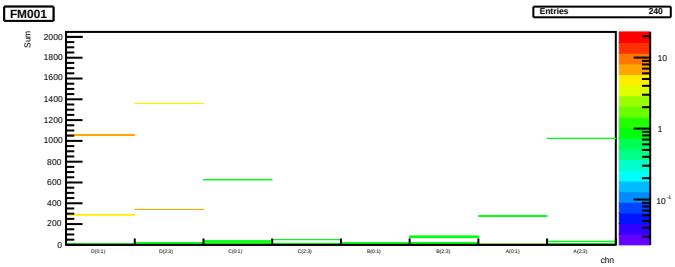

Entries 30
BBC-S TAC Diff
ZDC TAC Diff
0
500
1000
1500
2000
3000
4000
5000
6000
7000
8000
1
10

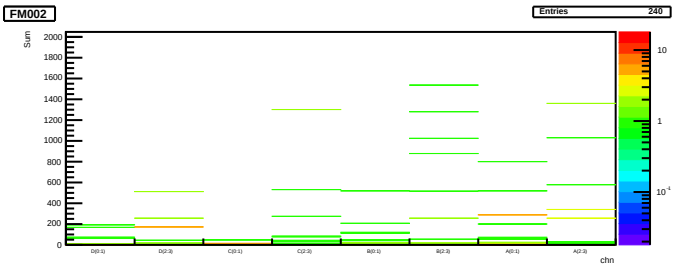

Input to QT1 crate (south-top)
Entries 544
ADC
channel

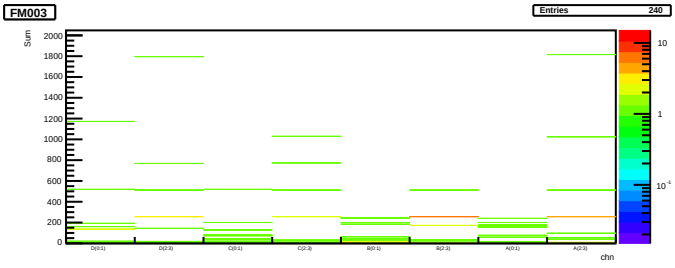

Input to QT3 crate (north-top)
Entries 718
ADC
channel

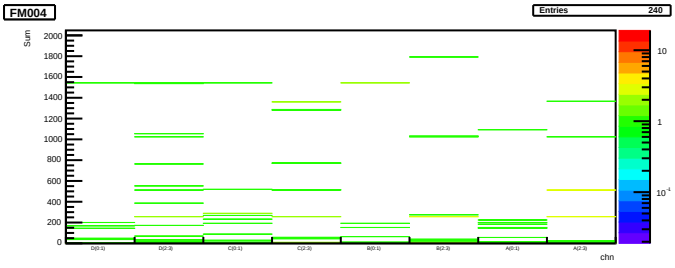

Input to QT2 crate (south-bottom)
Entries 648
ADC
channel

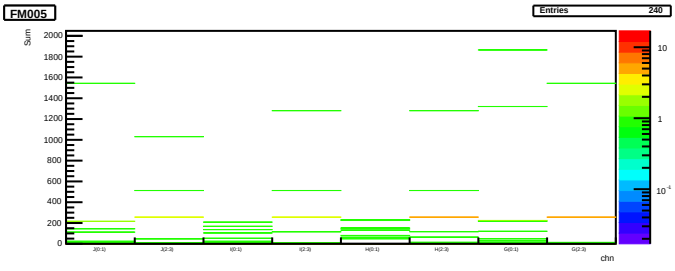

Input to QT4 crate (north-bottom)
Entries 464
ADC
channel

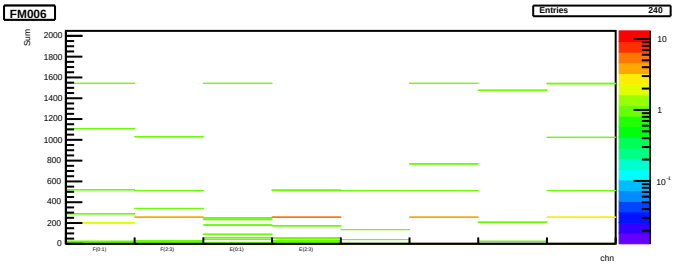

FM001
Entries 240
Sum
chn

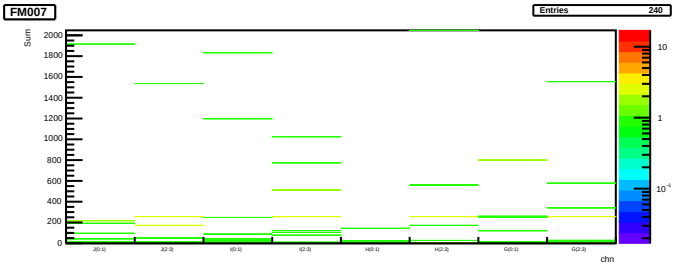

FM002
Entries 240
Sum
chn

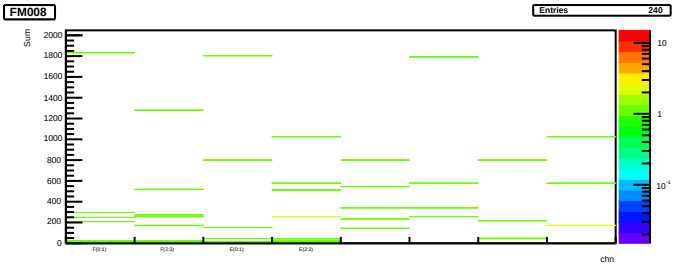

FM003
Entries 240
Sum
chn

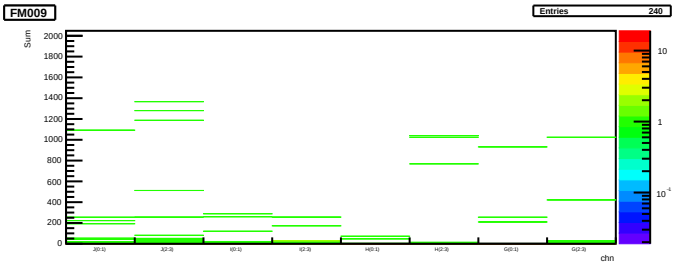

FM004
Entries 240
Sum
chn

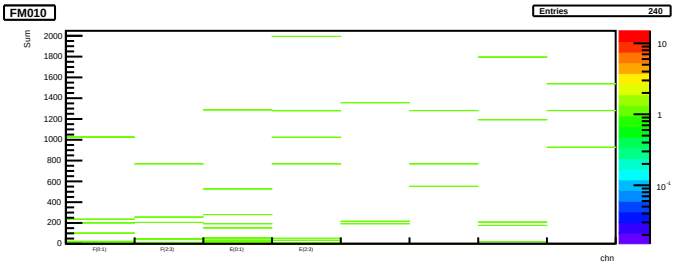

FM005
Entries 240
Sum
chn

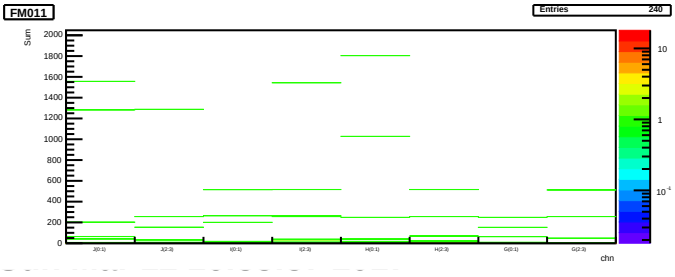

FM006
Entries 240
Sum
chn

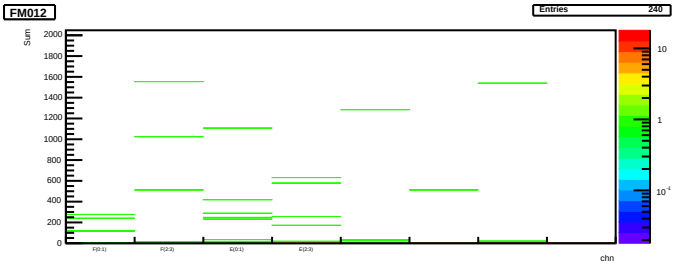

FM007
Entries 240
Sum
chn

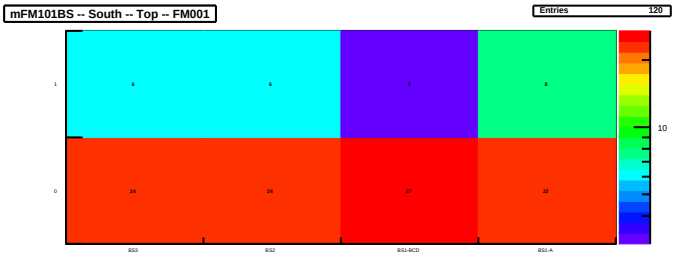

FM008
Entries 240
Sum
chn

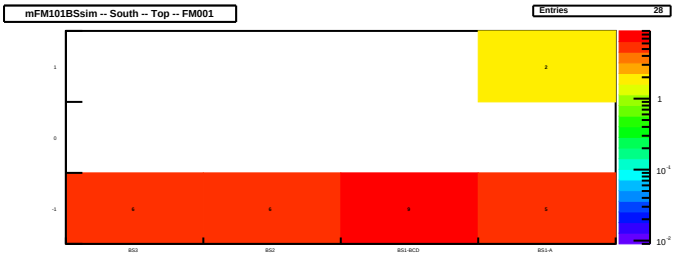

FM009
Entries 240
Sum
chn

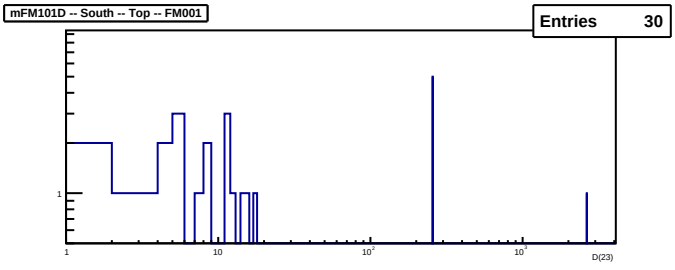

FM010
Entries 240
Sum
chn

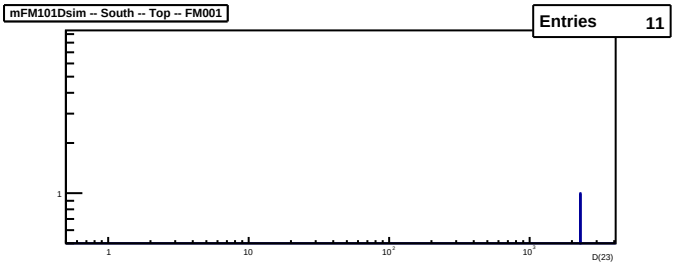

FM011
Entries 240
Sum
chn

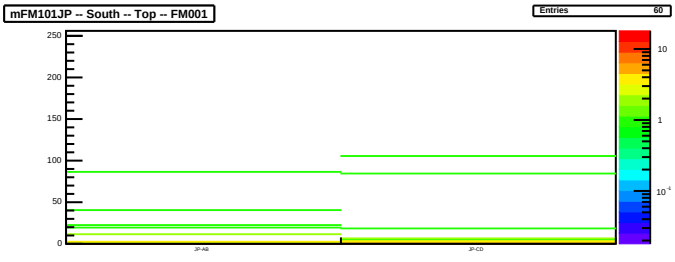

FM012
Entries 240
Sum
chn

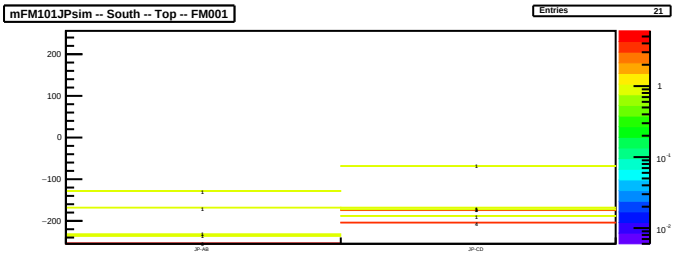

mFM101BS -- South -- Top -- FM001
Entries 120

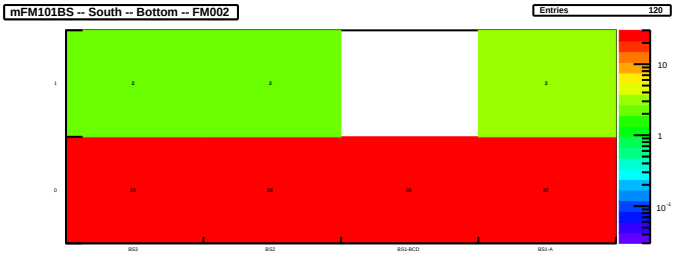

mFM101BSsim -- South -- Top -- FM001
Entries 28

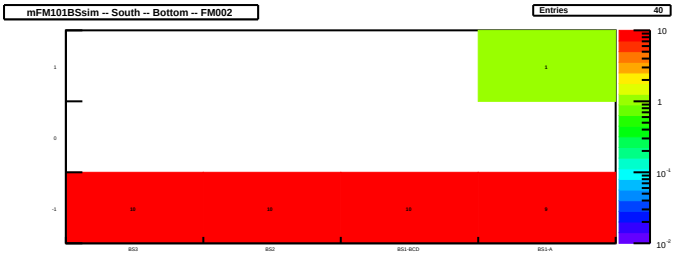

mFM101D -- South -- Top -- FM001
Entries 30
D(23)

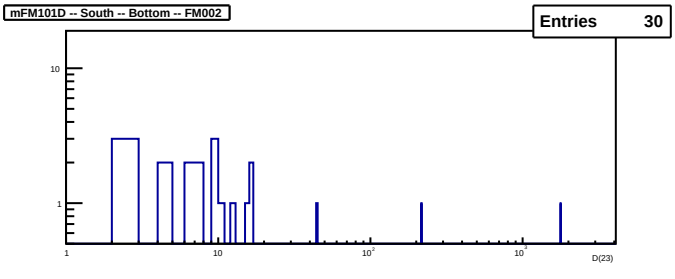

mFM101Dsim -- South -- Top -- FM001
Entries 11
D(23)

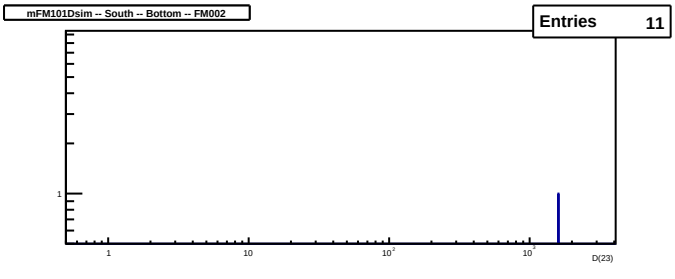

mFM101JP -- South -- Top -- FM001
Entries 60


mFM101JPsim -- South -- Top -- FM001
Entries 21


mFM101BS -- South -- Bottom -- FM002
Entries 120


mFM101BSsim -- South -- Bottom -- FM002
Entries 40


mFM101D -- South -- Bottom -- FM002
Entries 30
D(23)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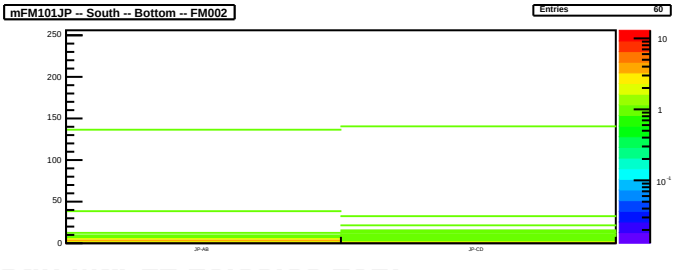
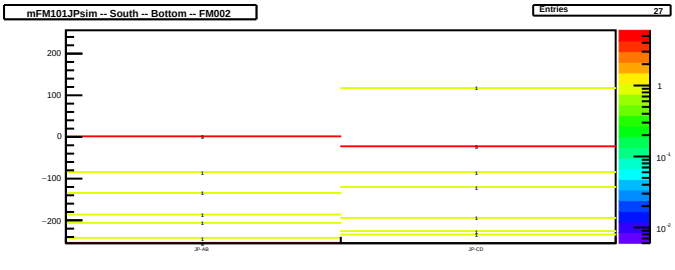
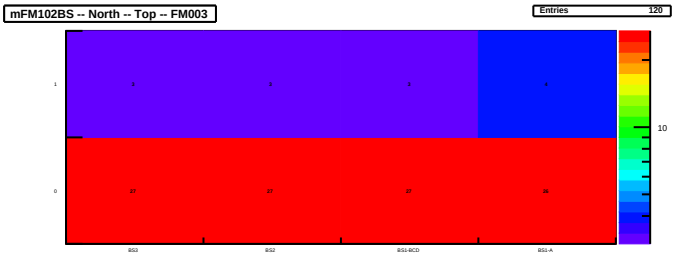
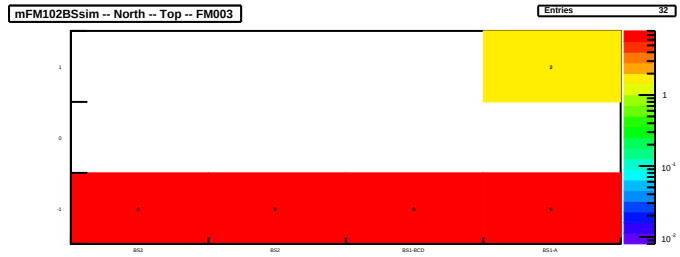
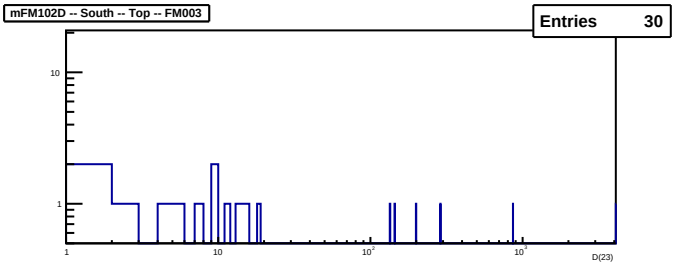
Entries 11
D(23)

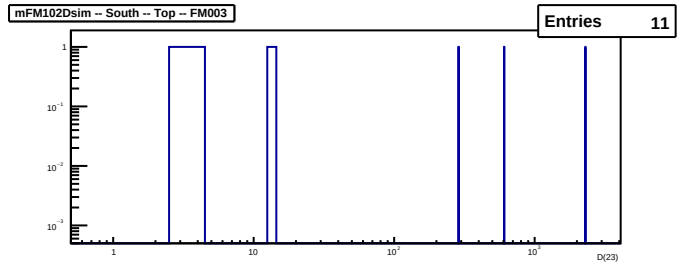

mFM101JP -- South -- Bottom -- FM002
Entries 60

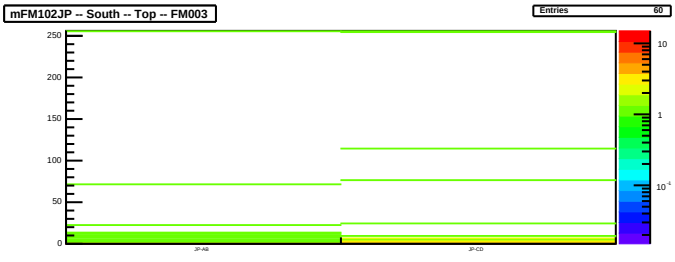

mFM101JPsim -- South -- Bottom -- FM002
Entries 27

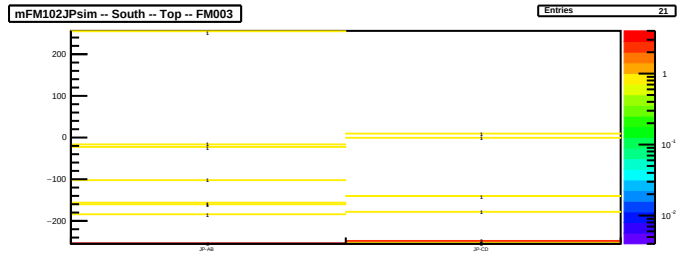

mFM102BS -- North -- Top -- FM003
Entries 120

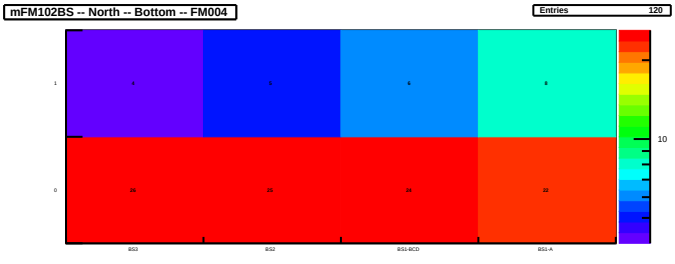

mFM102BSsim -- North -- Top -- FM003
Entries 32

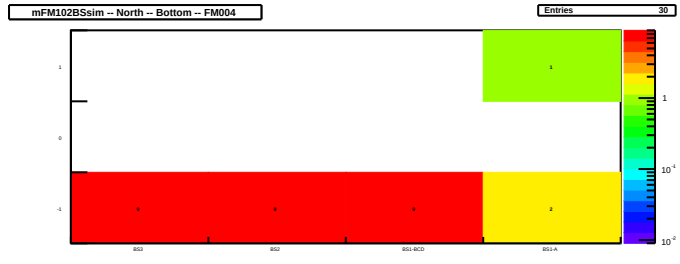

mFM102D -- South -- Top -- FM003
Entries 30
D(23)

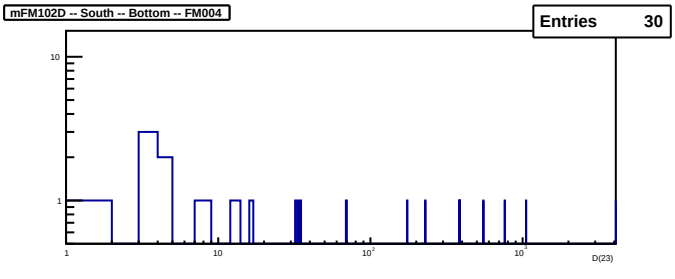

mFM102Dsim -- South -- Top -- FM003
Entries 11
D(23)

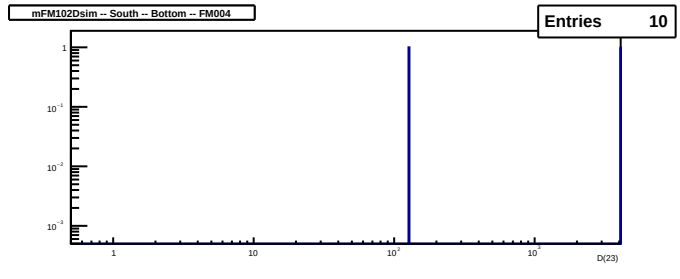

mFM102JP -- South -- Top -- FM003
Entries 60

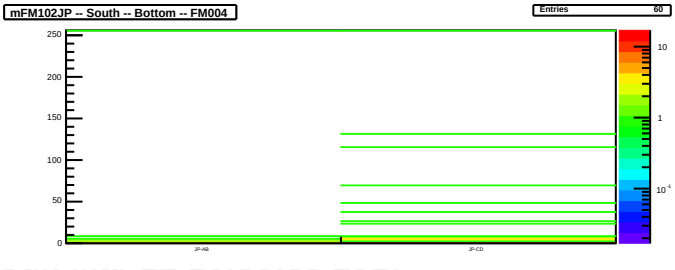

mFM102JPsim -- South -- Top -- FM003
Entries 21

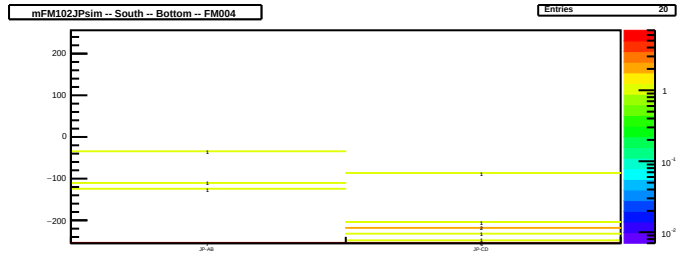

mFM102BS -- North -- Bottom -- FM004
Entries 120

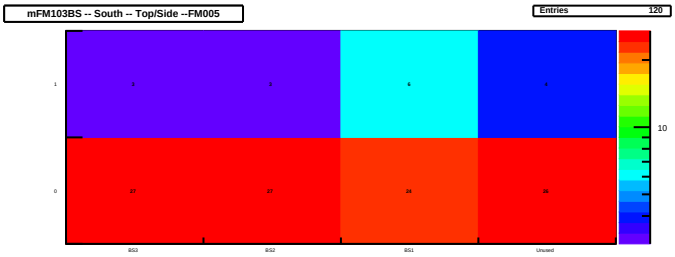

mFM102BSsim -- North -- Bottom -- FM004
Entries 30

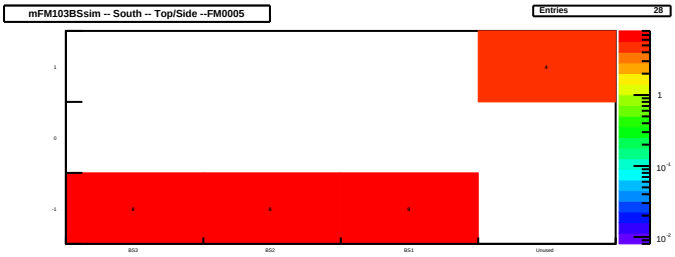

mFM102D -- South -- Bottom -- FM004
Entries 30
D(23)

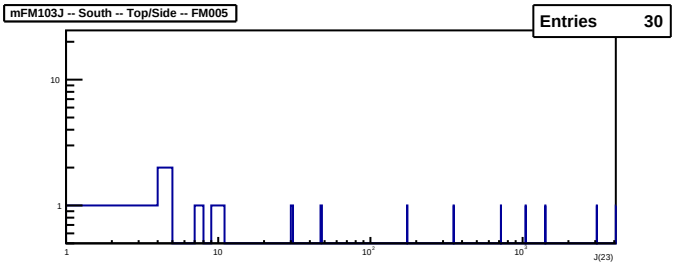

mFM102Dsim -- South -- Bottom -- FM004
Entries 10
D(23)

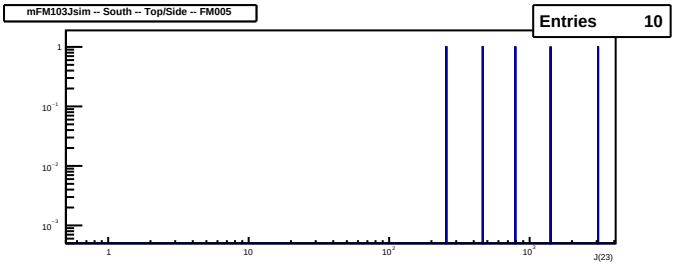

mFM102JP -- South -- Bottom -- FM004
Entries 60

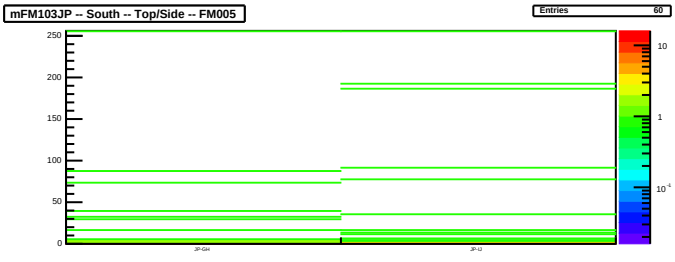

mFM102JPsim -- South -- Bottom -- FM004
Entries 20

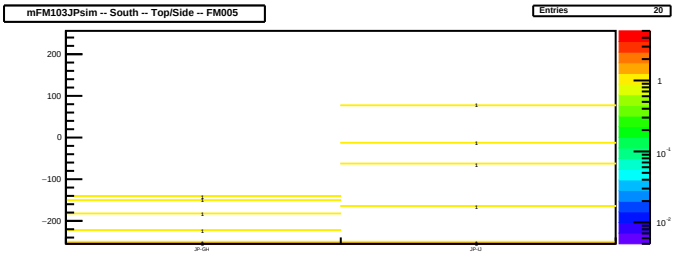

mFM103BS -- South -- Top/Side --FM005
Entries 120

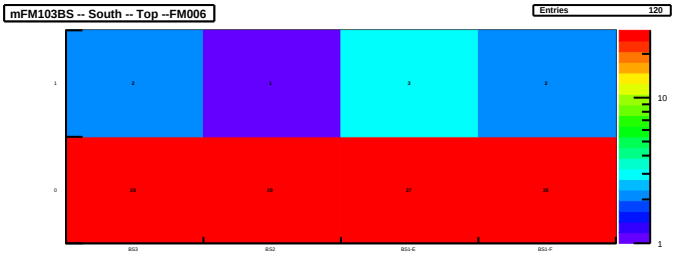

mFM103BSsim -- South -- Top/Side --FM0005
Entries 28

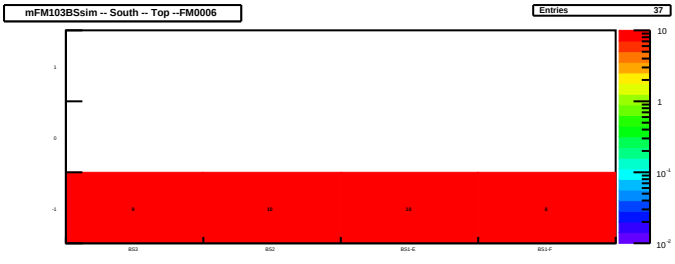

mFM103J -- South -- Top/Side -- FM005
Entries 30
J(23)

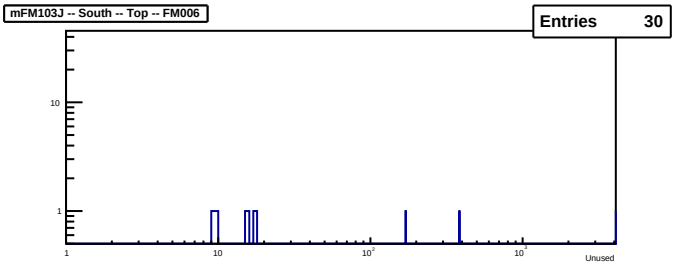

mFM103Jsim -- South -- Top/Side -- FM005
Entries 10
J(23)

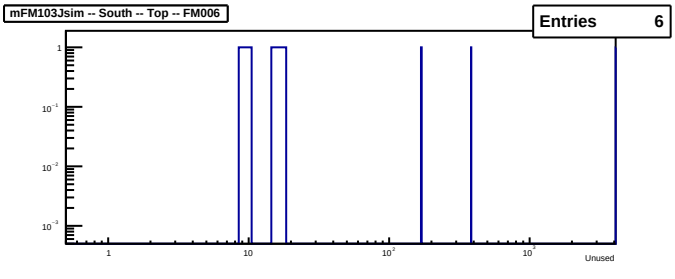

mFM103JP -- South -- Top/Side -- FM005
Entries 60

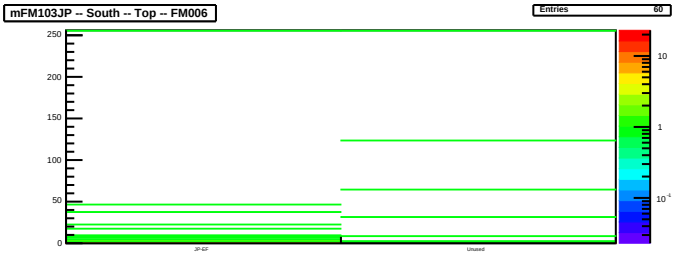

mFM103JPsim -- South -- Top/Side -- FM005
Entries 20

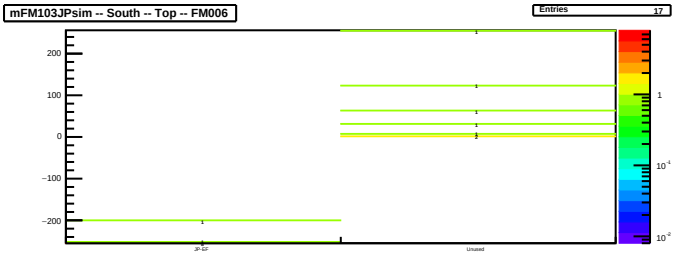

mFM103BS -- South -- Top --FM006
Entries 120

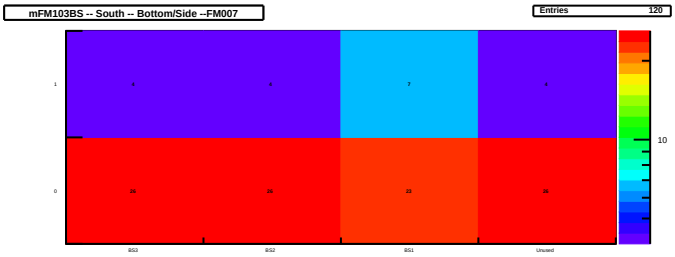

mFM103BSsim -- South -- Top --FM0006
Entries 37

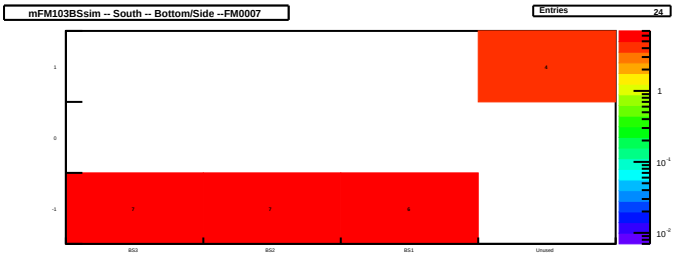

mFM103J -- South -- Top -- FM006
Entries 30
Unused

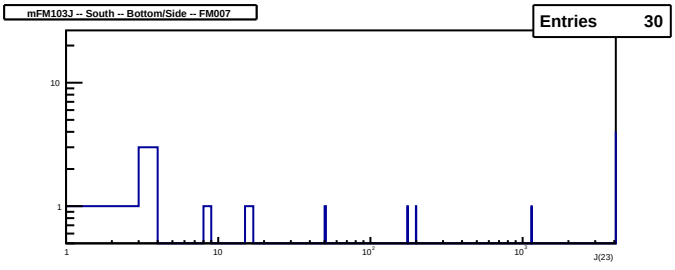

mFM103Jsim -- South -- Top -- FM006
Entries 6
Unused

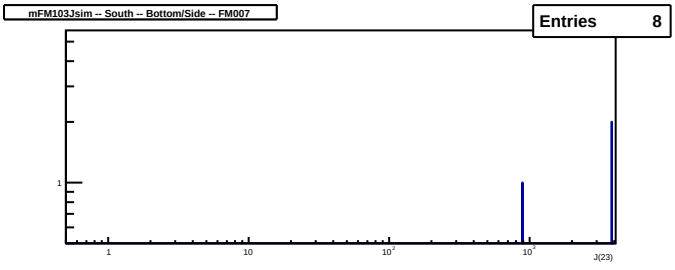

mFM103JP -- South -- Top -- FM006
Entries 60

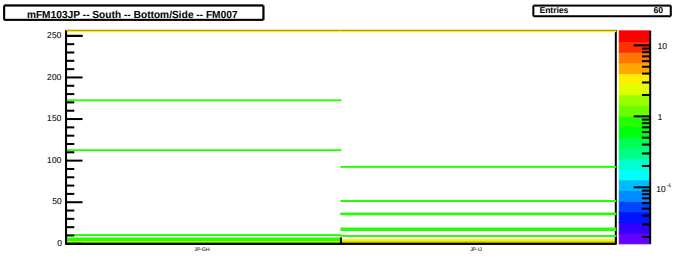

mFM103JPsim -- South -- Top -- FM006
Entries 17

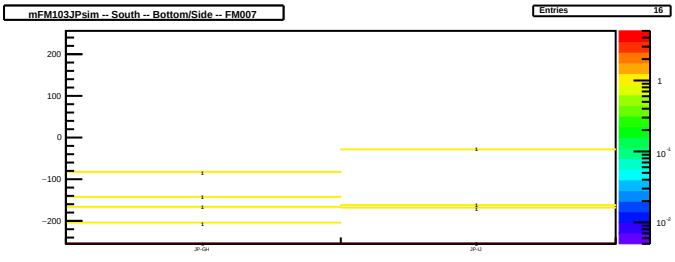

mFM103BS -- South -- Bottom/Side --FM007
Entries 120
BS3
BS2
BS1
Unused

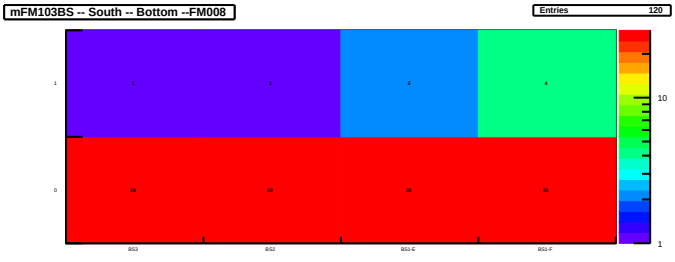

mFM103BSsim -- South -- Bottom/Side --FM0007
Entries 24
BS3
BS2
BS1
Unused

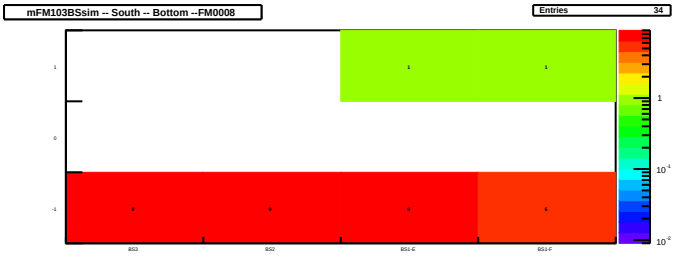

mFM103J -- South -- Bottom/Side -- FM007
Entries 30
J(23)

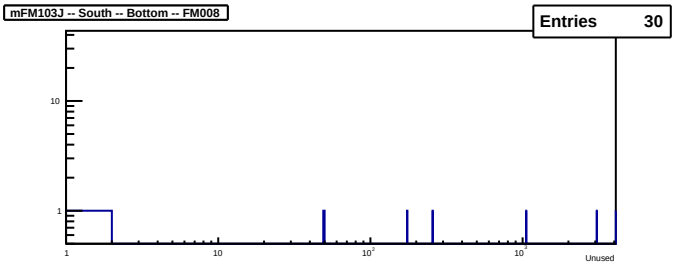

mFM103Jsim -- South -- Bottom/Side -- FM007
Entries 8
J(23)

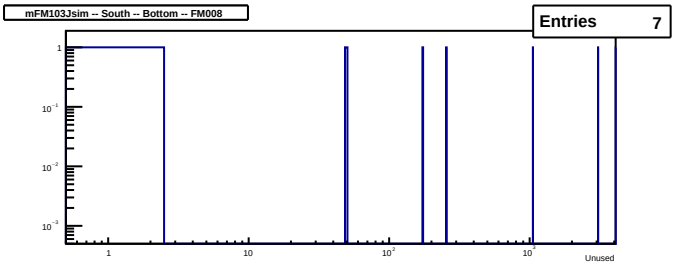

mFM103JP -- South -- Bottom/Side -- FM007
Entries 60
JP-G4
JP-I2

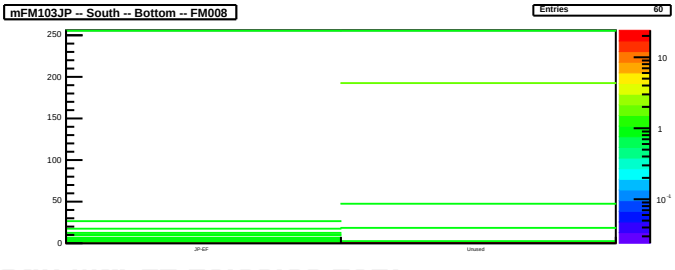

mFM103JPsim -- South -- Bottom/Side -- FM007
Entries 16
JP-G4
JP-I2

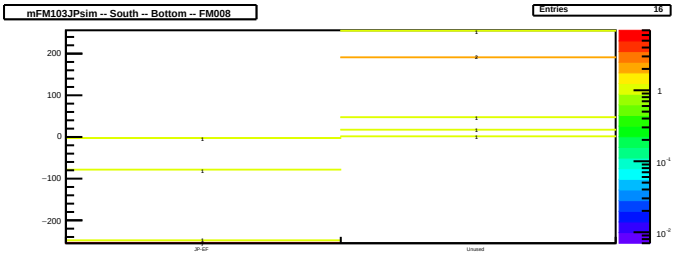

mFM103BS -- South -- Bottom --FM008
Entries 120
BS3
BS2
BS1-E
BS1-F

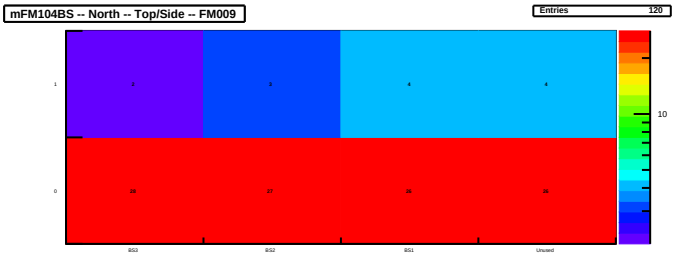

mFM103BSsim -- South -- Bottom --FM0008
Entries 34
BS3
BS2
BS1-E
BS1-F

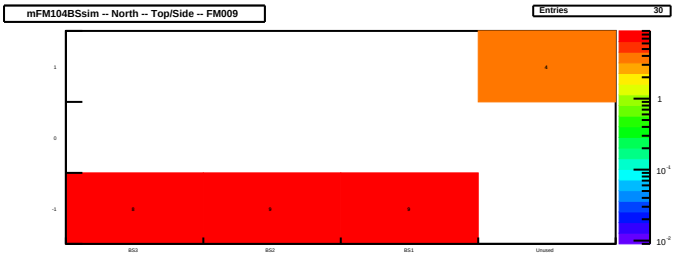

mFM103J -- South -- Bottom -- FM008
Entries 30
Unused

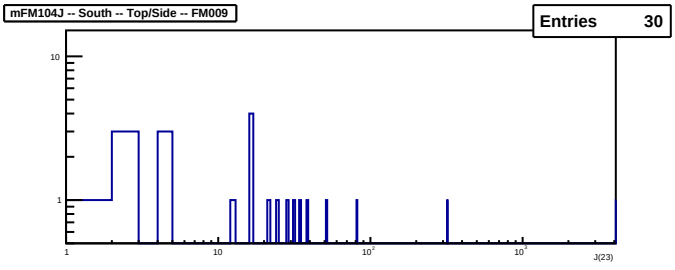

mFM103Jsim -- South -- Bottom -- FM008
Entries 7
Unused

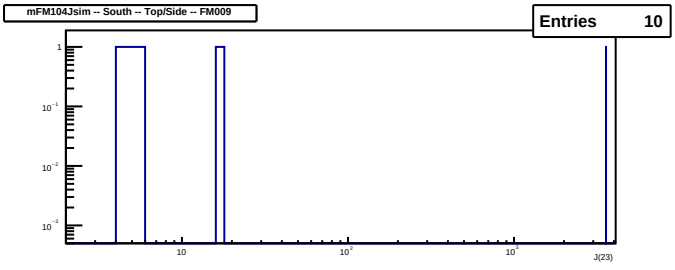

mFM103JP -- South -- Bottom -- FM008
Entries 60
JP-E2
Unused

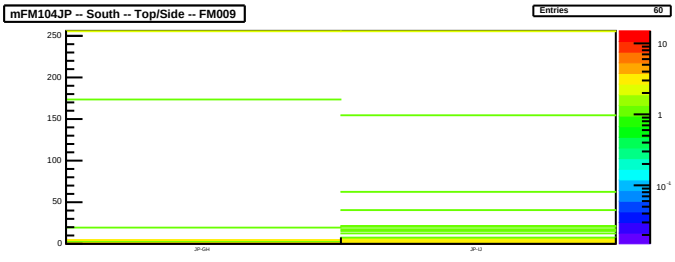

mFM103JPsim -- South -- Bottom -- FM008
Entries 16
JP-E2
Unused

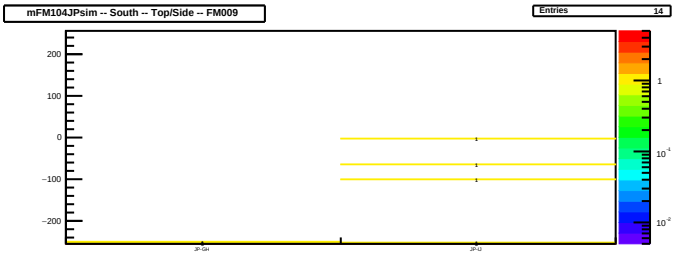

mFM104BS -- North -- Top/Side -- FM009
Entries 120

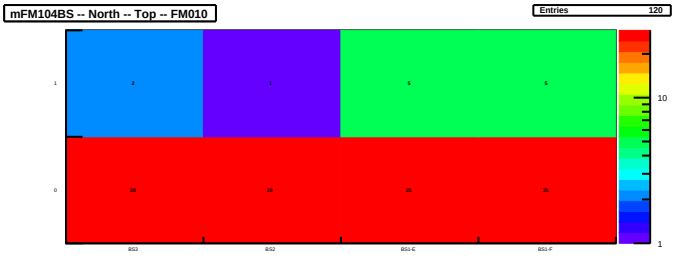

mFM104BSsim -- North -- Top/Side -- FM009
Entries 30

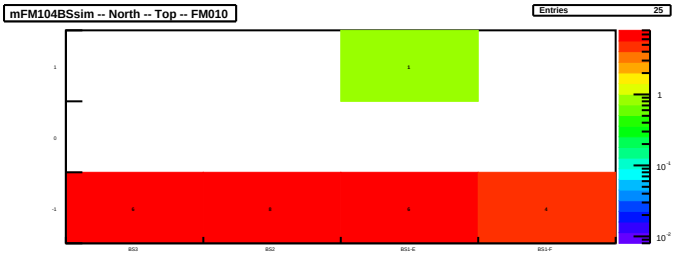

mFM104J -- South -- Top/Side -- FM009
Entries 30
J(23)

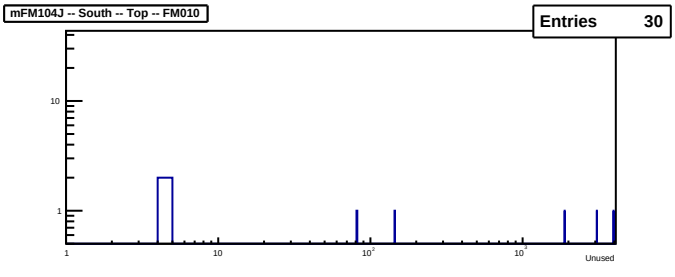

mFM104Jsim -- South -- Top/Side -- FM009
Entries 10
J(23)

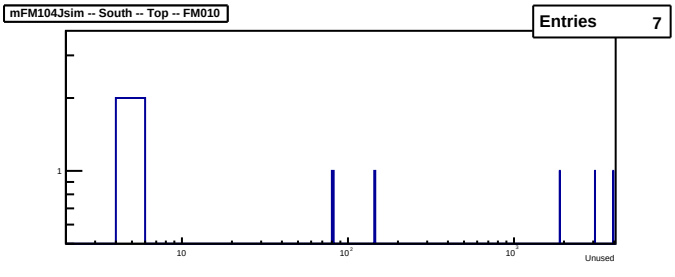

mFM104JP -- South -- Top/Side -- FM009
Entries 60

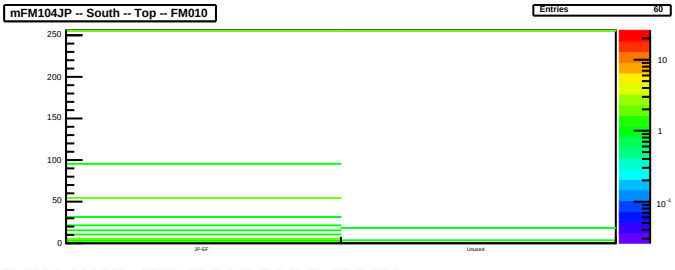

mFM104JPsim -- South -- Top/Side -- FM009
Entries 14

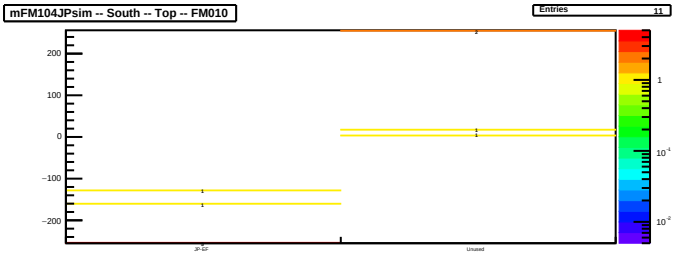

mFM104BS -- North -- Top -- FM010
Entries 120

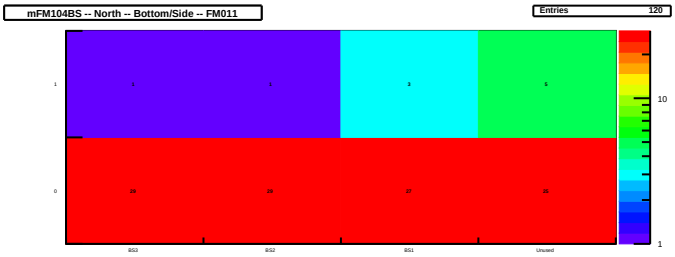

mFM104BSsim -- North -- Top -- FM010
Entries 25

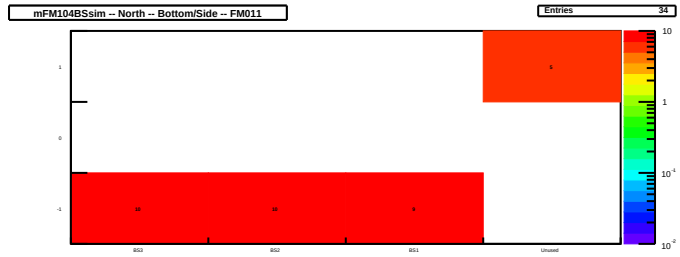

mFM104J -- South -- Top -- FM010
Entries 30
Unused

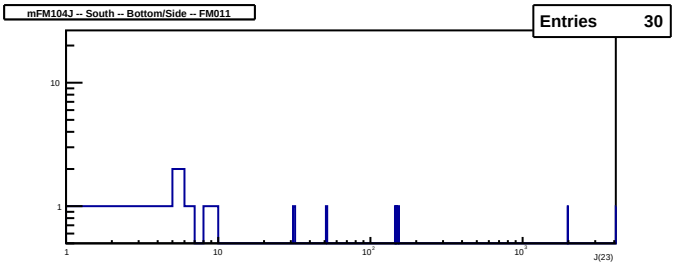

mFM104Jsim -- South -- Top -- FM010
Entries 7
Unused

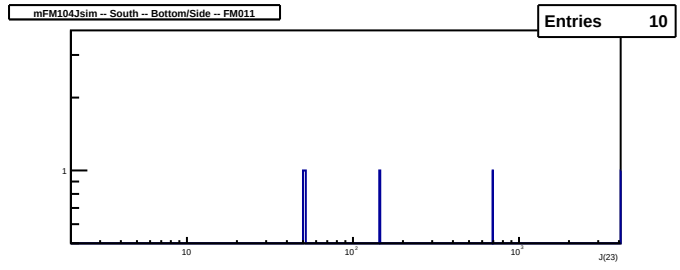

mFM104JP -- South -- Top -- FM010
Entries 60

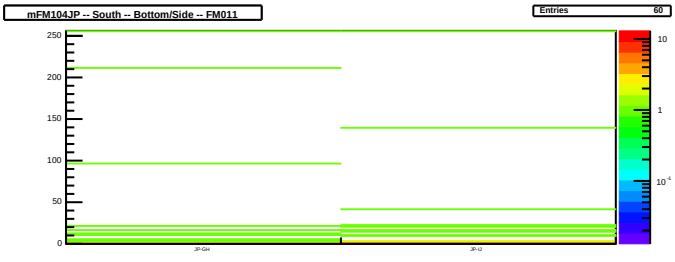

mFM104JPsim -- South -- Top -- FM010
Entries 11

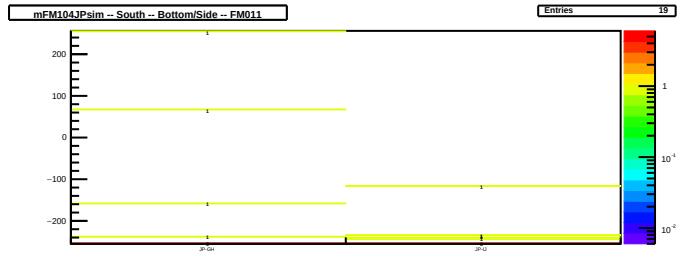

mFM104BS -- North -- Bottom/Side -- FM011
Entries 120

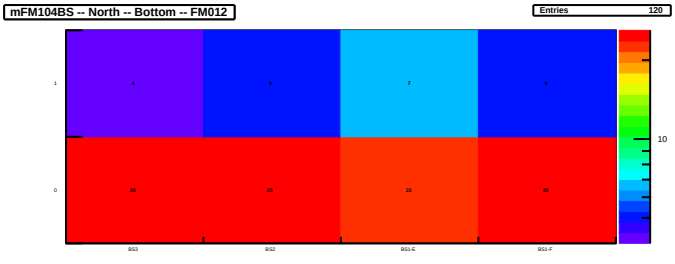

mFM104BSsim -- North -- Bottom/Side -- FM011
Entries 34

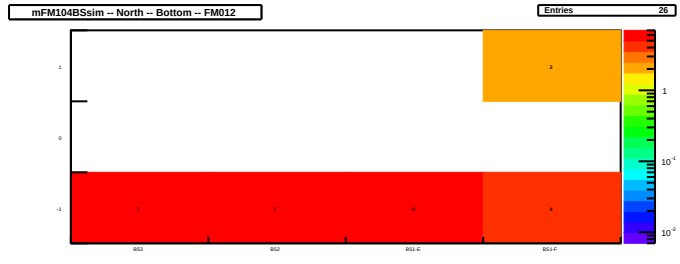

mFM104J -- South -- Bottom/Side -- FM011
Entries 30
J(23)

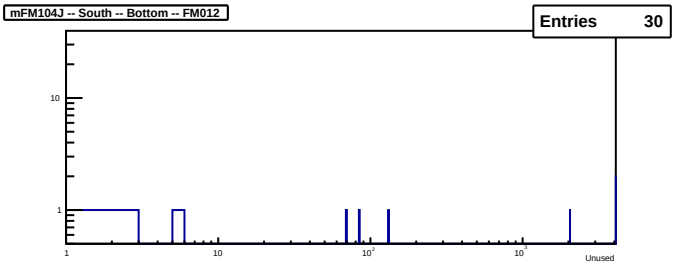

mFM104Jsim -- South -- Bottom/Side -- FM011
Entries 10
J(23)

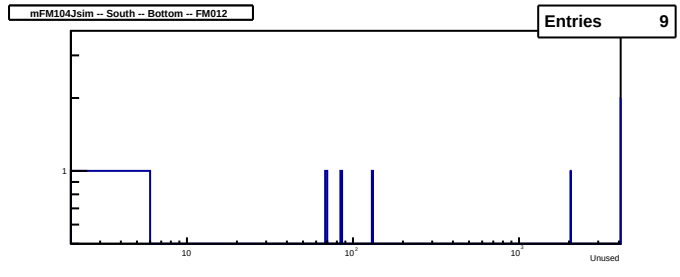

mFM104JP -- South -- Bottom/Side -- FM011
Entries 60

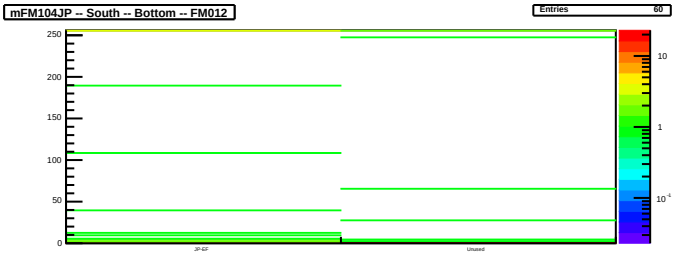

mFM104JPsim -- South -- Bottom/Side -- FM011
Entries 19

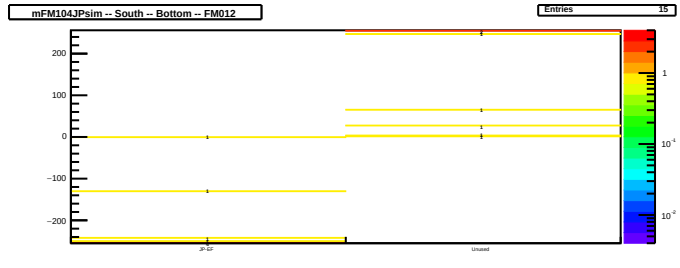

mFM104BS -- North -- Bottom -- FM012
Entries 120

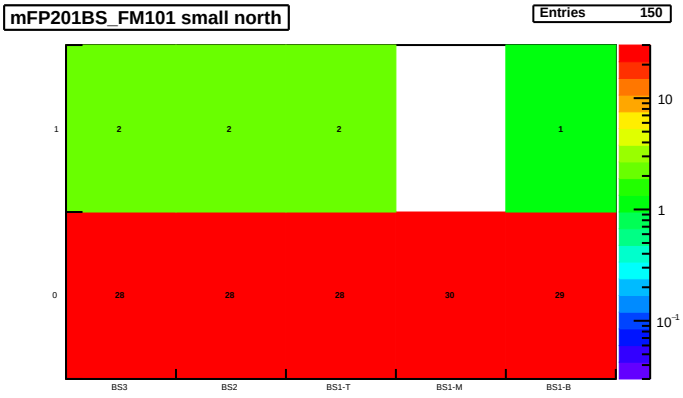

mFM104BSsim -- North -- Bottom -- FM012
Entries 26

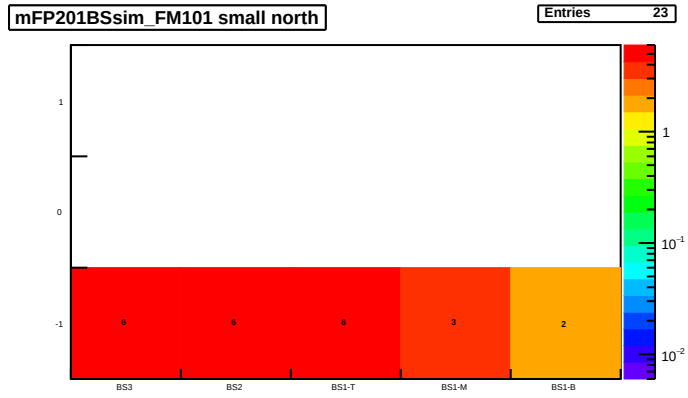

mFM104J -- South -- Bottom -- FM012
Entries 30
Unused

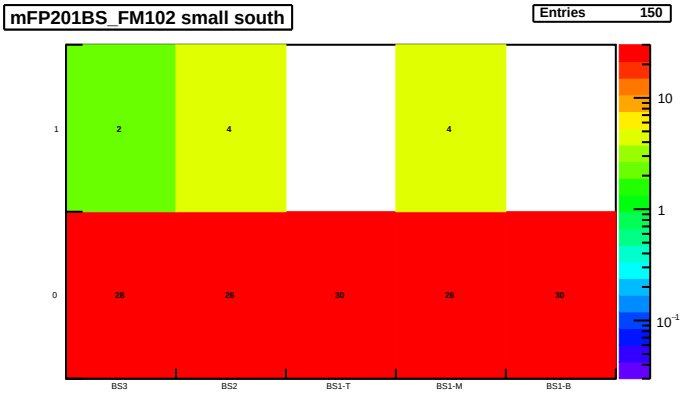

mFM104Jsim -- South -- Bottom -- FM012
Entries 9
Unused

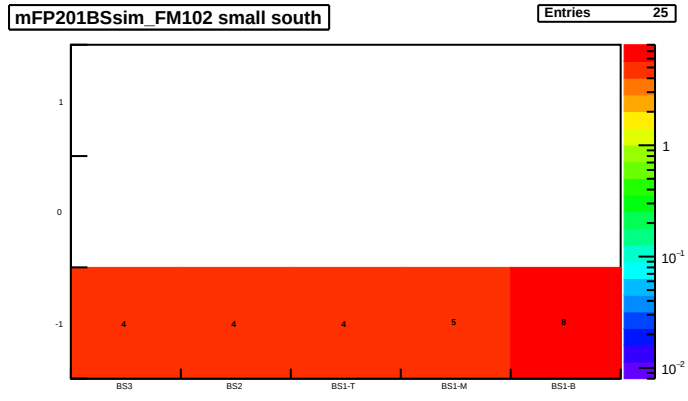

mFM104JP -- South -- Bottom -- FM012
Entries 60

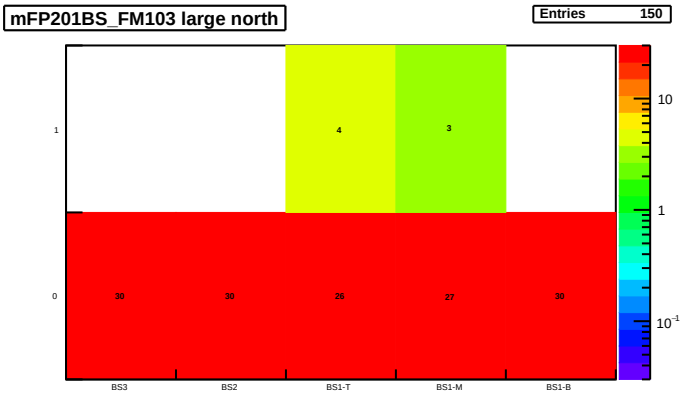

mFM104JPsim -- South -- Bottom -- FM012
Entries 15

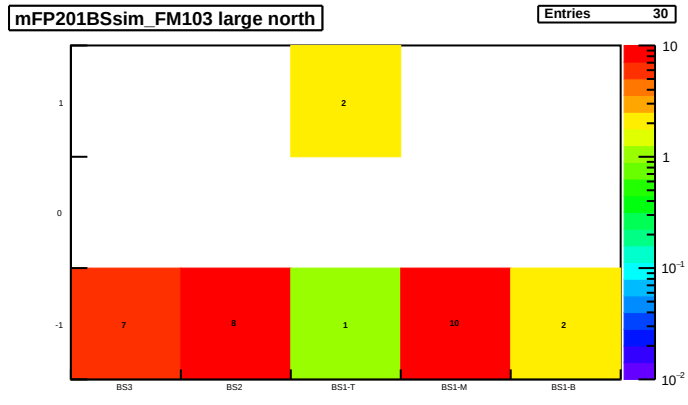

mFP201BS_FM101 small north
Entries 150
1
0
2 2 2 1
28 28 28 30 29
BS3 BS2 BS1-T BS1-M BS1-B
10
1
10^{-1}

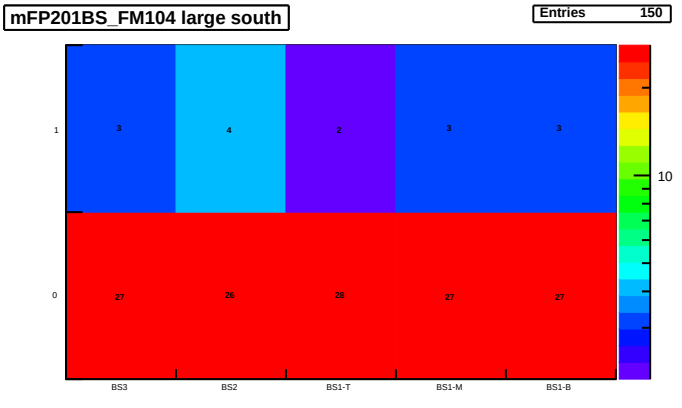

mFP201BSsim_FM101 small north
Entries 23
1
0
-1
6 6 6 3 2
BS3 BS2 BS1-T BS1-M BS1-B
1
10^{-1}
10^{-2}

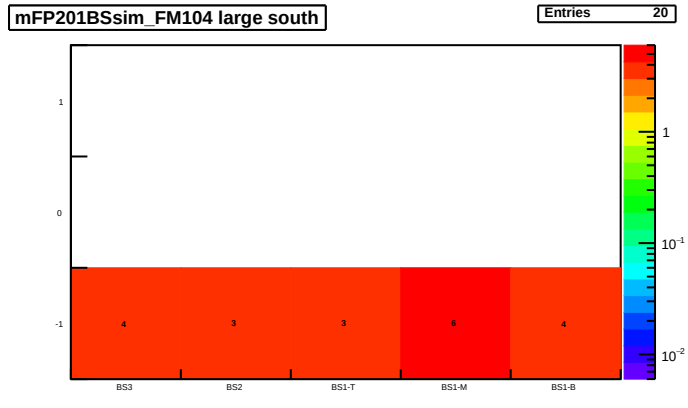

mFP201BS_FM102 small south
Entries 150
1
0
2 4 4
28 26 30 26 30
BS3 BS2 BS1-T BS1-M BS1-B
10
1
10^{-1}

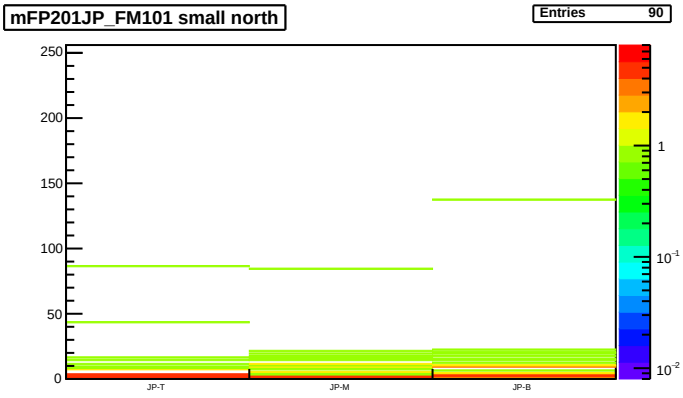

mFP201BSsim_FM102 small south
Entries 25
1
0
-1
4 4 4 5 8
BS3 BS2 BS1-T BS1-M BS1-B
1
10^{-1}
10^{-2}

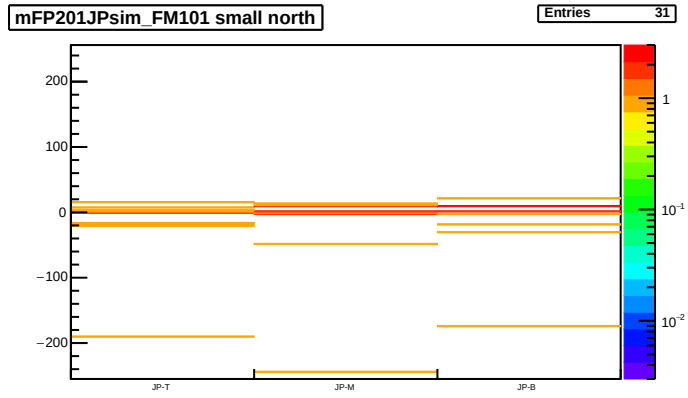

mFP201BS_FM103 large north
Entries 150
1
0
4 3
30 30 26 27 30
BS3 BS2 BS1-T BS1-M BS1-B
10
1
10^{-1}

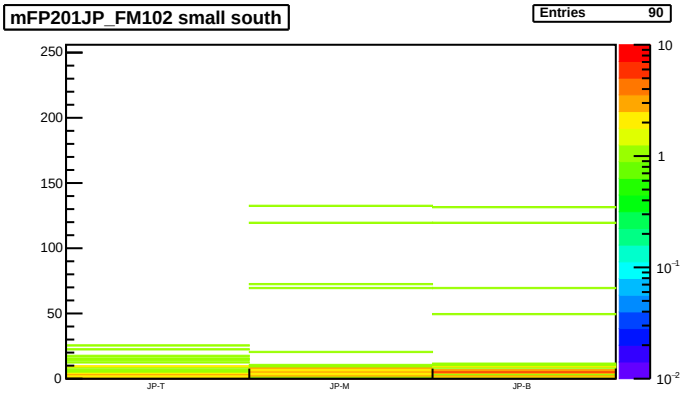

mFP201BSsim_FM103 large north
Entries 30
1
0
-1
2
7 8 1 10 2
BS3 BS2 BS1-T BS1-M BS1-B
10
1
10^{-1}
10^{-2}

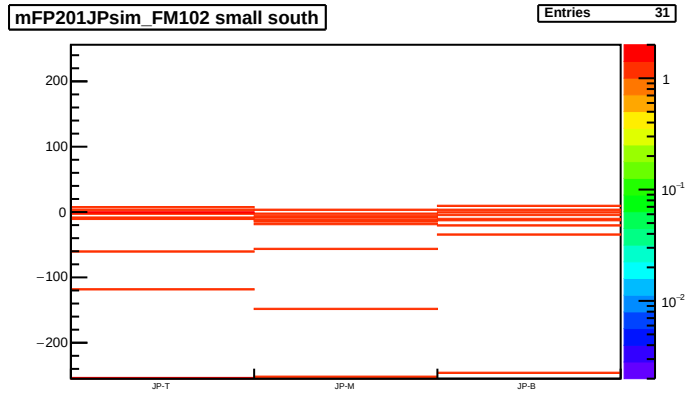

mFP201BS_FM104 large south
Entries 150
1
0
3 4 2 3 3
27 26 28 27 27
BS3 BS2 BS1-T BS1-M BS1-B
10

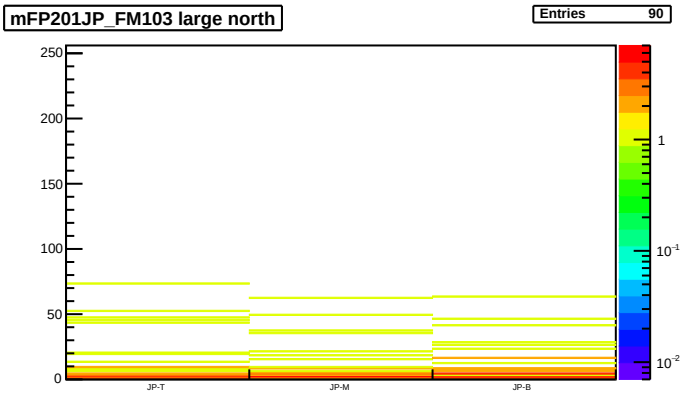

mFP201BSsim_FM104 large south
Entries 20
1
0
-1
4 3 3 6 4
BS3 BS2 BS1-T BS1-M BS1-B
1
10^{-1}
10^{-2}

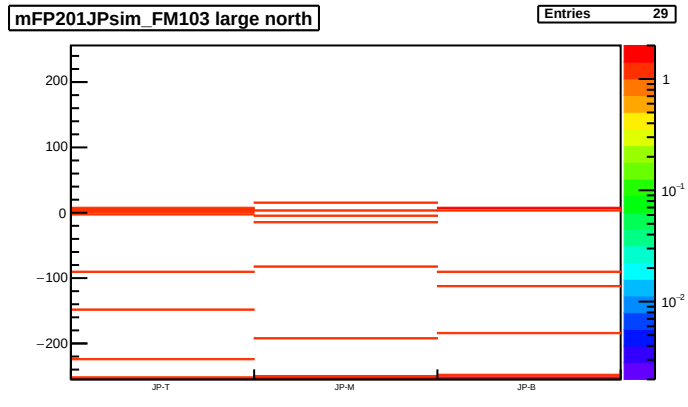

mFP201JP_FM101 small north
Entries 90
JP-T
JP-M
JP-B

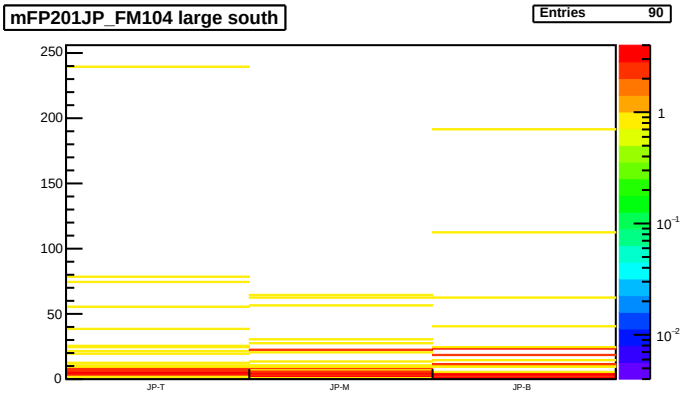

mFP201JPsim_FM101 small north
Entries 31
JP-T
JP-M
JP-B

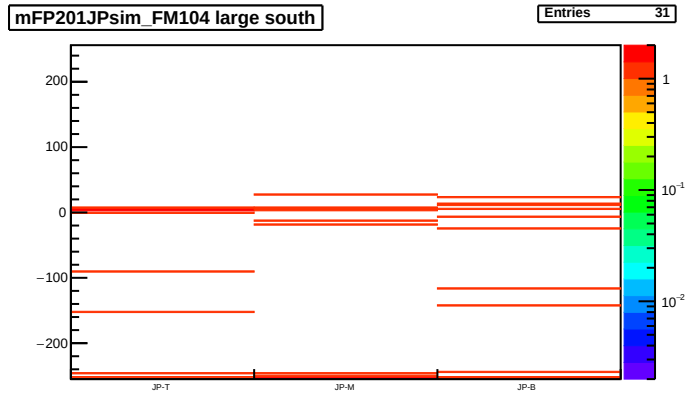

mFP201JP_FM102 small south
Entries 90
JP-T
JP-M
JP-B

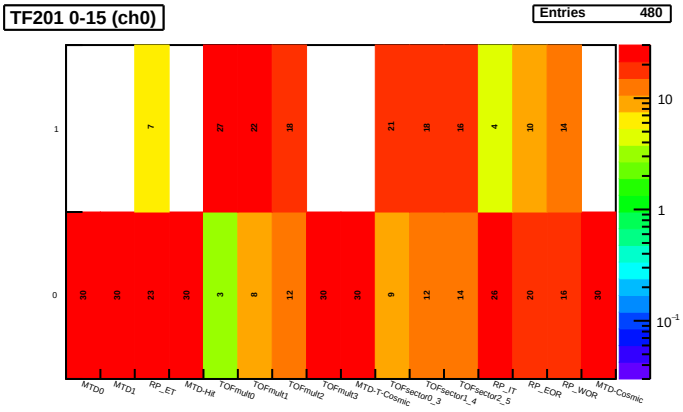

mFP201JPsim_FM102 small south
Entries 31
JP-T
JP-M
JP-B

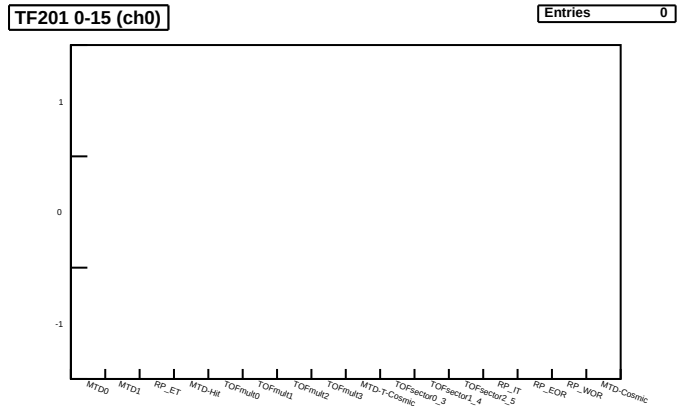

mFP201JP_FM103 large north
Entries 90
JP-T
JP-M
JP-B

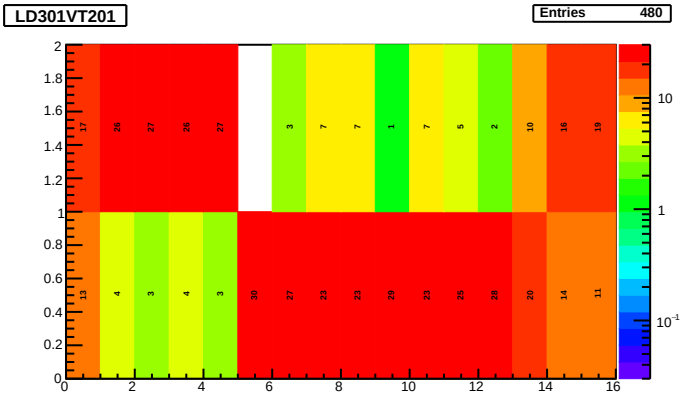

mFP201JPsim_FM103 large north
Entries 29
JP-T
JP-M
JP-B

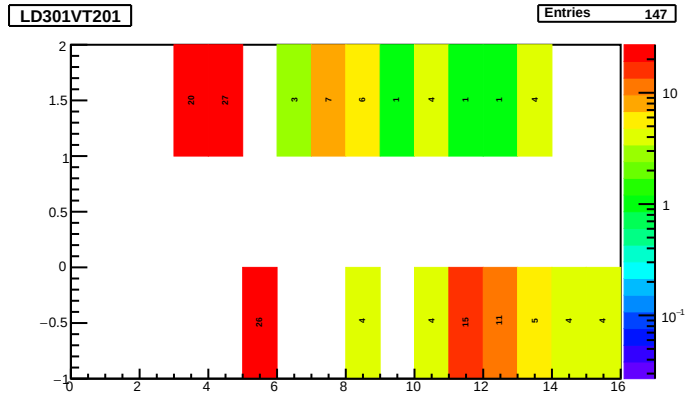

mFP201JP_FM104 large south
Entries 90
JP-T
JP-M
JP-B

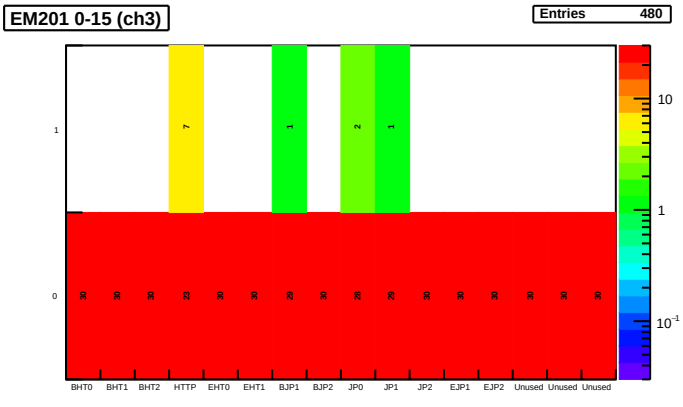

mFP201JPsim_FM104 large south
Entries 31
JP-T
JP-M
JP-B

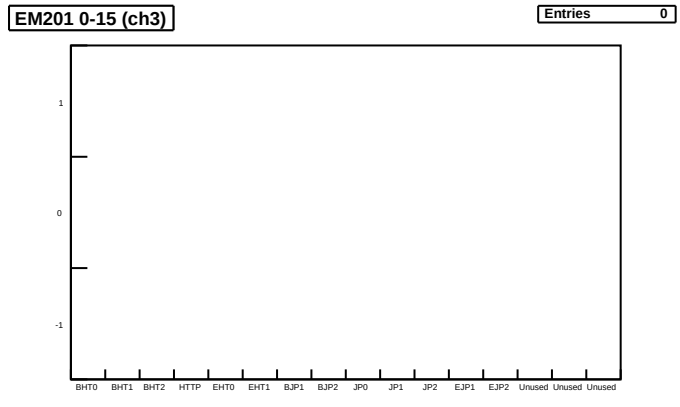

TF201 0-15 (ch0)
Entries 480
MTD0 MTD1 RP_ET MTD-Hit TOFmult0 TOFmult1 TOFmult2 TOFmult3 MTD-T-Cosmic TOFsector0_3 TOFsector1_4 TOFsector2_5 RP_IT RP_EOR RP_WOR MTD-Cosmic

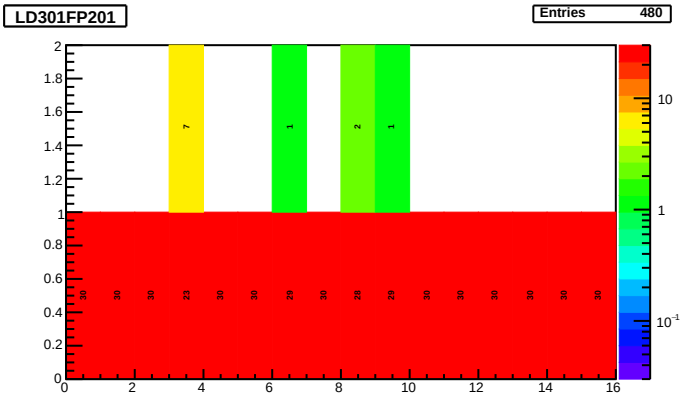

TF201 0-15 (ch0)
Entries 0
MTD0 MTD1 RP_ET MTD-Hit TOFmult0 TOFmult1 TOFmult2 TOFmult3 MTD-T-Cosmic TOFsector0_3 TOFsector1_4 TOFsector2_5 RP_IT RP_EOR RP_WOR MTD-Cosmic

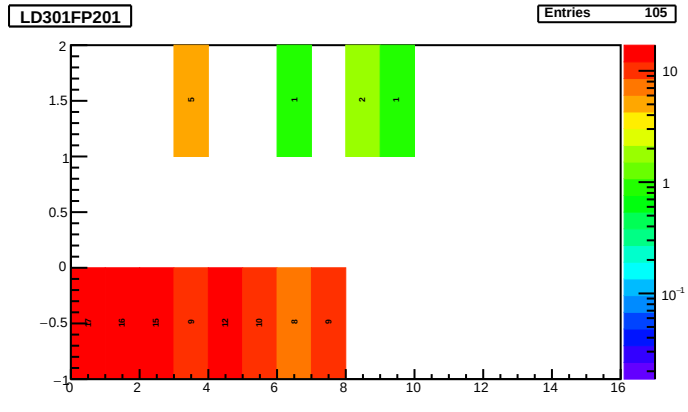

LD301VT201
Entries 480


LD301VT201
Entries 147


EM201 0-15 (ch3)
Entries 480
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


LD301FP201
Entries 480


LD301FP201
Entries 105