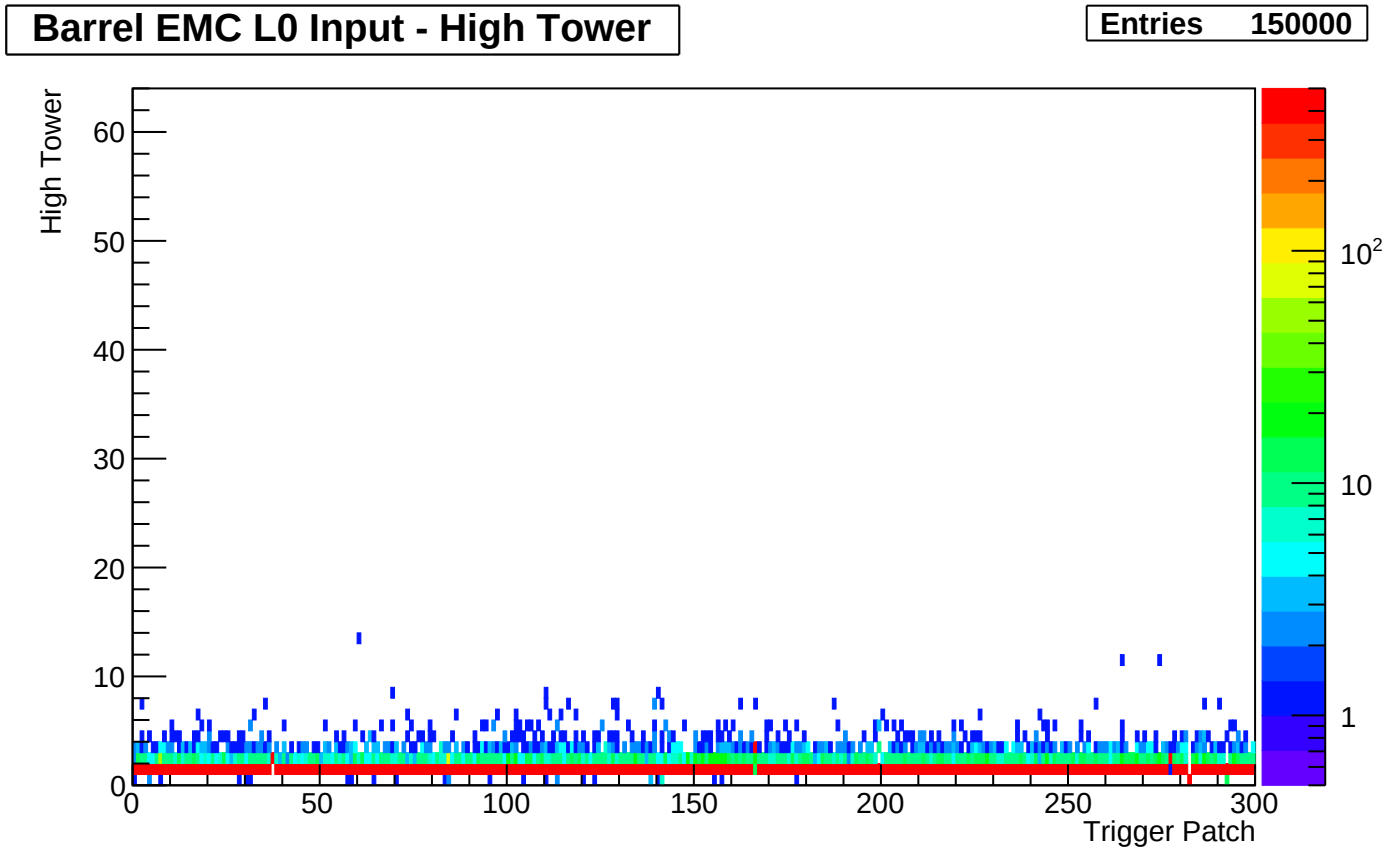
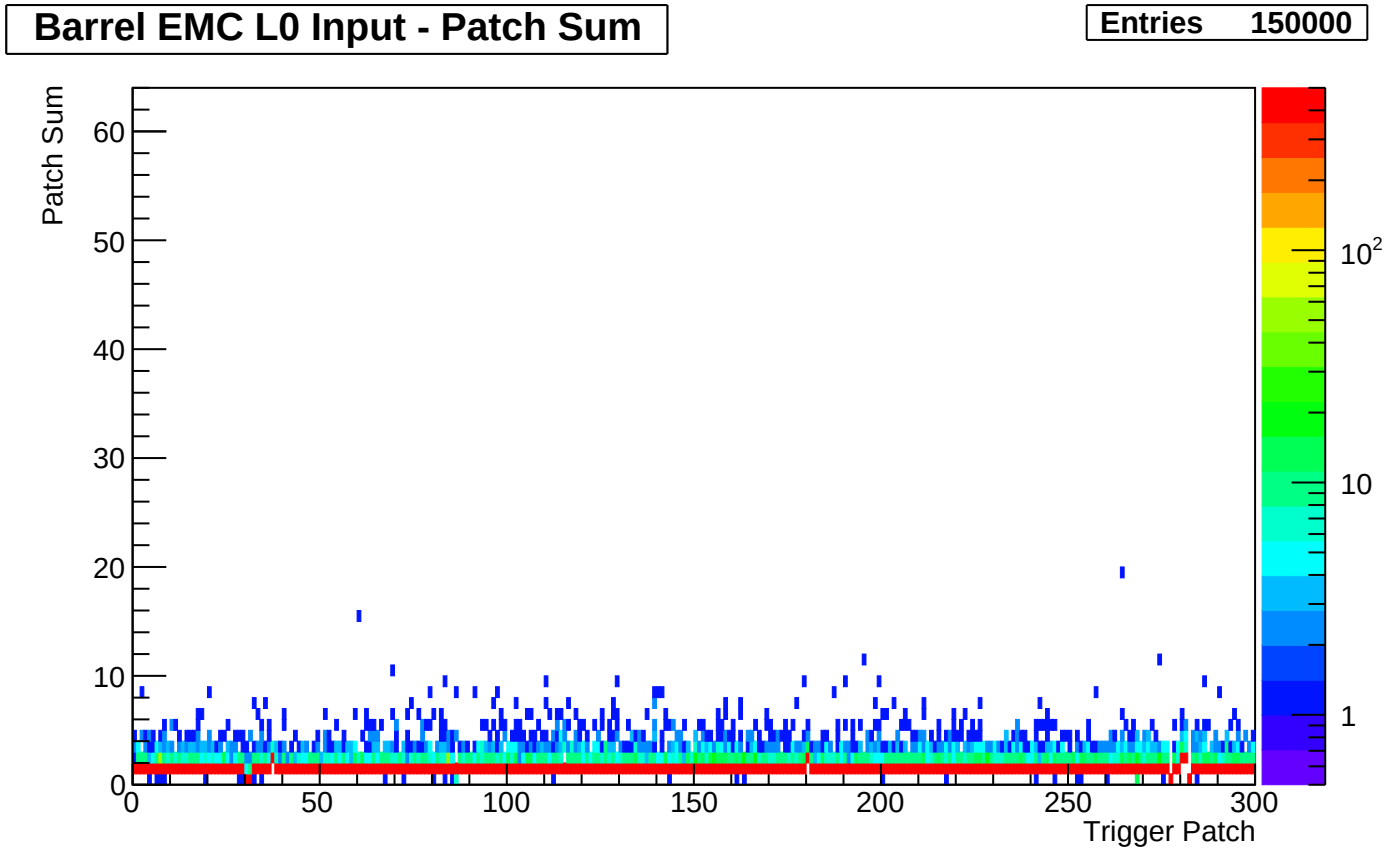
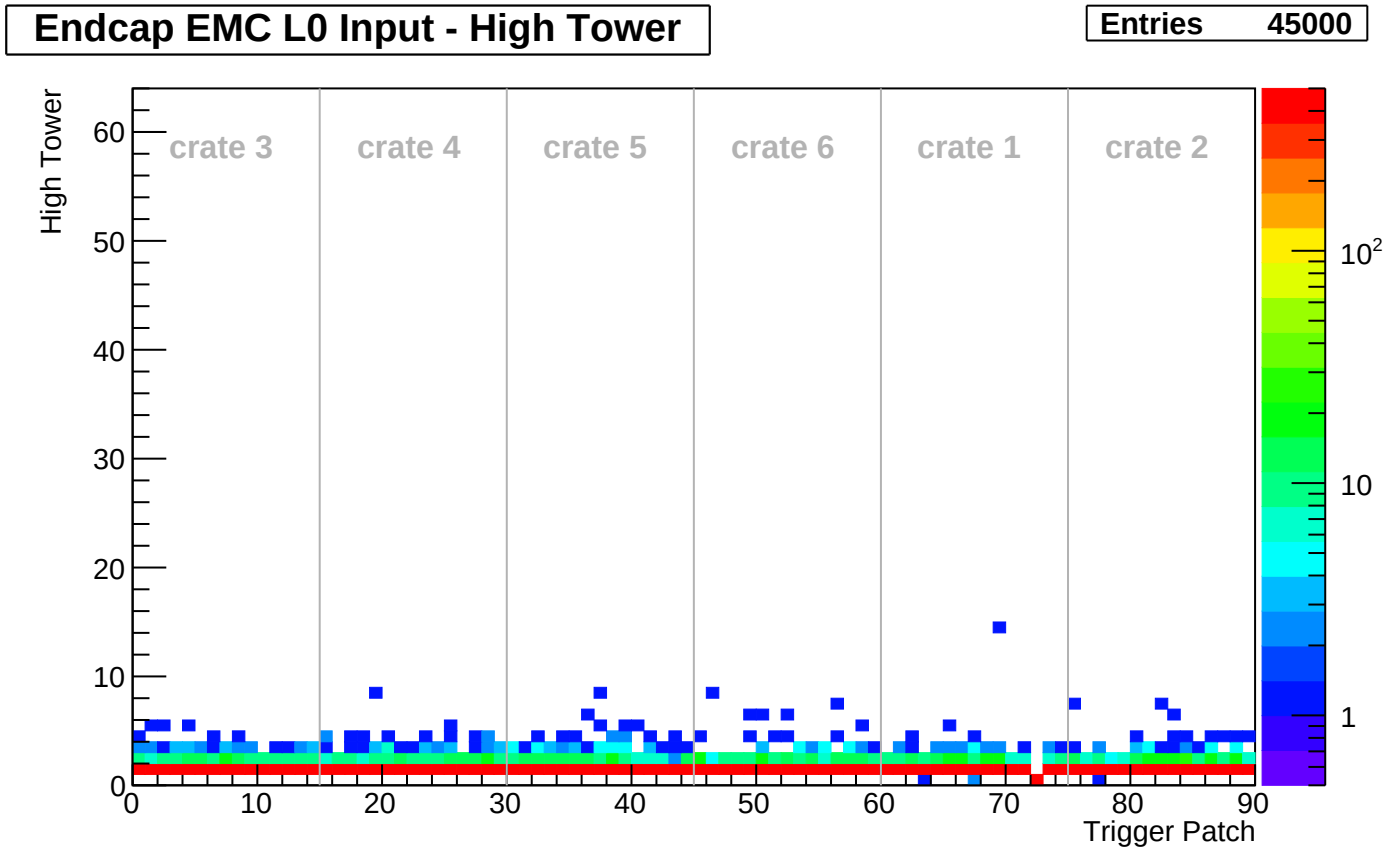
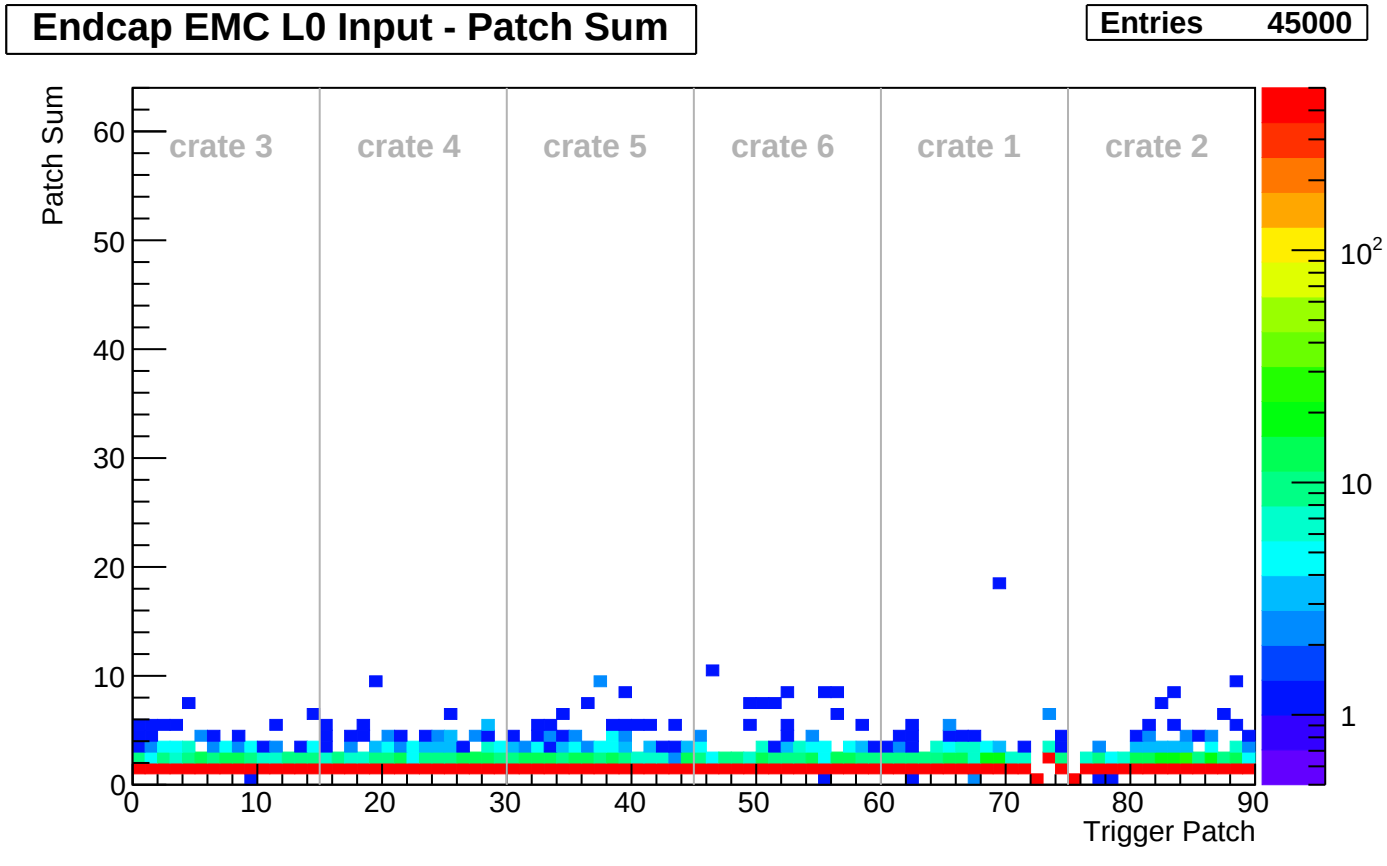
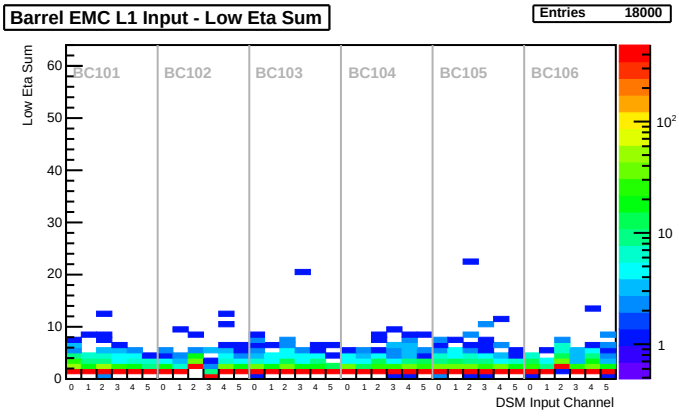
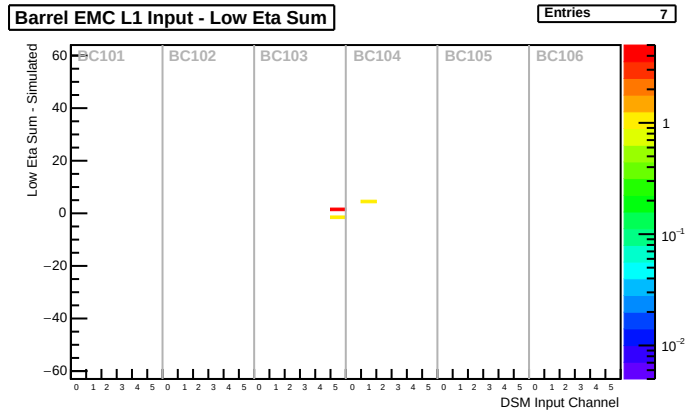

Barrel EMC L0 Input - High Tower
Entries 150000
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2

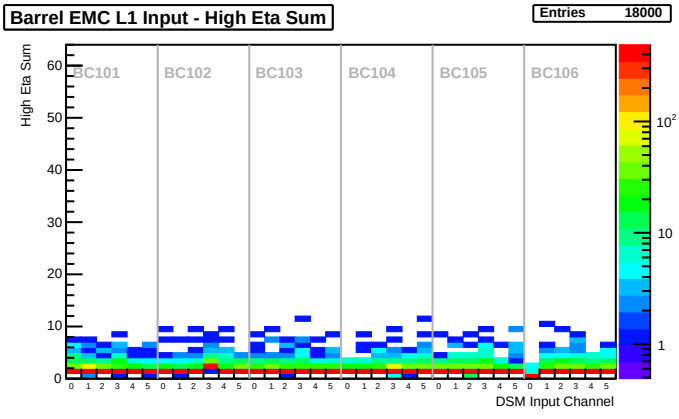

Barrel EMC L0 Input - Patch Sum
Entries 150000
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2

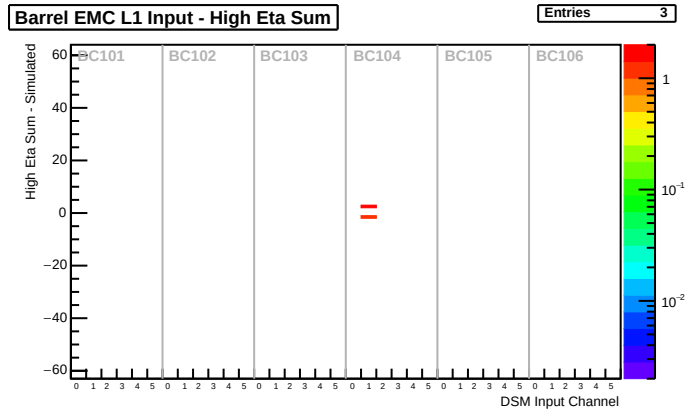

Endcap EMC L0 Input - High Tower
Entries 45000
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2

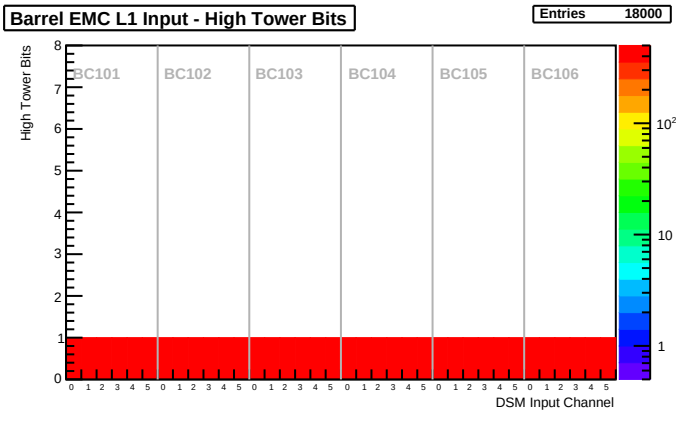

Endcap EMC L0 Input - Patch Sum
Entries 45000
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2

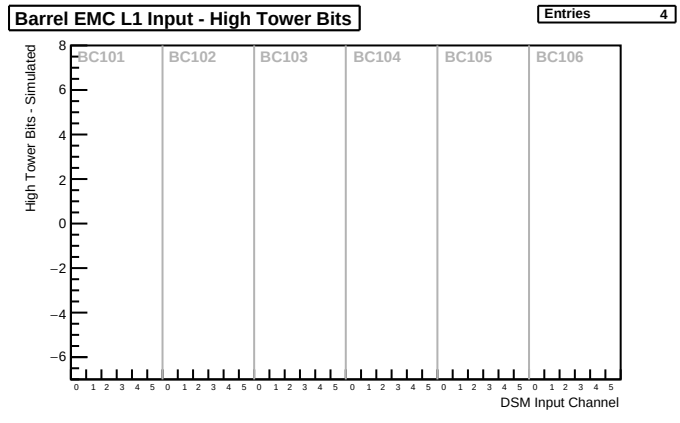

Barrel EMC L1 Input - Low Eta Sum
Entries 18000
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

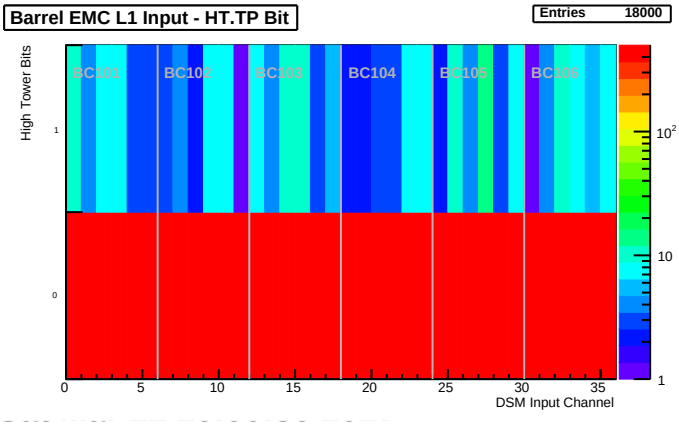

Barrel EMC L1 Input - Low Eta Sum
Entries 7
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

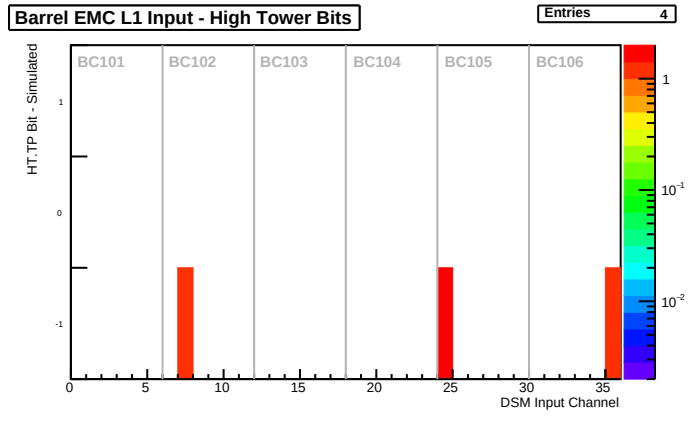

Barrel EMC L1 Input - High Eta Sum
Entries 18000
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

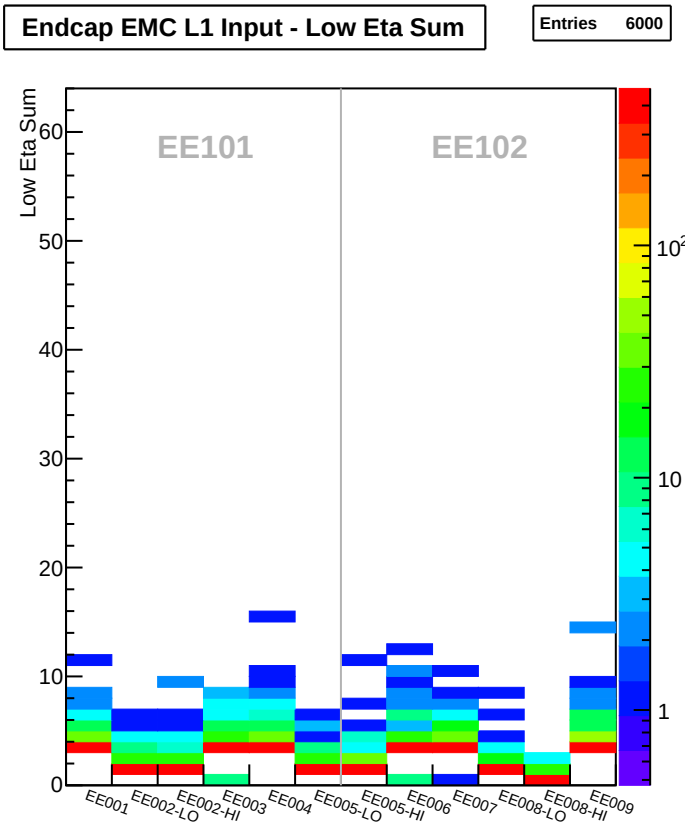

Barrel EMC L1 Input - High Eta Sum
Entries 3
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

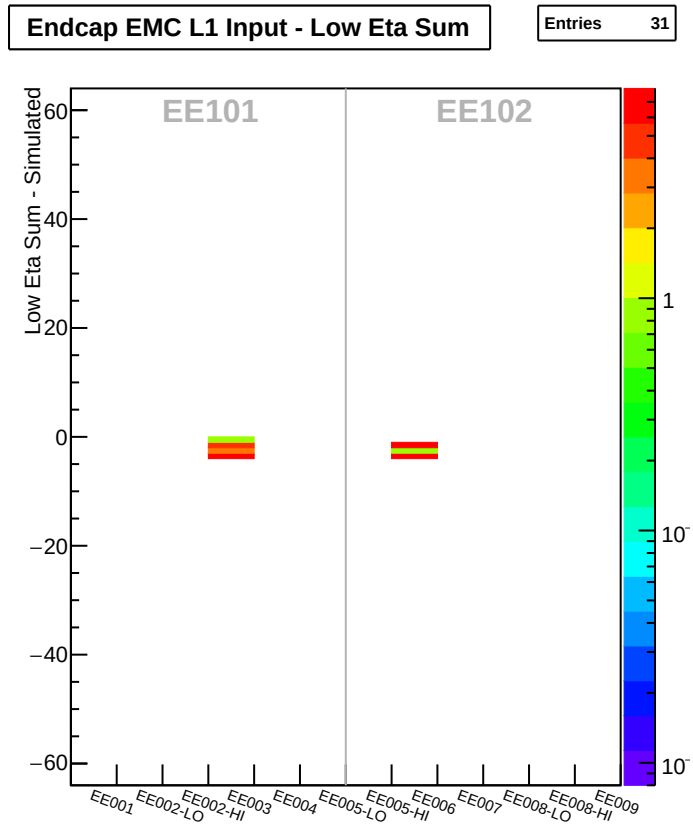

Barrel EMC L1 Input - High Tower Bits
Entries 18000
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

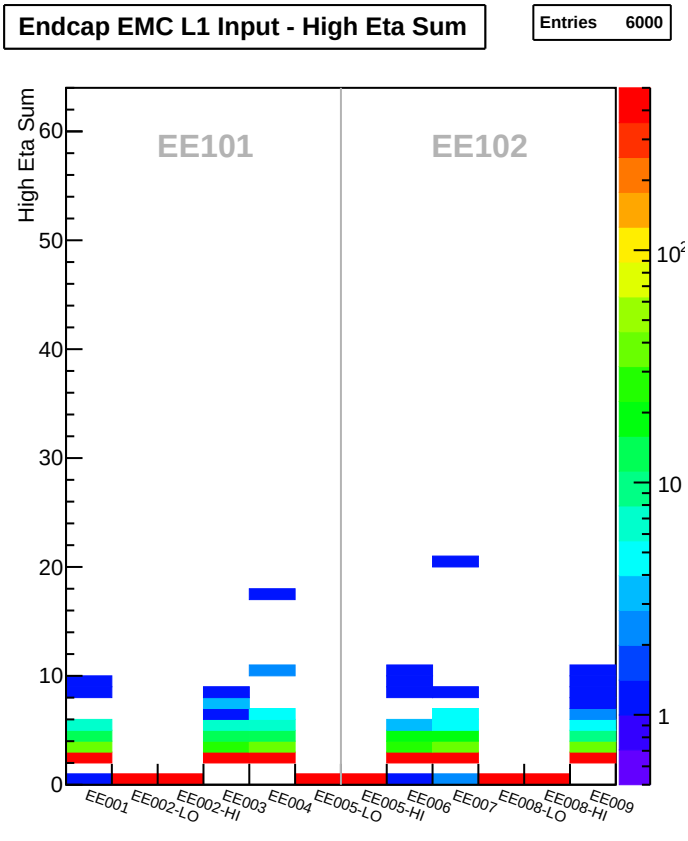

Barrel EMC L1 Input - High Tower Bits
Entries 4
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

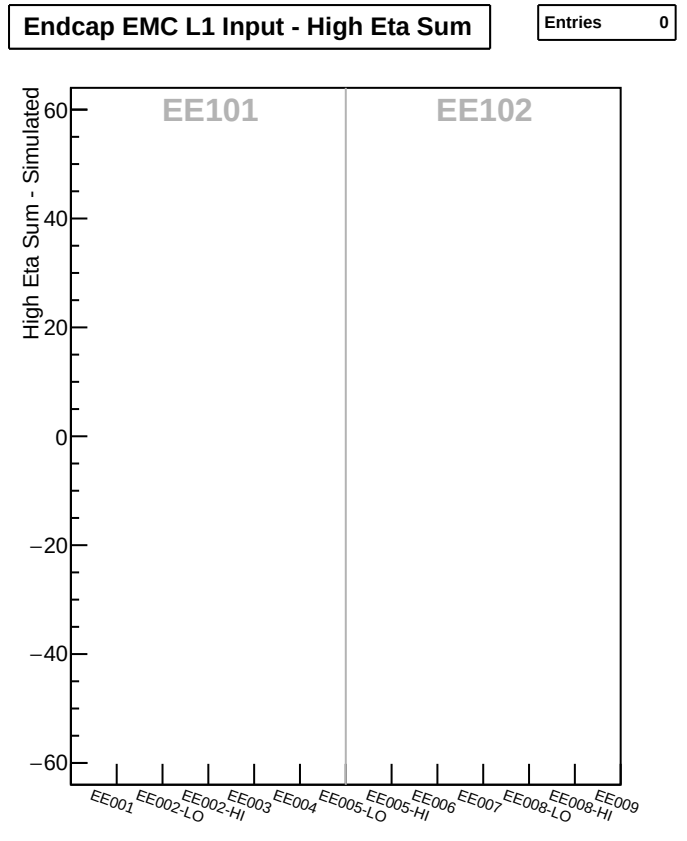

Barrel EMC L1 Input - HT.TP Bit
Entries 18000
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

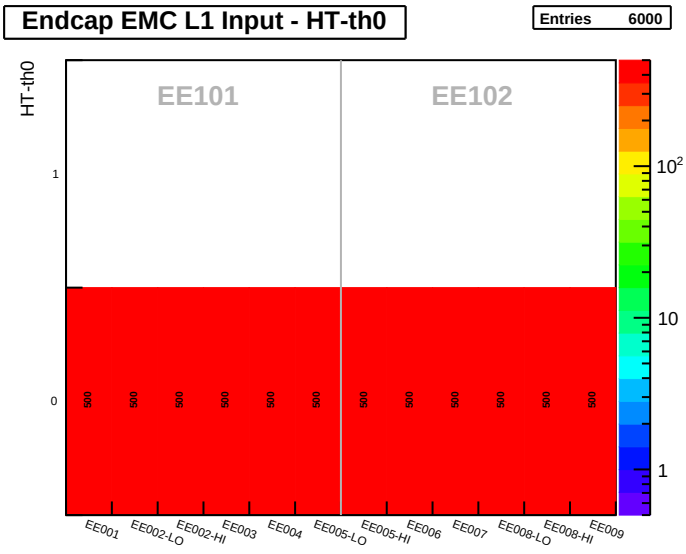

Barrel EMC L1 Input - High Tower Bits
Entries 4
HT.TP Bit - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

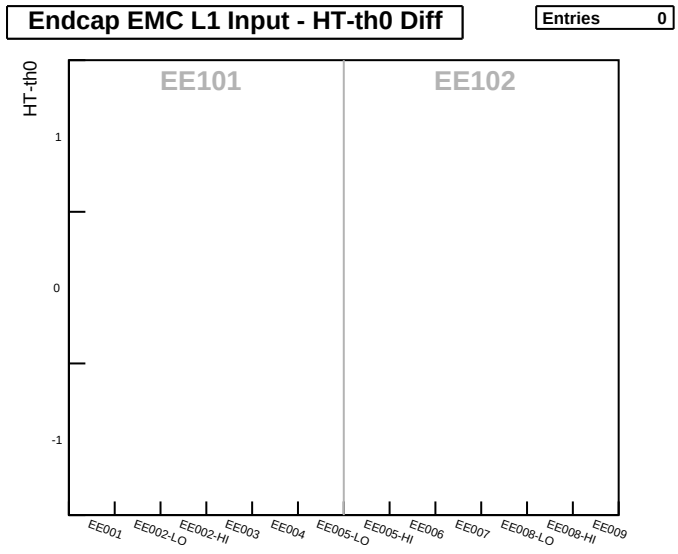

Endcap EMC L1 Input - Low Eta Sum
Entries 6000
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

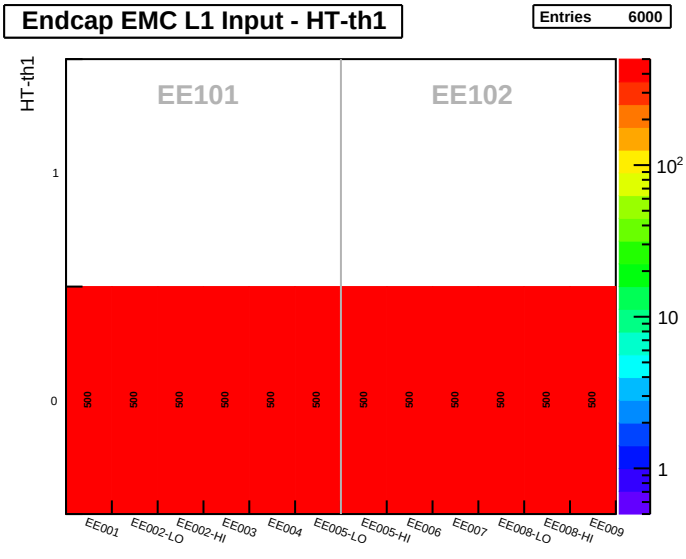

Endcap EMC L1 Input - Low Eta Sum
Entries 31
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

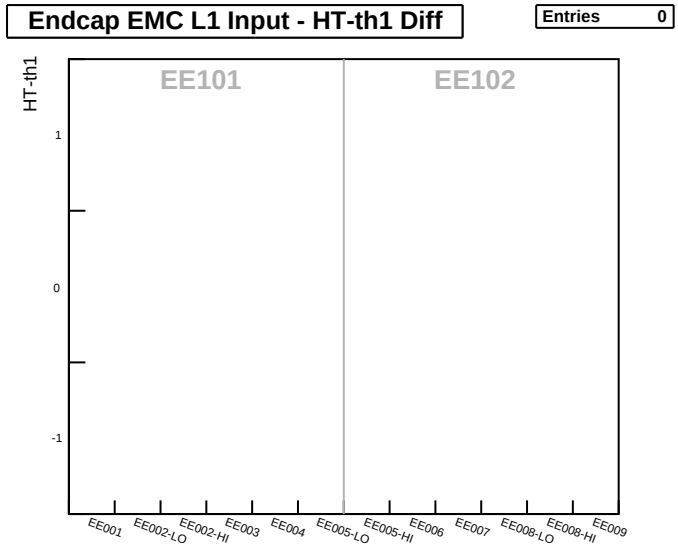

Endcap EMC L1 Input - High Eta Sum
Entries 6000
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

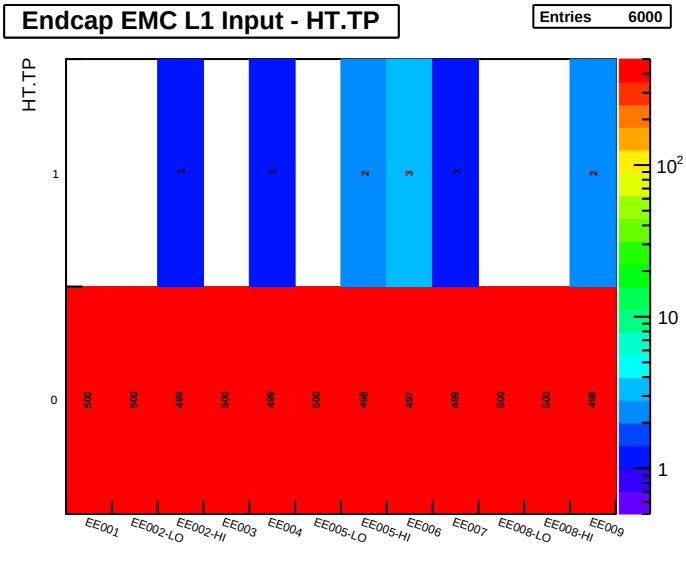

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

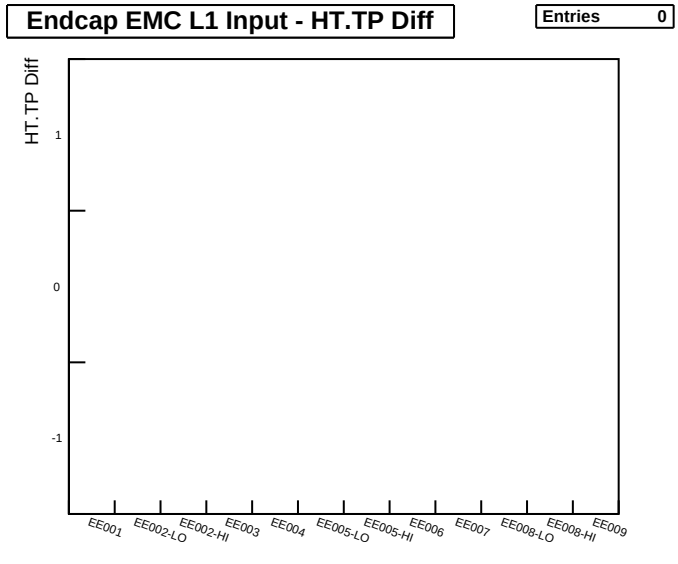

Endcap EMC L1 Input - HT-th0
Entries 6000
HT-th0
EE101
EE102
1
0
500
10^2
10
1
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

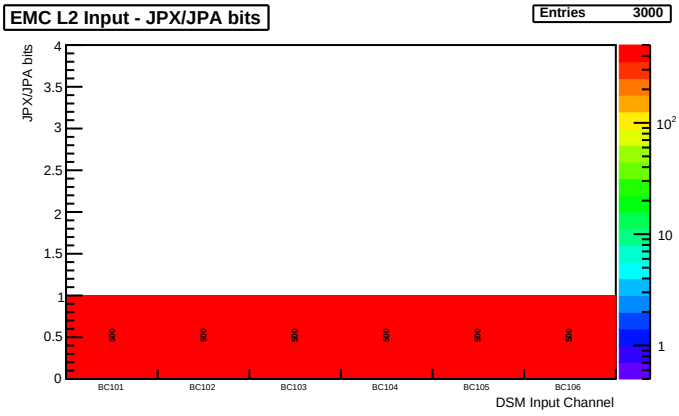

Endcap EMC L1 Input - HT-th0 Diff
Entries 0
HT-th0
EE101
EE102
1
0
-1
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

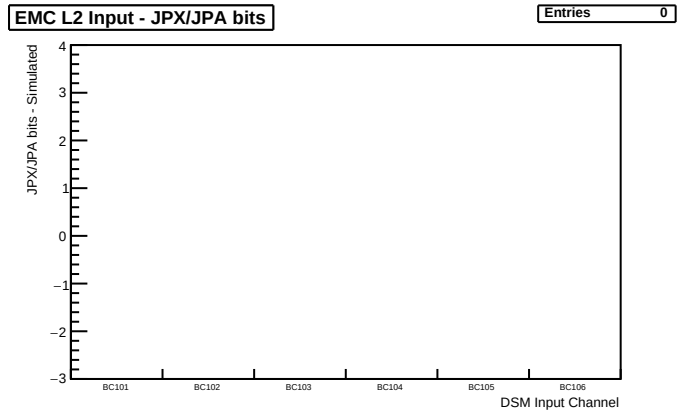

Endcap EMC L1 Input - HT-th1
Entries 6000
HT-th1
EE101
EE102
1
0
500
10^2
10
1
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

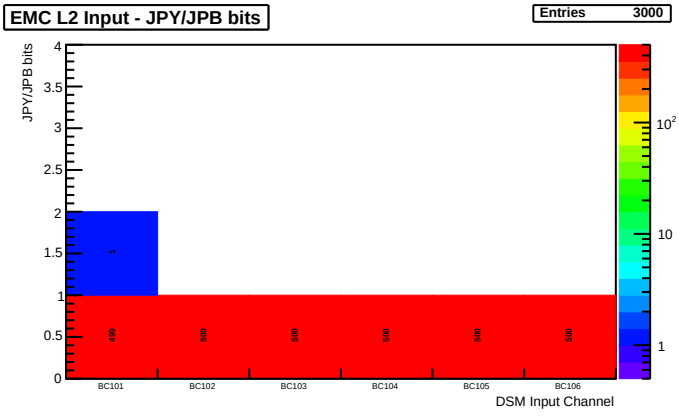

Endcap EMC L1 Input - HT-th1 Diff
Entries 0
HT-th1
EE101
EE102
1
0
-1
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

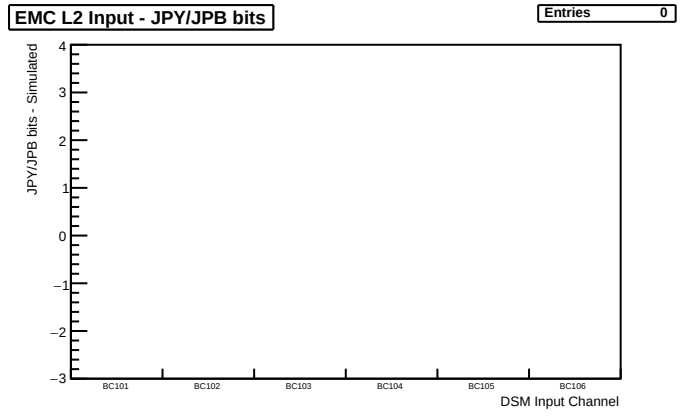

Endcap EMC L1 Input - HT.TP
Entries 6000
HT.TP
1
0
1
1
2
3
1
2
500
500
499
500
499
500
498
497
499
500
500
498
10^2
10
1
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

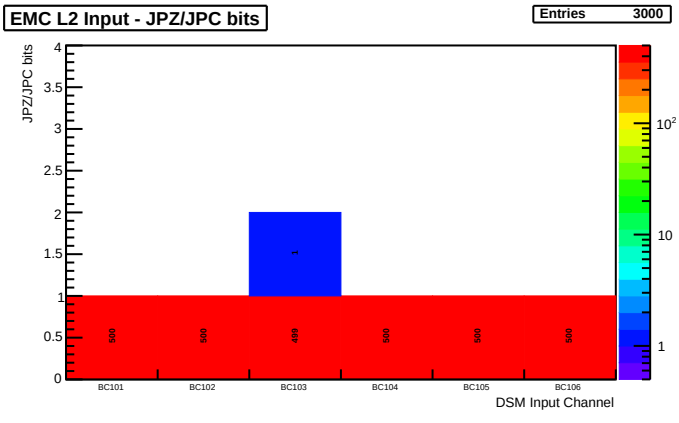

Endcap EMC L1 Input - HT.TP Diff
Entries 0
HT.TP Diff
1
0
-1
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

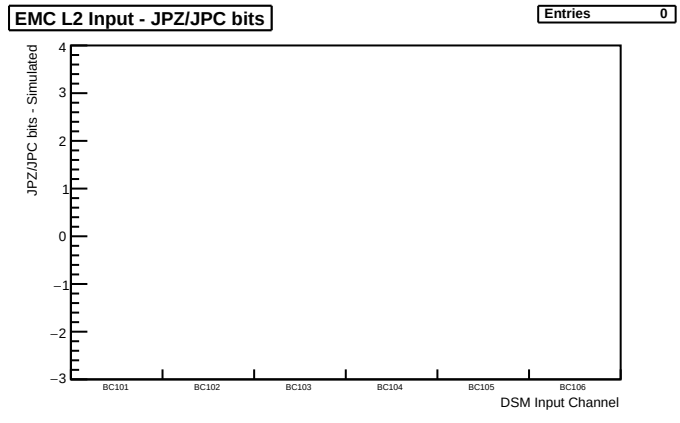

EMC L2 Input - JPX/JPA bits
Entries 3000
JPX/JPA bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
500
500
500
500
500
500

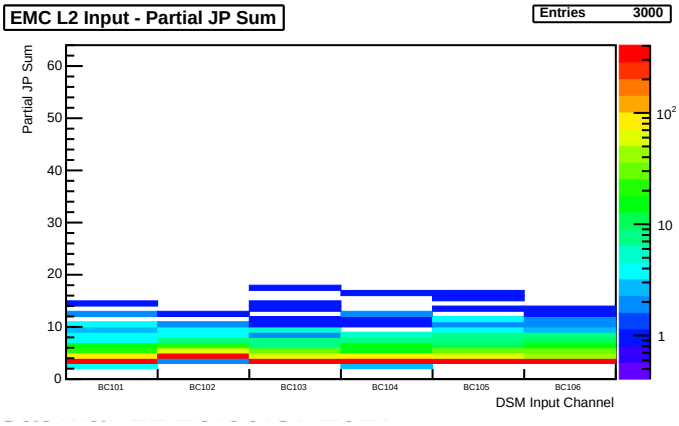

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

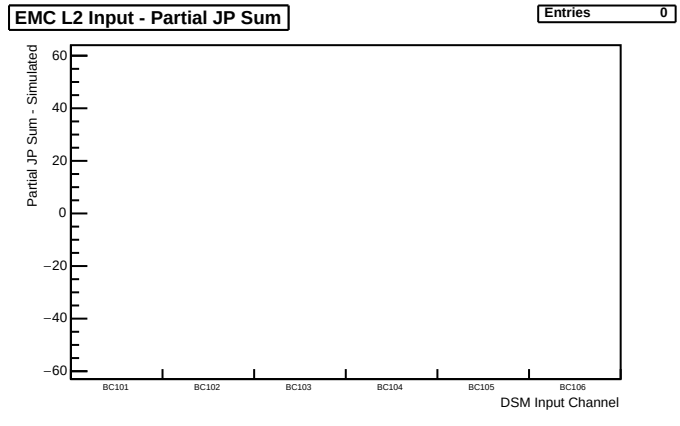

EMC L2 Input - JPY/JPB bits
Entries 3000
JPY/JPB bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
1
499
500
500
500
500
500

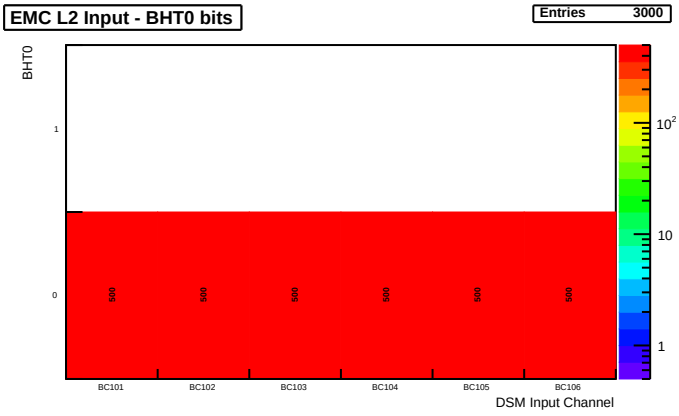

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

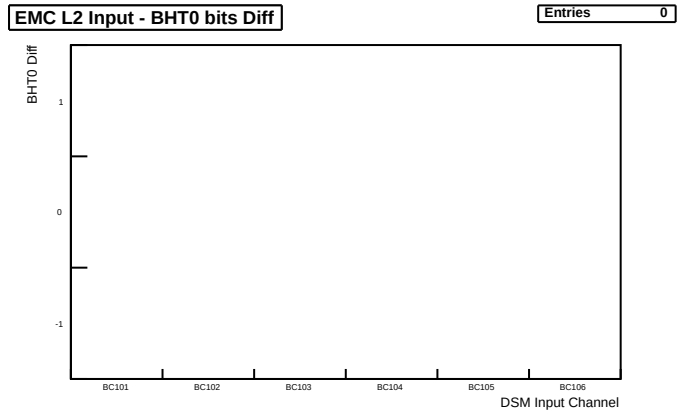

EMC L2 Input - JPZ/JPC bits
Entries 3000
JPZ/JPC bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
500
500
1
499
500
500
500

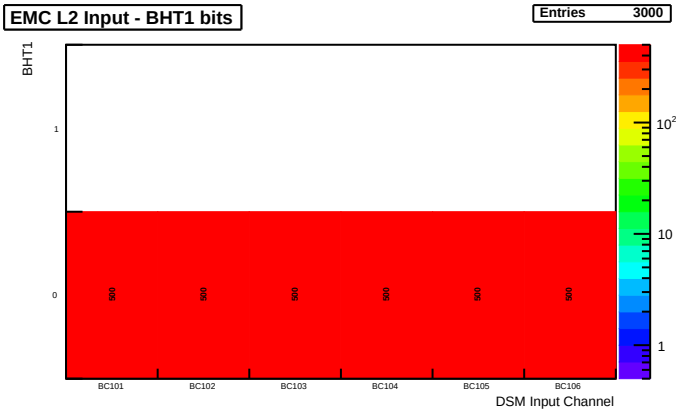

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

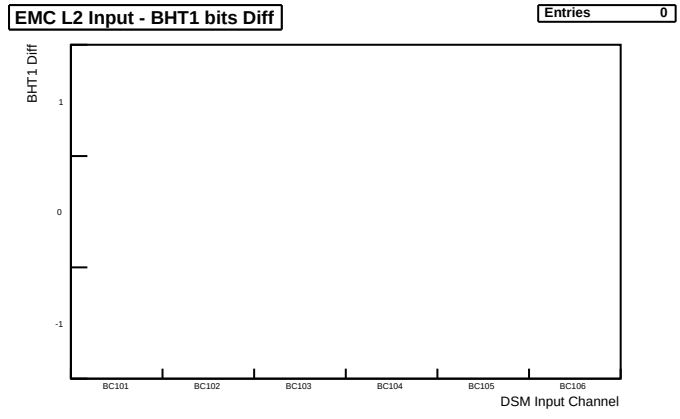

EMC L2 Input - Partial JP Sum
Entries 3000
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

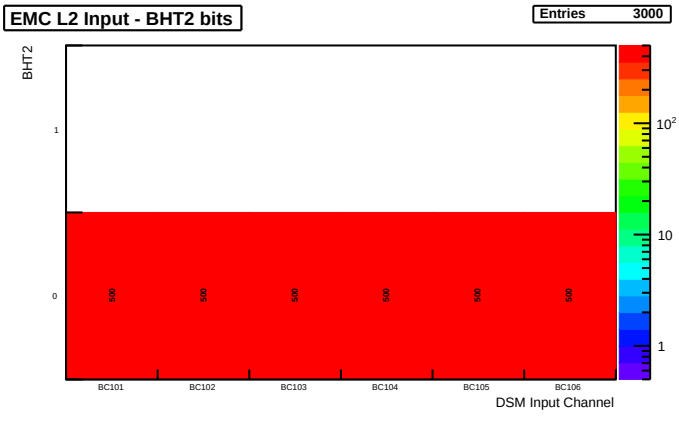

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

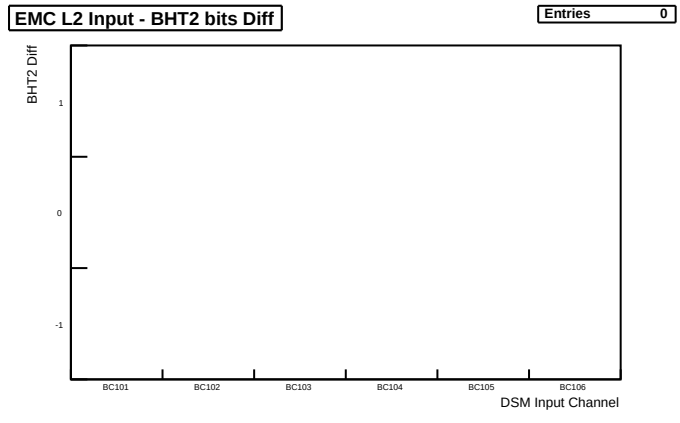

EMC L2 Input - BHT0 bits
Entries 3000
BHT0
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
500
10^2
10
1

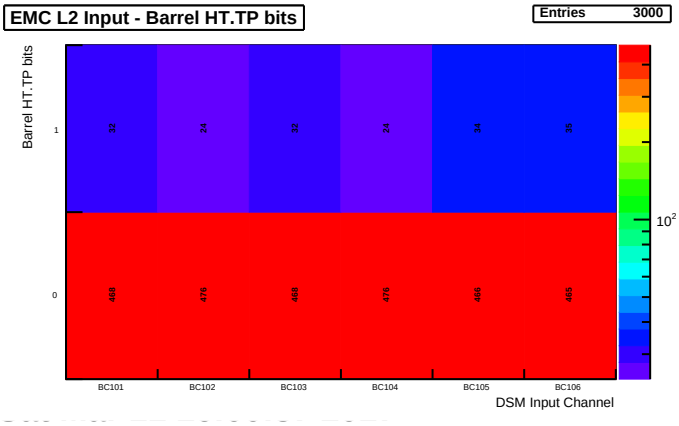

EMC L2 Input - BHT0 bits Diff
Entries 0
BHT0 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

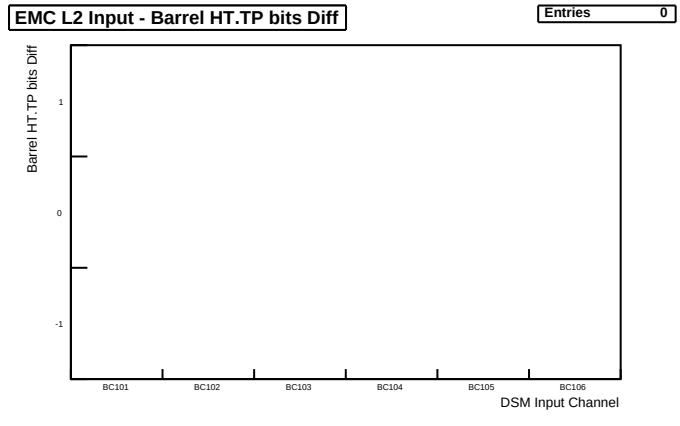

EMC L2 Input - BHT1 bits
Entries 3000
BHT1
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
500
10^2
10
1

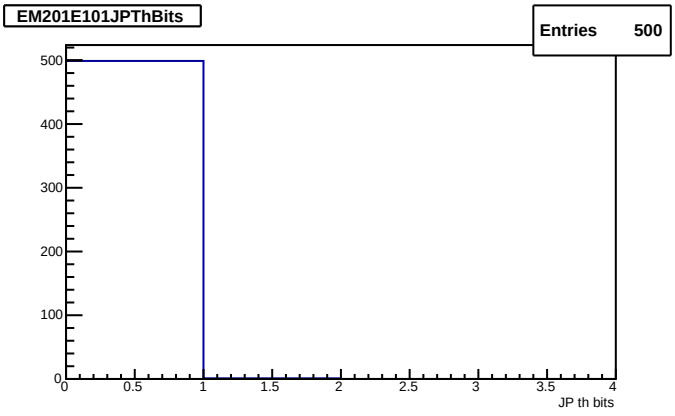

EMC L2 Input - BHT1 bits Diff
Entries 0
BHT1 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

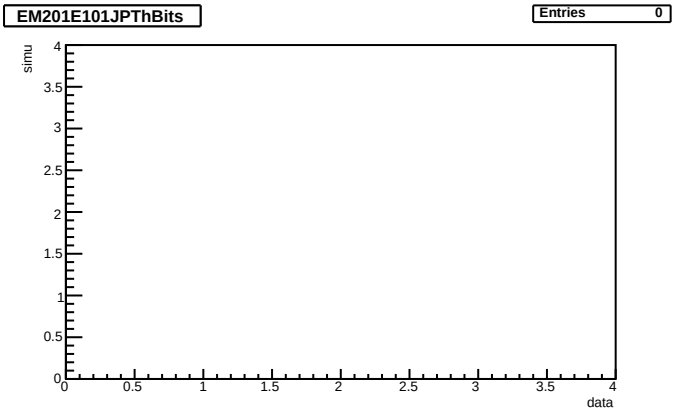

EMC L2 Input - BHT2 bits
Entries 3000
BHT2
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
500
10^2
10
1

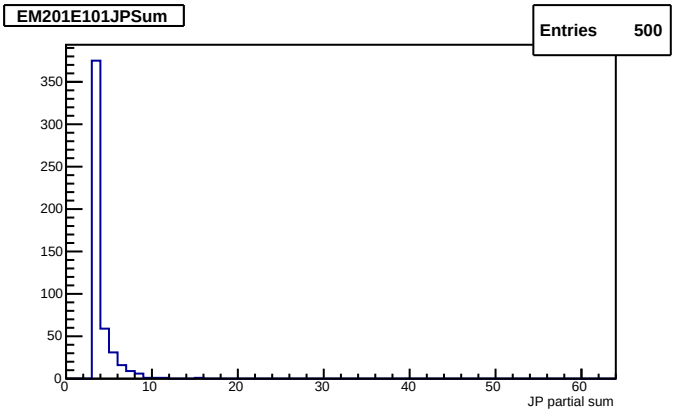

EMC L2 Input - BHT2 bits Diff
Entries 0
BHT2 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

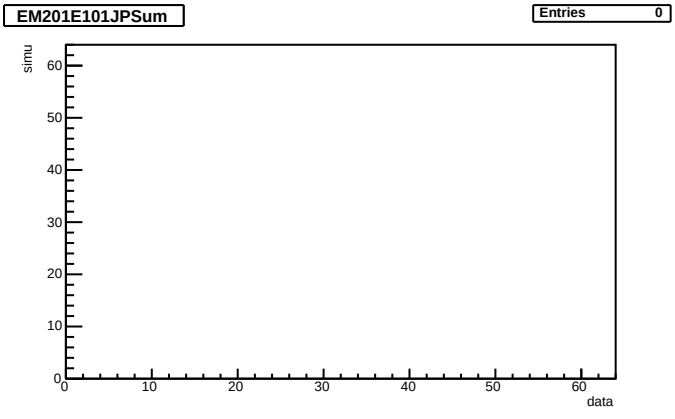

EMC L2 Input - Barrel HT.TP bits
Entries 3000
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
32
24
32
24
34
35
468
476
468
476
466
465
10^2

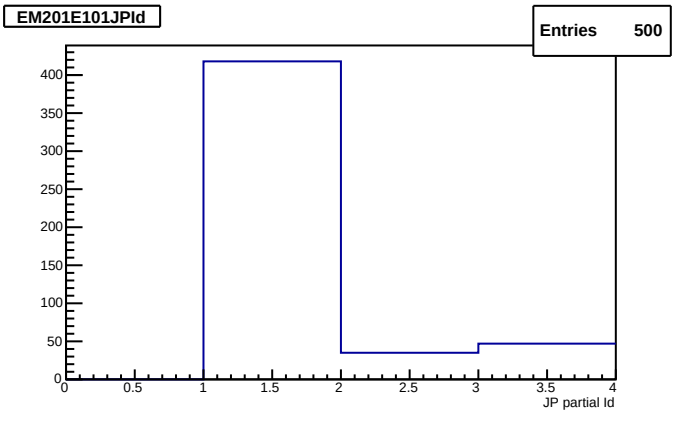

EMC L2 Input - Barrel HT.TP bits Diff
Entries 0
Barrel HT.TP bits Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

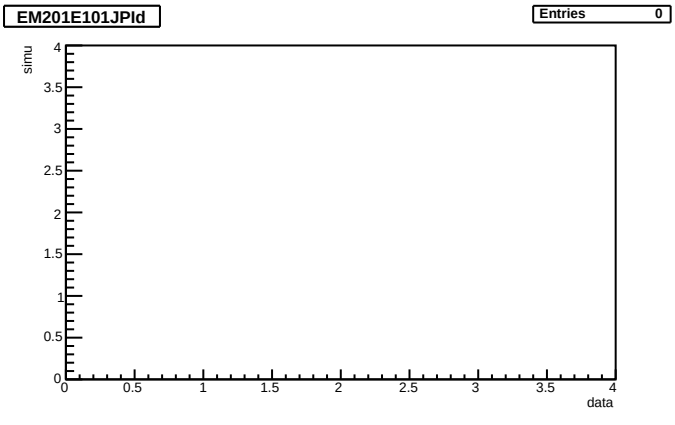

EM201E101JPThBits
Entries 500
JP th bits

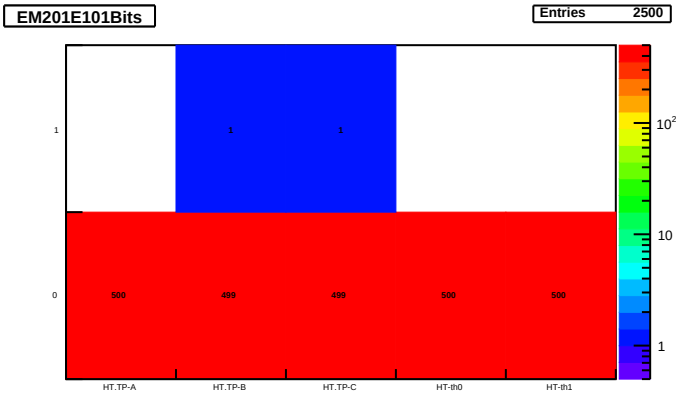

EM201E101JPThBits
Entries 0
simu
data

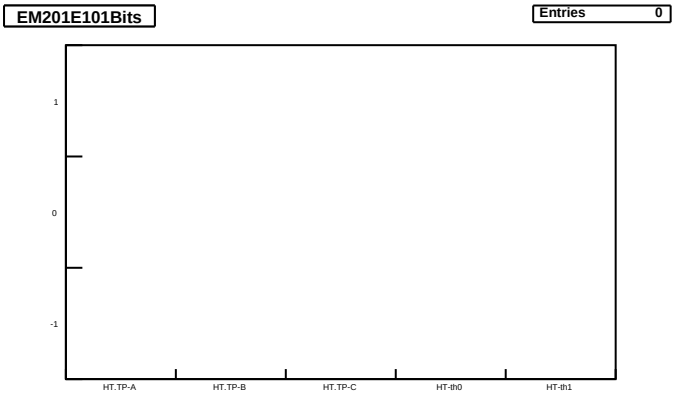

EM201E101JPSum
Entries 500
JP partial sum

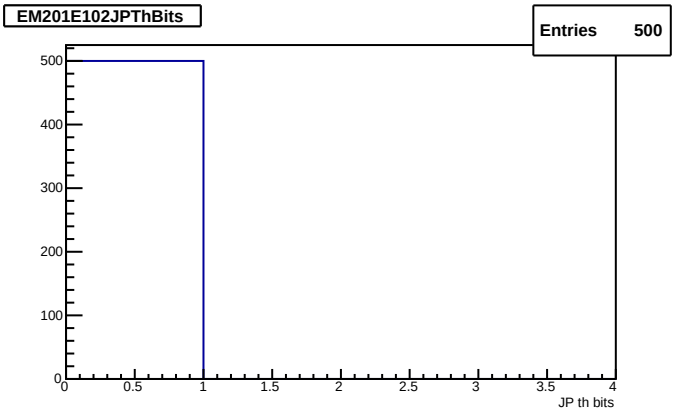

EM201E101JPSum
Entries 0
simu
data

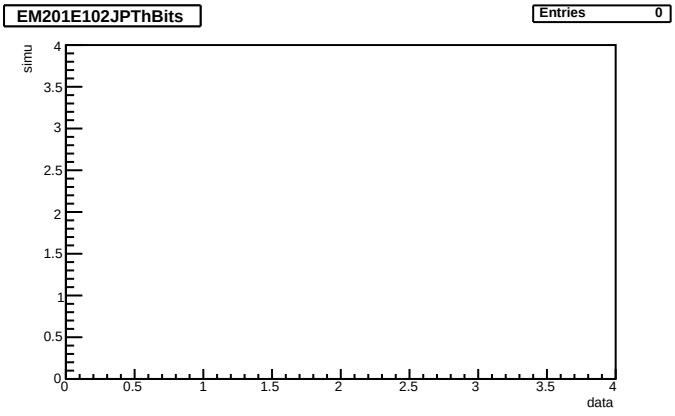

EM201E101JPId
Entries 500
JP partial Id

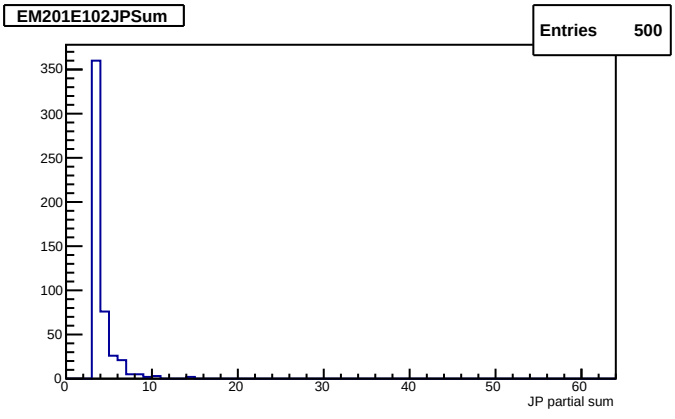

EM201E101JPId
Entries 0
simu
data

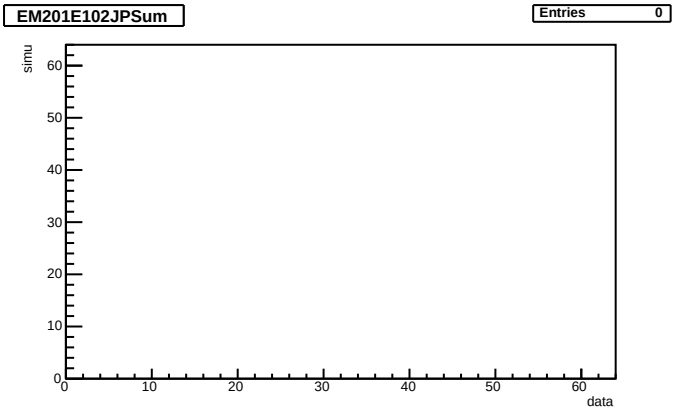

EM201E101Bits
Entries 2500
1
1
1
0
500
499
499
500
500
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

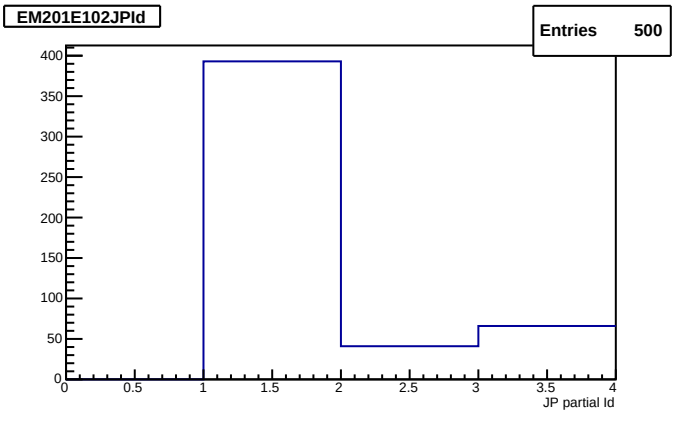

EM201E101Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

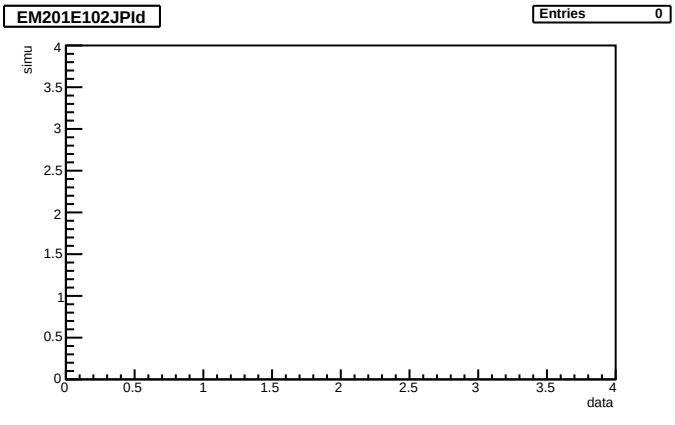

EM201E102JPThBits
Entries 500
JP th bits

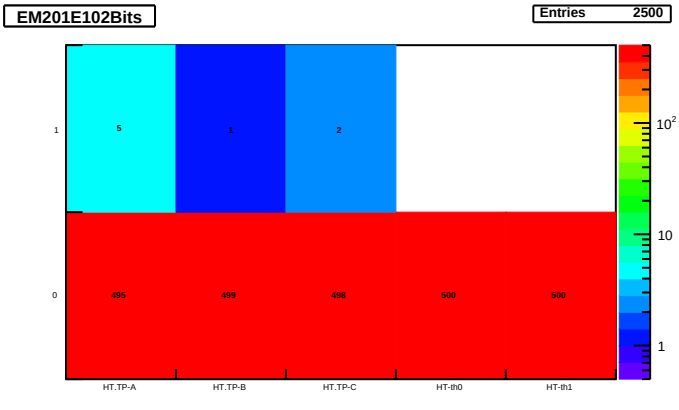

EM201E102JPThBits
Entries 0
simu
data

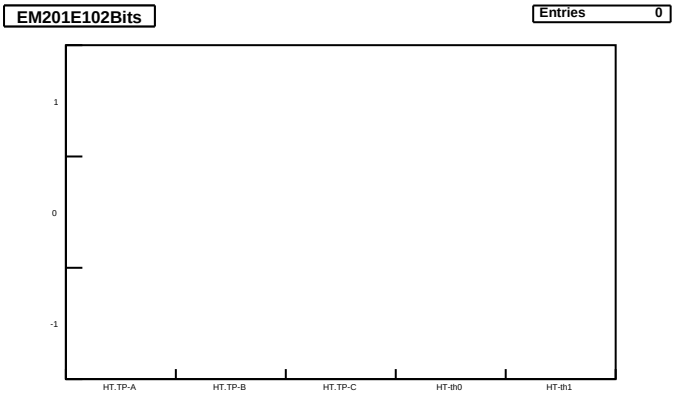

EM201E102JPSum
Entries 500
JP partial sum

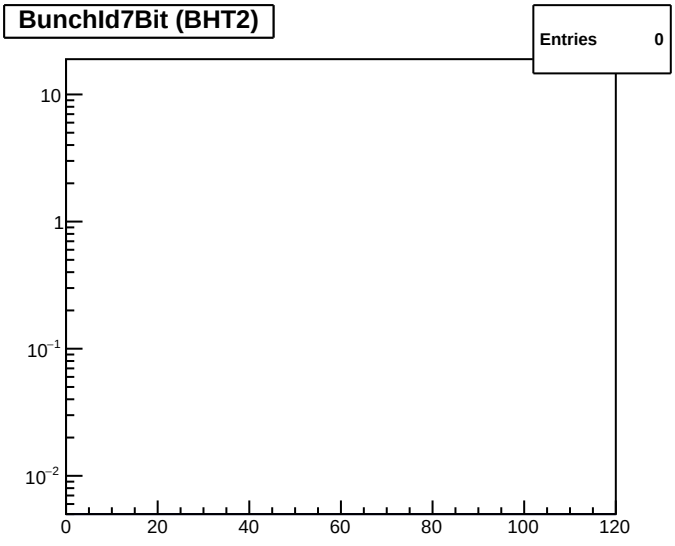

EM201E102JPSum
Entries 0
simu
data

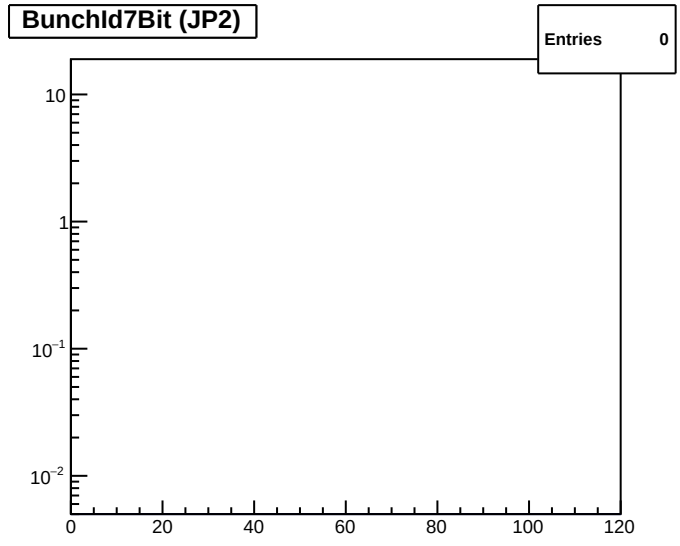

EM201E102JPId
Entries 500
JP partial Id

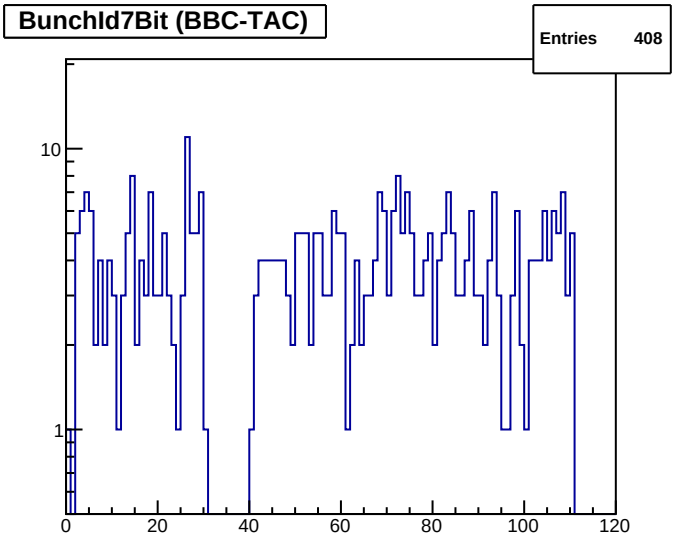

EM201E102JPId
Entries 0
simu
data

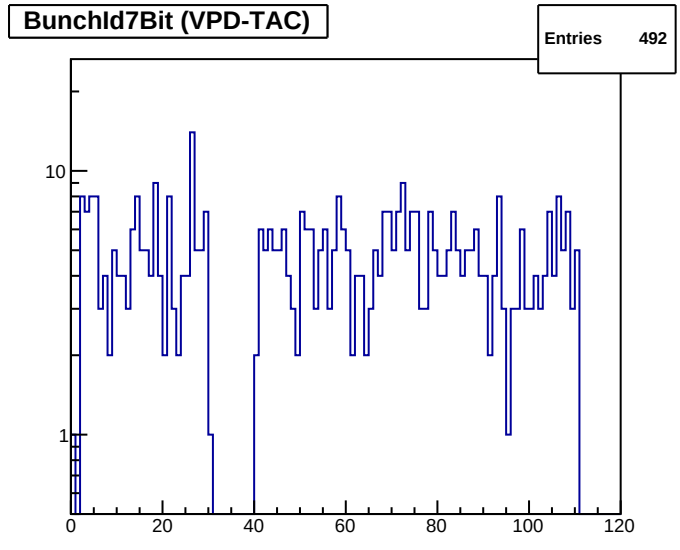

EM201E102Bits
Entries 2500
5
1
2
495
499
498
500
500
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

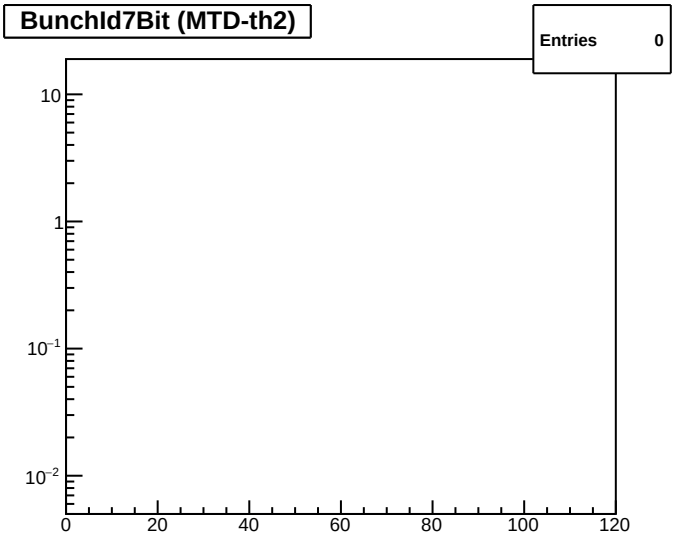

EM201E102Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

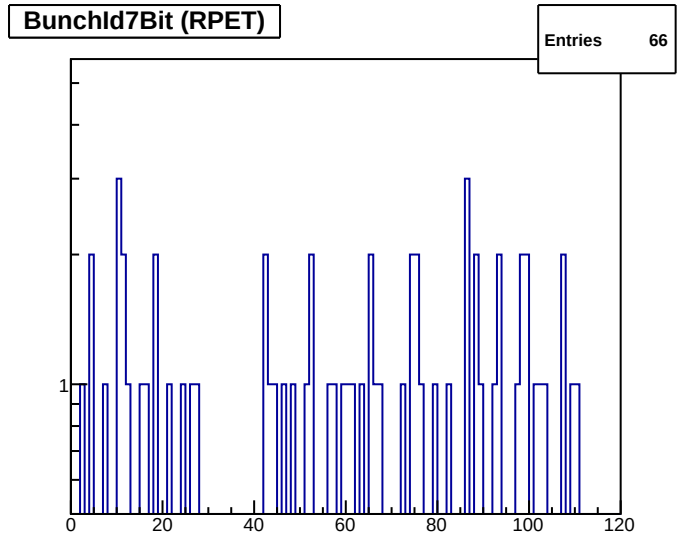

BunchId7Bit (BHT2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

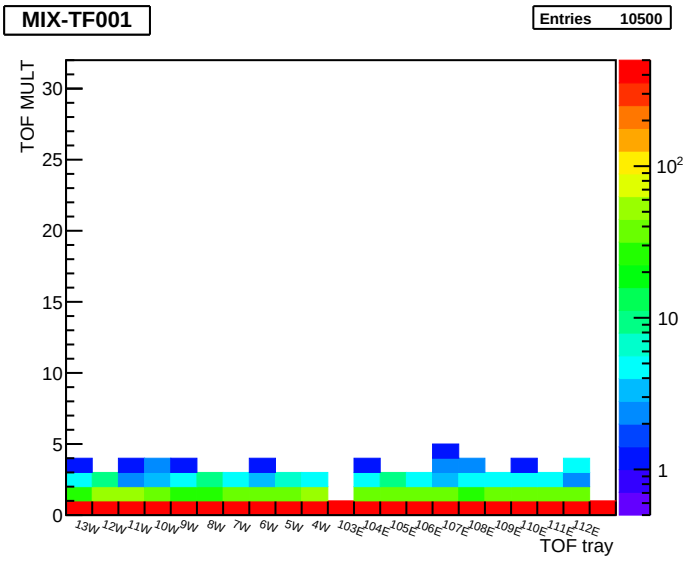

BunchId7Bit (JP2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

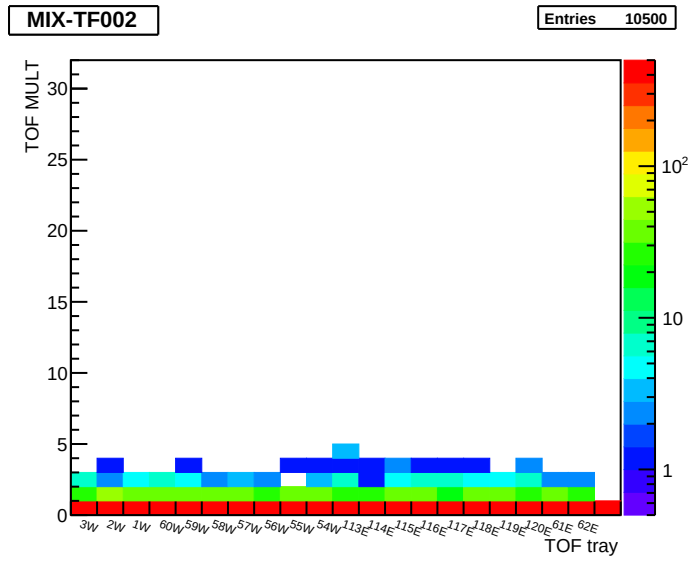

BunchId7Bit (BBC-TAC)
Entries 408
10
1
0 20 40 60 80 100 120

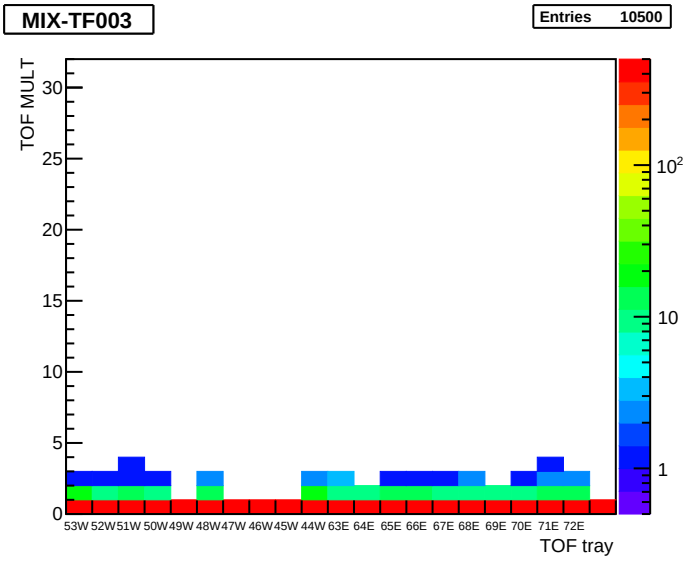

BunchId7Bit (VPD-TAC)
Entries 492
10
1
0 20 40 60 80 100 120

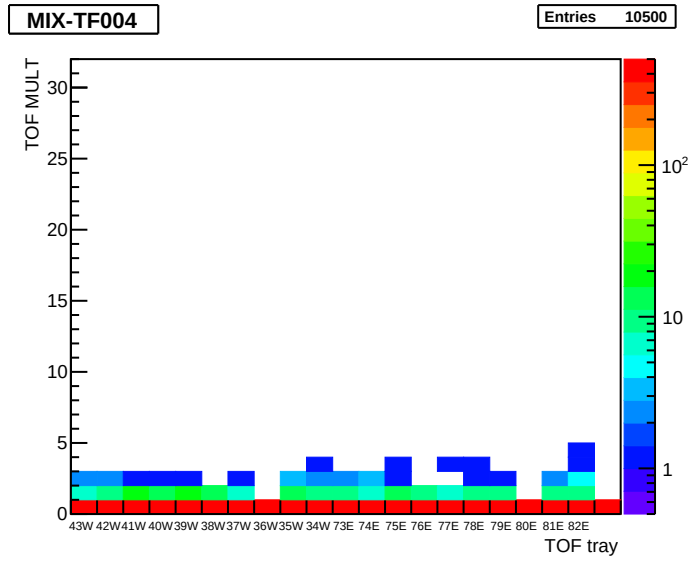

BunchId7Bit (MTD-th2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

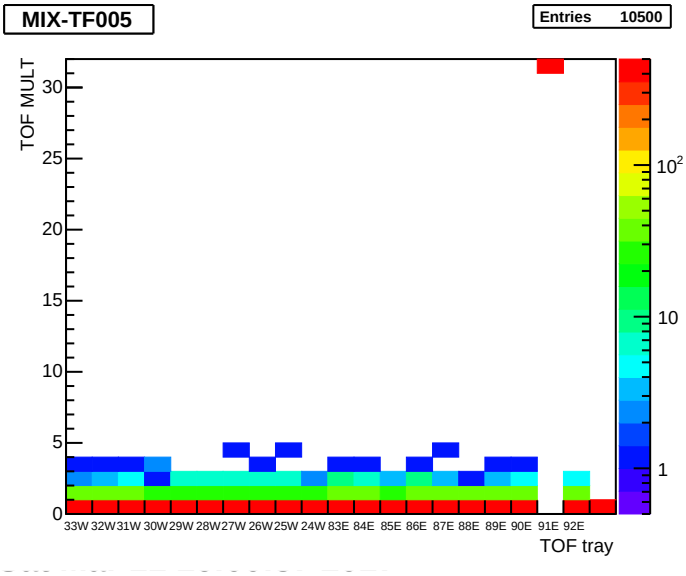

BunchId7Bit (RPET)
Entries 66
1
0 20 40 60 80 100 120

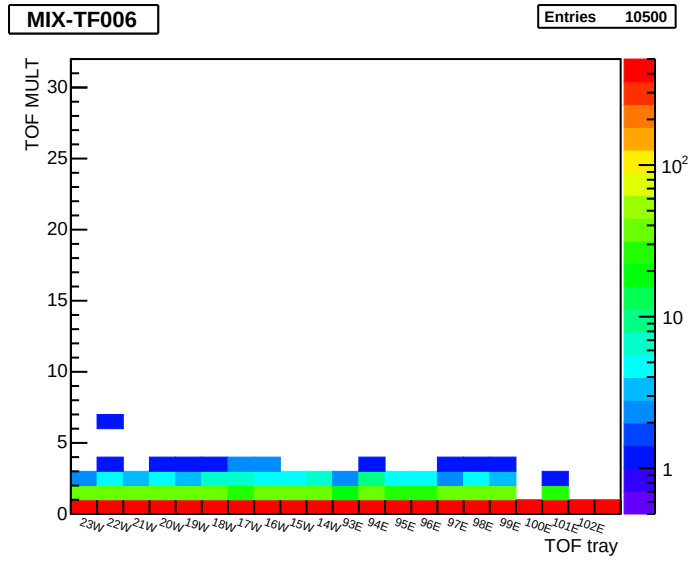

MIX-TF001
Entries 10500
TOF MULT
TOF tray
13W 12W 11W 10W 9W 8W 7W 6W 5W 4W 103E 104E 105E 106E 107E 108E 109E 110E 111E 112E

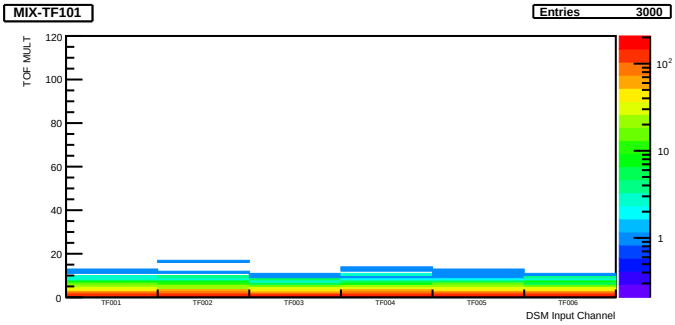

MIX-TF002
Entries 10500
TOF MULT
TOF tray
3W 2W 1W 60W 59W 58W 57W 56W 55W 54W 113E 114E 115E 116E 117E 118E 119E 120E 61E 62E

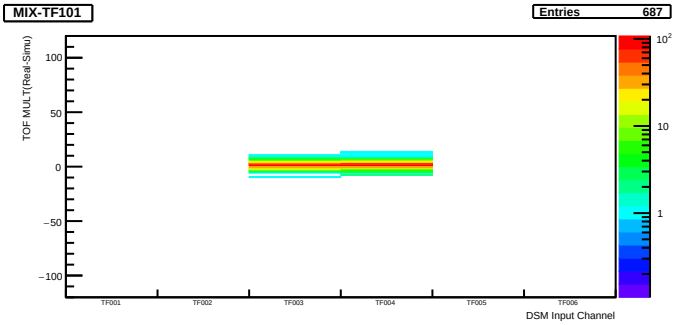

MIX-TF003
Entries 10500
TOF MULT
TOF tray
53W 52W 51W 50W 49W 48W 47W 46W 45W 44W 63E 64E 65E 66E 67E 68E 69E 70E 71E 72E

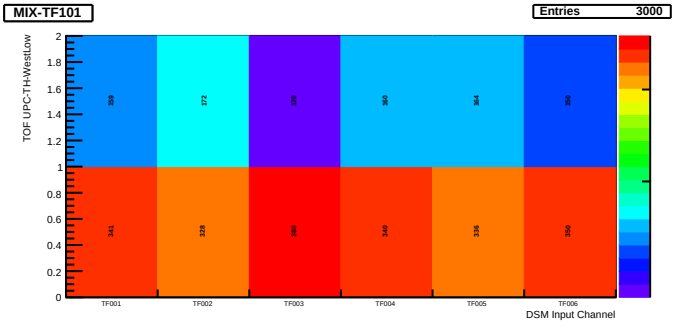

MIX-TF004
Entries 10500
TOF MULT
TOF tray
43W 42W 41W 40W 39W 38W 37W 36W 35W 34W 73E 74E 75E 76E 77E 78E 79E 80E 81E 82E

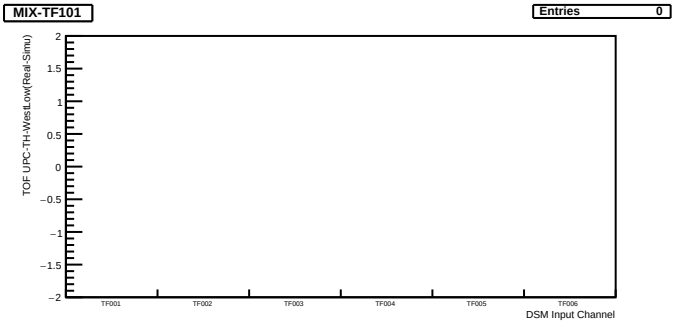

MIX-TF005
Entries 10500
TOF MULT
TOF tray
33W 32W 31W 30W 29W 28W 27W 26W 25W 24W 83E 84E 85E 86E 87E 88E 89E 90E 91E 92E

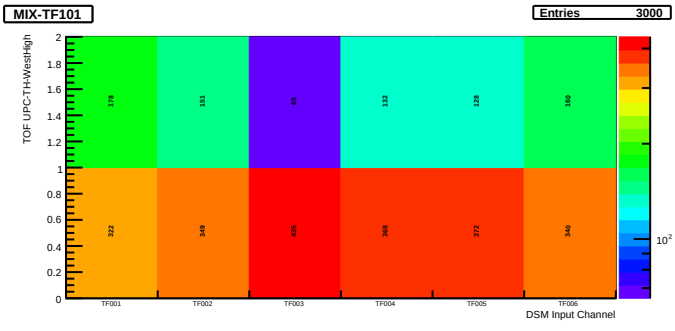

MIX-TF006
Entries 10500
TOF MULT
TOF tray
23W 22W 21W 20W 19W 18W 17W 16W 15W 14W 93E 94E 95E 96E 97E 98E 99E 100E 101E 102E

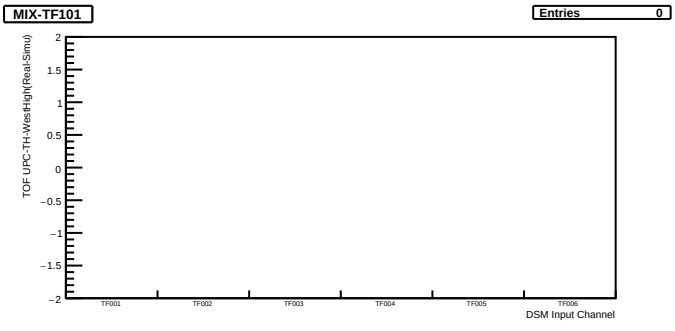

MIX-TF101
Entries 3000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

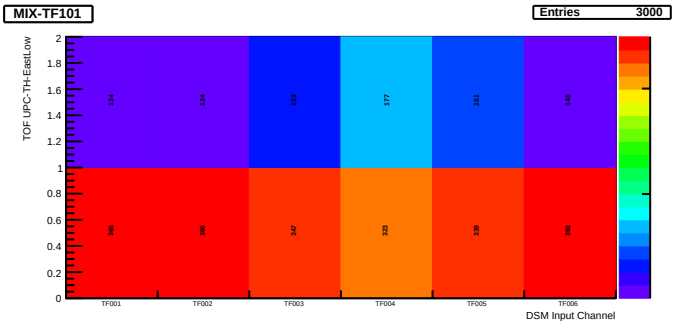

MIX-TF101
Entries 687
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

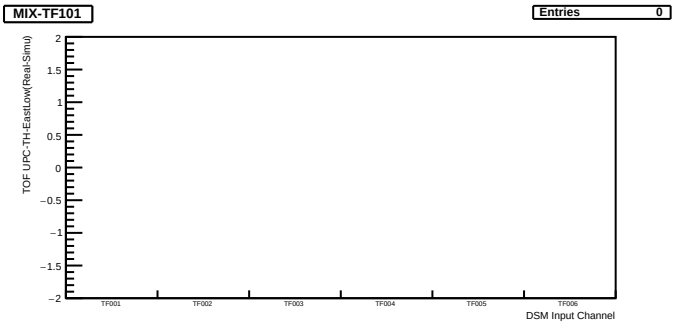

MIX-TF101
Entries 3000
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

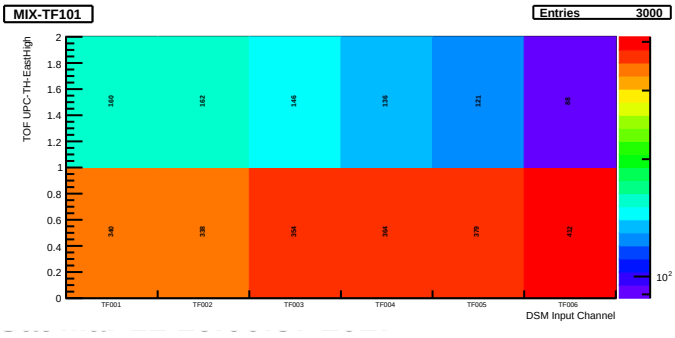

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

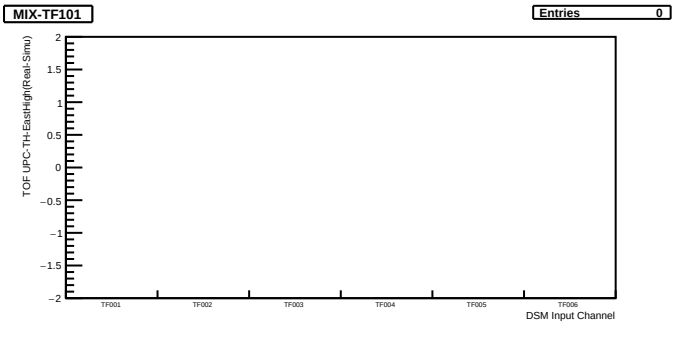

MIX-TF101
Entries 3000
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

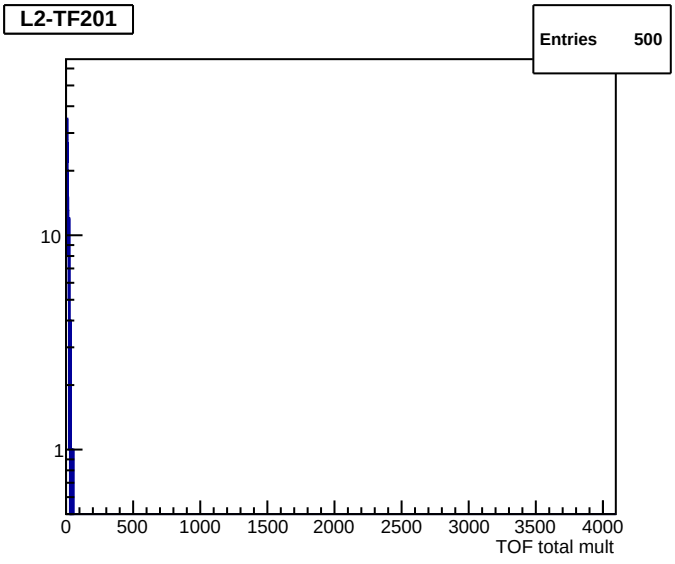

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

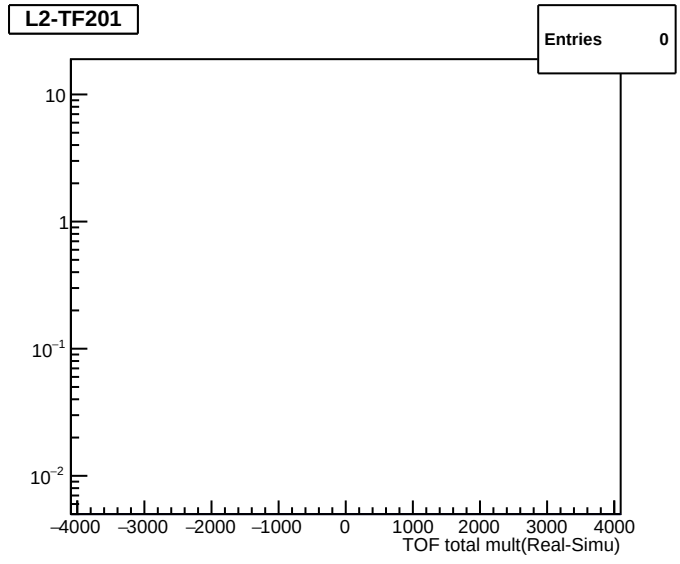

MIX-TF101
Entries 3000
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

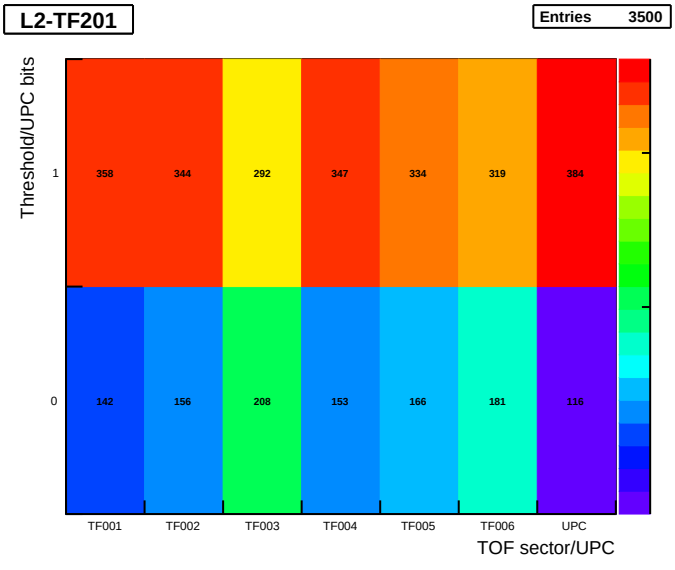

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

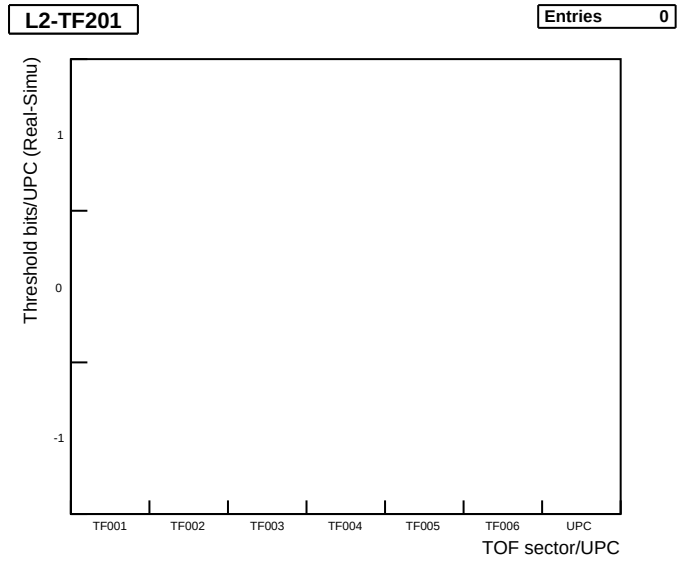

MIX-TF101
Entries 3000
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

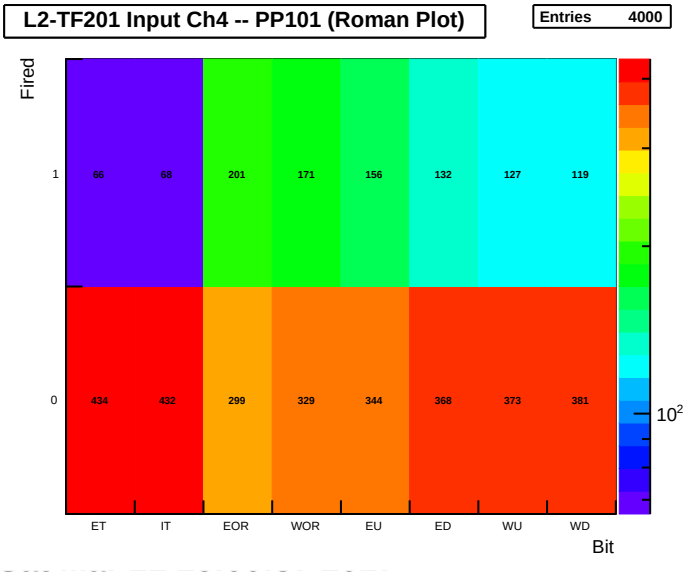

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

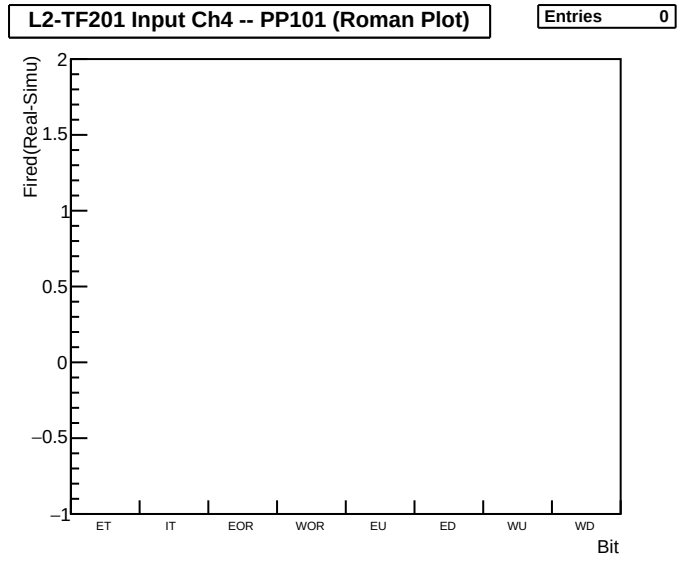

L2-TF201
Entries 500
10
1
0 500 1000 1500 2000 2500 3000 3500 4000
TOF total mult

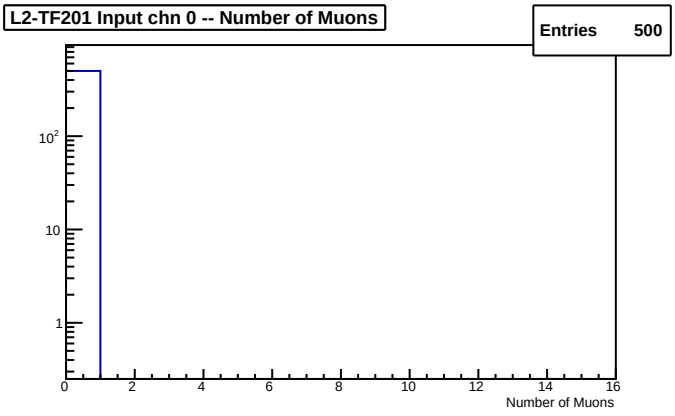

L2-TF201
Entries 0
10
1
10^{-1}
10^{-2}
−4000 −3000 −2000 −1000 0 1000 2000 3000 4000
TOF total mult(Real-Simu)

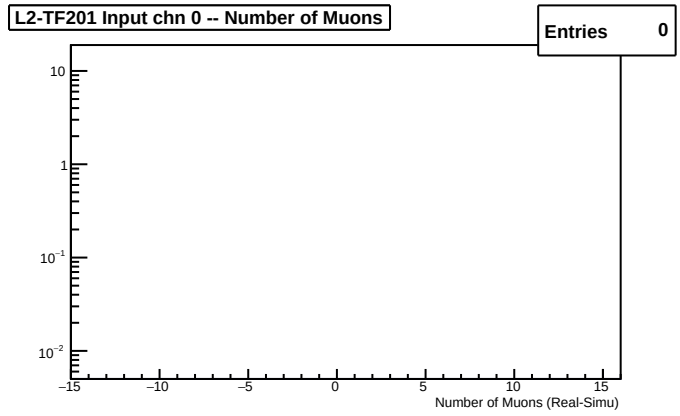

L2-TF201
Entries 3500
Threshold/UPC bits
1
0
358 344 292 347 334 319 384
142 156 208 153 166 181 116
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

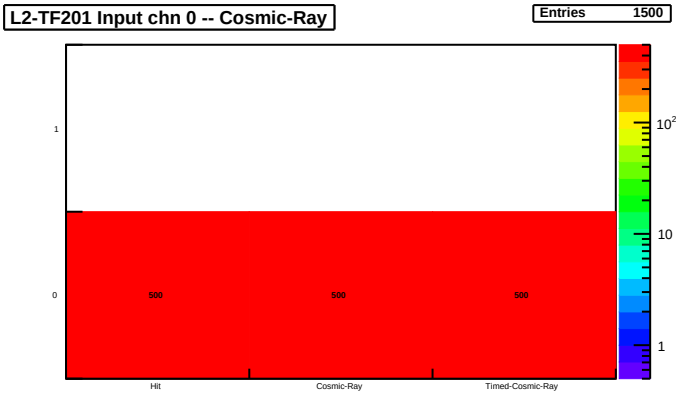

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
1
0
-1
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

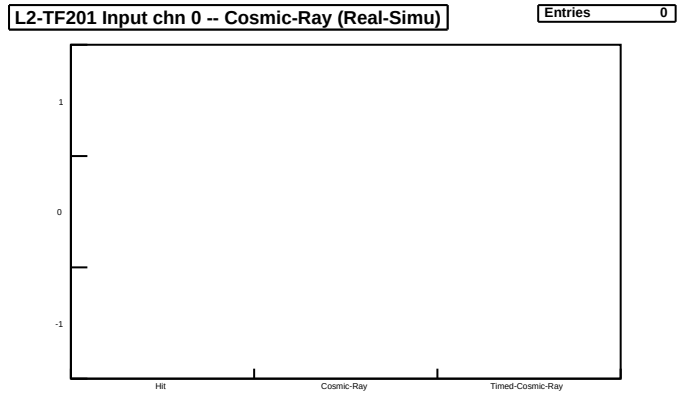

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 4000
Fired
1
0
66 68 201 171 156 132 127 119
434 432 299 329 344 368 373 381
10^2
ET IT EOR WOR EU ED WU WD
Bit

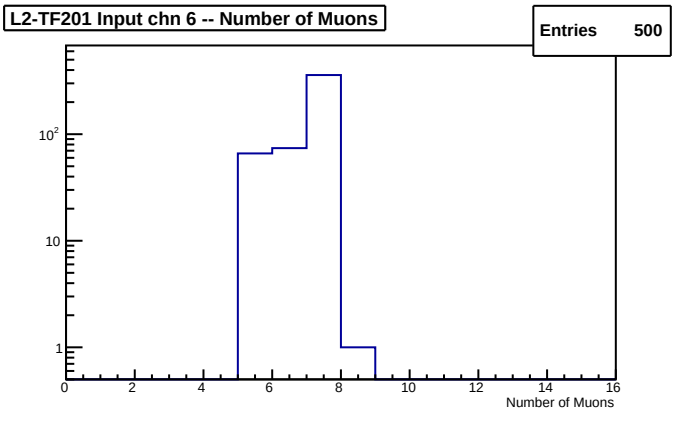

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 0
Fired(Real-Simu)
2
1.5
1
0.5
0
−0.5
−1
ET IT EOR WOR EU ED WU WD
Bit

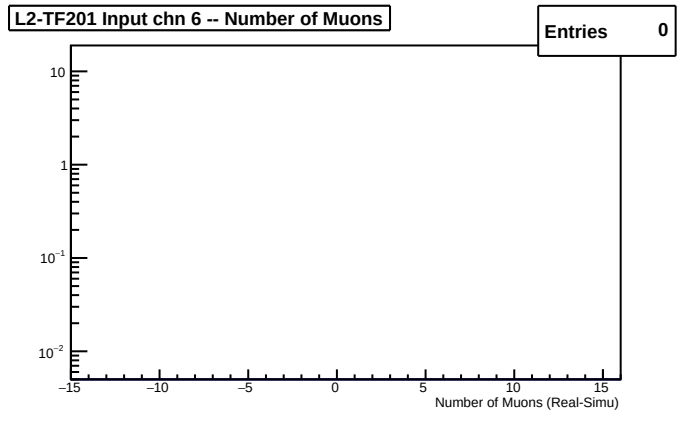

L2-TF201 Input chn 0 -- Number of Muons
Entries 500
Number of Muons

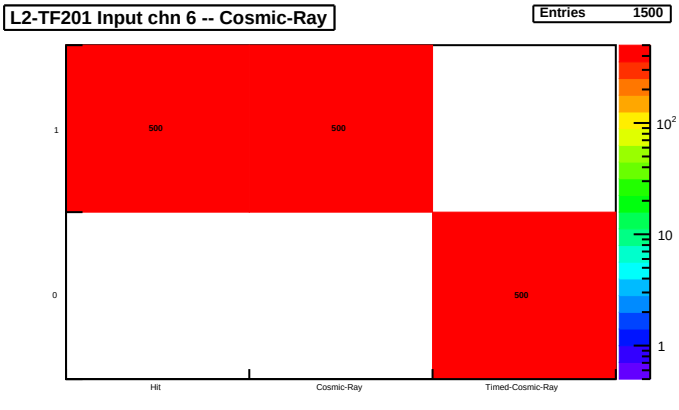

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

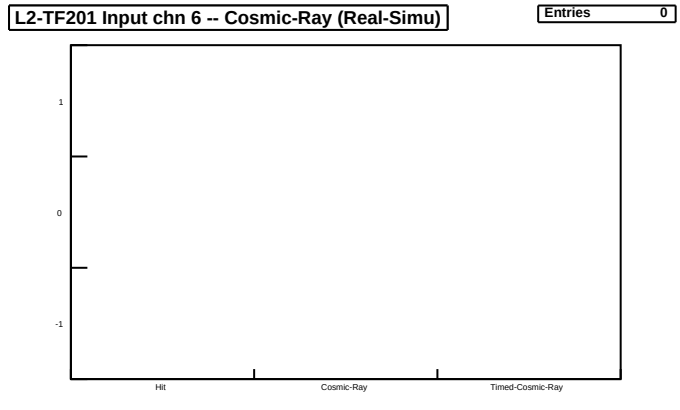

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 1500
500
500
500
Hit
Cosmic-Ray
Timed-Cosmic-Ray

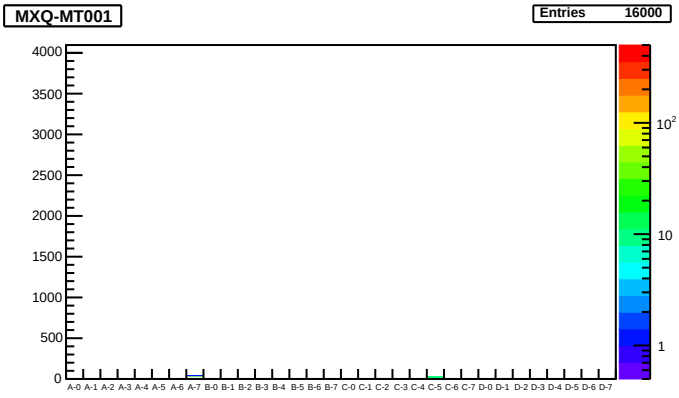

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

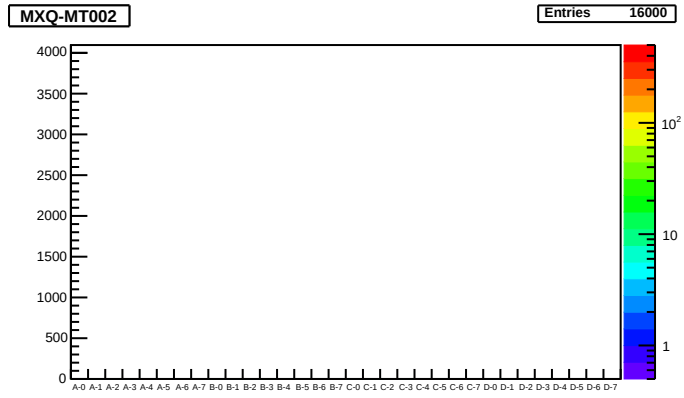

L2-TF201 Input chn 6 -- Number of Muons
Entries 500
Number of Muons

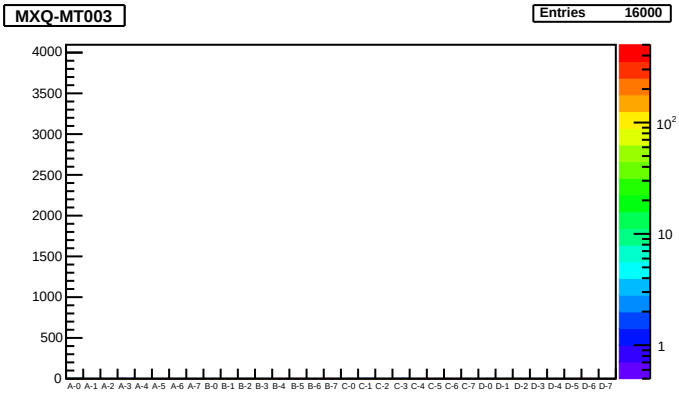

L2-TF201 Input chn 6 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

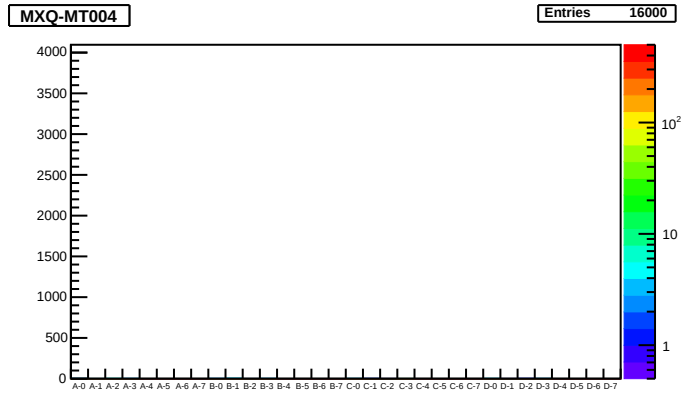

L2-TF201 Input chn 6 -- Cosmic-Ray
Entries 1500
500
500
500
Hit
Cosmic-Ray
Timed-Cosmic-Ray

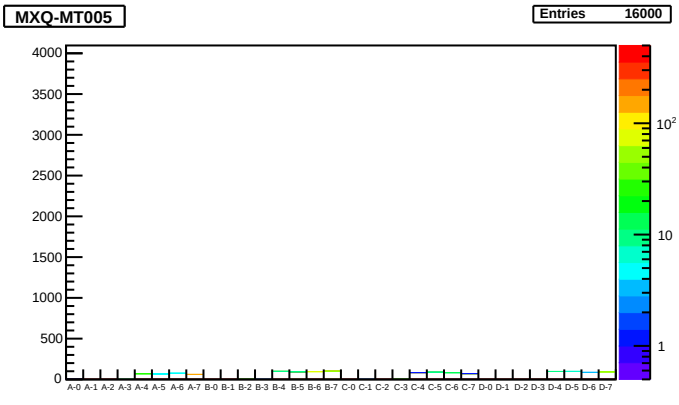

L2-TF201 Input chn 6 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

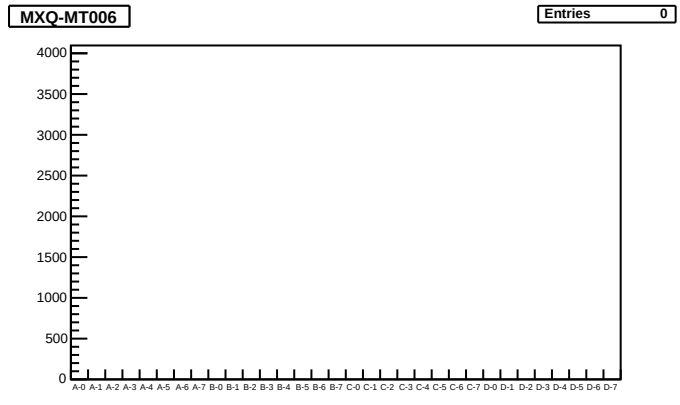

MXQ-MT001
Entries 16000

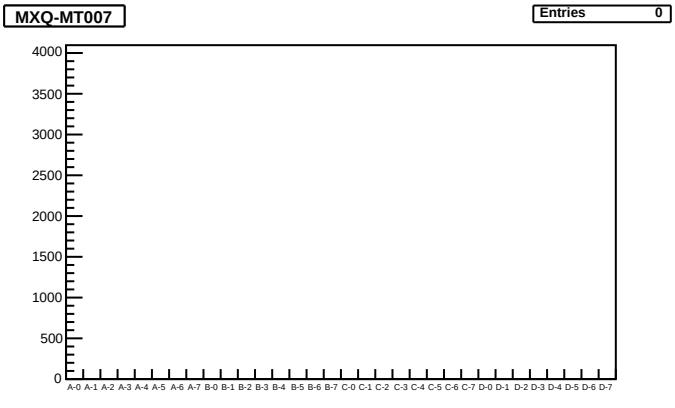

MXQ-MT002
Entries 16000

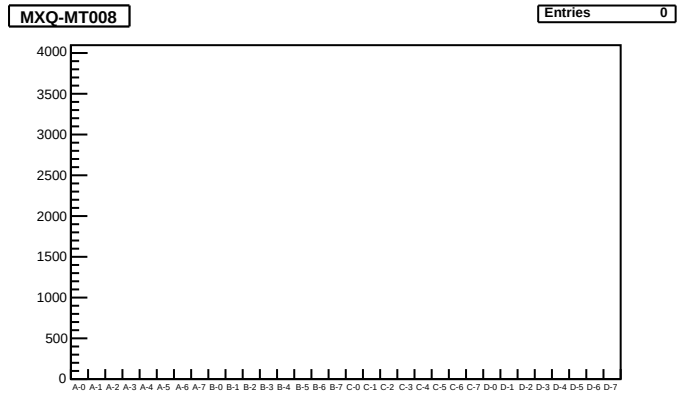

MXQ-MT003
Entries 16000

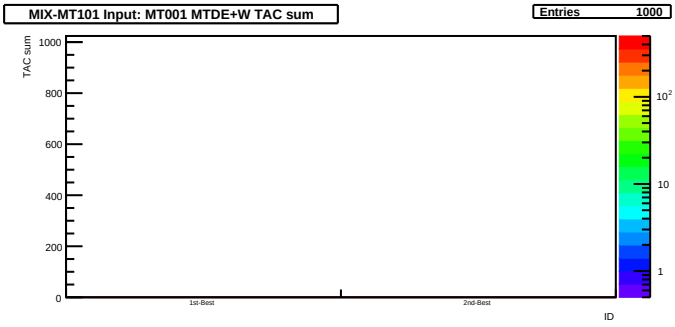

MXQ-MT004
Entries 16000

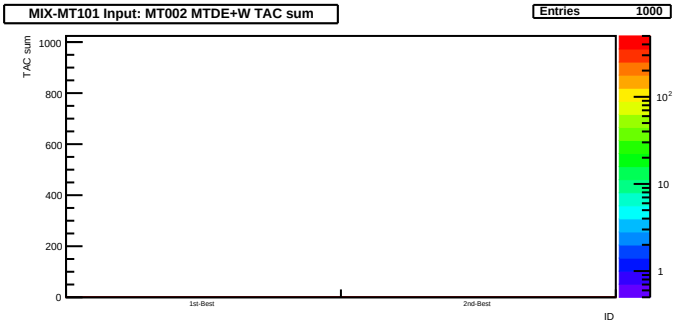

MXQ-MT005
Entries 16000

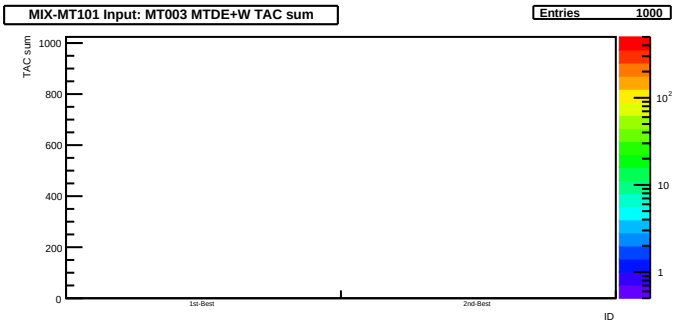

MXQ-MT006
Entries 0

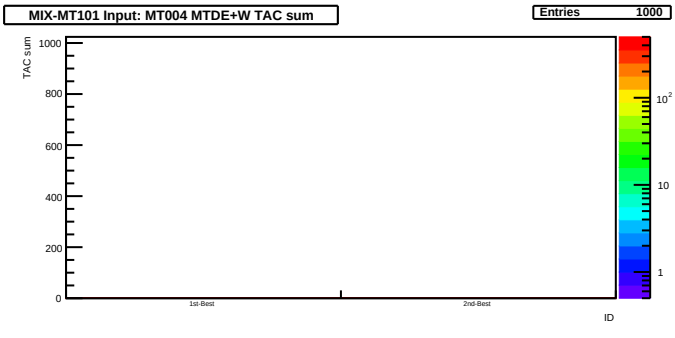

MXQ-MT007
Entries 0

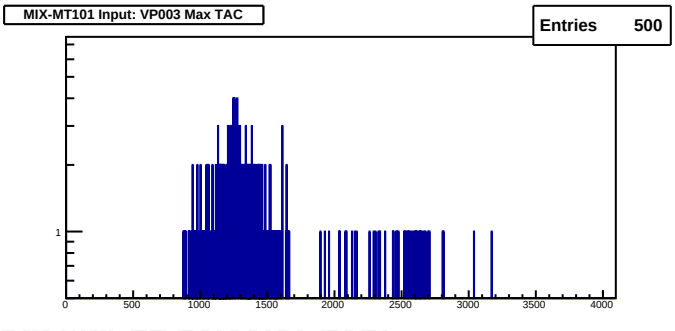

MXQ-MT008
Entries 0

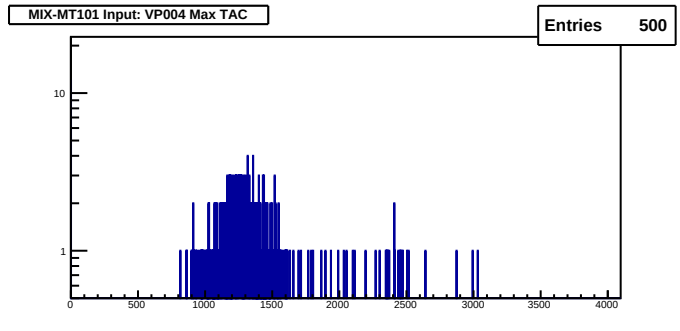

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 1000
TAC sum
1000
800
600
400
200
0
1st-Best
2nd-Best
ID
10^2
10
1

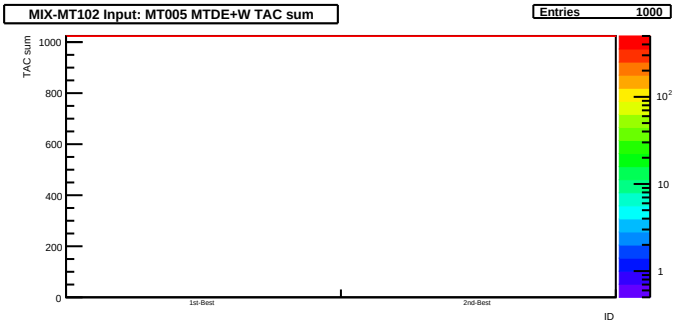

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 1000
TAC sum
1000
800
600
400
200
0
1st-Best
2nd-Best
ID
10^2
10
1

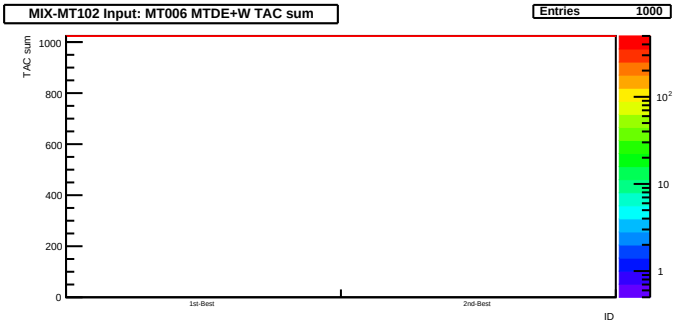

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 1000
TAC sum
1000
800
600
400
200
0
1st-Best
2nd-Best
ID
10^2
10
1

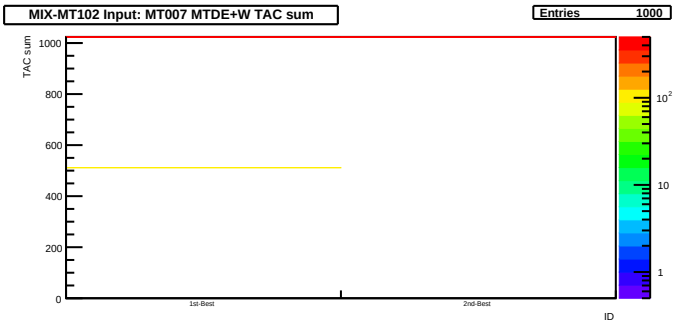

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 1000
TAC sum
1000
800
600
400
200
0
1st-Best
2nd-Best
ID
10^2
10
1

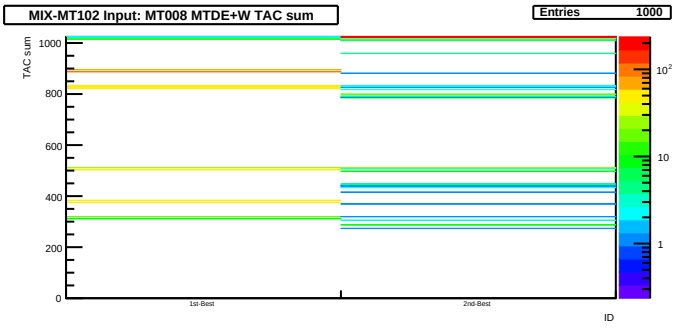

MIX-MT101 Input: VP003 Max TAC
Entries 500
1
0
500
1000
1500
2000
2500
3000
3500
4000

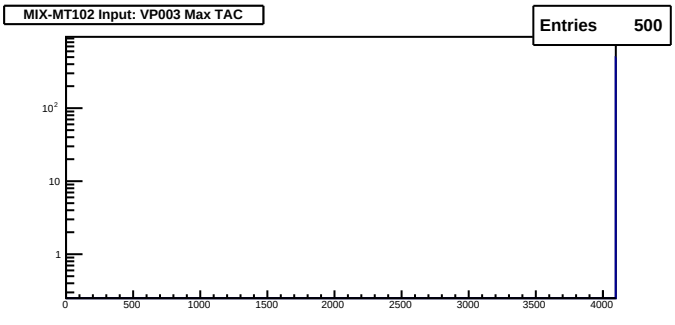

MIX-MT101 Input: VP004 Max TAC
Entries 500
10
1
0
500
1000
1500
2000
2500
3000
3500
4000

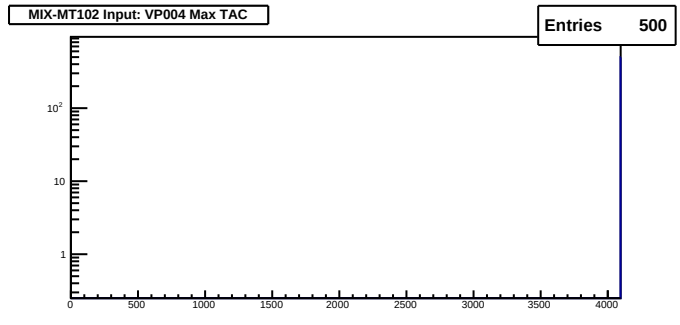

MIX-MT102 Input: MT005 MTDE+W TAC sum
Entries 1000
TAC sum
1000
800
600
400
200
0
1st-Best
2nd-Best
ID
10^2
10
1

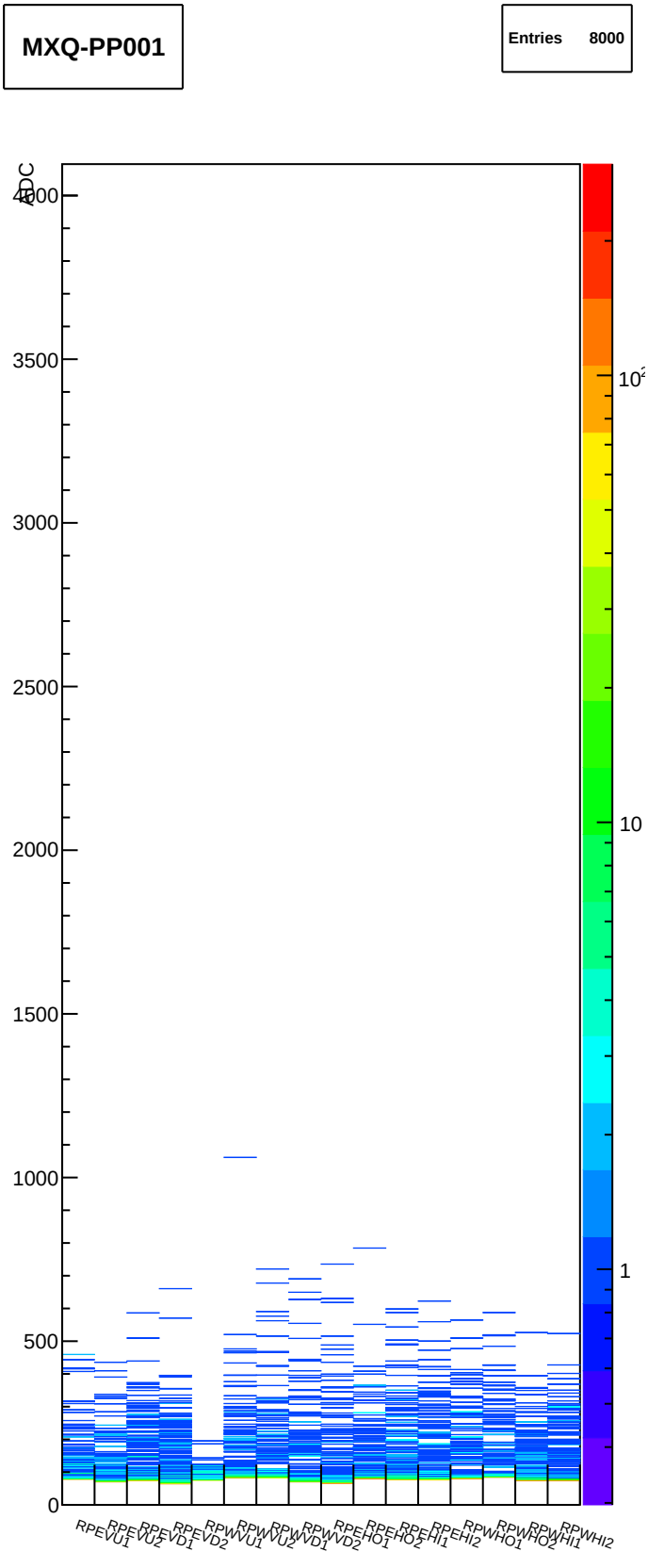

MIX-MT102 Input: MT006 MTDE+W TAC sum
Entries 1000
TAC sum
1000
800
600
400
200
0
1st-Best
2nd-Best
ID
10^2
10
1

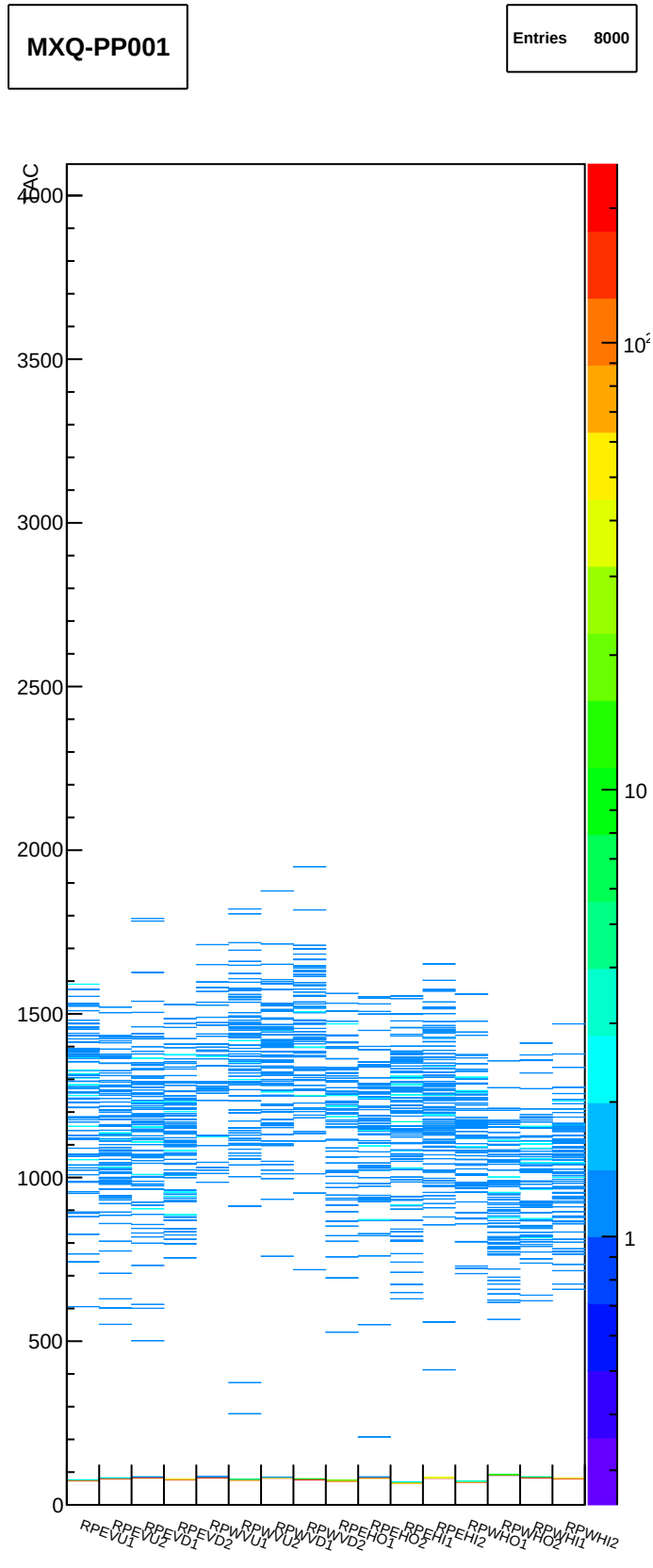

MIX-MT102 Input: MT007 MTDE+W TAC sum
Entries 1000
TAC sum
1000
800
600
400
200
0
1st-Best
2nd-Best
ID
10^2
10
1

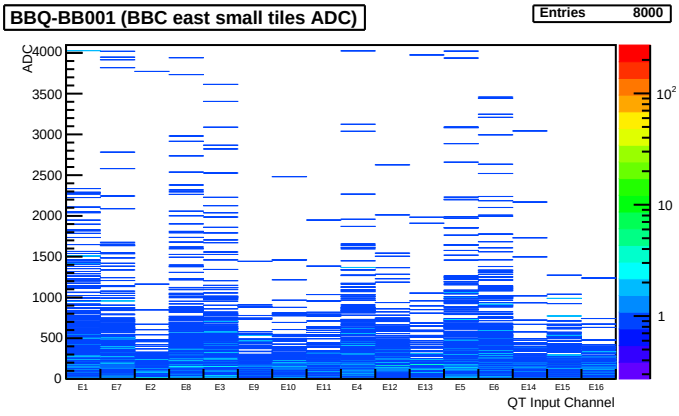

MIX-MT102 Input: MT008 MTDE+W TAC sum
Entries 1000
TAC sum
1000
800
600
400
200
0
1st-Best
2nd-Best
ID
10^2
10
1

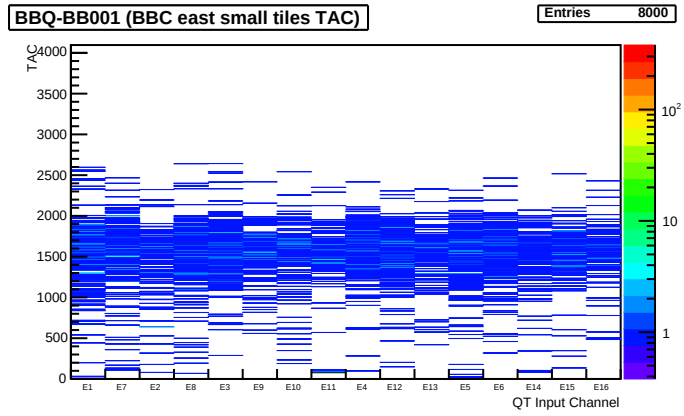

MIX-MT102 Input: VP003 Max TAC
Entries 500
10^2
10
1
0
500
1000
1500
2000
2500
3000
3500
4000

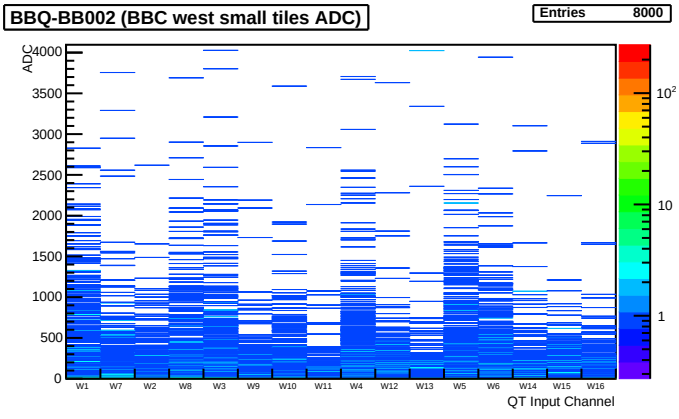

MIX-MT102 Input: VP004 Max TAC
Entries 500
10^2
10
1
0
500
1000
1500
2000
2500
3000
3500
4000

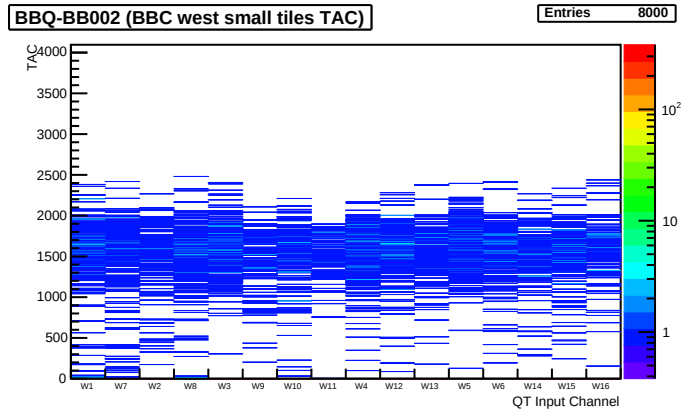

MXQ-PP001
Entries 8000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10²
10
1

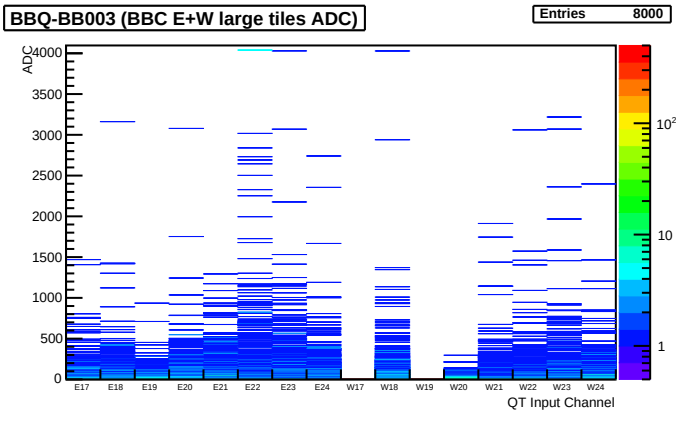

MXQ-PP001
Entries 8000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10²
10
1

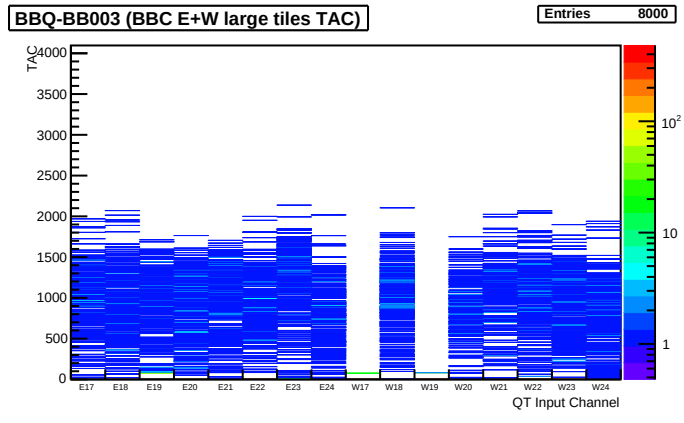

BBQ-BB001 (BBC east small tiles ADC)
Entries 8000
ADC
QT Input Channel

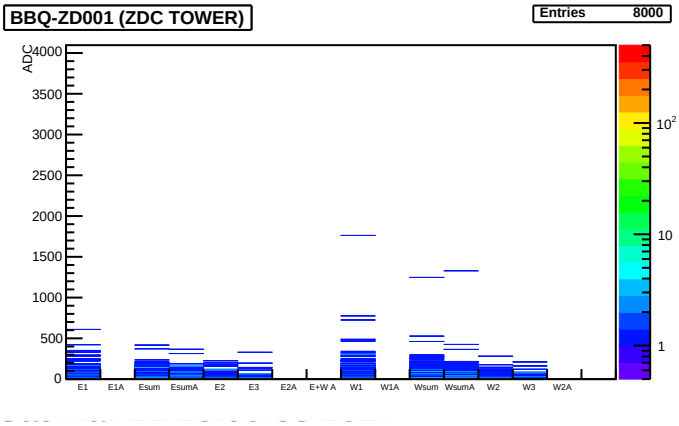

BBQ-BB001 (BBC east small tiles TAC)
Entries 8000
TAC
QT Input Channel

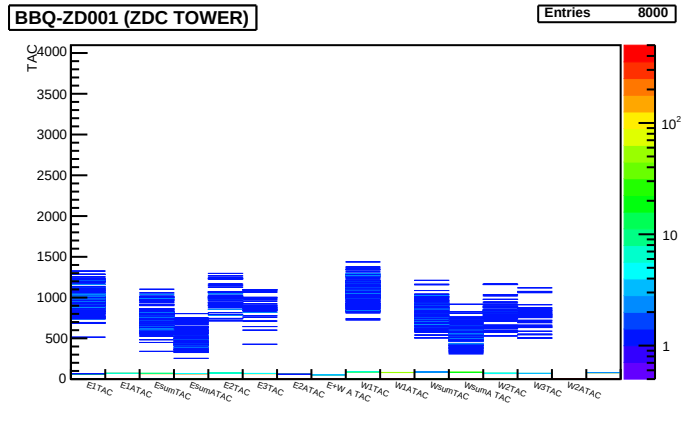

BBQ-BB002 (BBC west small tiles ADC)
Entries 8000
ADC
QT Input Channel

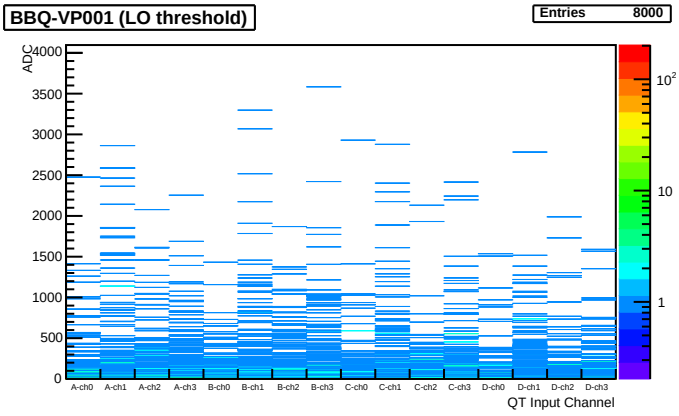

BBQ-BB002 (BBC west small tiles TAC)
Entries 8000
TAC
QT Input Channel

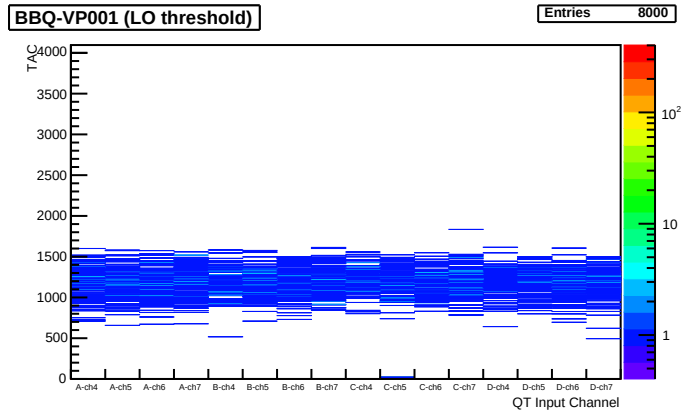

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 8000
ADC
QT Input Channel

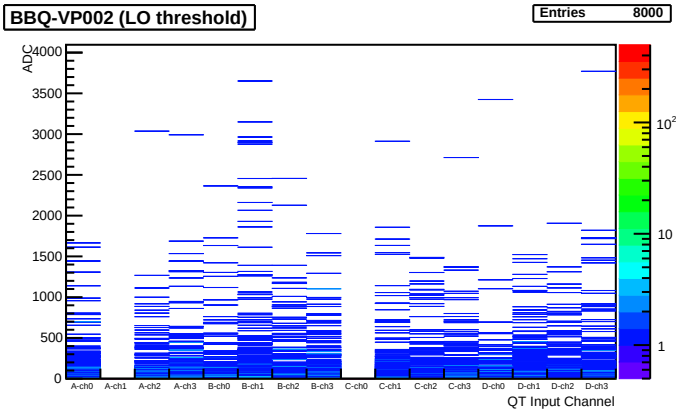

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 8000
TAC
QT Input Channel

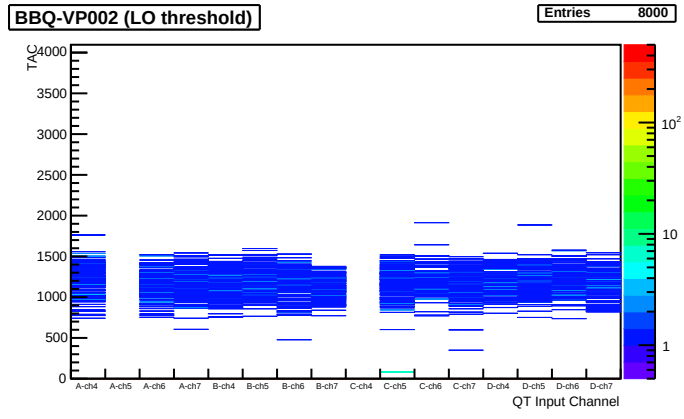

BBQ-ZD001 (ZDC TOWER)
Entries 8000
ADC

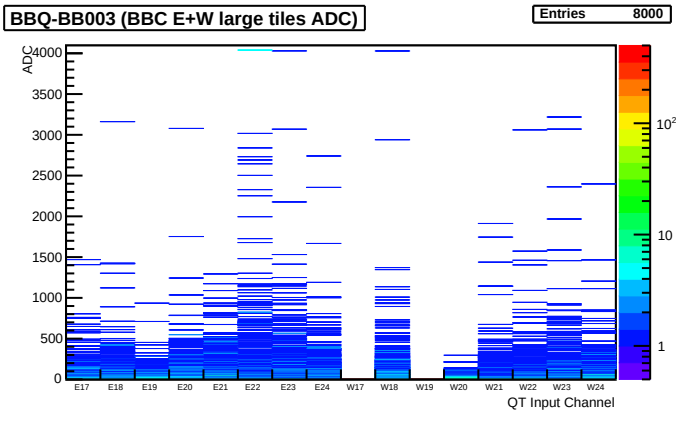

BBQ-ZD001 (ZDC TOWER)
Entries 8000
TAC

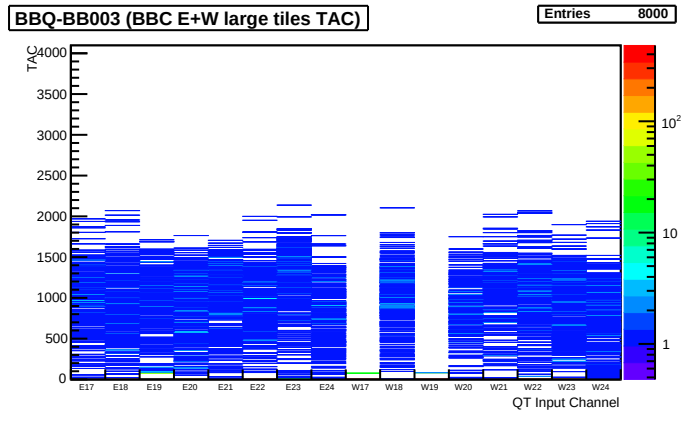

BBQ-VP001 (LO threshold)
Entries 8000
ADC
QT Input Channel

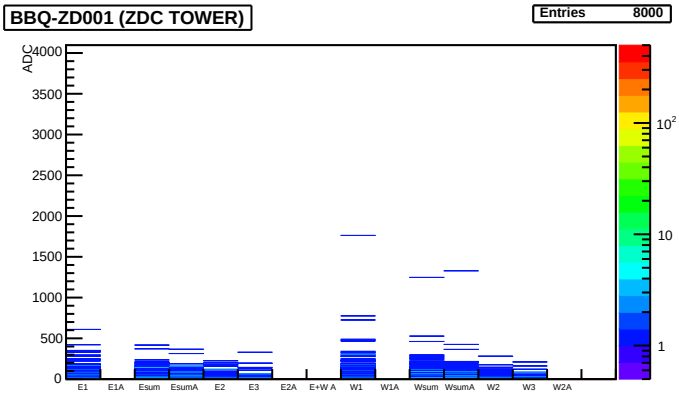

BBQ-VP001 (LO threshold)
Entries 8000
TAC
QT Input Channel

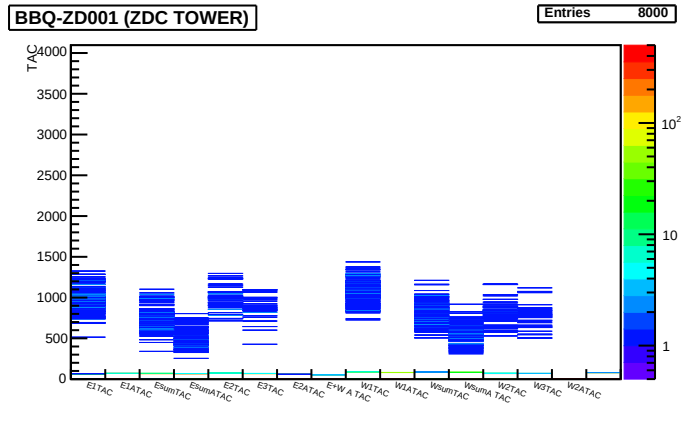

BBQ-VP002 (LO threshold)
Entries 8000
ADC
QT Input Channel

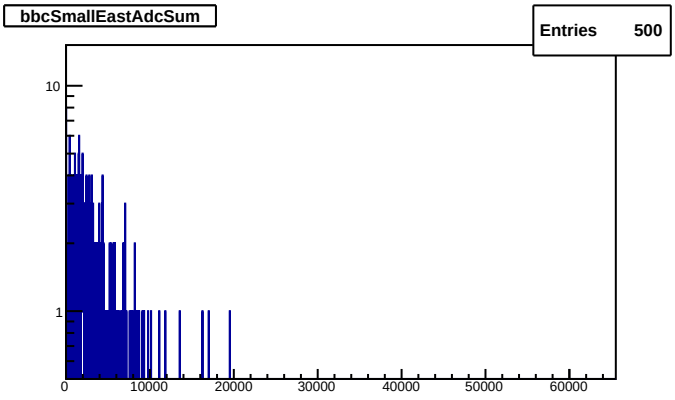

BBQ-VP002 (LO threshold)
Entries 8000
TAC
QT Input Channel

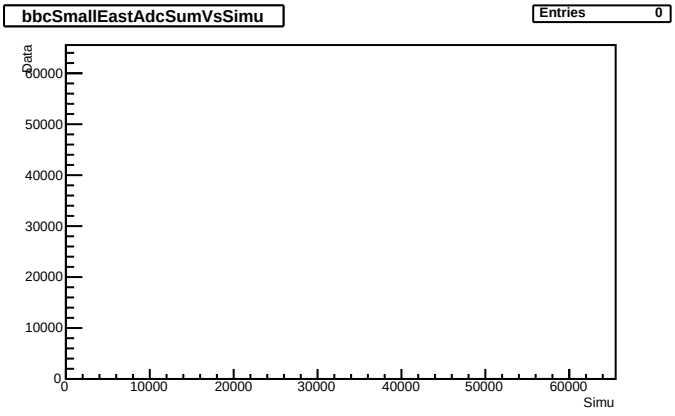

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 8000
ADC
QT Input Channel

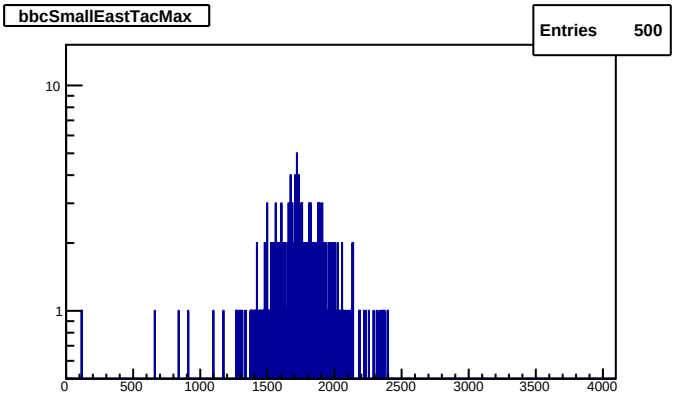

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 8000
TAC
QT Input Channel

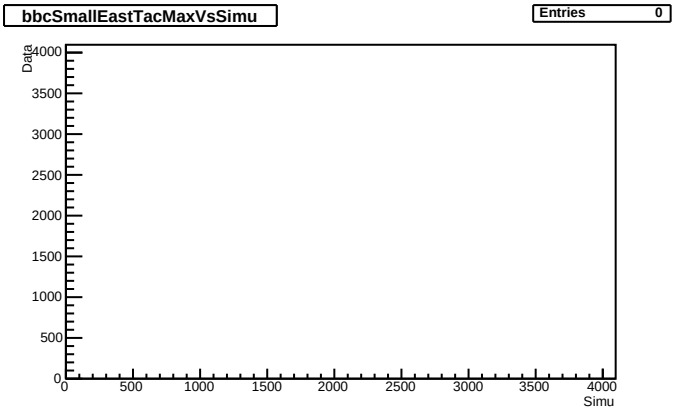

BBQ-ZD001 (ZDC TOWER)
Entries 8000
ADC

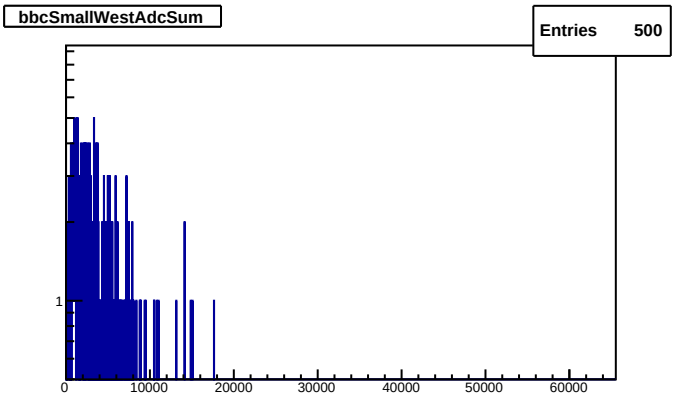

BBQ-ZD001 (ZDC TOWER)
Entries 8000
TAC

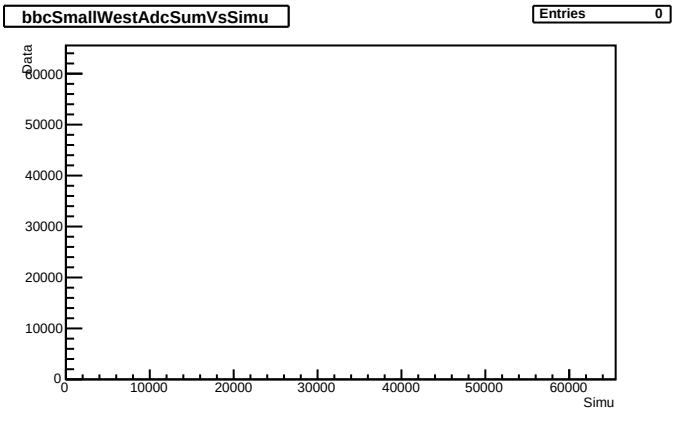

bbcSmallEastAdcSum
Entries 500
10
1
0
10000
20000
30000
40000
50000
60000

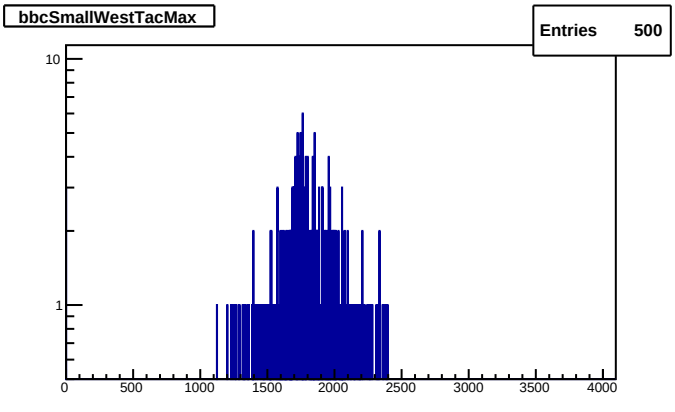

bbcSmallEastAdcSumVsSimu
Entries 0
Data
60000
50000
40000
30000
20000
10000
0
0
10000
20000
30000
40000
50000
60000
Simu

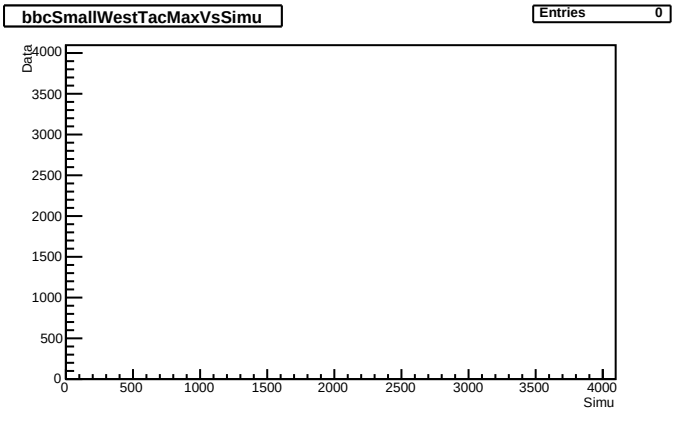

bbcSmallEastTacMax
Entries 500
10
1
0
500
1000
1500
2000
2500
3000
3500
4000

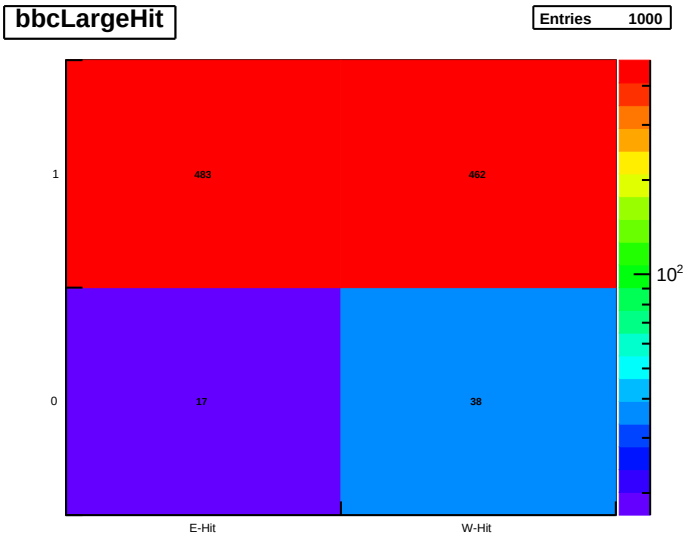

bbcSmallEastTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

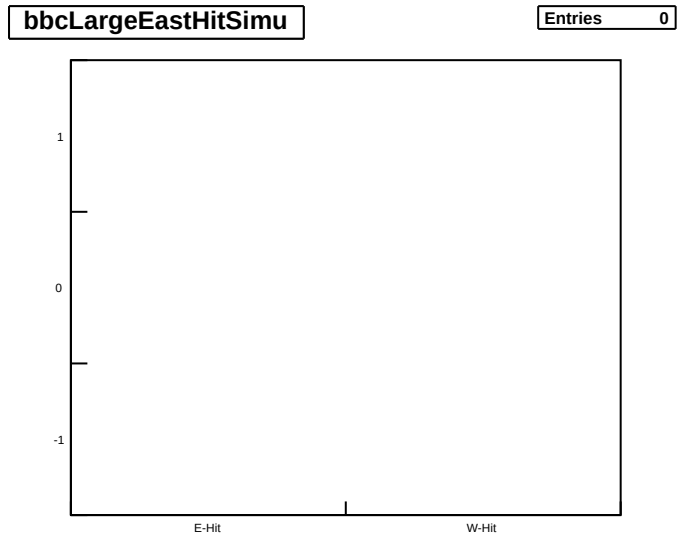

bbcSmallWestAdcSum
Entries 500
1
0
10000
20000
30000
40000
50000
60000

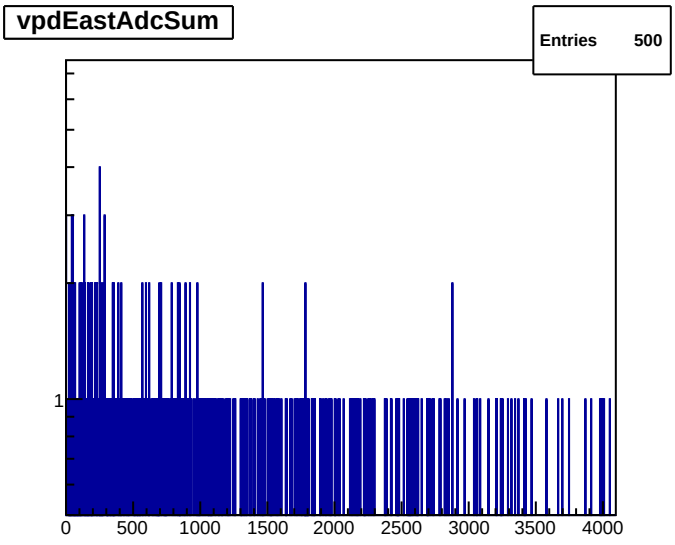

bbcSmallWestAdcSumVsSimu
Entries 0
Data
60000
50000
40000
30000
20000
10000
0
0
10000
20000
30000
40000
50000
60000
Simu

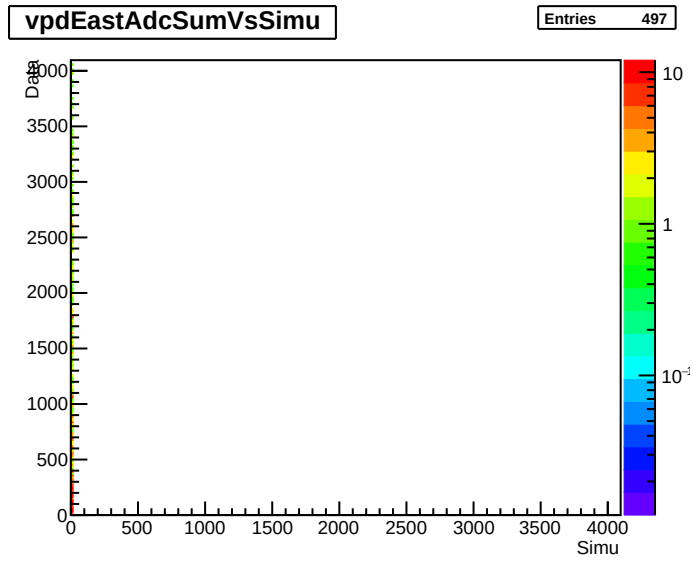

bbcSmallWestTacMax
Entries 500
10
1
0
500
1000
1500
2000
2500
3000
3500
4000

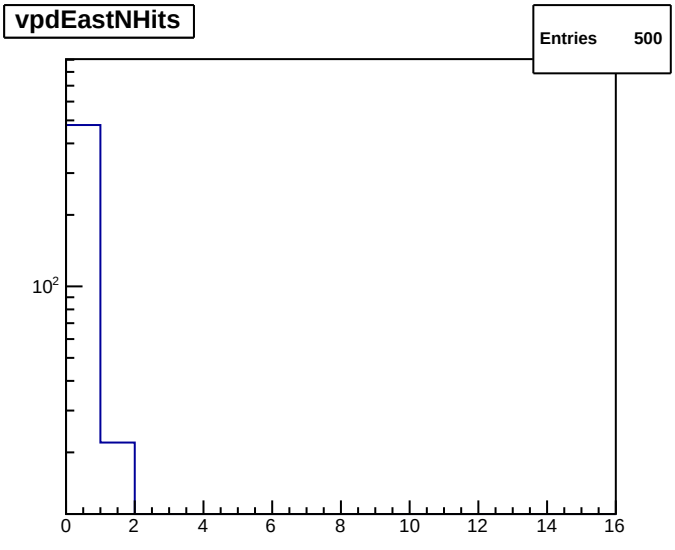

bbcSmallWestTacMaxVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0
500
1000
1500
2000
2500
3000
3500
4000
Simu

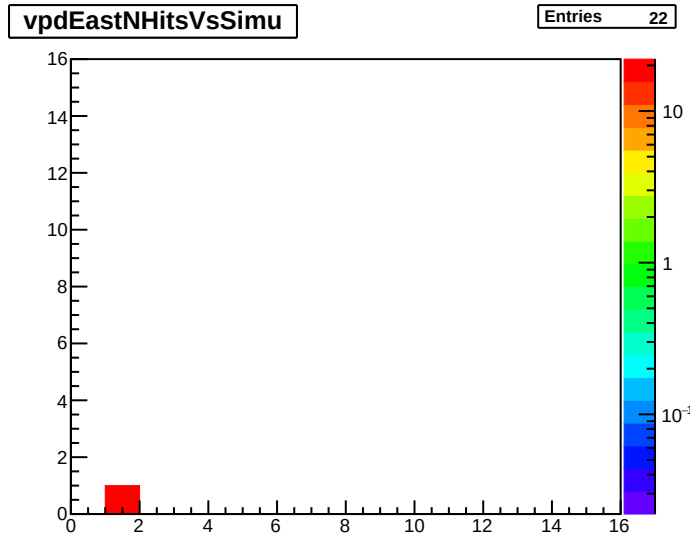

bbcLargeHit
Entries 1000
483
462
17
38
1
0
E-Hit
W-Hit
10^2

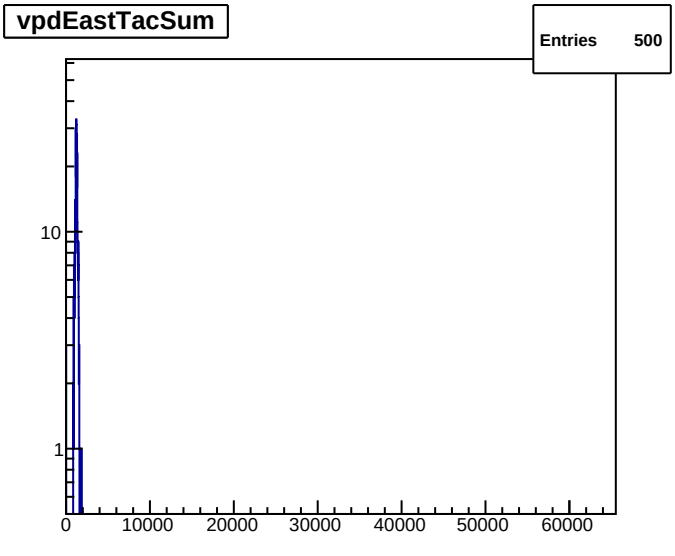

bbcLargeEastHitSimu
Entries 0
1
0
-1
E-Hit
W-Hit

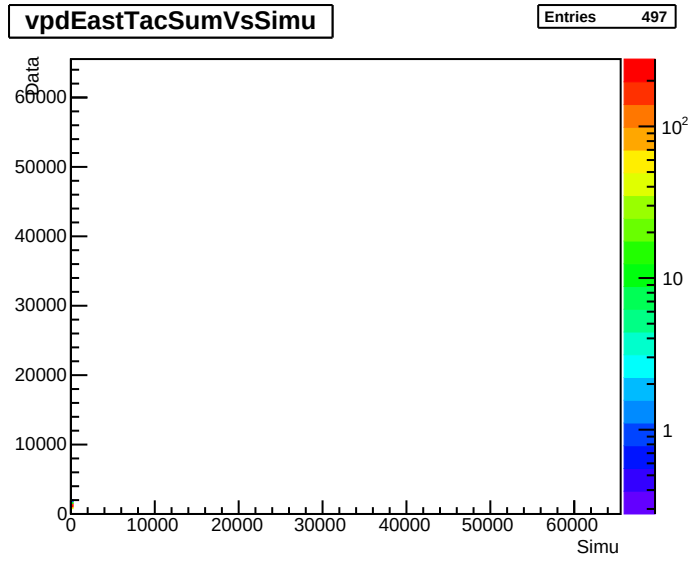

vpdEastAdcSum
Entries 500
1
0 500 1000 1500 2000 2500 3000 3500 4000

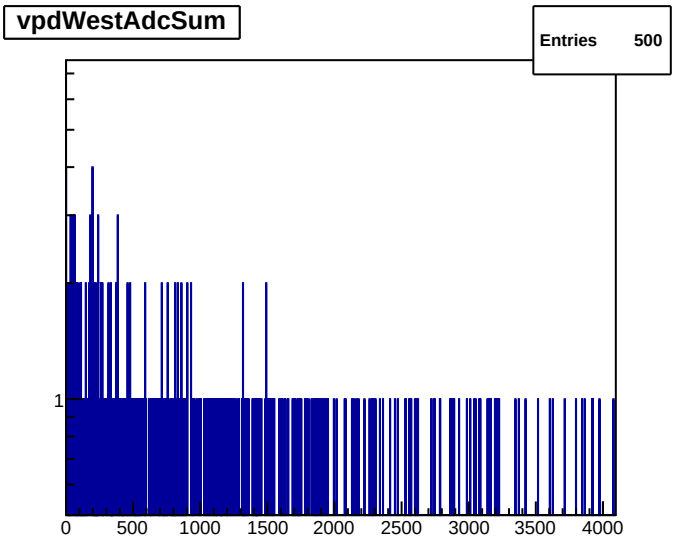

vpdEastAdcSumVsSimu
Entries 497
Data
0 500 1000 1500 2000 2500 3000 3500 4000
Simu
10
1
10⁻¹

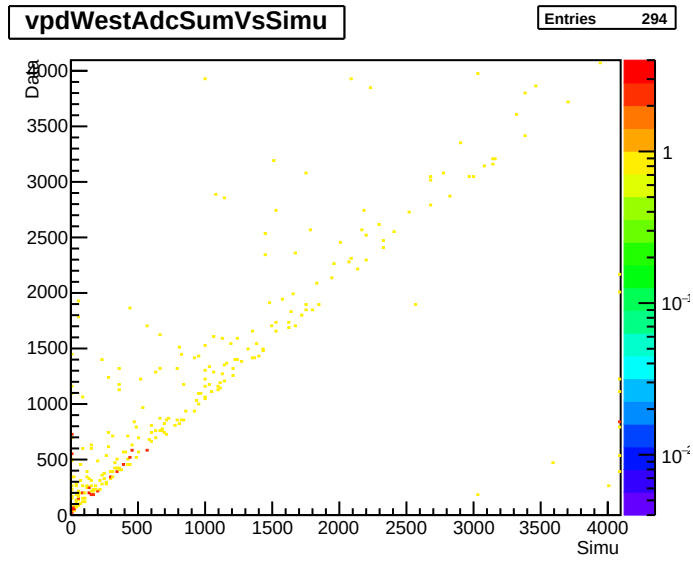

vpdEastNHits
Entries 500
10²
0 2 4 6 8 10 12 14 16

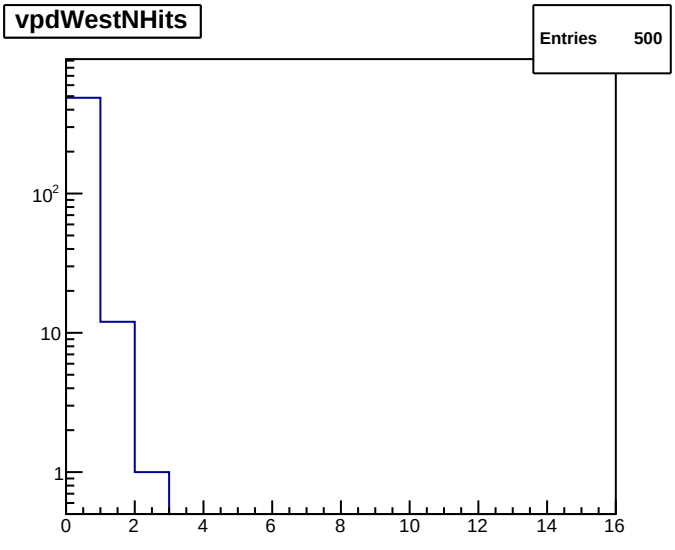

vpdEastNHitsVsSimu
Entries 22
0 2 4 6 8 10 12 14 16
10
1
10⁻¹

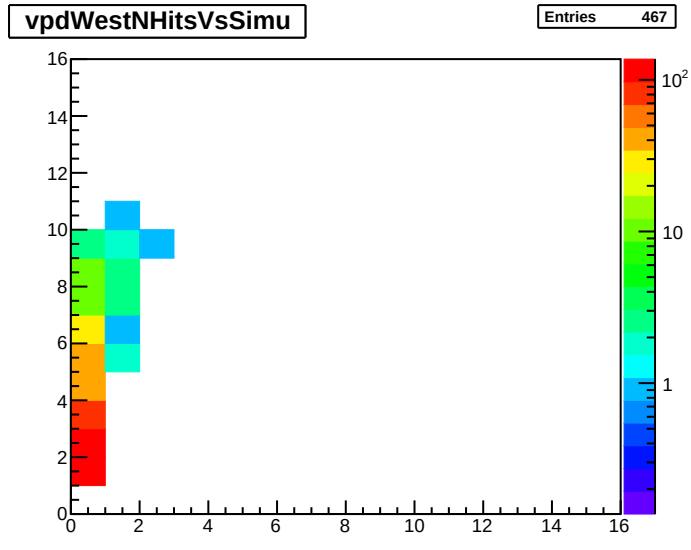

vpdEastTacSum
Entries 500
10
1
0 10000 20000 30000 40000 50000 60000

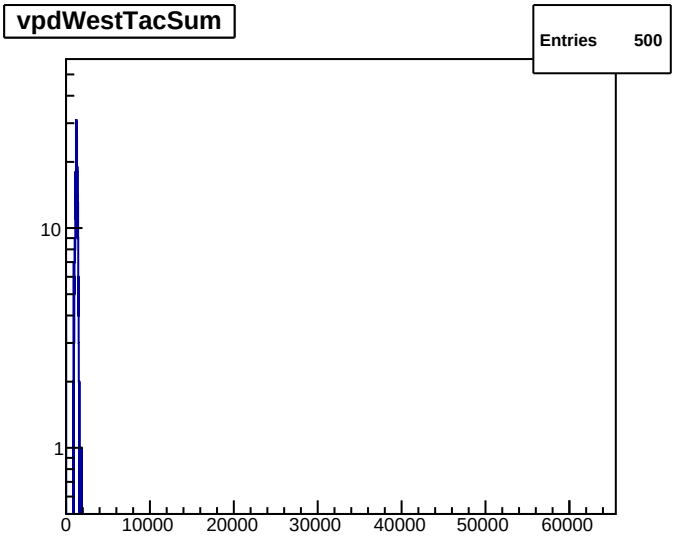

vpdEastTacSumVsSimu
Entries 497
Data
0 10000 20000 30000 40000 50000 60000
Simu
10²
10
1

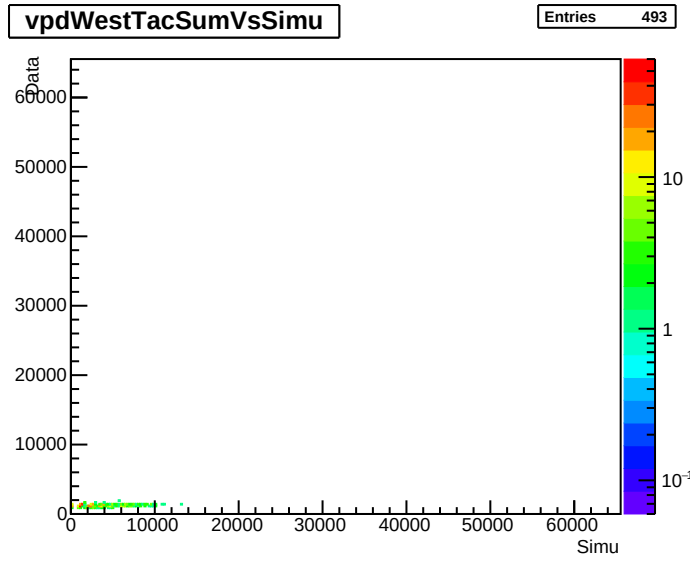

vpdWestAdcSum
Entries 500
1
0 500 1000 1500 2000 2500 3000 3500 4000

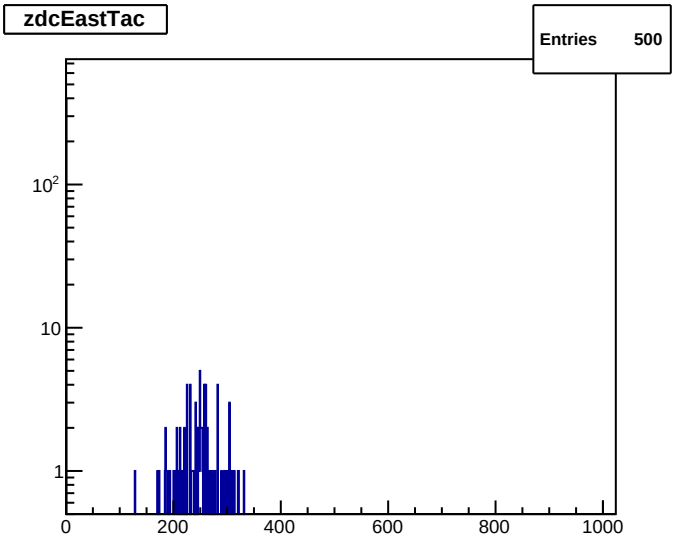

vpdWestAdcSumVsSimu
Entries 294
Data
Simu
0 500 1000 1500 2000 2500 3000 3500 4000
1

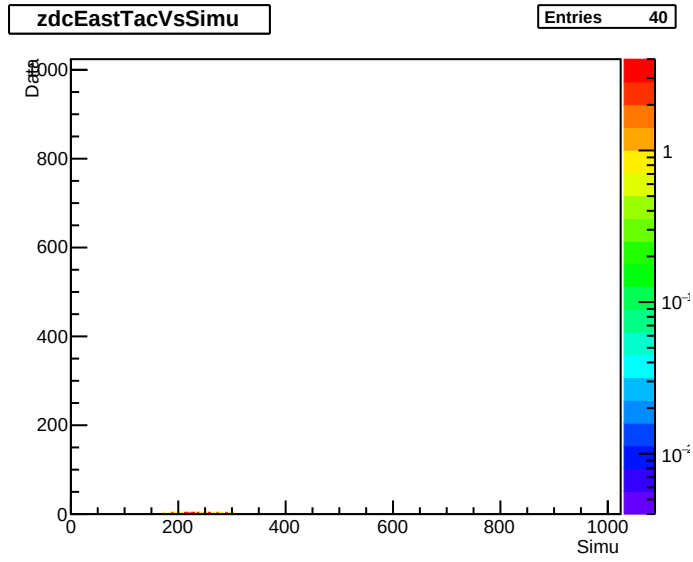

vpdWestNHits
Entries 500
10^2
10
1
0 2 4 6 8 10 12 14 16

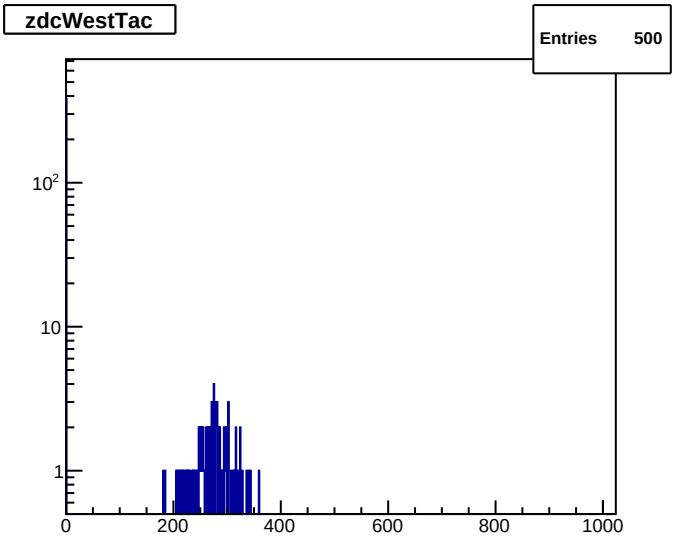

vpdWestNHitsVsSimu
Entries 467
0 2 4 6 8 10 12 14 16
10^2
10
1

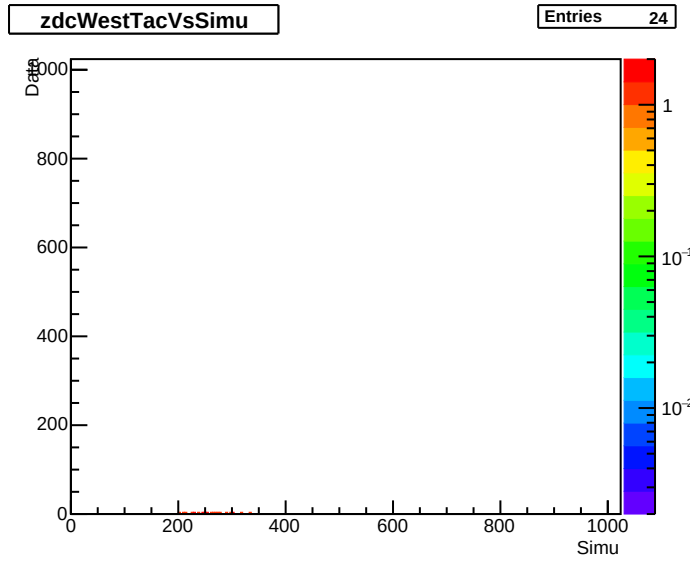

vpdWestTacSum
Entries 500
10
1
0 10000 20000 30000 40000 50000 60000

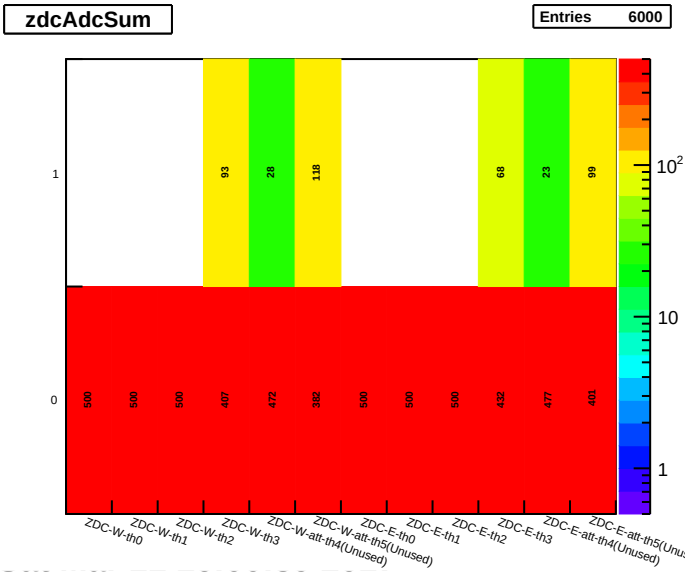

vpdWestTacSumVsSimu
Entries 493
Data
Simu
0 10000 20000 30000 40000 50000 60000
10
1

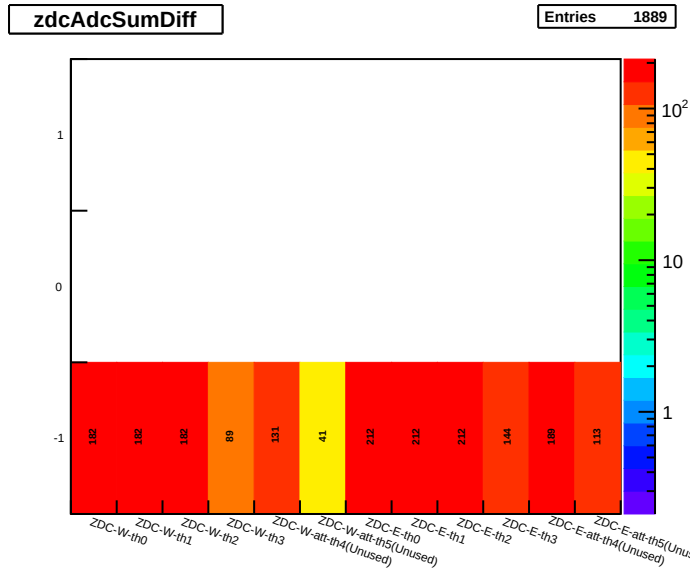

zdcEastTac
Entries 500
10^2
10
1
0
200
400
600
800
1000

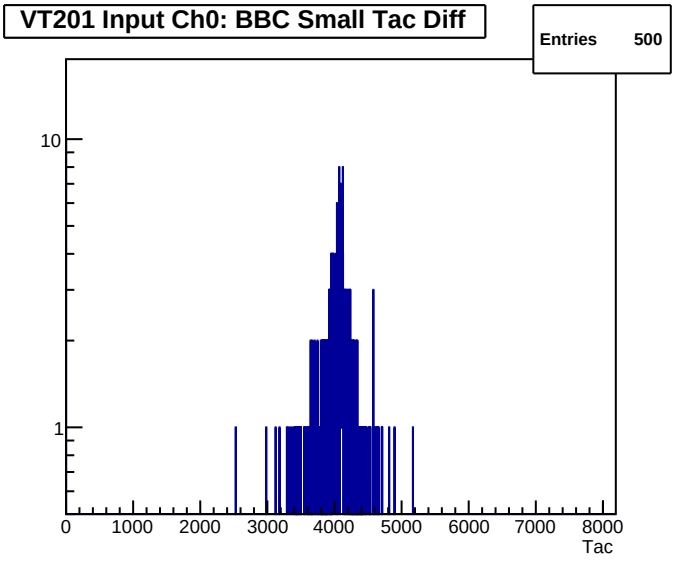

zdcEastTacVsSimu
Entries 40
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu
1
10^{-1}
10^{-2}

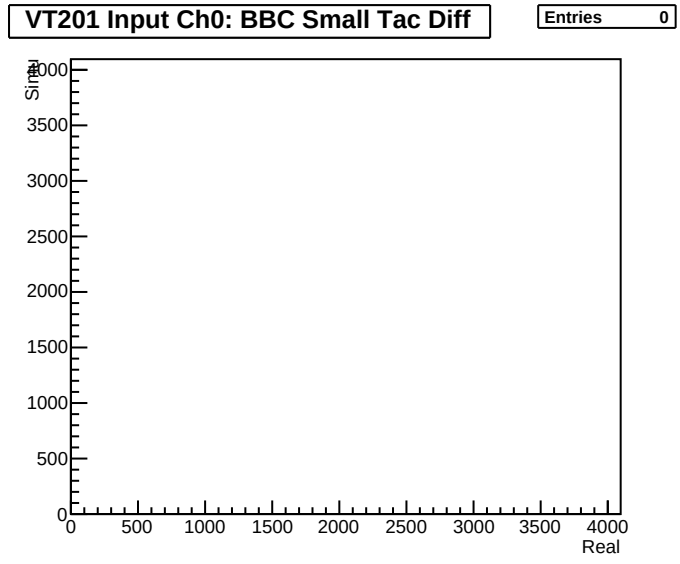

zdcWestTac
Entries 500
10^2
10
1
0
200
400
600
800
1000

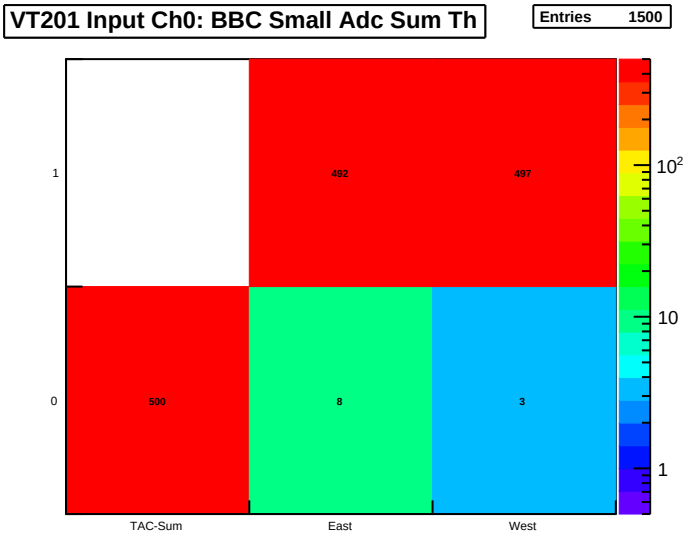

zdcWestTacVsSimu
Entries 24
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu
1
10^{-1}
10^{-2}

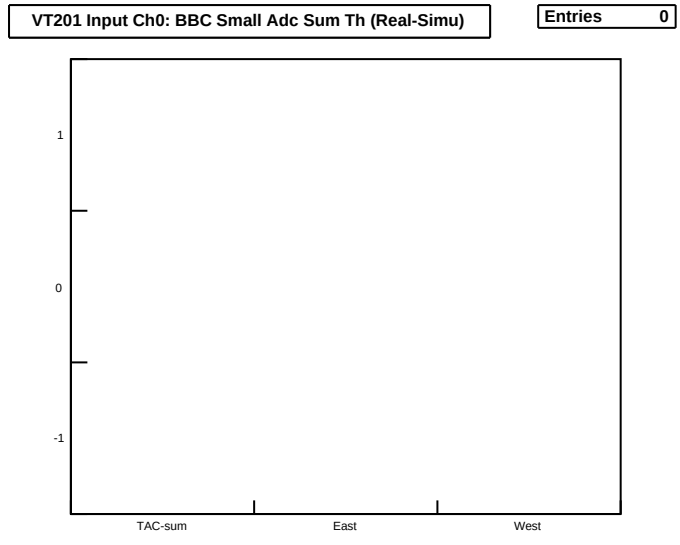

zdcAdcSum
Entries 6000
1
0
93
28
118
68
23
99
500
500
500
407
472
392
500
500
500
432
477
401
10^2
10
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

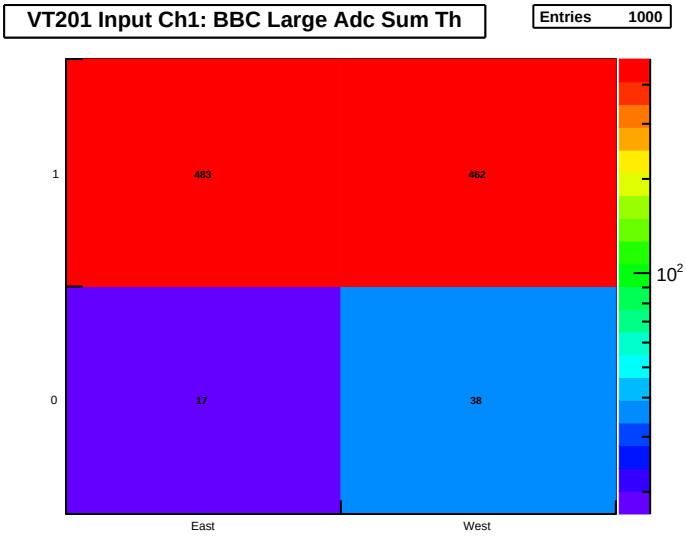

zdcAdcSumDiff
Entries 1889
1
0
-1
182
182
182
89
131
41
212
212
212
144
189
113
10^2
10
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

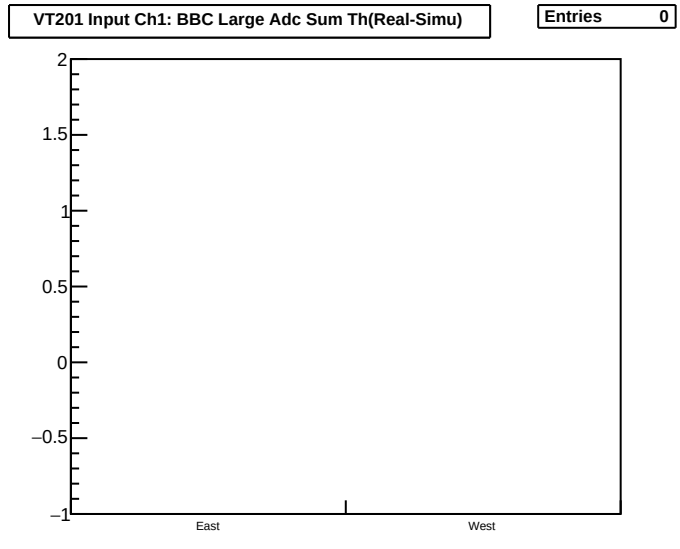

VT201 Input Ch0: BBC Small Tac Diff
Entries 500
10
1
0 1000 2000 3000 4000 5000 6000 7000 8000
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Real

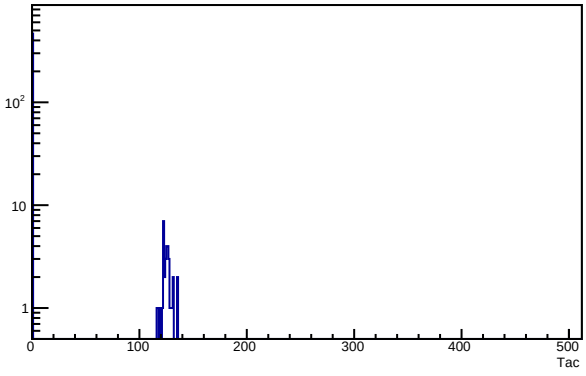

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 1500
1
0
492
497
500
8
3
TAC-Sum
East
West
10^2
10
1


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
1
0
-1
TAC-sum
East
West

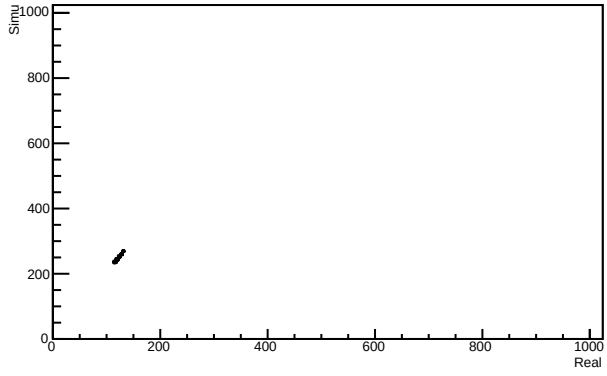

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 1000
1
0
483
462
17
38
East
West
10^2


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
2
1.5
1
0.5
0
−0.5
−1
East
West

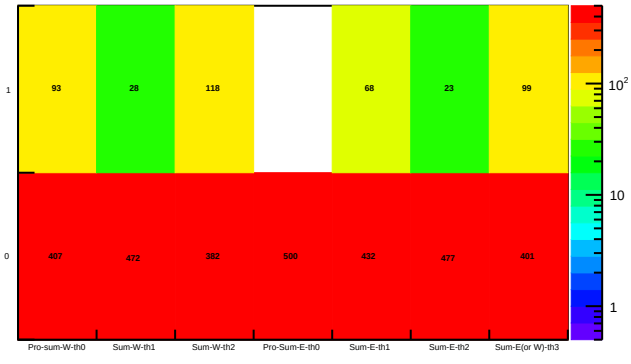
VT201 Input Ch2: ZDC Tac Diff



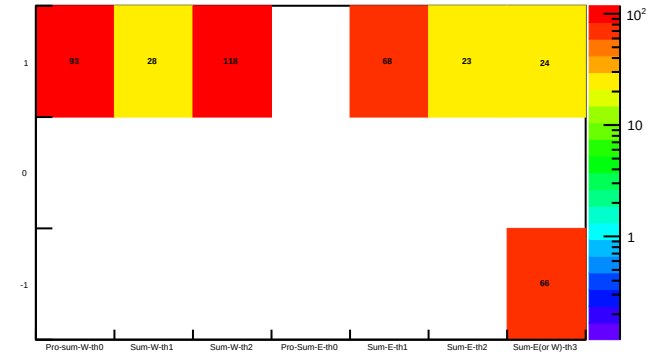
VT201 Input Ch2: ZDC Tac Diff



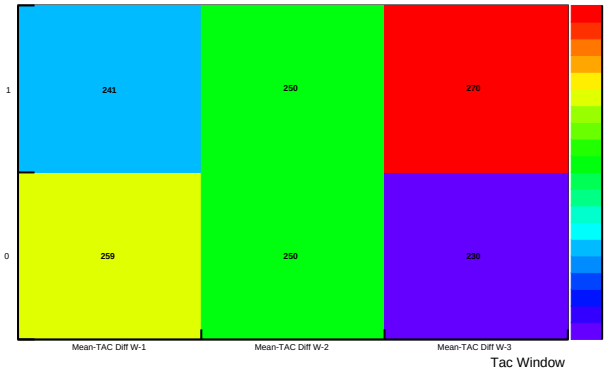
VT201 Input Ch2: ZDC Adc Sum Th



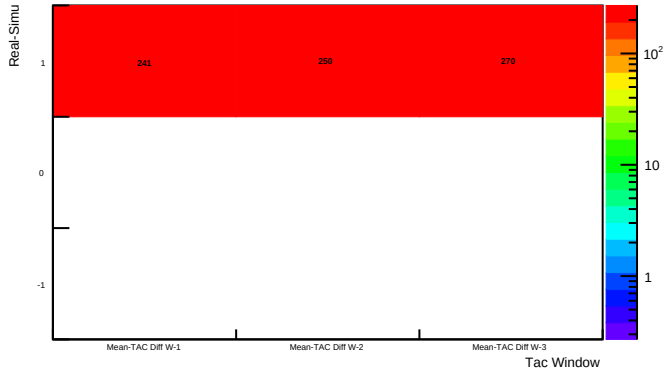
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



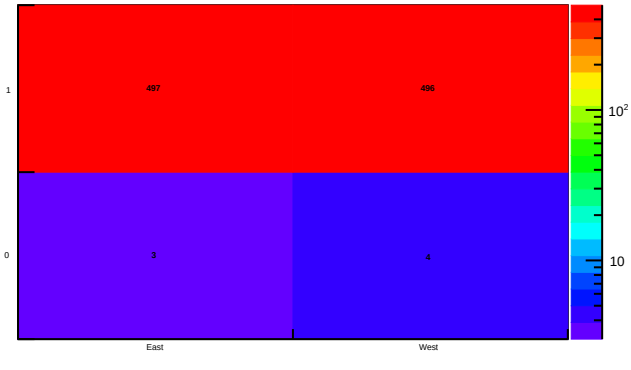
VT201 Input Ch4: VPD Tac Diff



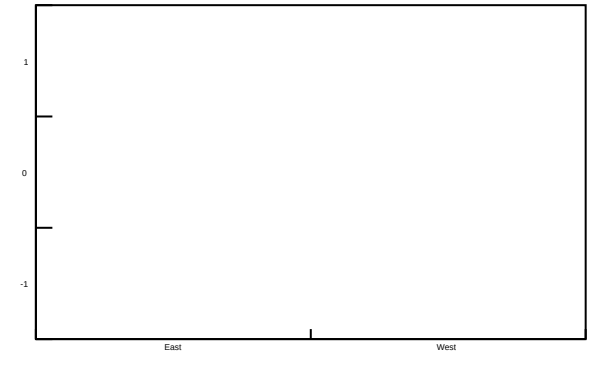
VT201 Input Ch4: VPD Tac Diff

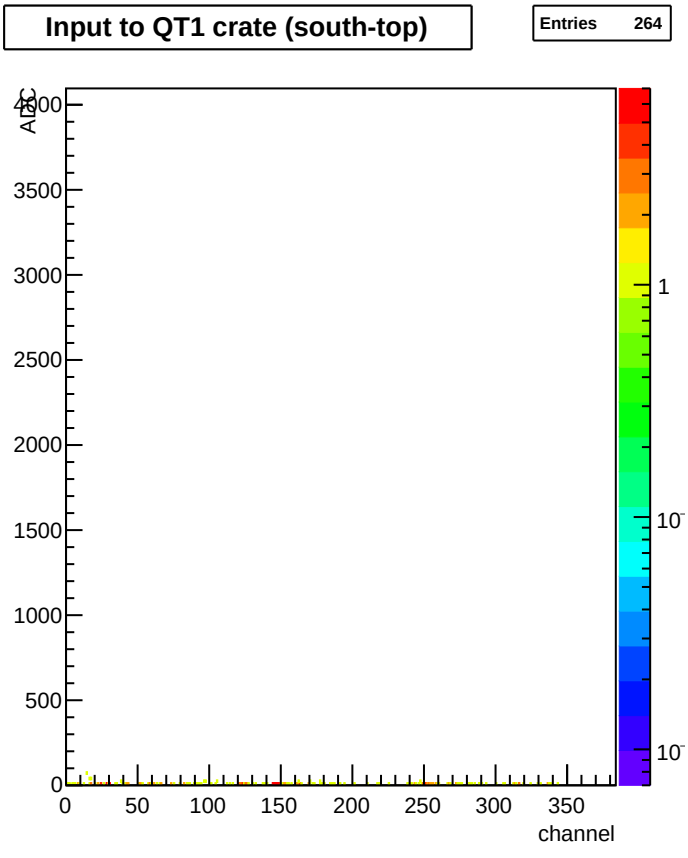


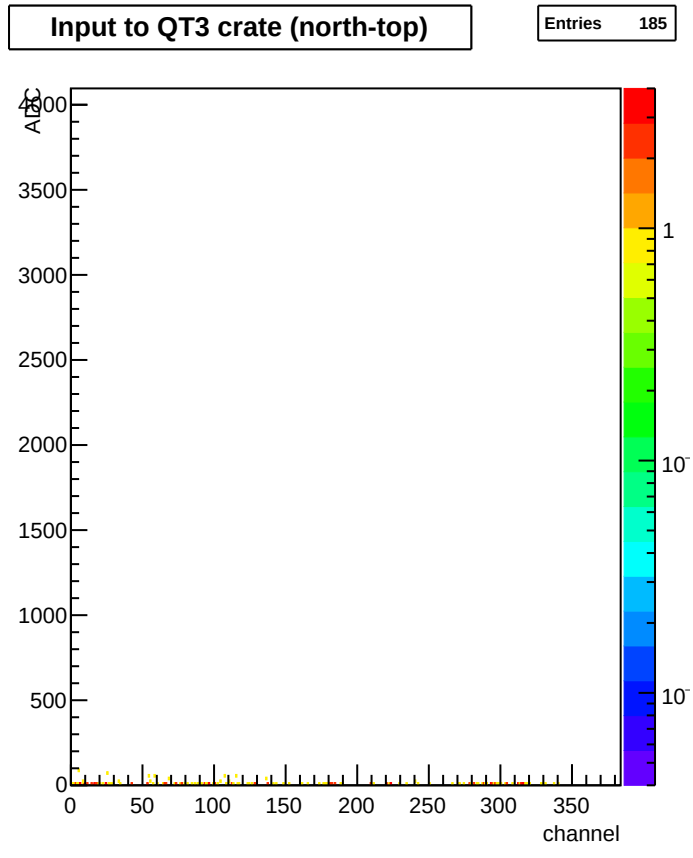
VT201 Input Ch4: VPD Adc Sum Th

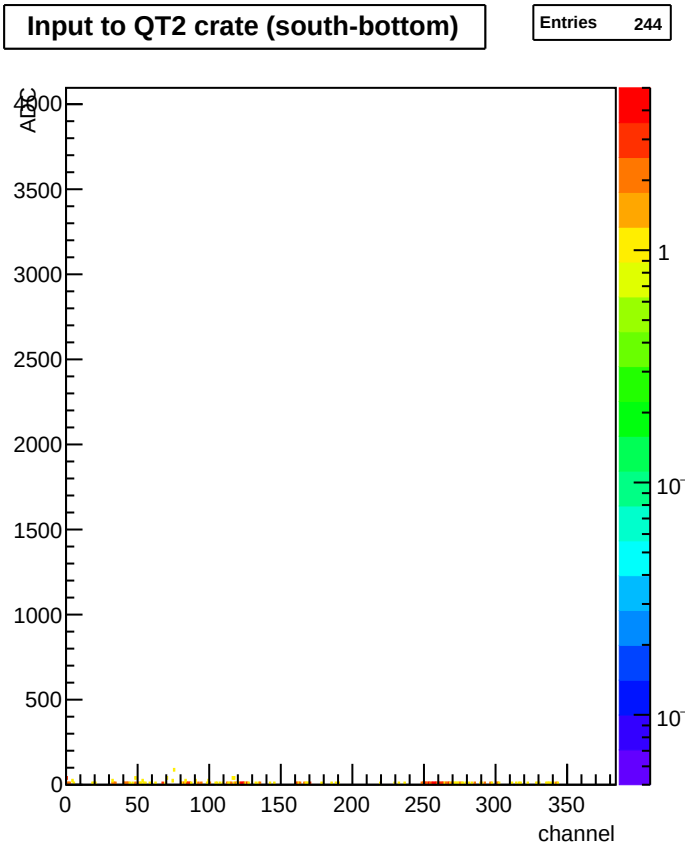


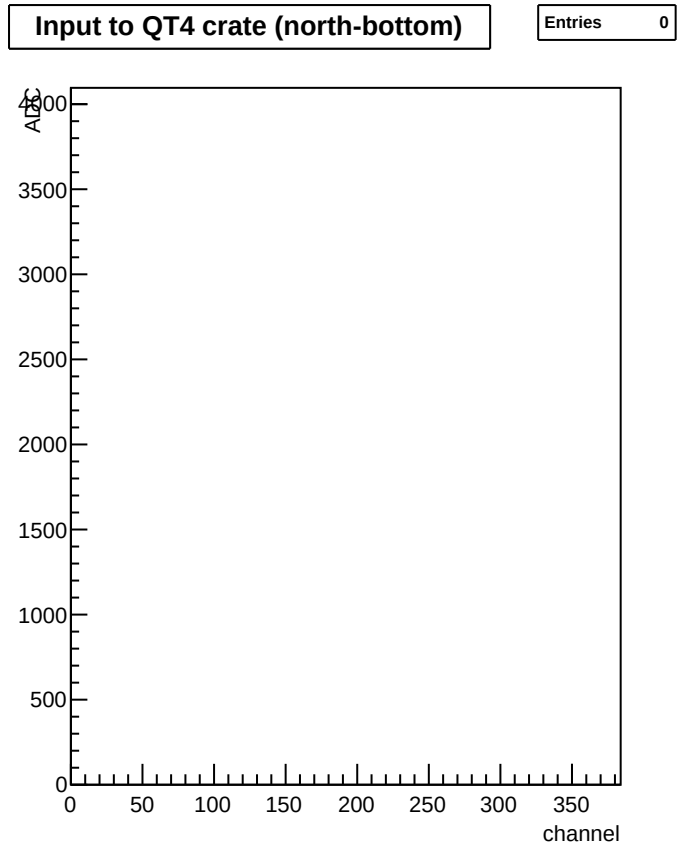
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

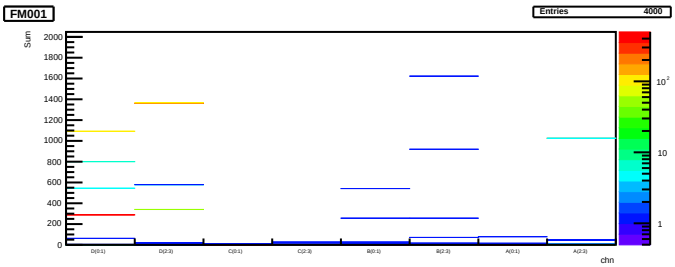


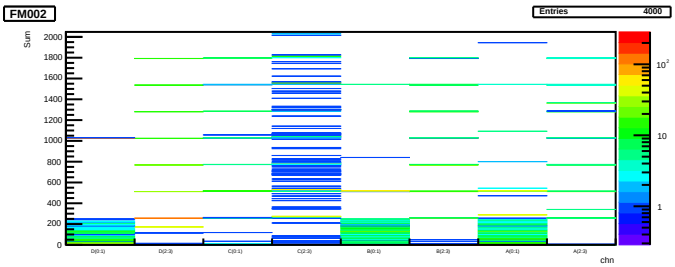

Input to QT1 crate (south-top)
Entries 264
ADC
channel

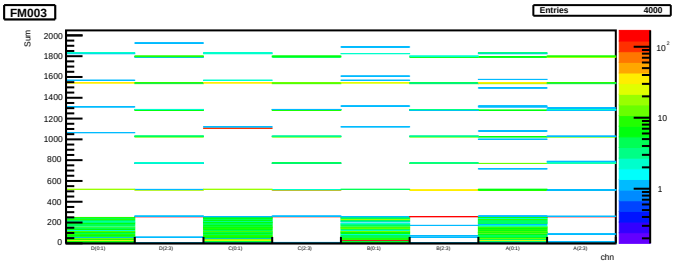

Input to QT3 crate (north-top)
Entries 185
ADC
channel

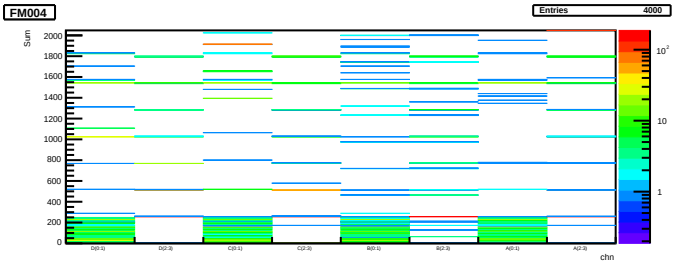

Input to QT2 crate (south-bottom)
Entries 244
ADC
channel

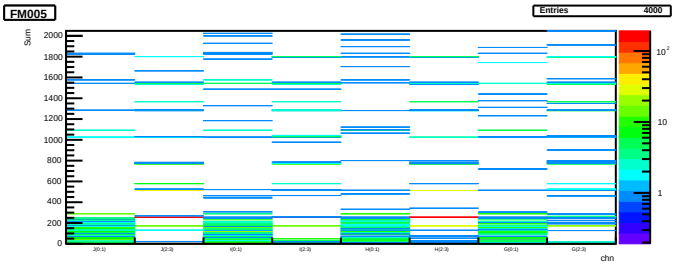

Input to QT4 crate (north-bottom)
Entries 0
ADC
channel

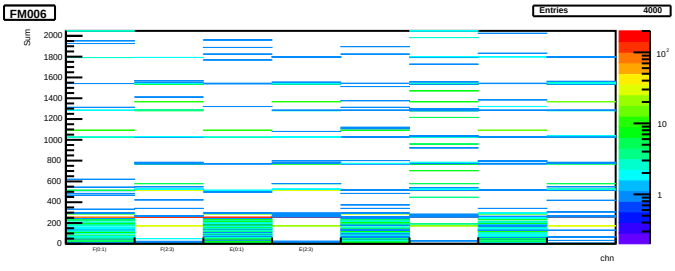

FM001
Entries 4000
Sum
chn

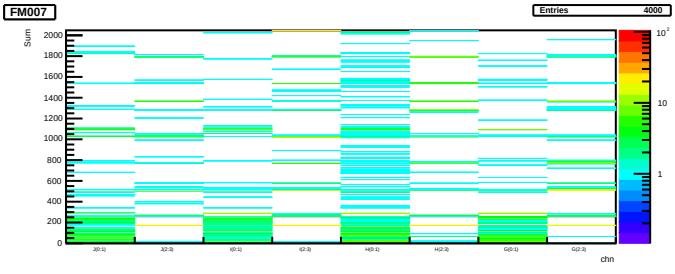

FM002
Entries 4000
Sum
chn

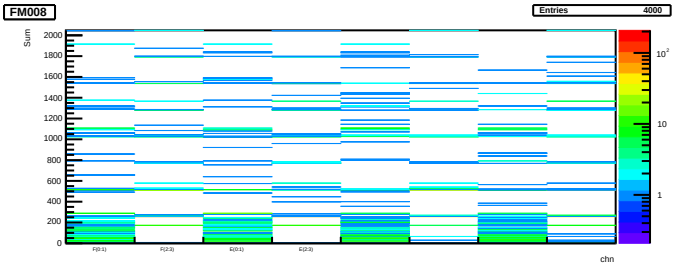

FM003
Entries 4000
Sum
chn

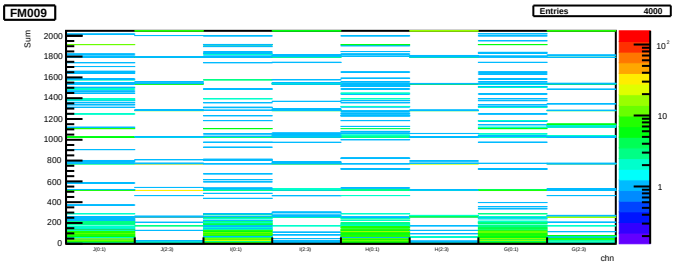

FM004
Entries 4000
Sum
chn


FM005
Entries 4000
Sum
chn


FM006
Entries 4000
Sum
chn


FM007
Entries 4000
Sum
chn


FM008
Entries 4000
Sum
chn


FM009
Entries 4000
Sum
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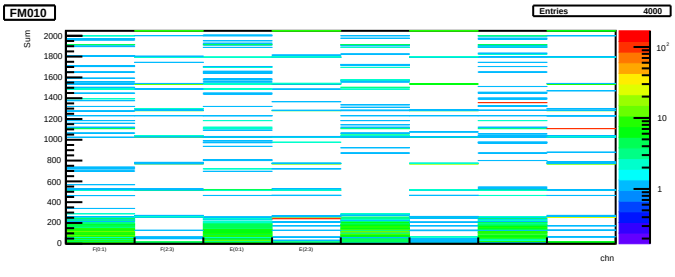
FM010
Entries 4000
Sum
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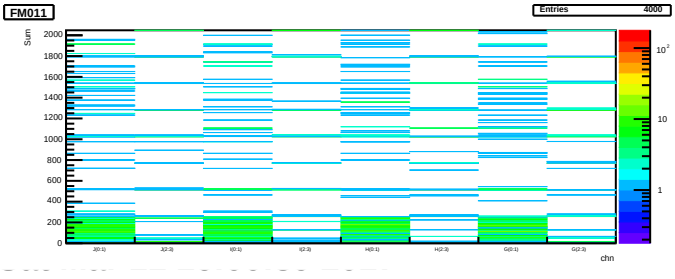
FM011
Entries 4000
Sum
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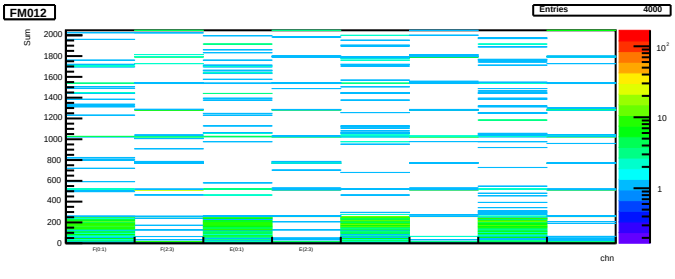
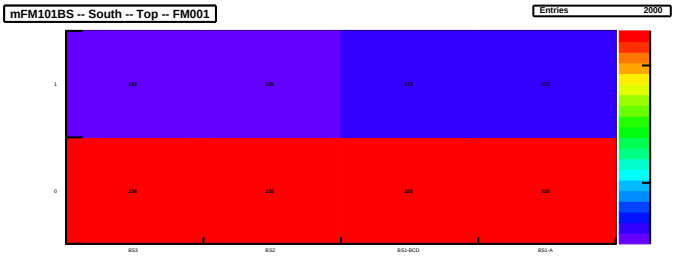
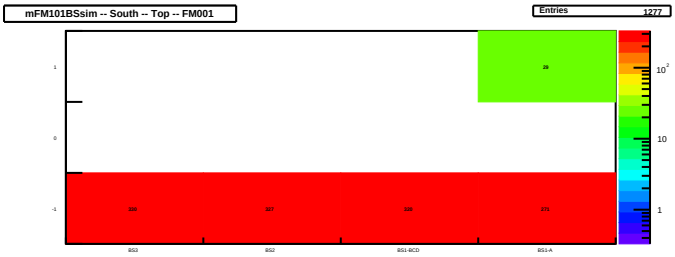
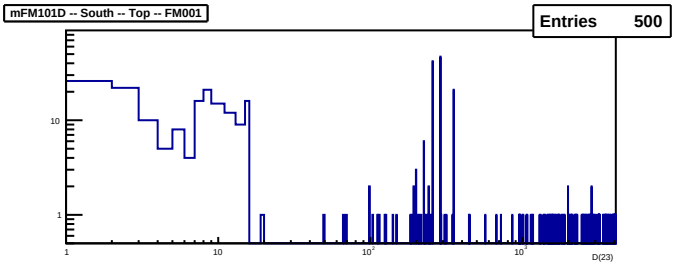
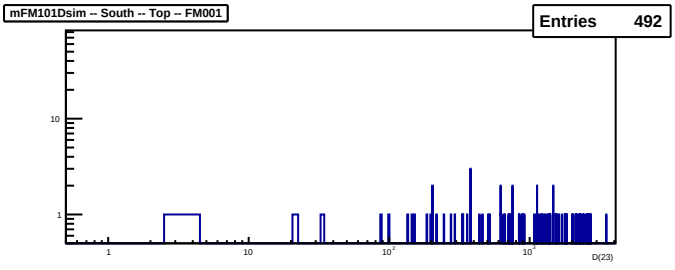
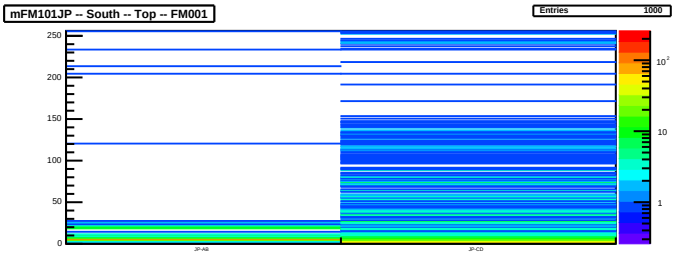

FM012
Entries 4000
Sum
chn

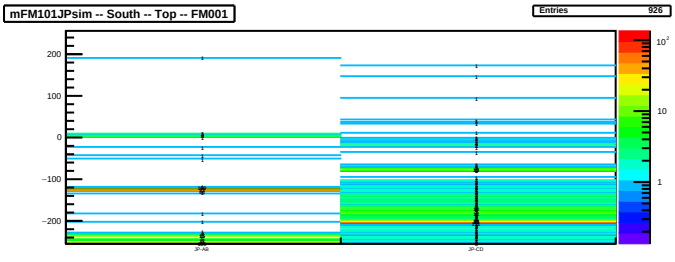

mFM101BS -- South -- Top -- FM001
Entries 2000

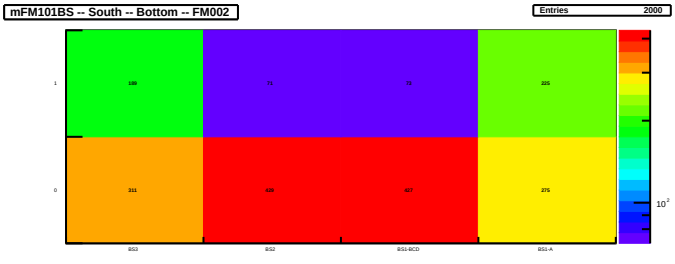

mFM101BSsim -- South -- Top -- FM001
Entries 1277

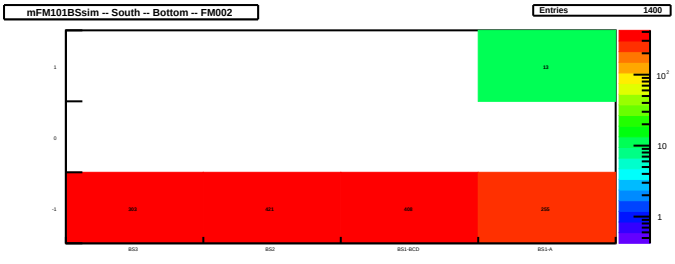

mFM101D -- South -- Top -- FM001
Entries 500
D(23)

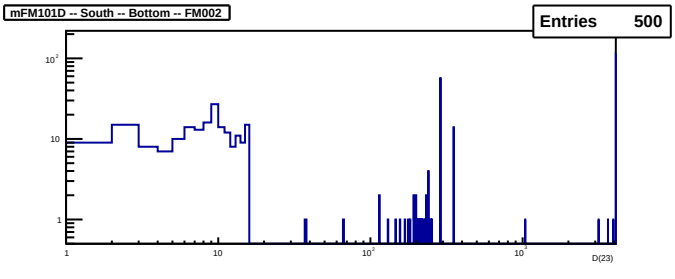

mFM101Dsim -- South -- Top -- FM001
Entries 492
D(23)

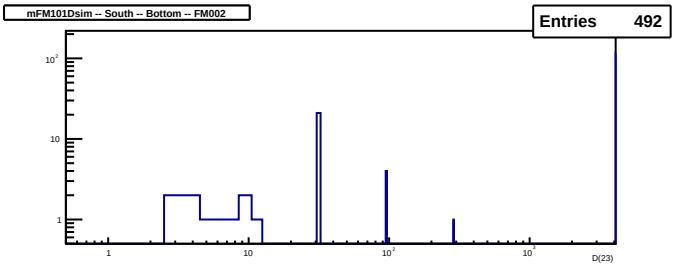

mFM101JP -- South -- Top -- FM001
Entries 1000

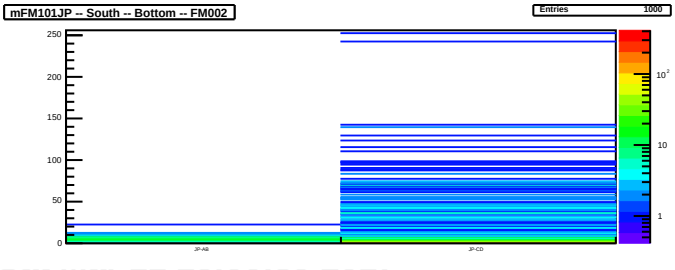

mFM101JPsim -- South -- Top -- FM001
Entries 926

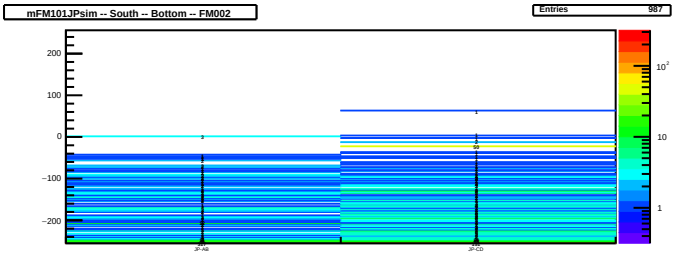

mFM101BS -- South -- Bottom -- FM002
Entries 2000

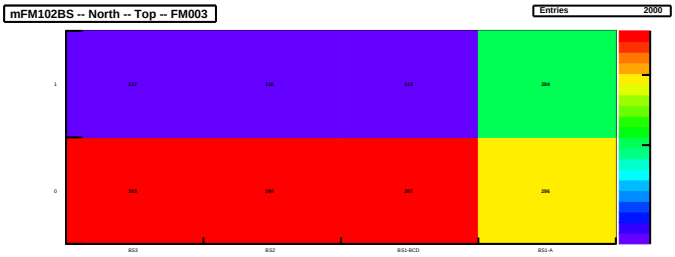

mFM101BSsim -- South -- Bottom -- FM002
Entries 1400

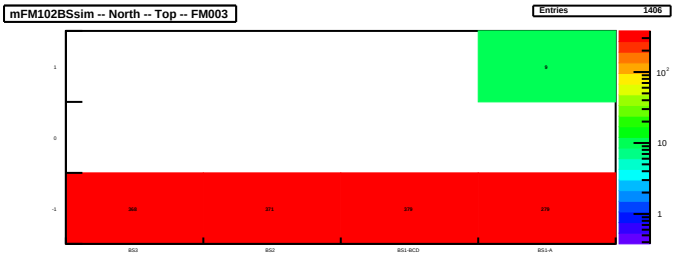

mFM101D -- South -- Bottom -- FM002
Entries 500
D(23)

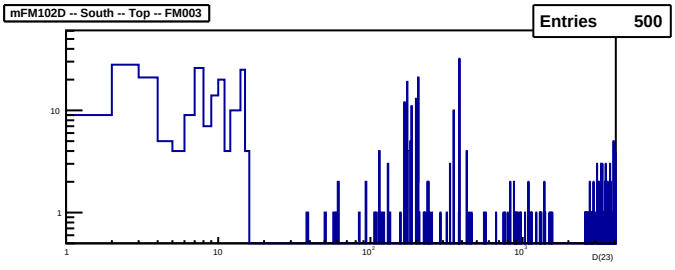

mFM101Dsim -- South -- Bottom -- FM002
Entries 492
D(23)

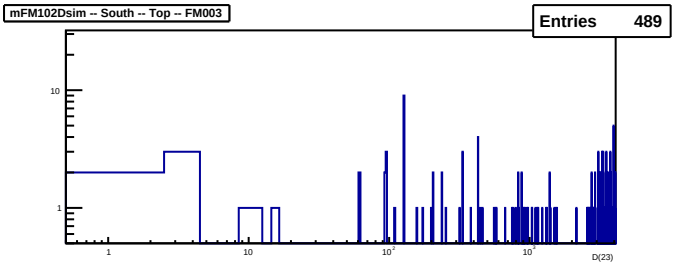

mFM101JP -- South -- Bottom -- FM002
Entries 1000


mFM101JPsim -- South -- Bottom -- FM002
Entries 987


mFM102BS -- North -- Top -- FM003
Entries 2000


mFM102BSsim -- North -- Top -- FM003
Entries 1406


mFM102D -- South -- Top -- FM003
Entries 500
D(23)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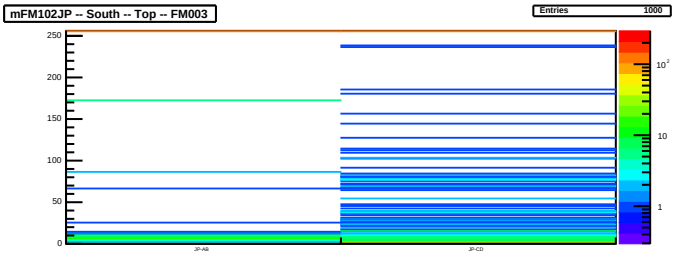
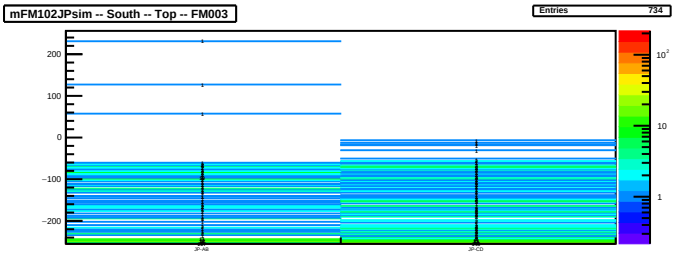
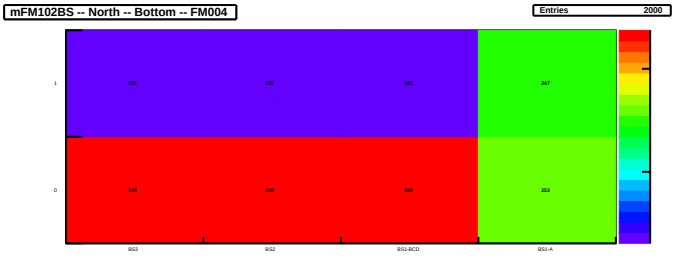
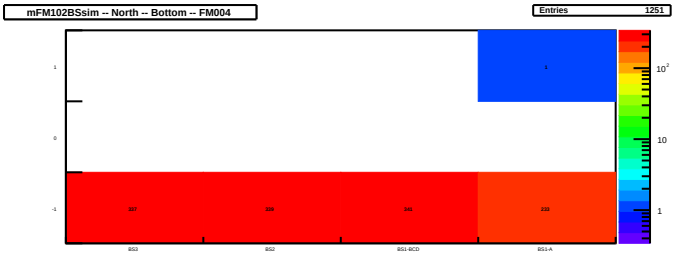
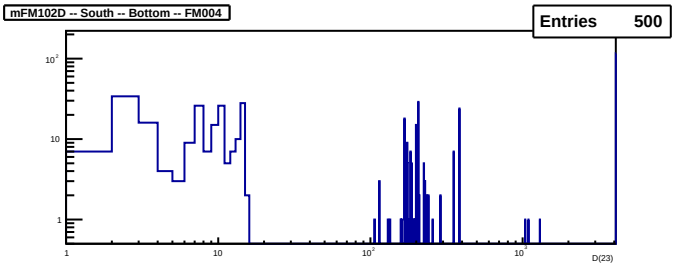
Entries 489
D(23)

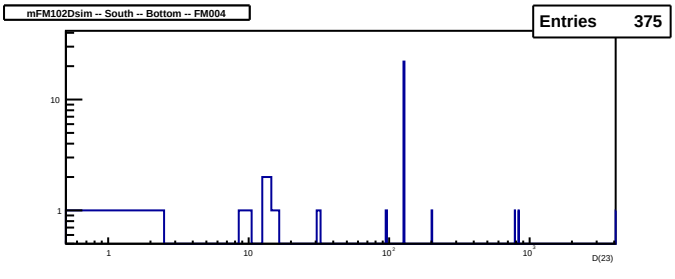

mFM102JP -- South -- Top -- FM003
Entries 1000

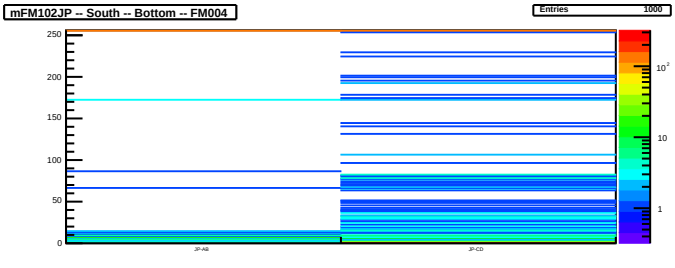

mFM102JPsim -- South -- Top -- FM003
Entries 734

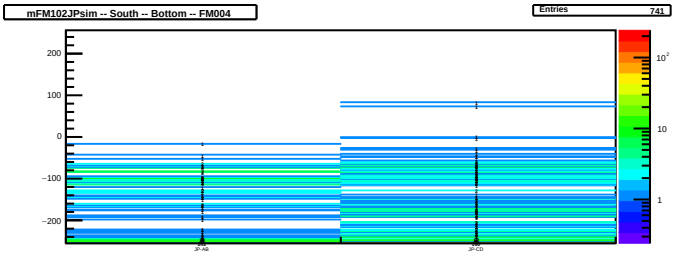

mFM102BS -- North -- Bottom -- FM004
Entries 2000

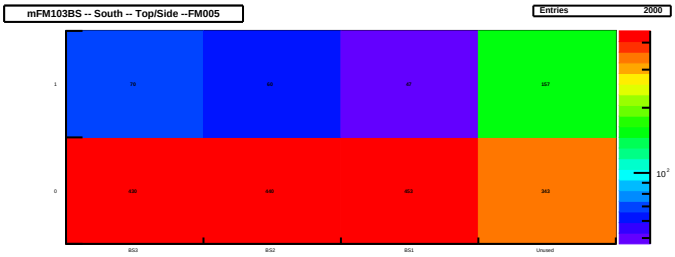

mFM102BSsim -- North -- Bottom -- FM004
Entries 1251

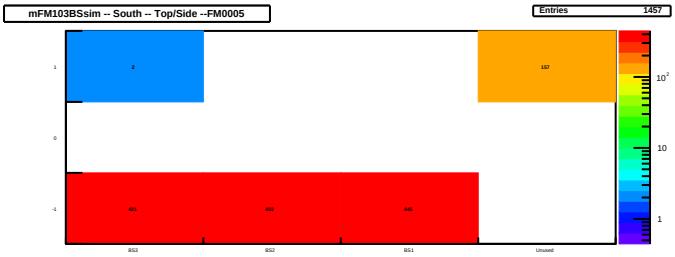

mFM102D -- South -- Bottom -- FM004
Entries 500
D(23)

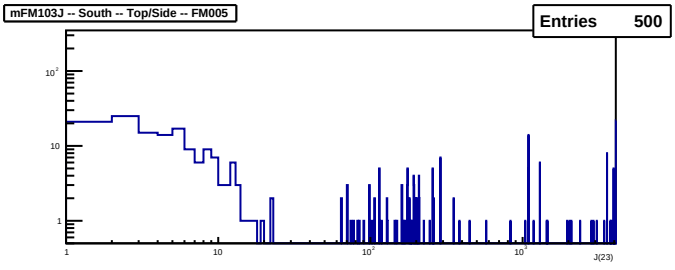

mFM102Dsim -- South -- Bottom -- FM004
Entries 375
D(23)

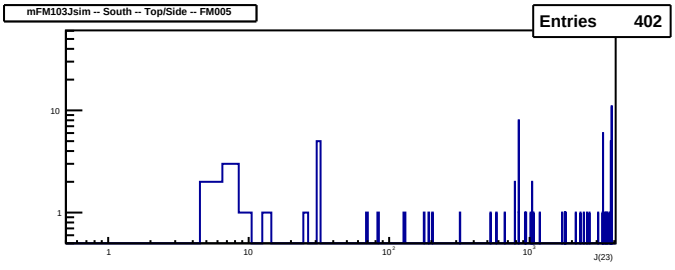

mFM102JP -- South -- Bottom -- FM004
Entries 1000

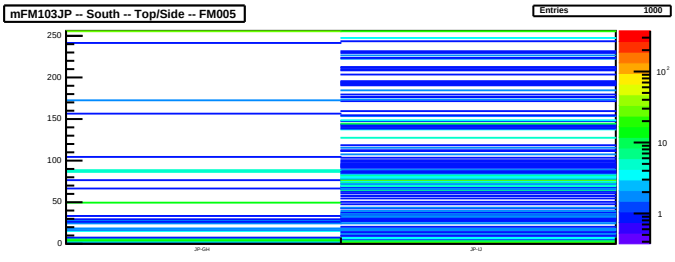

mFM102JPsim -- South -- Bottom -- FM004
Entries 741

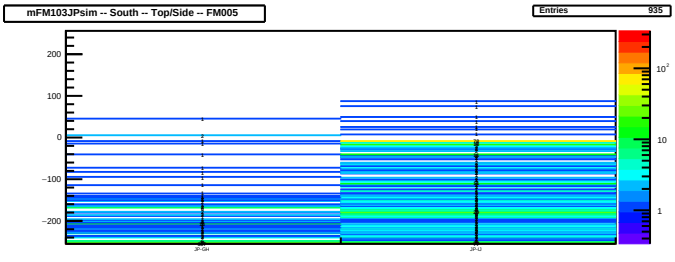

mFM103BS -- South -- Top/Side --FM005
Entries 2000
BS3
BS2
BS1
Unused

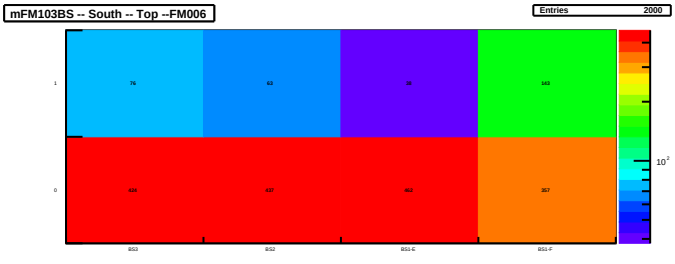

mFM103BSsim -- South -- Top/Side --FM0005
Entries 1457
BS3
BS2
BS1
Unused

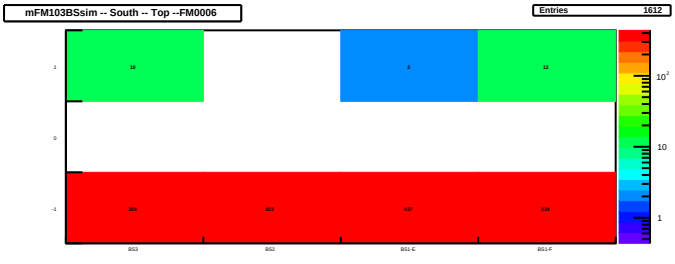

mFM103J -- South -- Top/Side -- FM005
Entries 500
J(23)

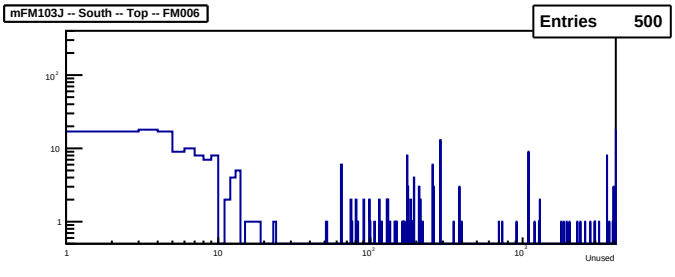

mFM103Jsim -- South -- Top/Side -- FM005
Entries 402
J(23)

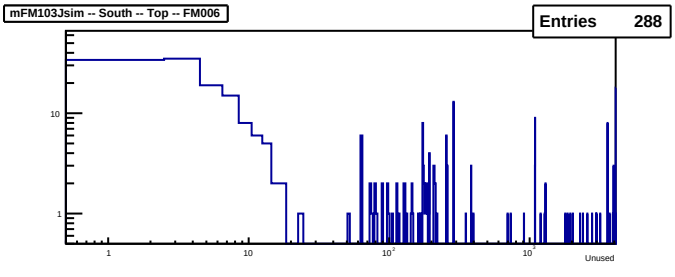

mFM103JP -- South -- Top/Side -- FM005
Entries 1000

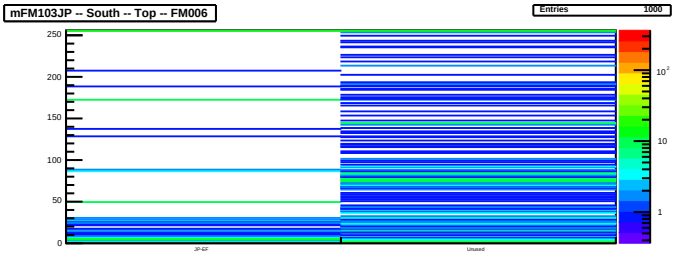

mFM103JPsim -- South -- Top/Side -- FM005
Entries 935

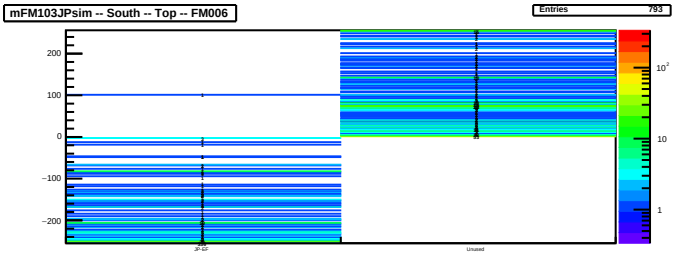

mFM103BS -- South -- Top --FM006
Entries 2000
BS3
BS2
BS1-E
BS1-F


mFM103BSsim -- South -- Top --FM0006
Entries 1612
BS3
BS2
BS1-E
BS1-F


mFM103J -- South -- Top -- FM006
Entries 500
Unused


mFM103Jsim -- South -- Top -- FM006
Entries 288
Unused


mFM103JP -- South -- Top -- FM006
Entries 1000
Unused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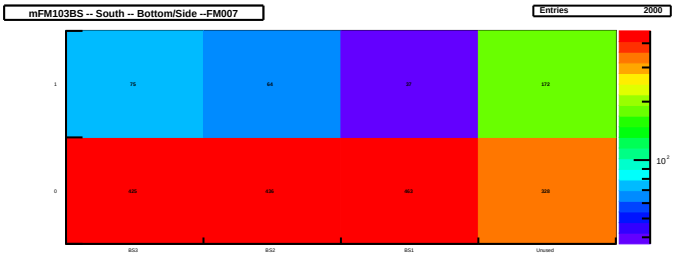
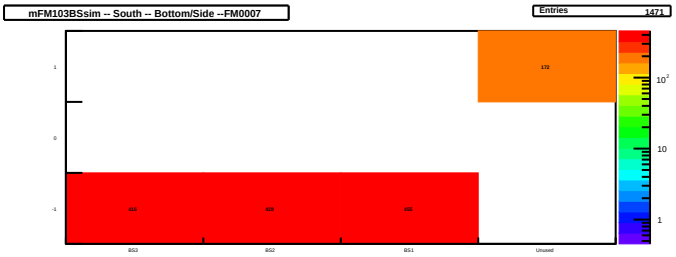
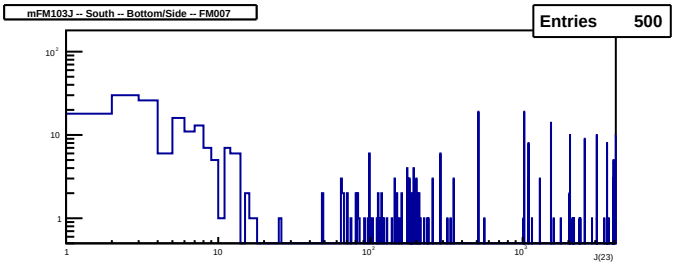
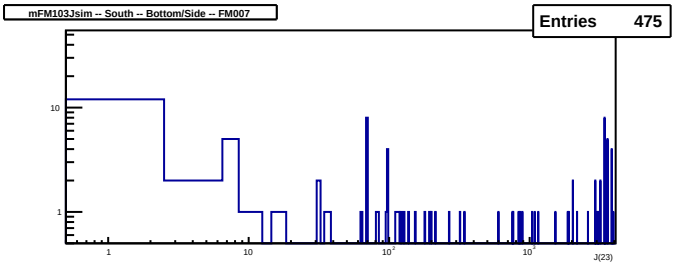
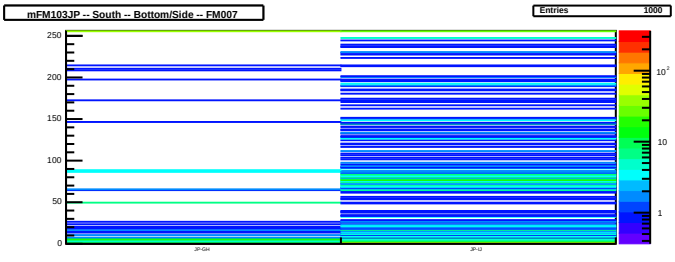
Entries 793
Unused

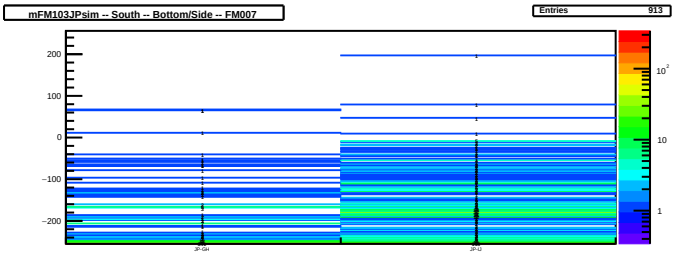

mFM103BS -- South -- Bottom/Side --FM007
Entries 2000

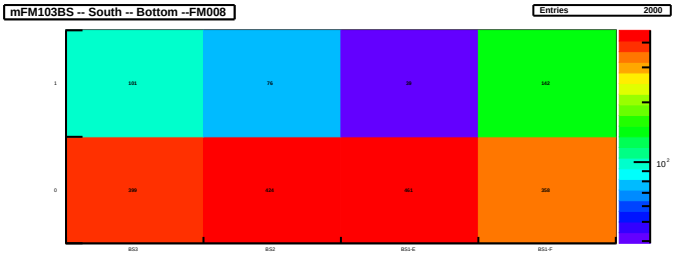

mFM103BSsim -- South -- Bottom/Side --FM0007
Entries 1471

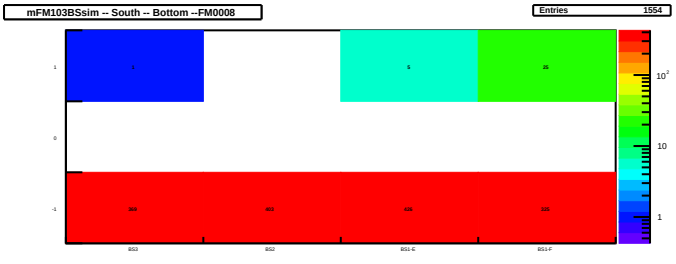

mFM103J -- South -- Bottom/Side -- FM007
Entries 500
J(23)

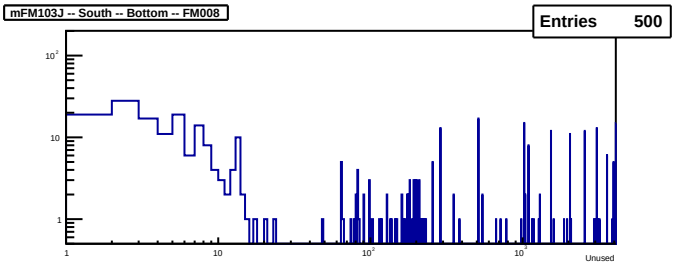

mFM103Jsim -- South -- Bottom/Side -- FM007
Entries 475
J(23)

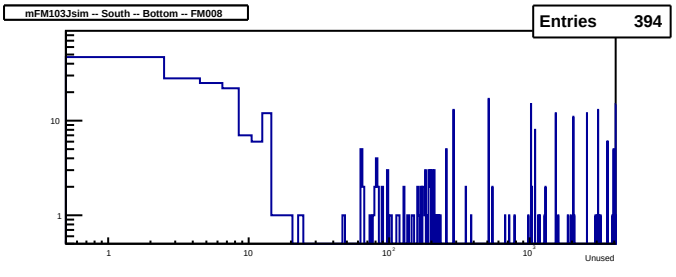

mFM103JP -- South -- Bottom/Side -- FM007
Entries 1000

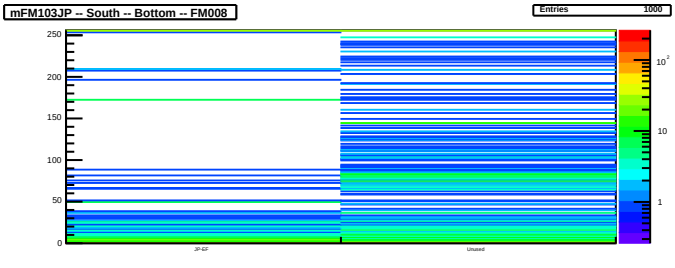

mFM103JPsim -- South -- Bottom/Side -- FM007
Entries 913

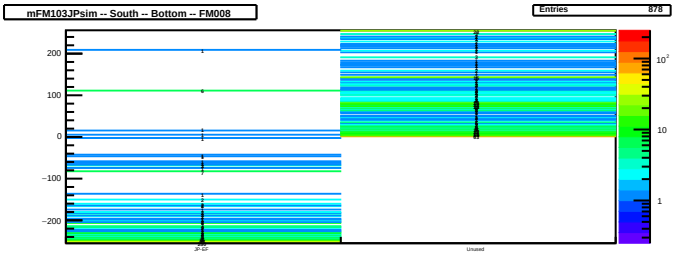

mFM103BS -- South -- Bottom --FM008
Entries 2000

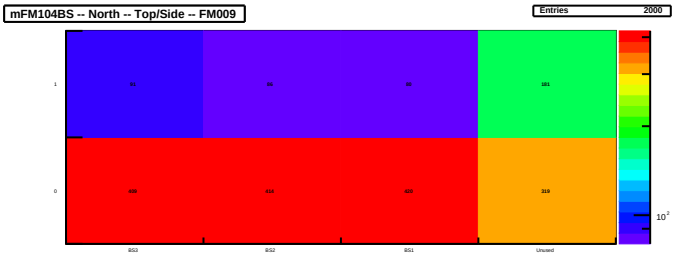

mFM103BSsim -- South -- Bottom --FM0008
Entries 1554

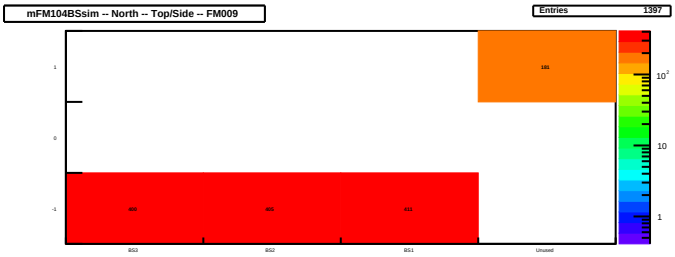

mFM103J -- South -- Bottom -- FM008
Entries 500
Unused

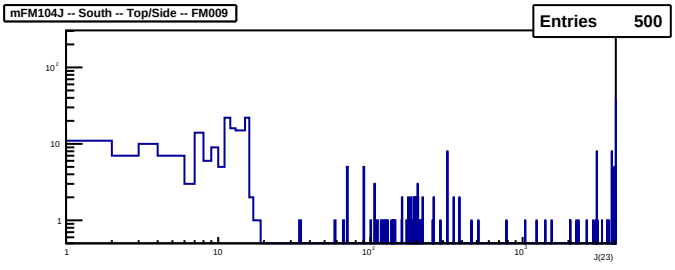

mFM103Jsim -- South -- Bottom -- FM008
Entries 394
Unused

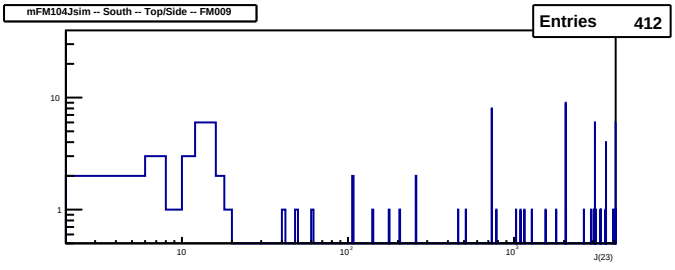

mFM103JP -- South -- Bottom -- FM008
Entries 1000

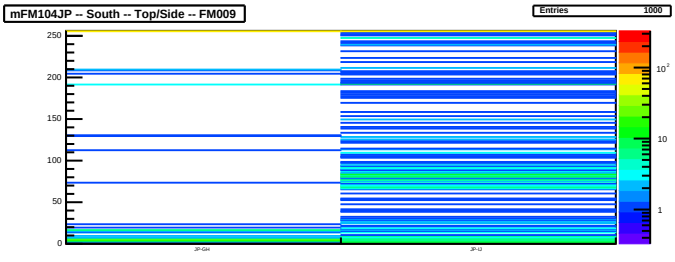

mFM103JPsim -- South -- Bottom -- FM008
Entries 878

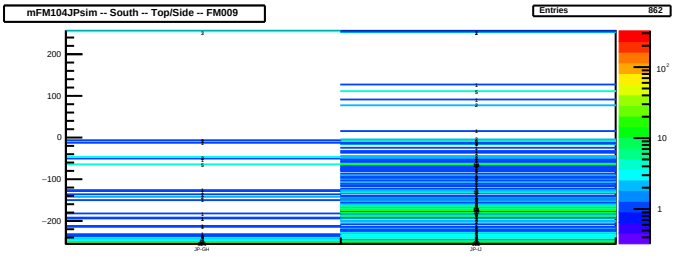

mFM104BS -- North -- Top/Side -- FM009
Entries 2000

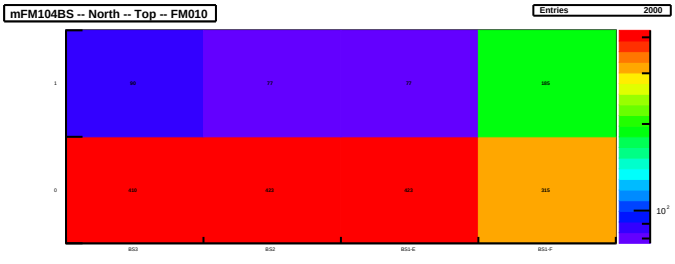

mFM104BSsim -- North -- Top/Side -- FM009
Entries 1397

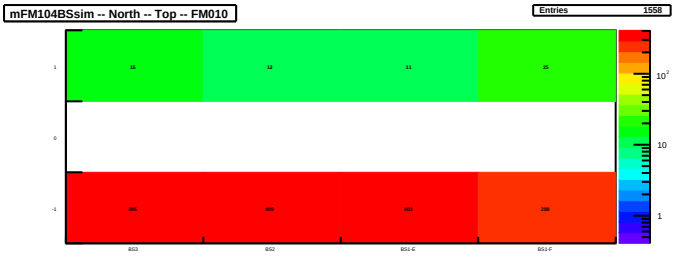

mFM104J -- South -- Top/Side -- FM009
Entries 500
J(23)

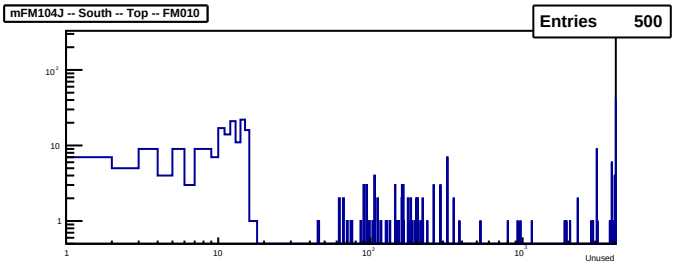

mFM104Jsim -- South -- Top/Side -- FM009
Entries 412
J(23)

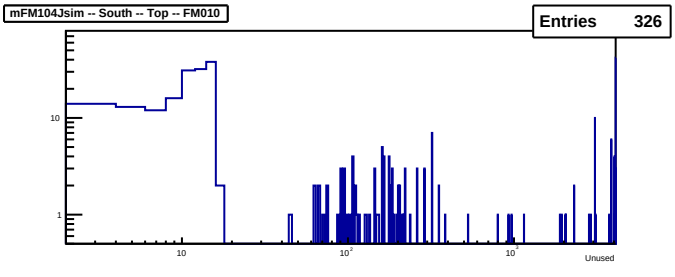

mFM104JP -- South -- Top/Side -- FM009
Entries 1000

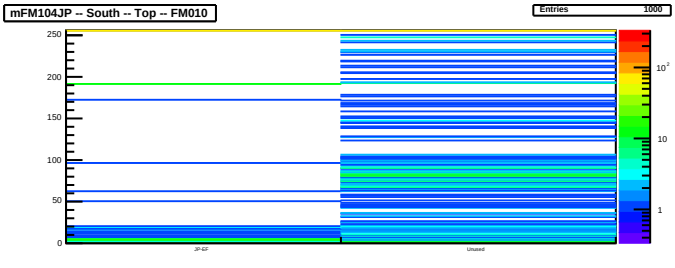

mFM104JPsim -- South -- Top/Side -- FM009
Entries 862

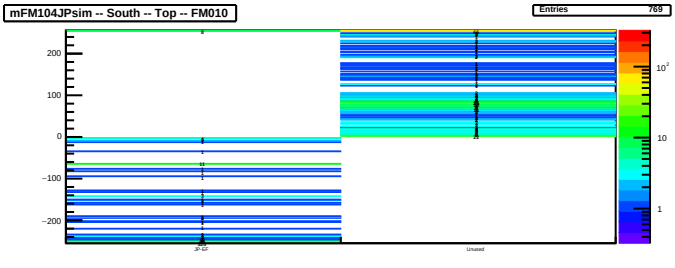

mFM104BS -- North -- Top -- FM010
Entries 2000

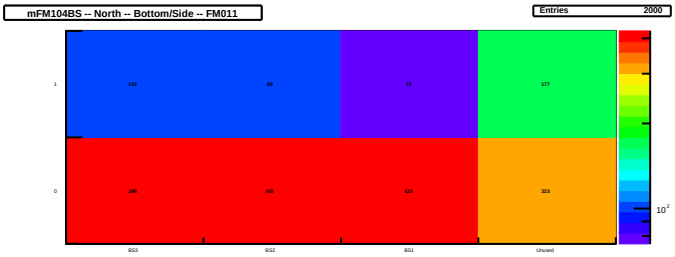

mFM104BSsim -- North -- Top -- FM010
Entries 1558

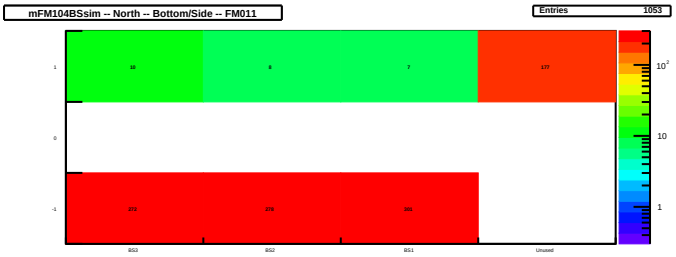

mFM104J -- South -- Top -- FM010
Entries 500
Unused

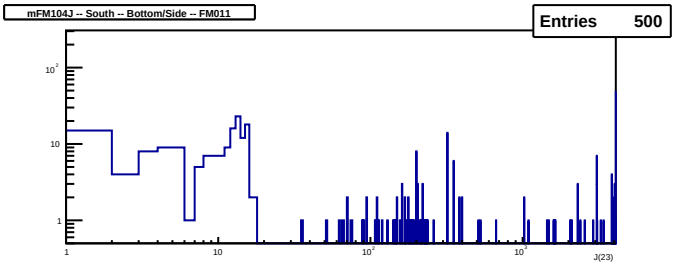

mFM104Jsim -- South -- Top -- FM010
Entries 326
Unused

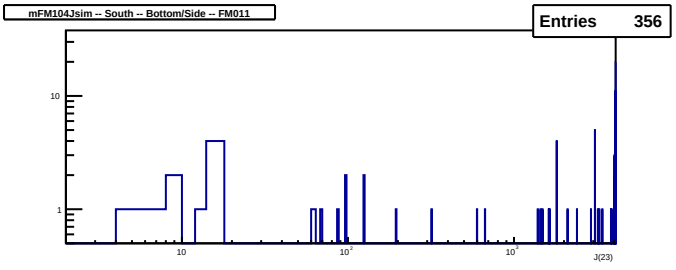

mFM104JP -- South -- Top -- FM010
Entries 1000

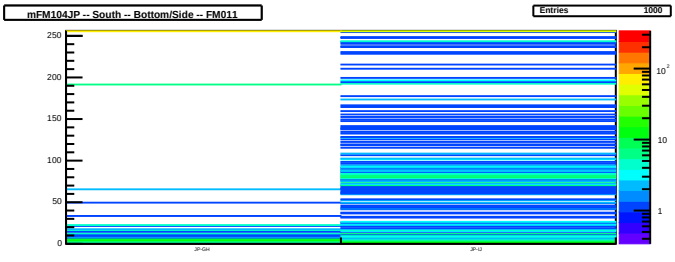

mFM104JPsim -- South -- Top -- FM010
Entries 769

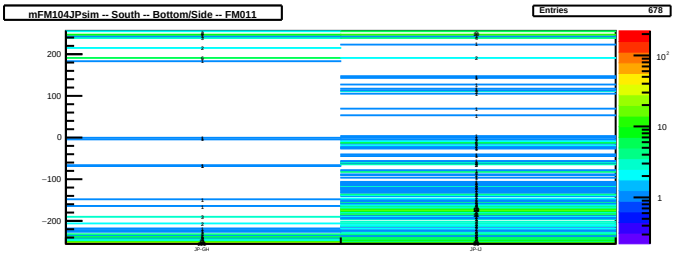

mFM104BS -- North -- Bottom/Side -- FM011
Entries 2000

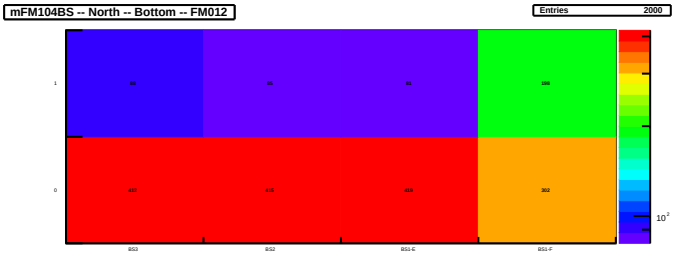

mFM104BSsim -- North -- Bottom/Side -- FM011
Entries 1053

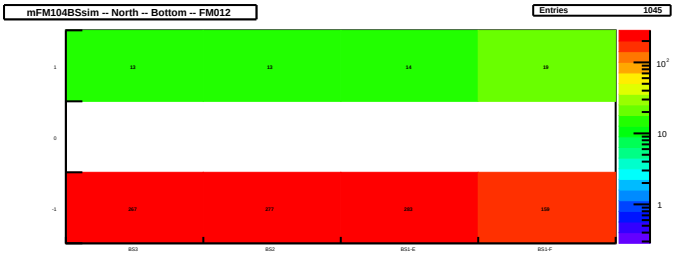

mFM104J -- South -- Bottom/Side -- FM011
Entries 500
J(23)

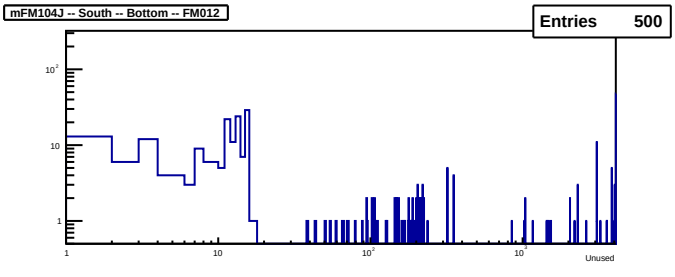

mFM104Jsim -- South -- Bottom/Side -- FM011
Entries 356
J(23)

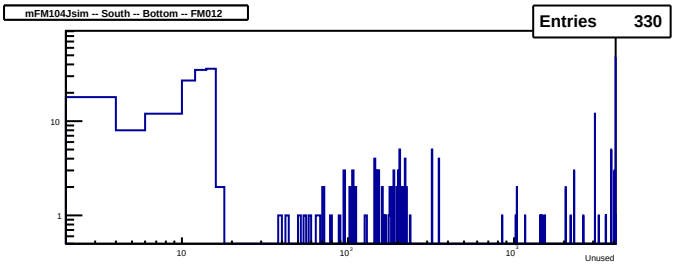

mFM104JP -- South -- Bottom/Side -- FM011
Entries 1000

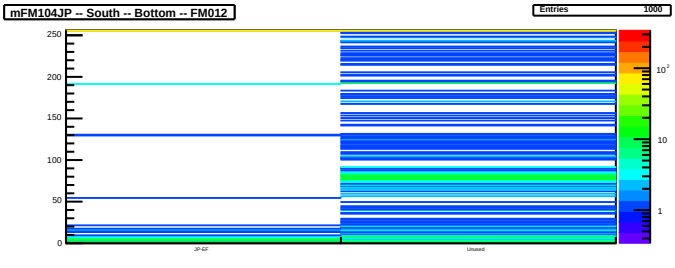

mFM104JPsim -- South -- Bottom/Side -- FM011
Entries 678

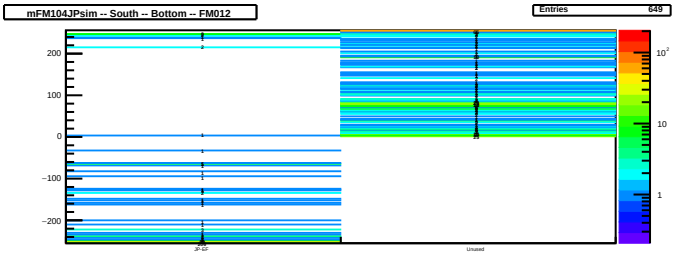

mFM104BS -- North -- Bottom -- FM012
Entries 2000

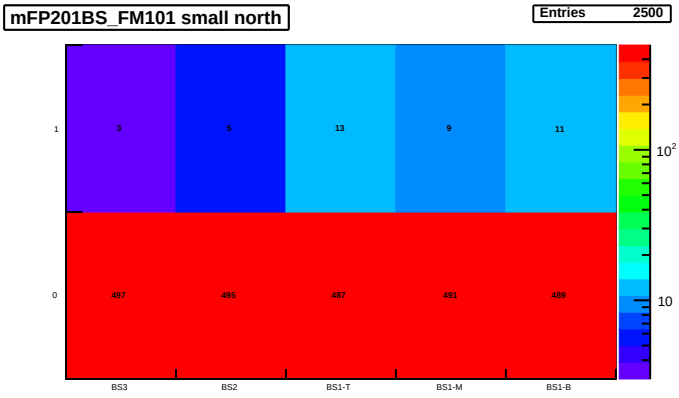

mFM104BSsim -- North -- Bottom -- FM012
Entries 1045

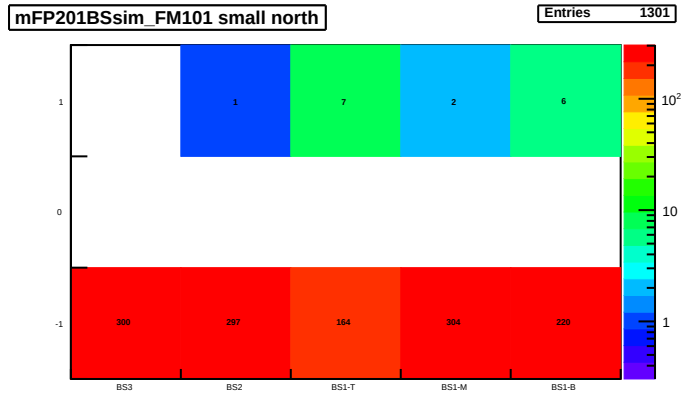

mFM104J -- South -- Bottom -- FM012
Entries 500
Unused

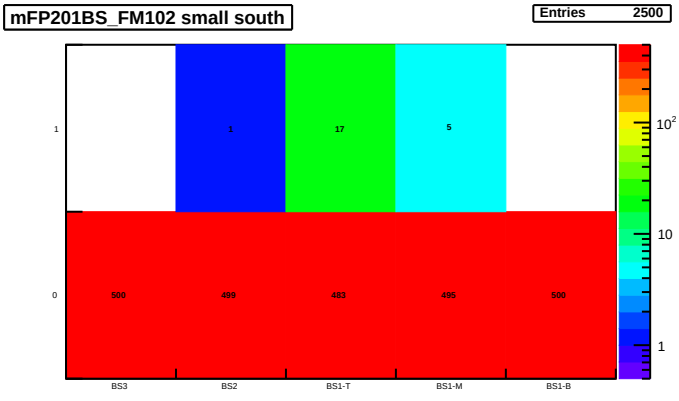

mFM104Jsim -- South -- Bottom -- FM012
Entries 330
Unused

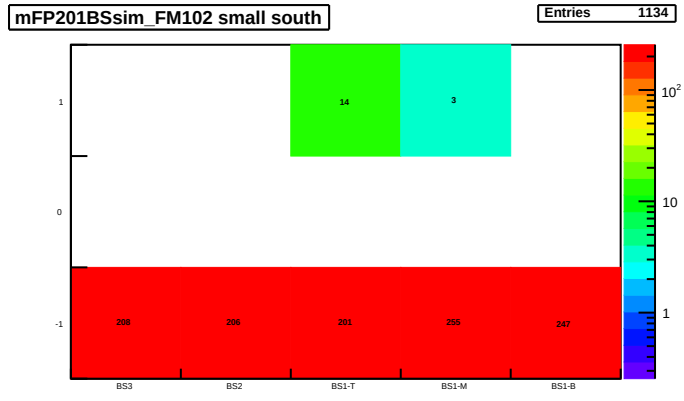

mFM104JP -- South -- Bottom -- FM012
Entries 1000

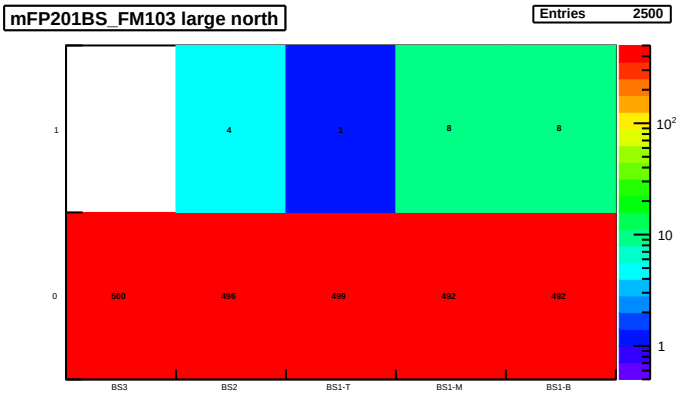

mFM104JPsim -- South -- Bottom -- FM012
Entries 649

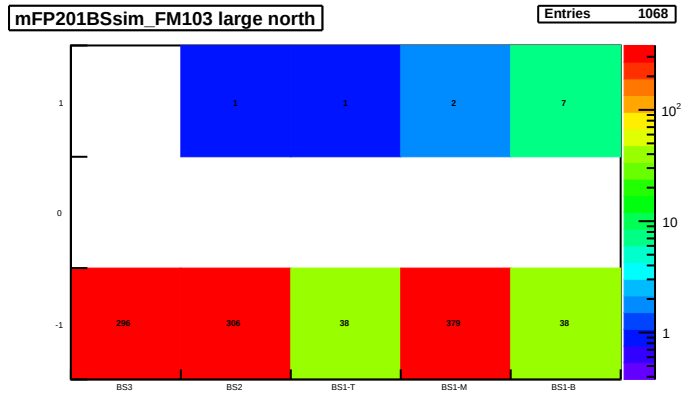

mFP201BS_FM101 small north
Entries 2500
1
0
3
5
13
9
11
497
495
487
491
489
BS3
BS2
BS1-T
BS1-M
BS1-B
10^2
10

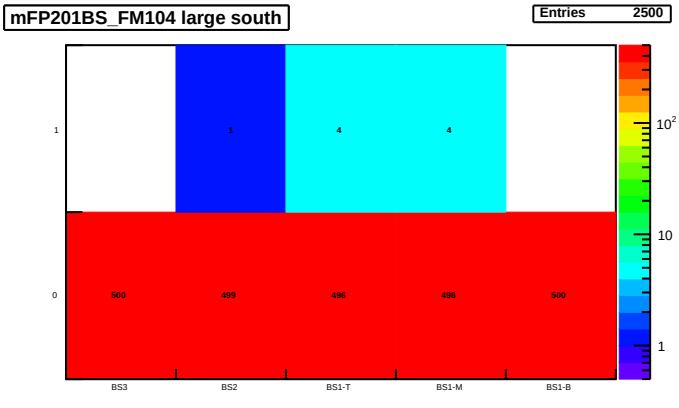

mFP201BSsim_FM101 small north
Entries 1301
1
0
-1
1
7
2
6
300
297
164
304
220
BS3
BS2
BS1-T
BS1-M
BS1-B
10^2
10
1

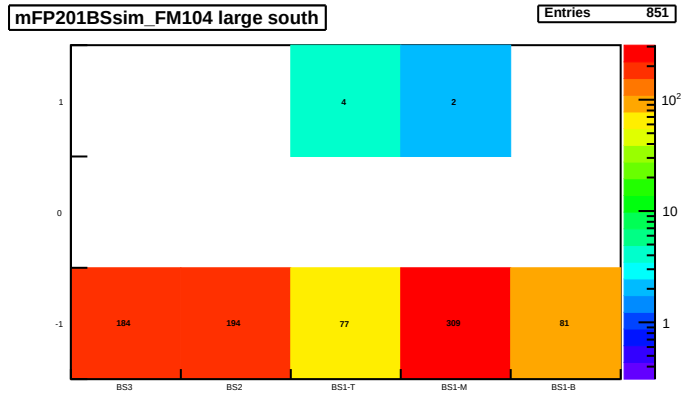

mFP201BS_FM102 small south
Entries 2500
1
0
1
17
5
500
499
483
495
500
BS3
BS2
BS1-T
BS1-M
BS1-B
10^2
10
1

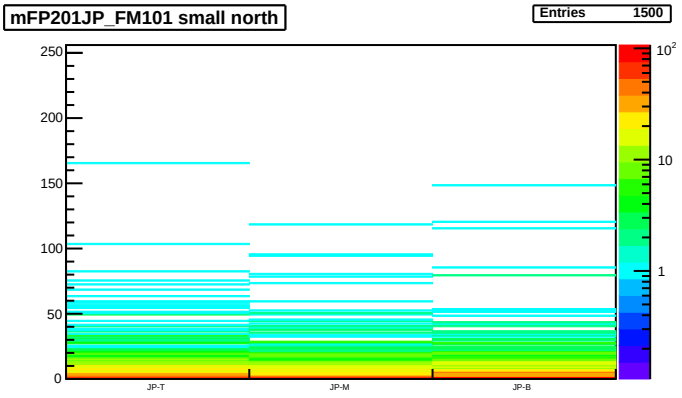

mFP201BSsim_FM102 small south
Entries 1134
1
0
-1
14
3
208
206
201
255
247
BS3
BS2
BS1-T
BS1-M
BS1-B
10^2
10
1

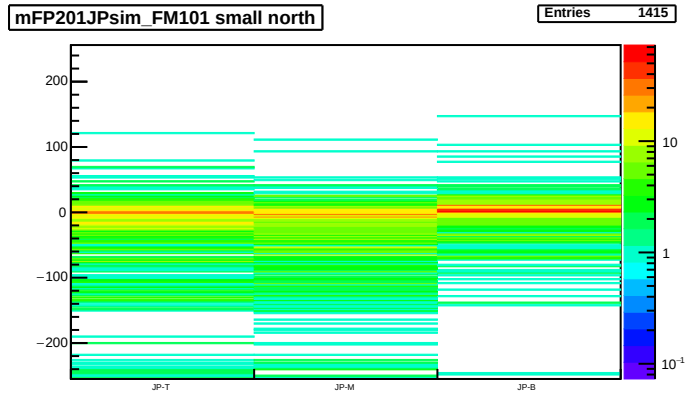

mFP201BS_FM103 large north
Entries 2500
1
0
4
1
8
8
500
496
499
492
492
BS3
BS2
BS1-T
BS1-M
BS1-B
10^2
10
1

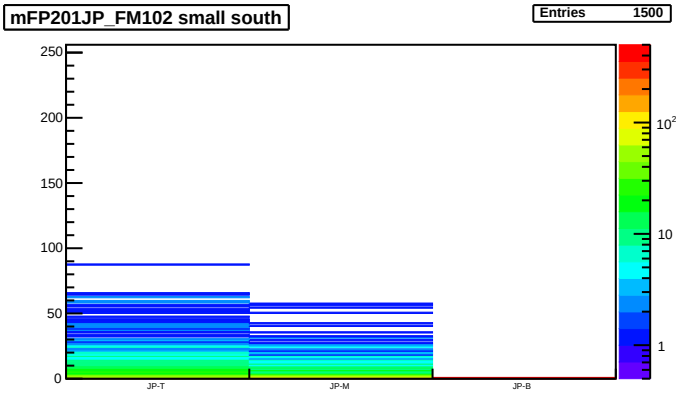

mFP201BSsim_FM103 large north
Entries 1068
1
0
-1
1
1
2
7
296
306
38
379
38
BS3
BS2
BS1-T
BS1-M
BS1-B
10^2
10
1

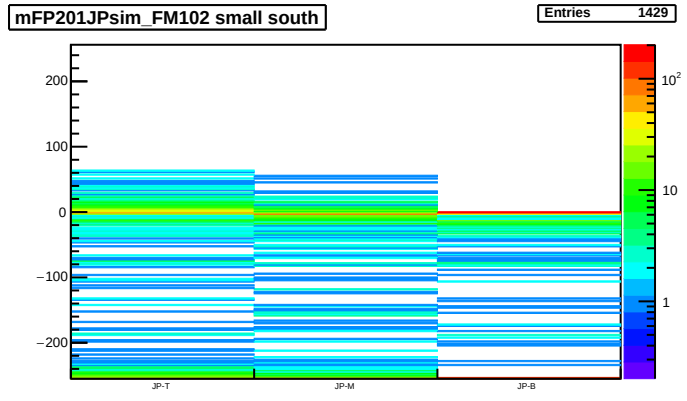

mFP201BS_FM104 large south
Entries 2500
1
0
1
4
4
500
499
496
496
500
BS3
BS2
BS1-T
BS1-M
BS1-B
10^2
10
1

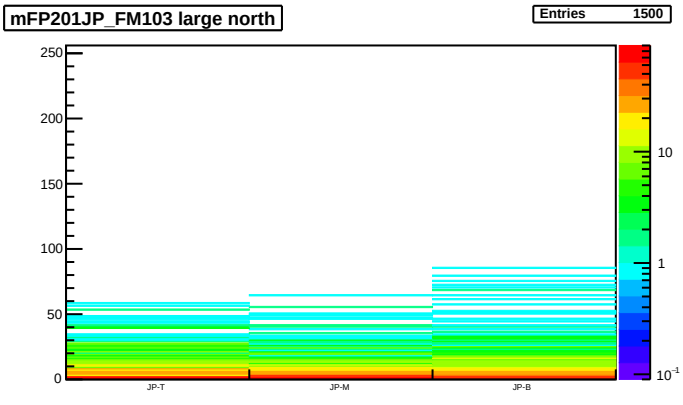

mFP201BSsim_FM104 large south
Entries 851
1
0
-1
4
2
184
194
77
309
81
BS3
BS2
BS1-T
BS1-M
BS1-B
10^2
10
1

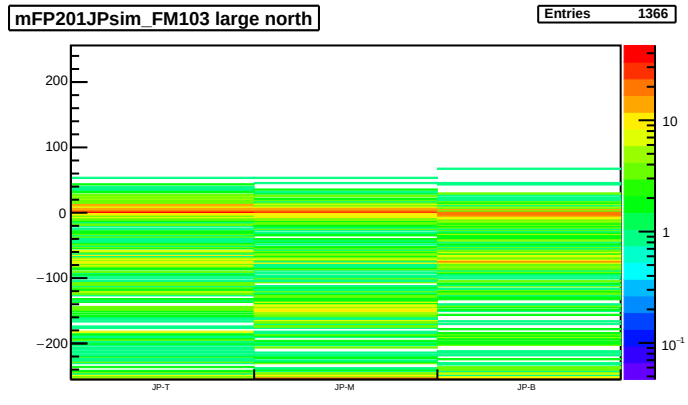

mFP201JP_FM101 small north
Entries 1500
JP-T
JP-M
JP-B

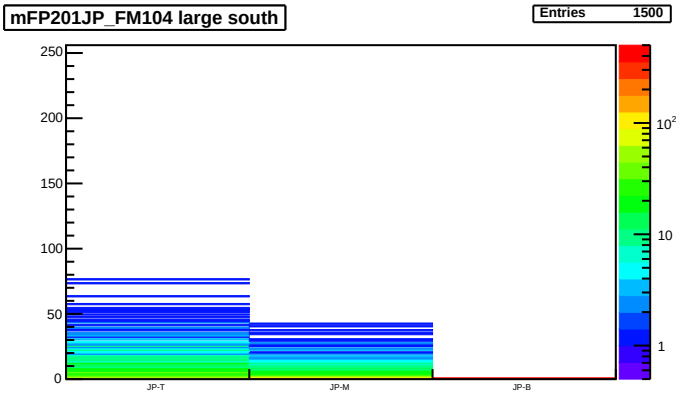

mFP201JPsim_FM101 small north
Entries 1415
JP-T
JP-M
JP-B

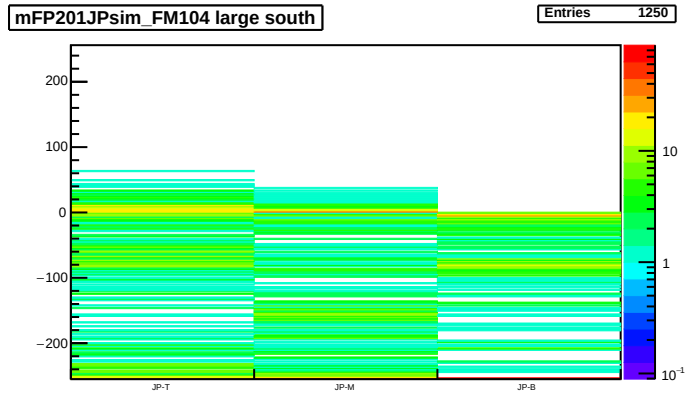

mFP201JP_FM102 small south
Entries 1500
JP-T
JP-M
JP-B

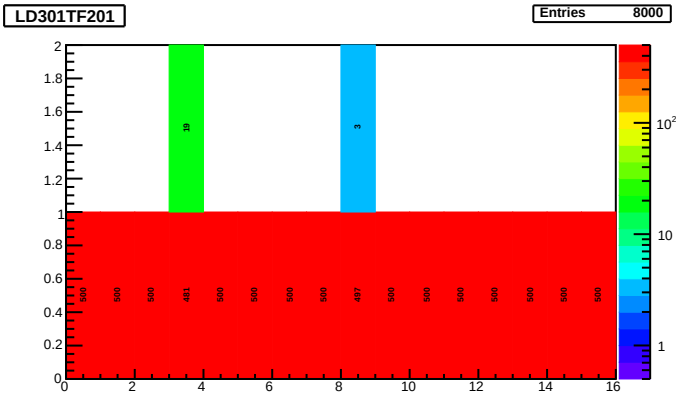

mFP201JPsim_FM102 small south
Entries 1429
JP-T
JP-M
JP-B

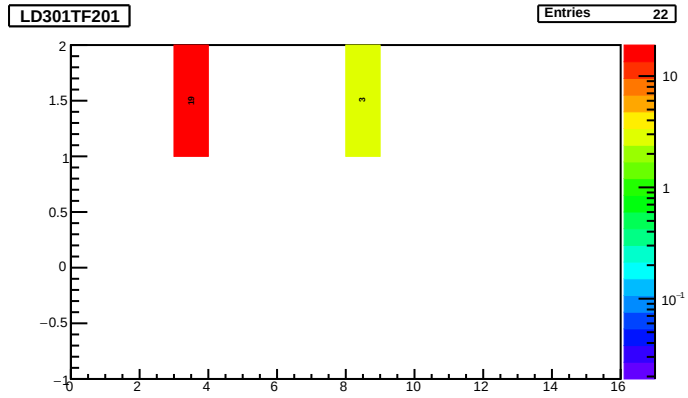

mFP201JP_FM103 large north
Entries 1500
JP-T
JP-M
JP-B

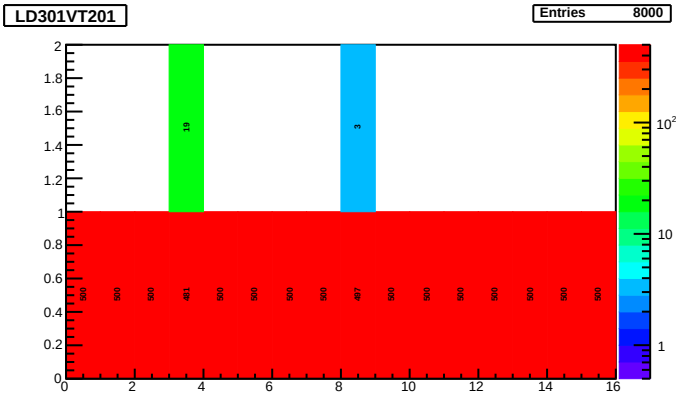

mFP201JPsim_FM103 large north
Entries 1366
JP-T
JP-M
JP-B

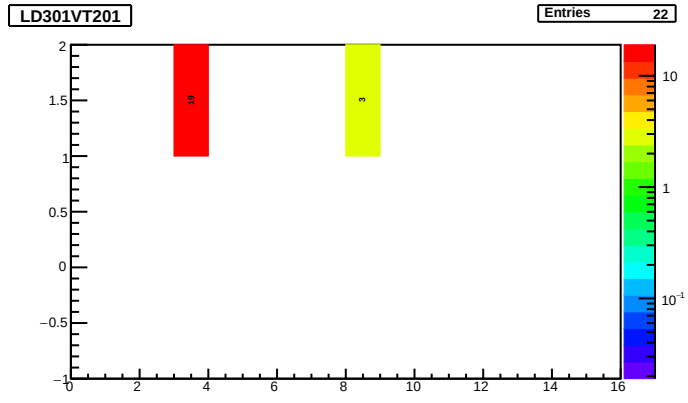

mFP201JP_FM104 large south
Entries 1500
JP-T
JP-M
JP-B

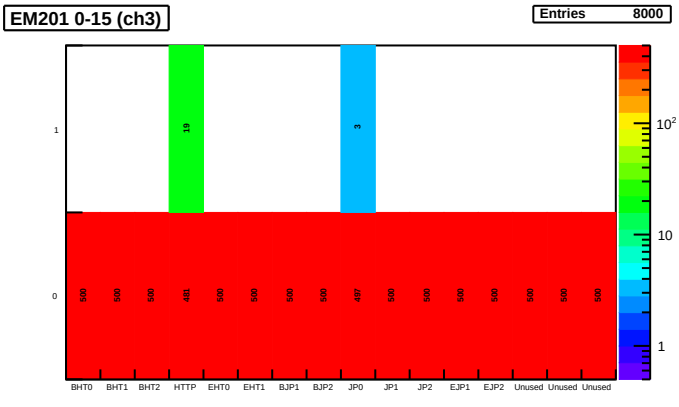

mFP201JPsim_FM104 large south
Entries 1250
JP-T
JP-M
JP-B

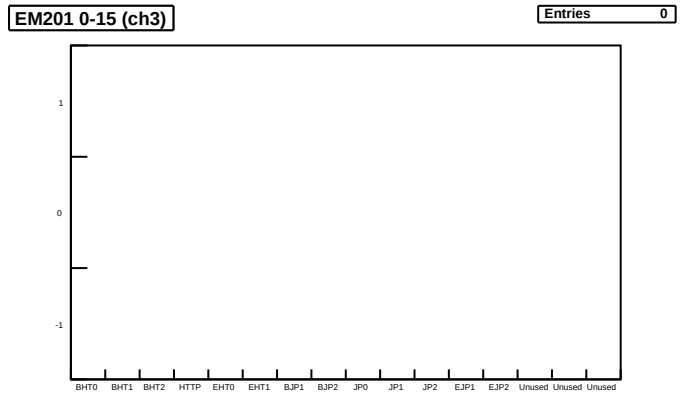

LD301TF201
Entries 8000

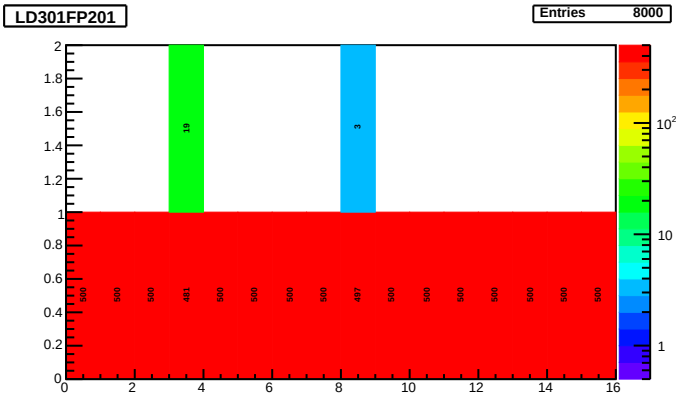

LD301TF201
Entries 22

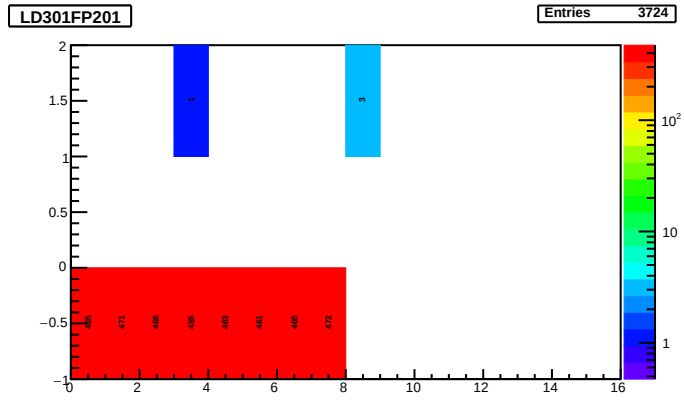

LD301VT201
Entries 8000


LD301VT201
Entries 22


EM201 0-15 (ch3)
Entries 8000
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


LD301FP201
Entries 8000


LD301FP201
Entries 3724