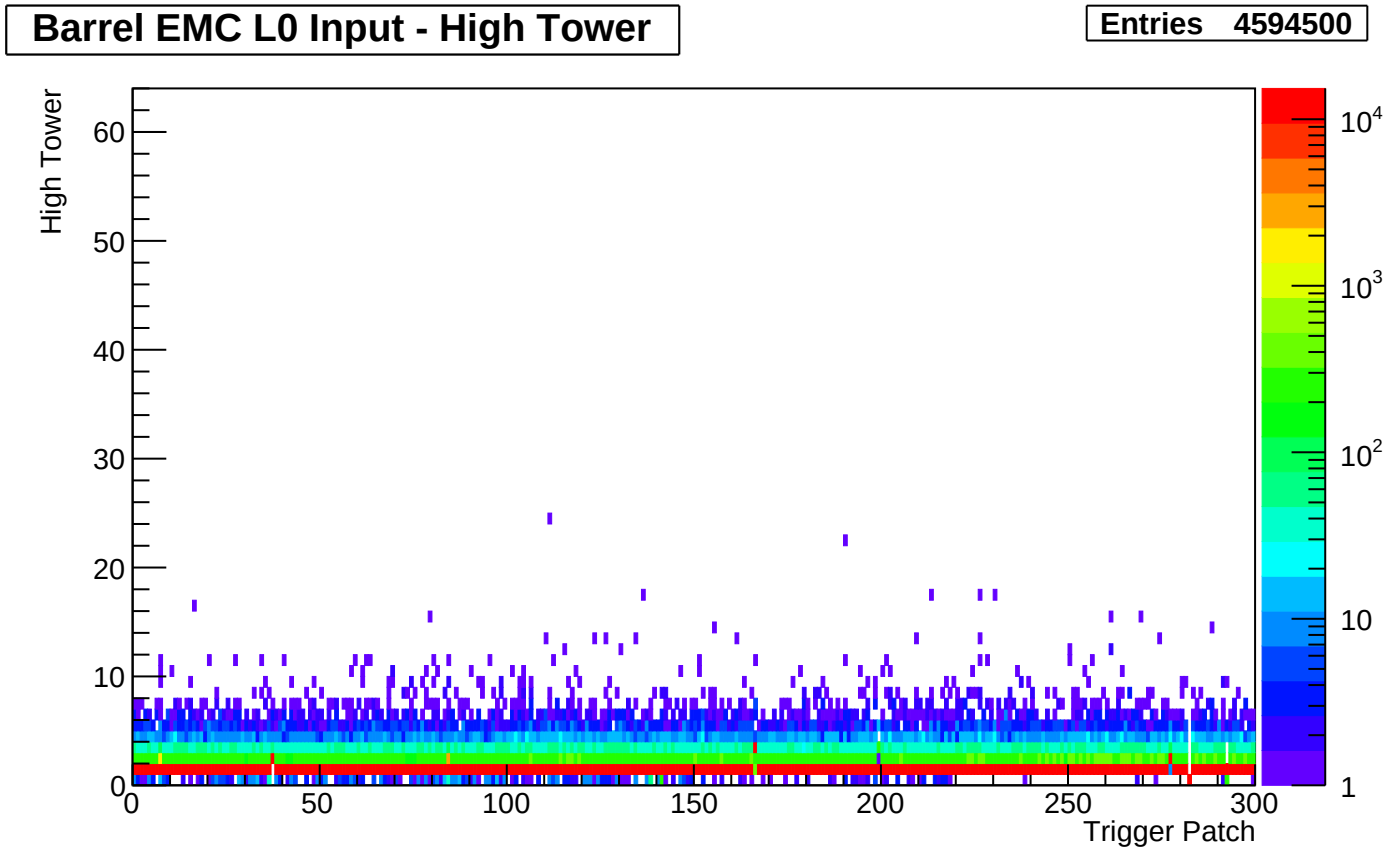
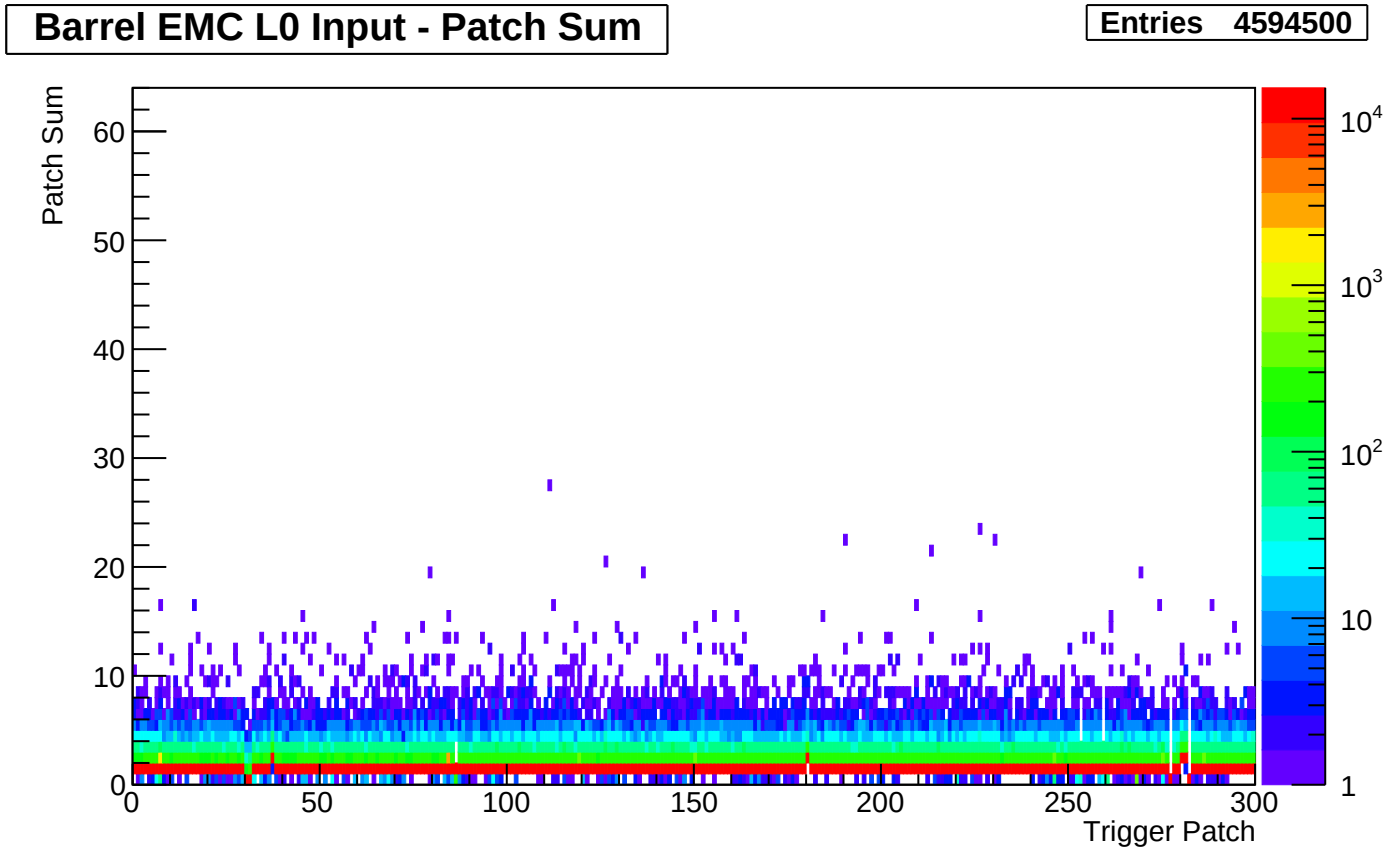
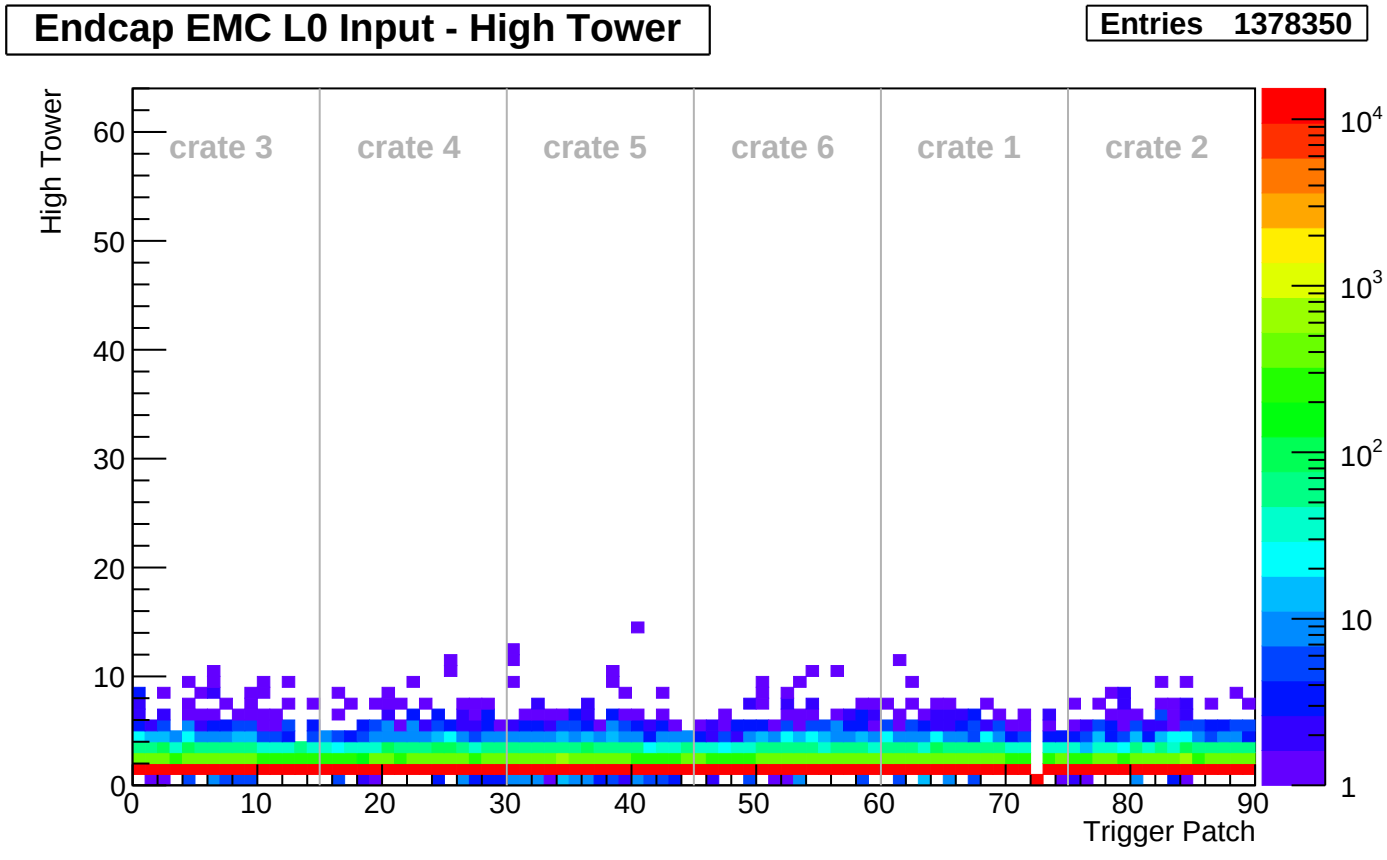
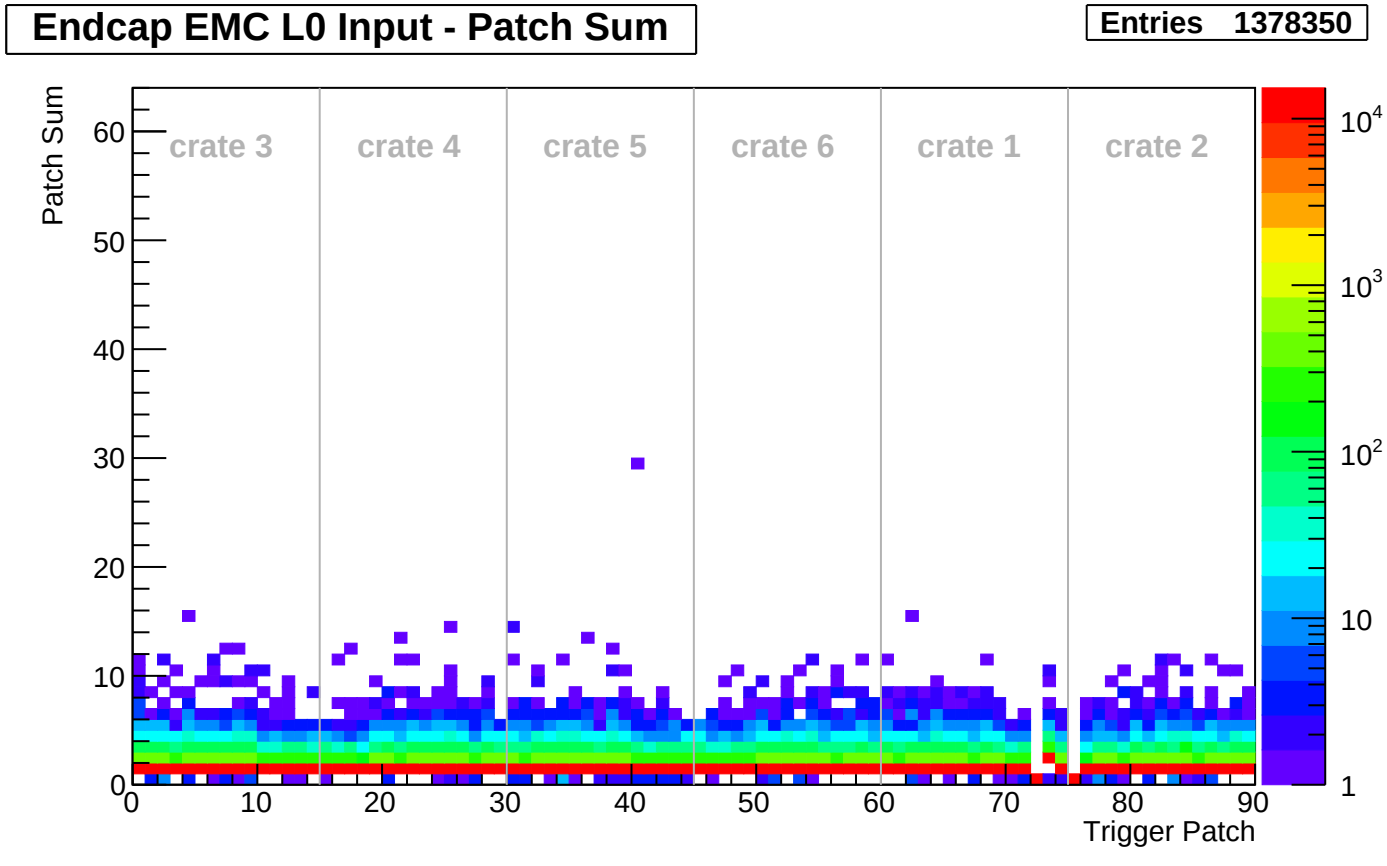
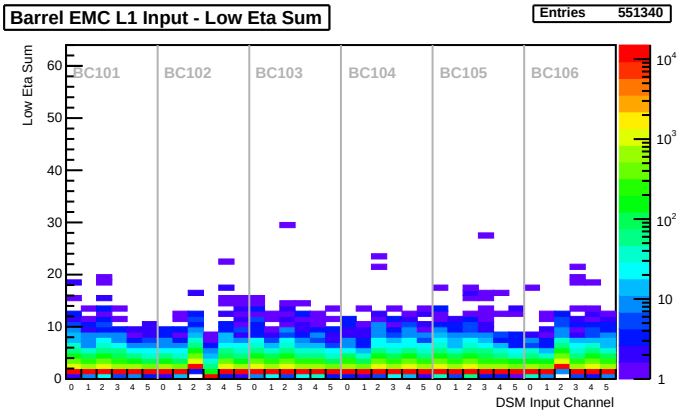
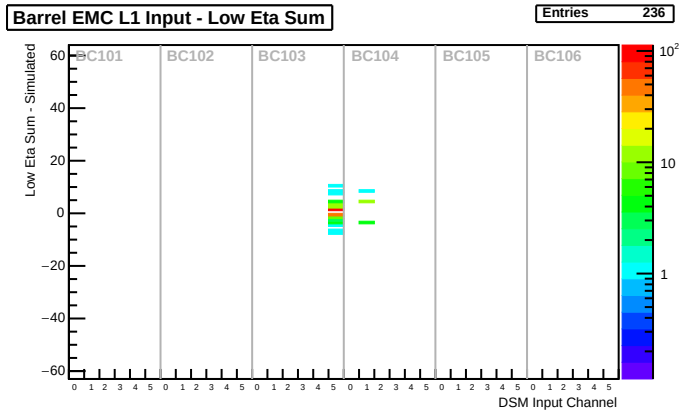

Barrel EMC L0 Input - High Tower
Entries 4594500
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

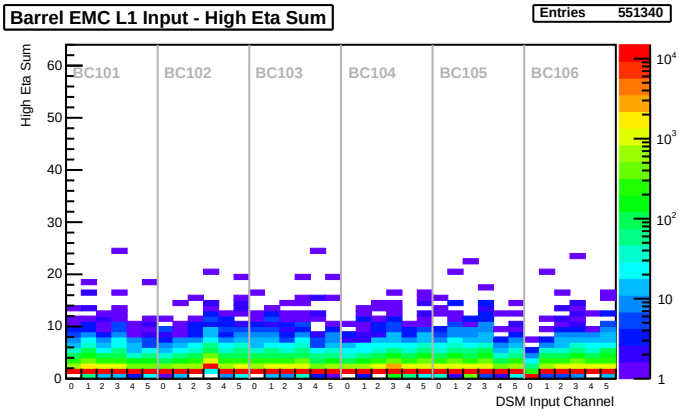

Barrel EMC L0 Input - Patch Sum
Entries 4594500
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

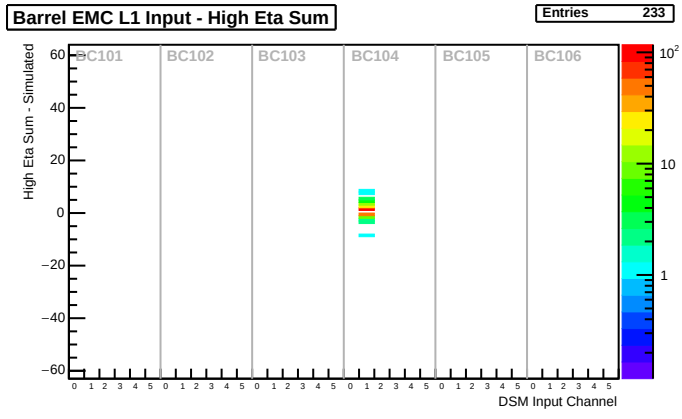

Endcap EMC L0 Input - High Tower
Entries 1378350
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

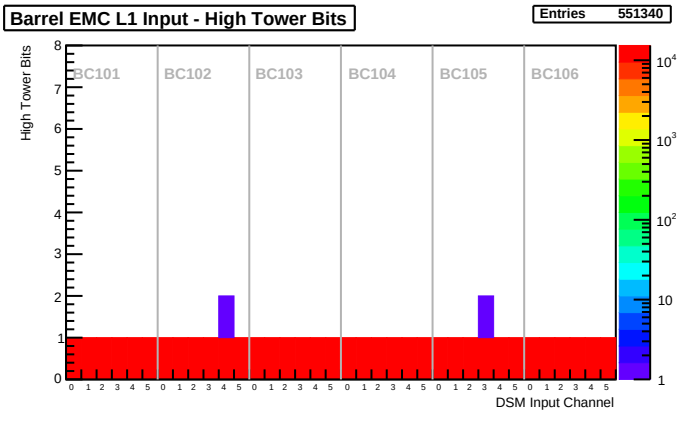

Endcap EMC L0 Input - Patch Sum
Entries 1378350
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

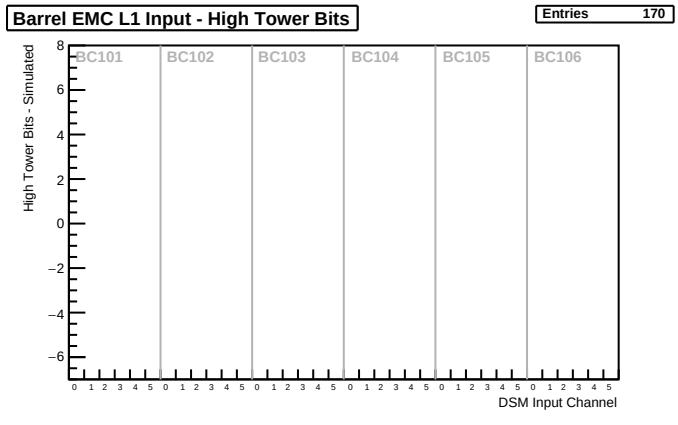

Barrel EMC L1 Input - Low Eta Sum
Entries 551340
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

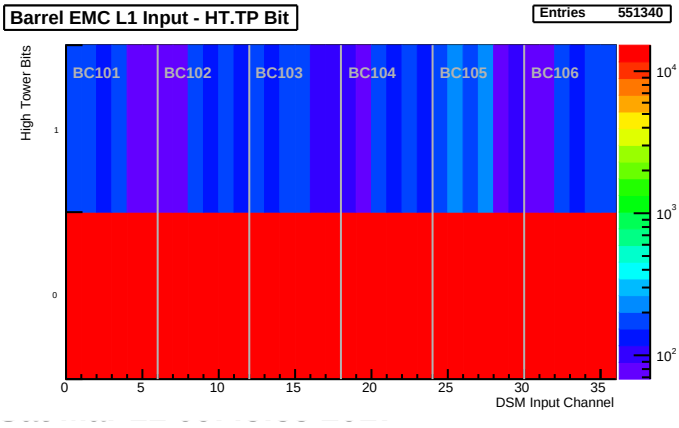

Barrel EMC L1 Input - Low Eta Sum
Entries 236
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

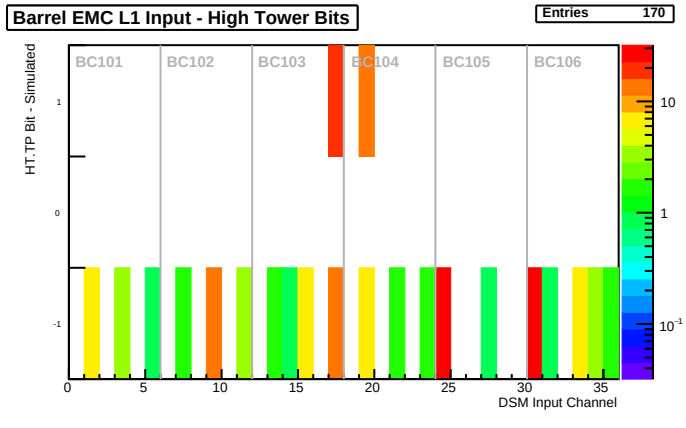

Barrel EMC L1 Input - High Eta Sum
Entries 551340
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

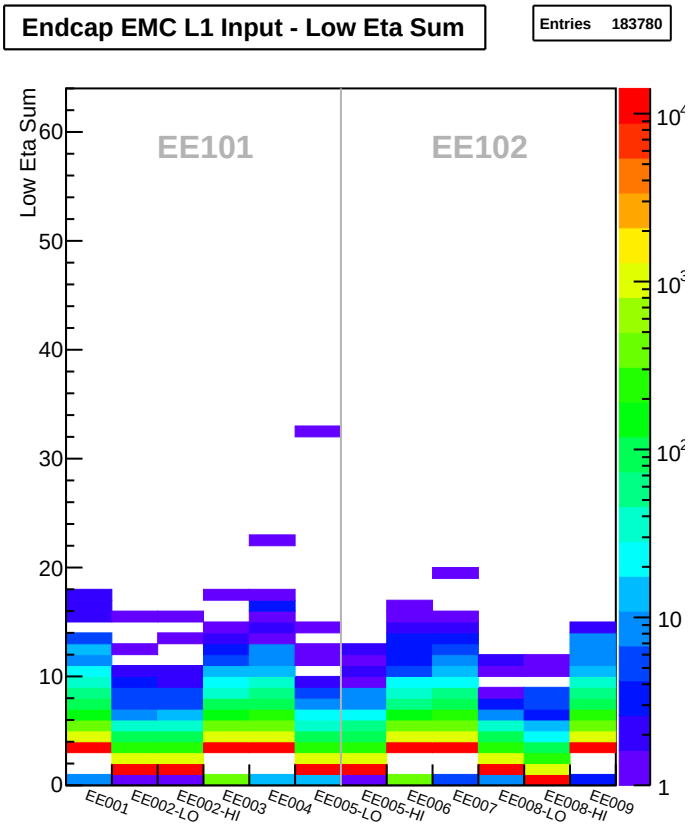

Barrel EMC L1 Input - High Eta Sum
Entries 233
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

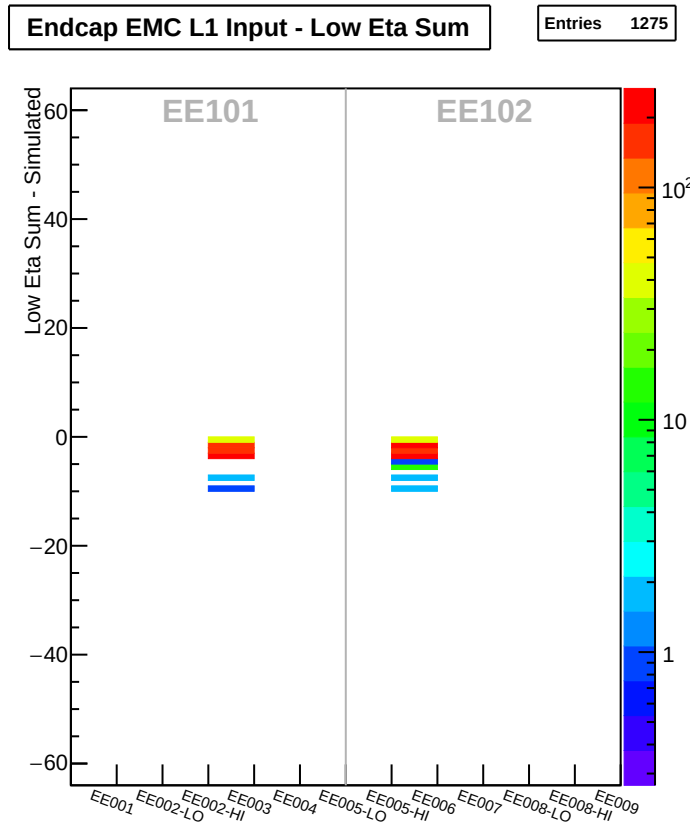

Barrel EMC L1 Input - High Tower Bits
Entries 551340
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

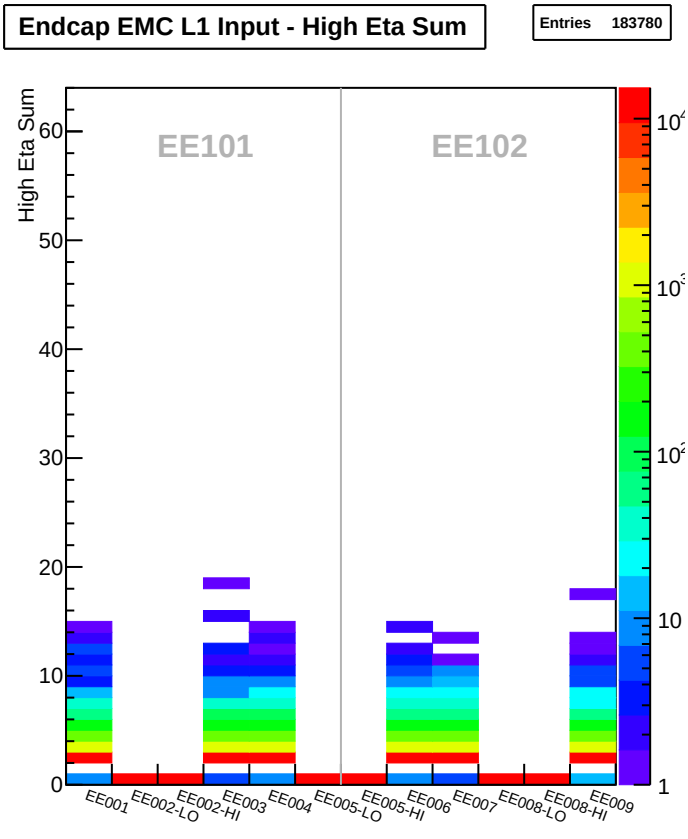

Barrel EMC L1 Input - High Tower Bits
Entries 170
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

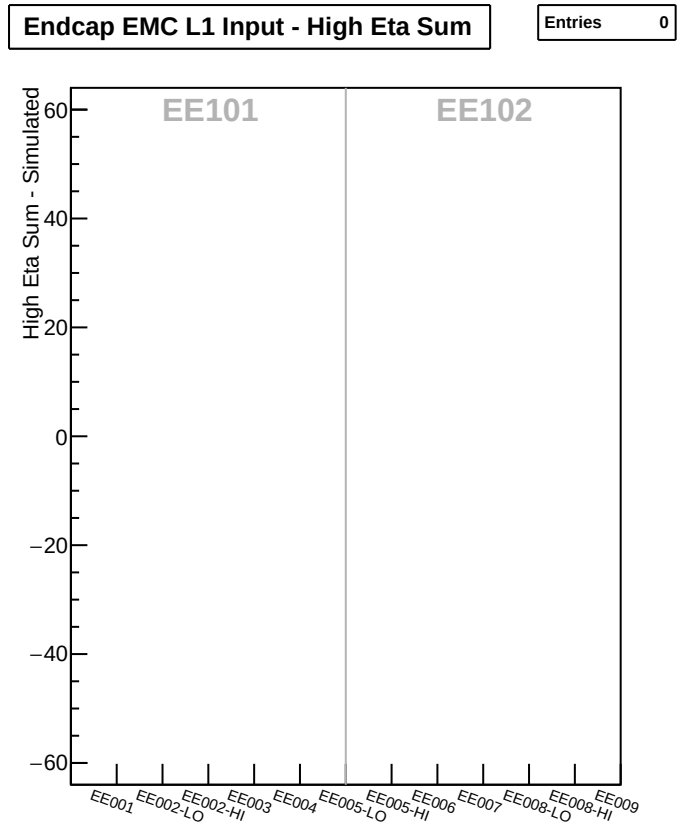

Barrel EMC L1 Input - HT.TP Bit
Entries 551340
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

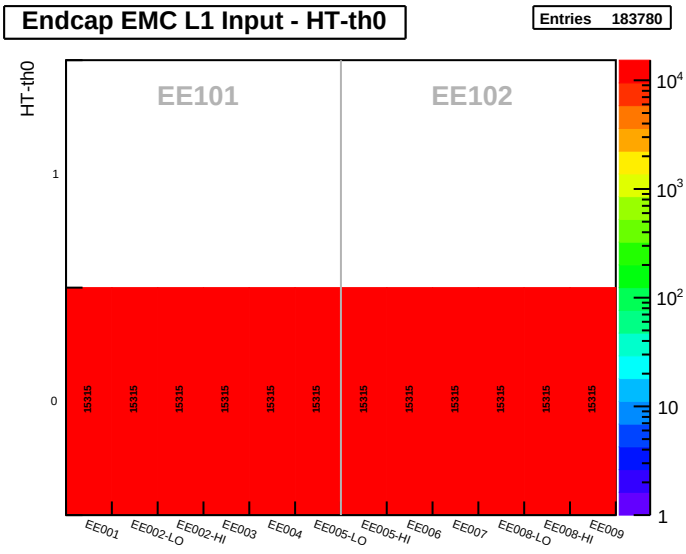

Barrel EMC L1 Input - High Tower Bits
Entries 170
HT.TP Bit - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

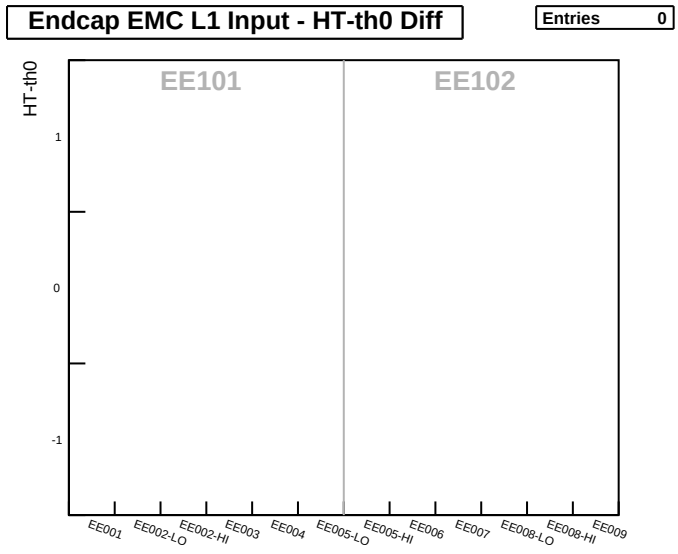

Endcap EMC L1 Input - Low Eta Sum
Entries 183780
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

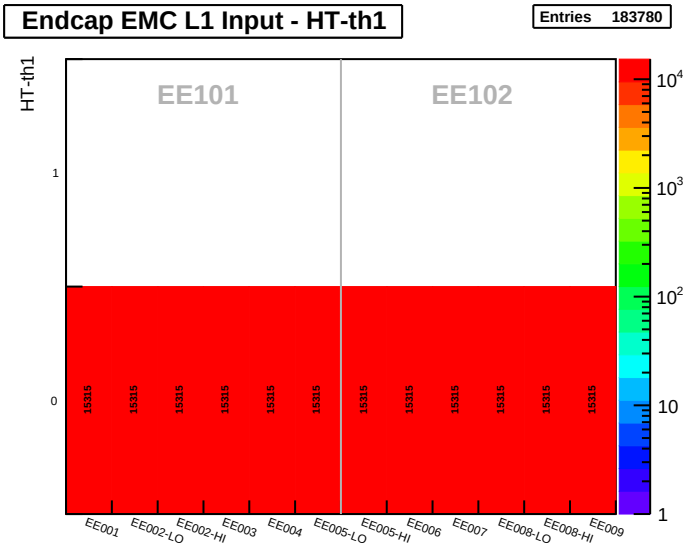

Endcap EMC L1 Input - Low Eta Sum
Entries 1275
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

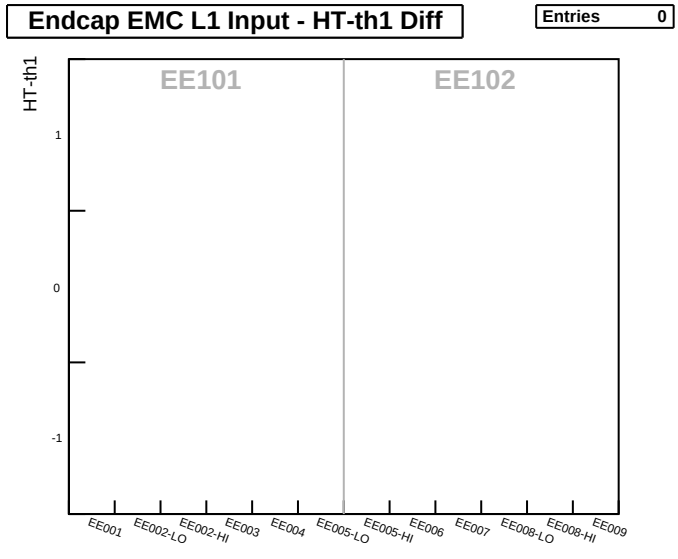

Endcap EMC L1 Input - High Eta Sum
Entries 183780
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

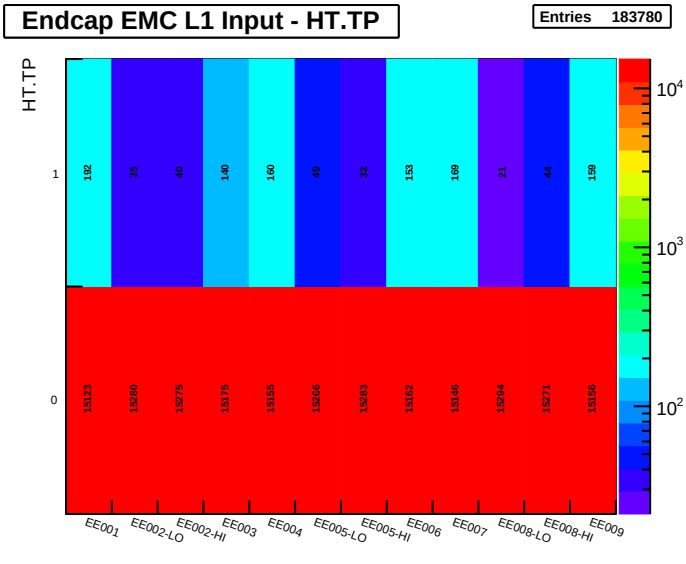

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

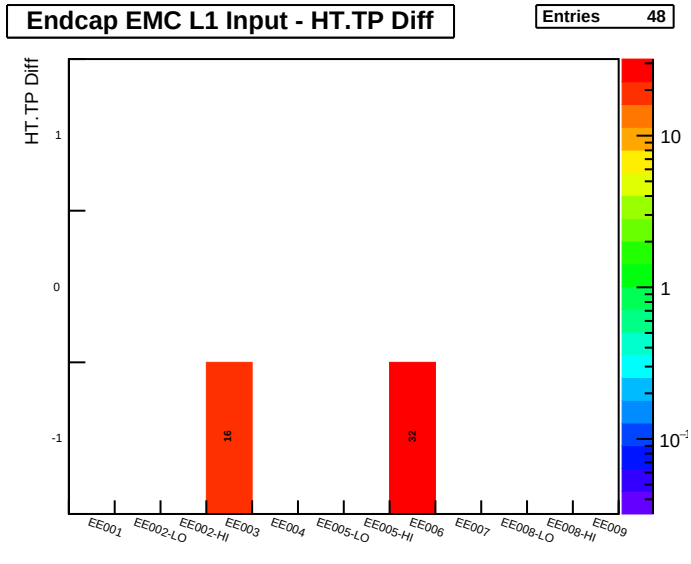

Endcap EMC L1 Input - HT-th0
Entries 183780
HT-th0
EE101
EE102
15315
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

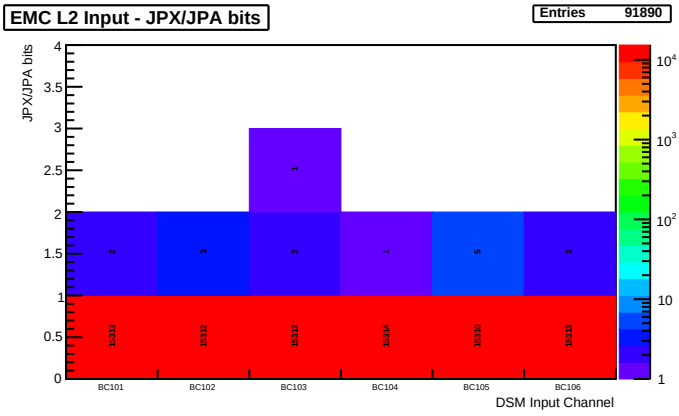

Endcap EMC L1 Input - HT-th0 Diff
Entries 0
HT-th0
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

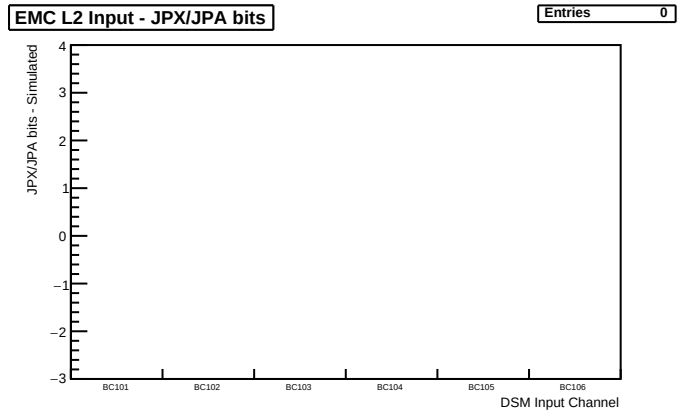

Endcap EMC L1 Input - HT-th1
Entries 183780
HT-th1
EE101
EE102
15315
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

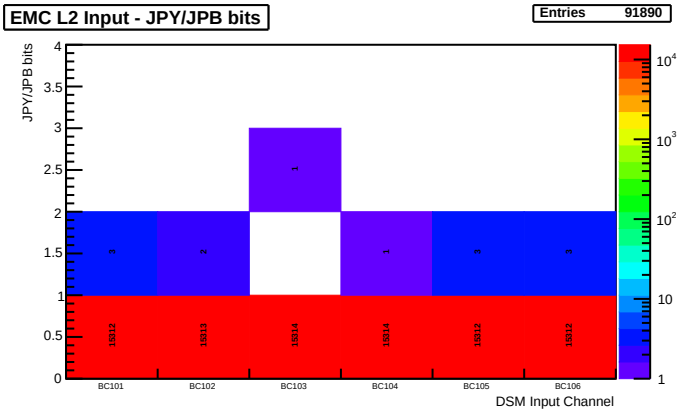

Endcap EMC L1 Input - HT-th1 Diff
Entries 0
HT-th1
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

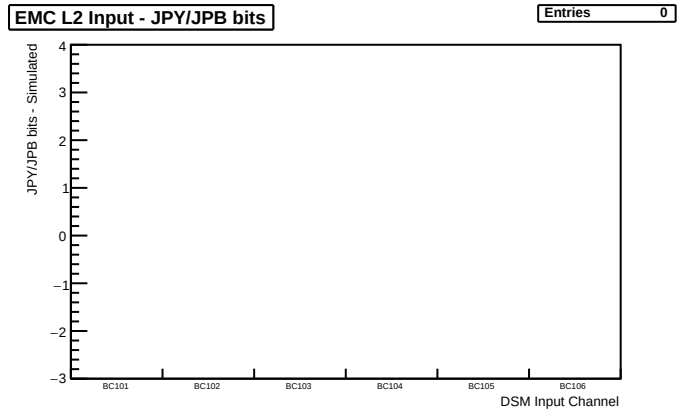

Endcap EMC L1 Input - HT.TP
Entries 183780
HT.TP
192 35 40 140 160 49 32 153 169 21 44 159
15123 15280 15275 15175 15155 15266 15283 15162 15146 15294 15271 15156
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

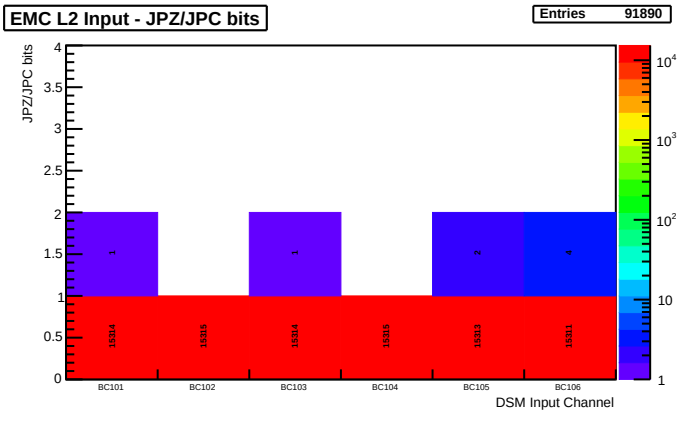

Endcap EMC L1 Input - HT.TP Diff
Entries 48
HT.TP Diff
16
32
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

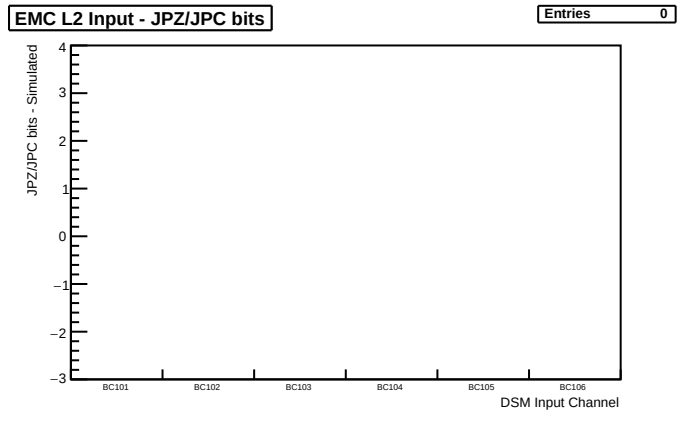

EMC L2 Input - JPX/JPA bits
Entries 91890
JPX/JPA bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

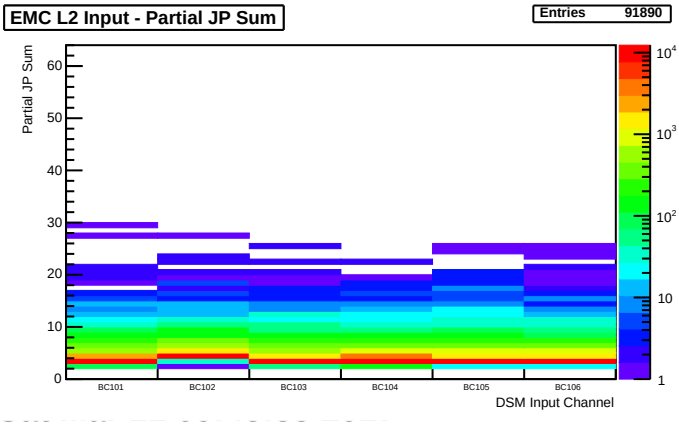

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

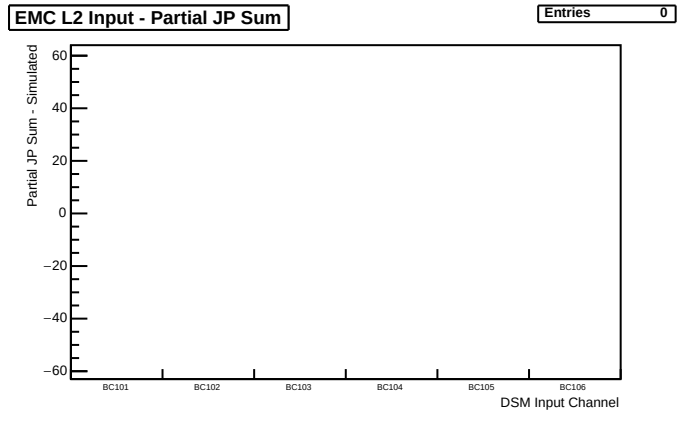

EMC L2 Input - JPY/JPB bits
Entries 91890
JPY/JPB bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

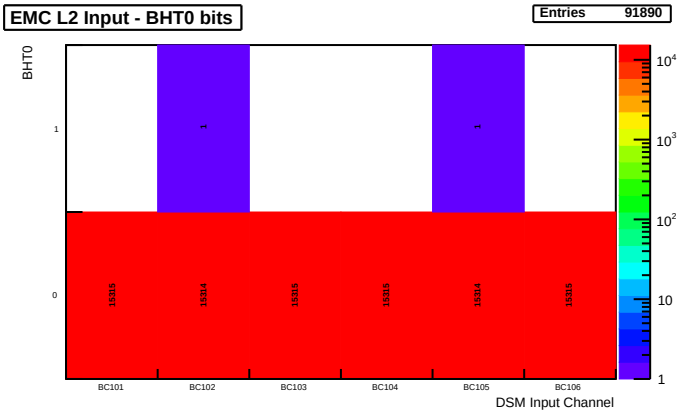

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

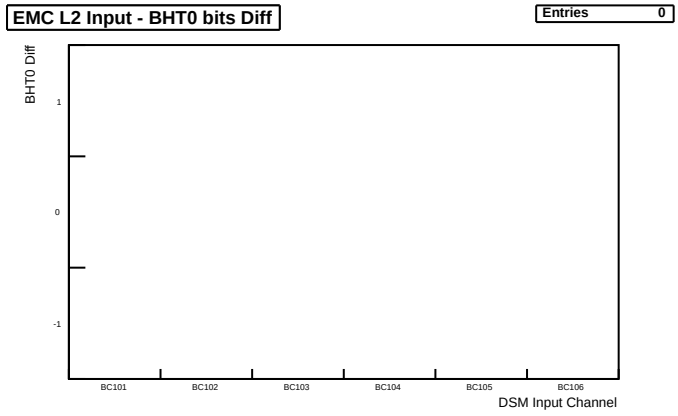

EMC L2 Input - JPZ/JPC bits
Entries 91890
JPZ/JPC bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

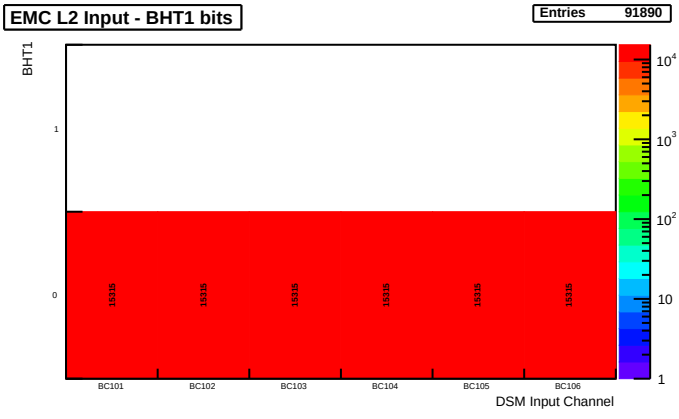

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

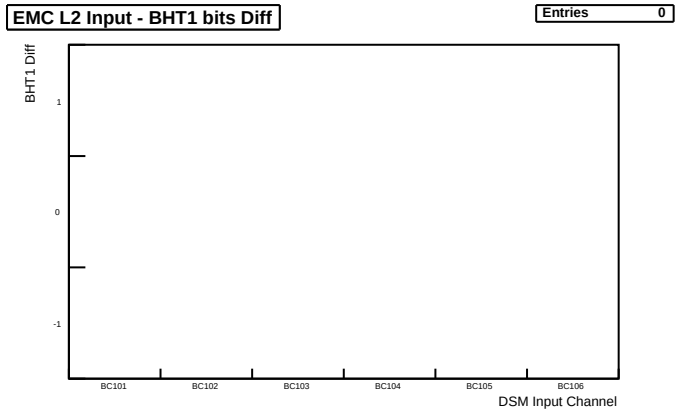

EMC L2 Input - Partial JP Sum
Entries 91890
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

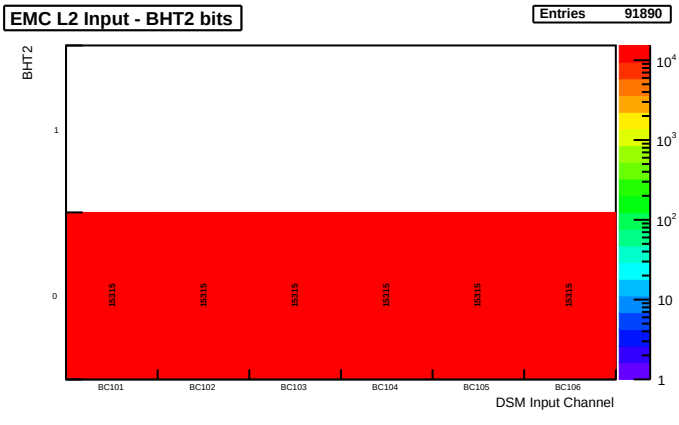

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

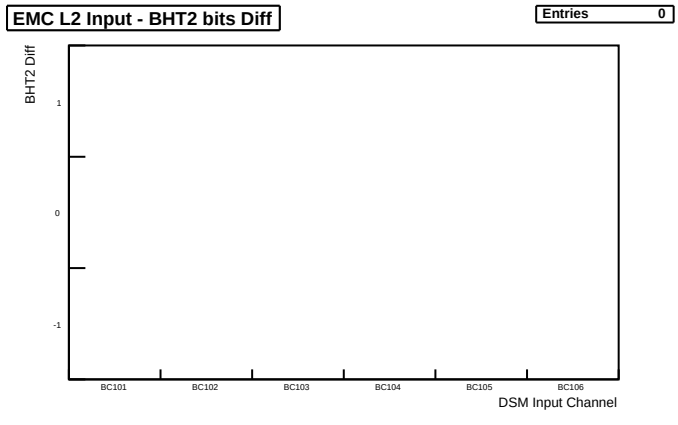

EMC L2 Input - BHT0 bits
Entries 91890
BHT0
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

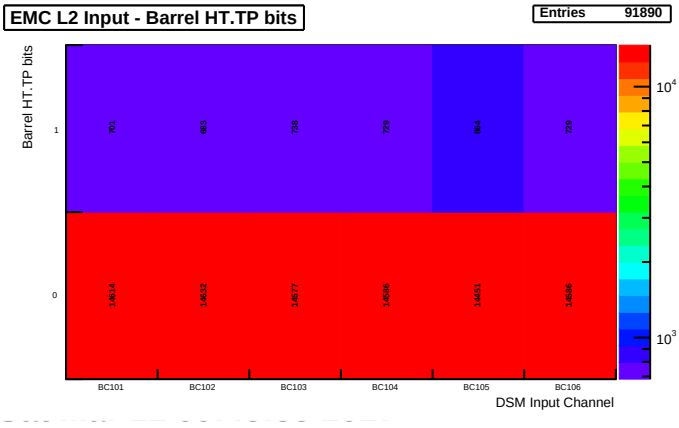

EMC L2 Input - BHT0 bits Diff
Entries 0
BHT0 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

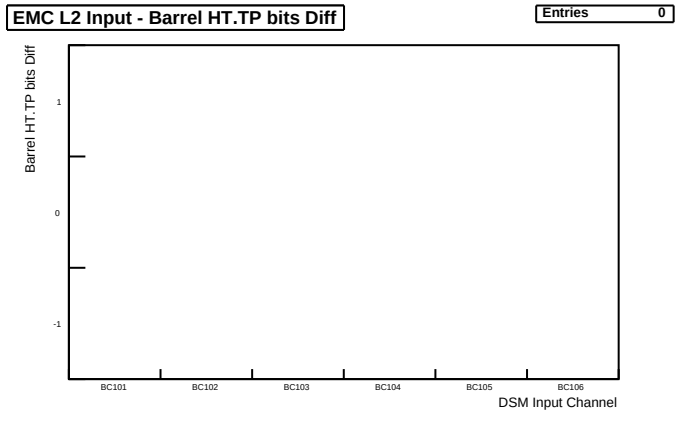

EMC L2 Input - BHT1 bits
Entries 91890
BHT1
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

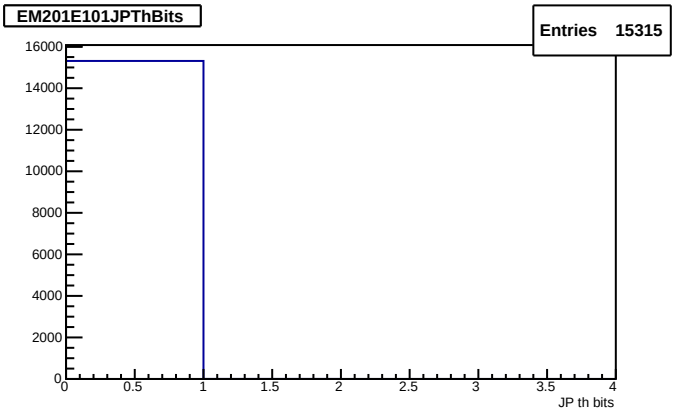

EMC L2 Input - BHT1 bits Diff
Entries 0
BHT1 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

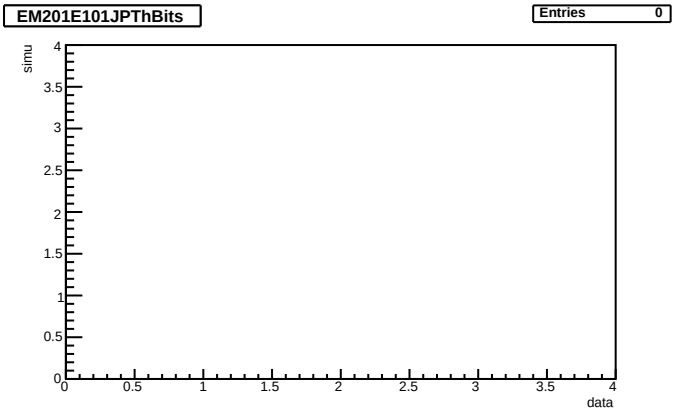

EMC L2 Input - BHT2 bits
Entries 91890
BHT2
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

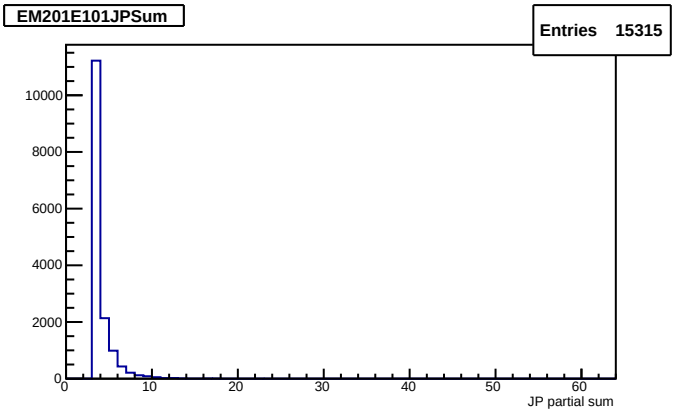

EMC L2 Input - BHT2 bits Diff
Entries 0
BHT2 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

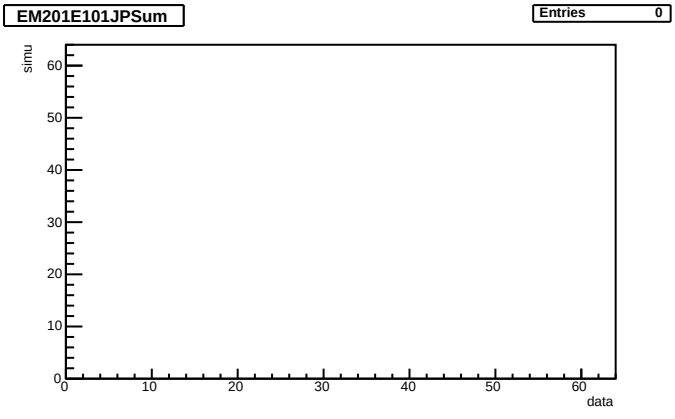

EMC L2 Input - Barrel HT.TP bits
Entries 91890
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

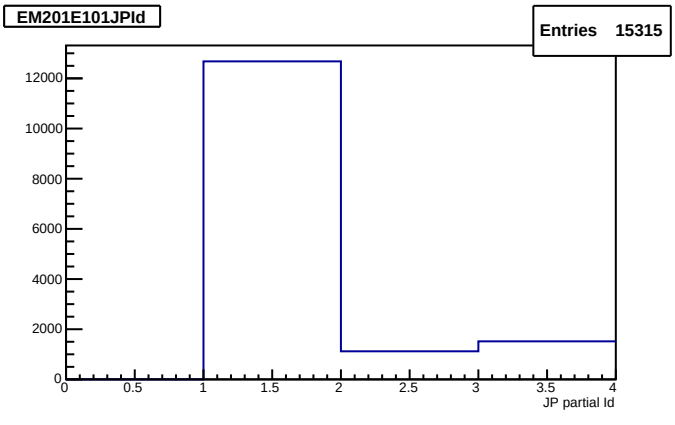

EMC L2 Input - Barrel HT.TP bits Diff
Entries 0
Barrel HT.TP bits Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

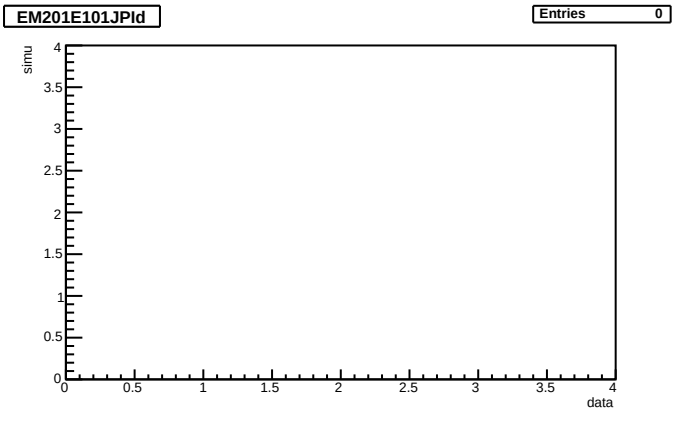

EM201E101JPThBits
Entries 15315
JP th bits

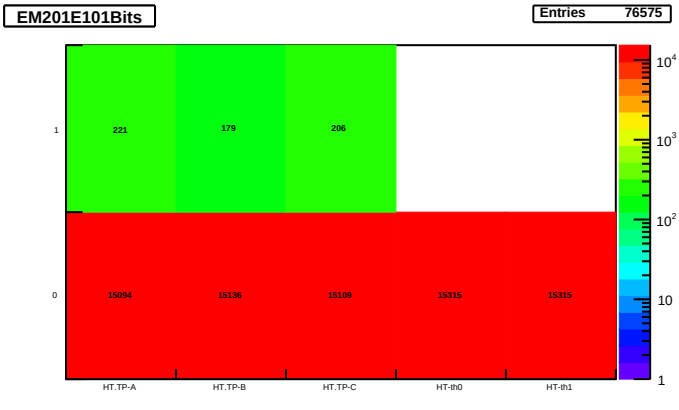

EM201E101JPThBits
Entries 0
simu
data

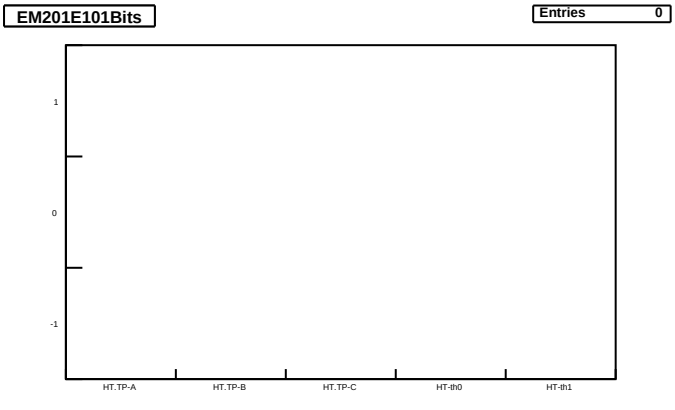

EM201E101JPSum
Entries 15315
JP partial sum

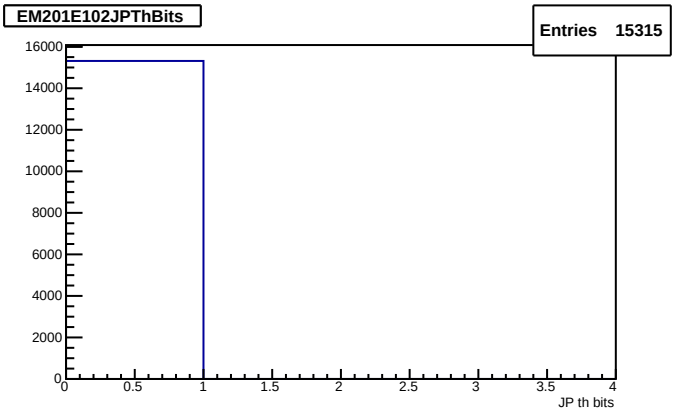

EM201E101JPSum
Entries 0
simu
data

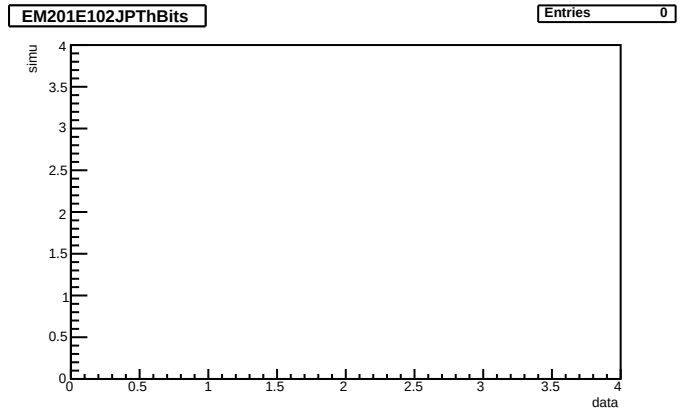

EM201E101JPId
Entries 15315
JP partial Id

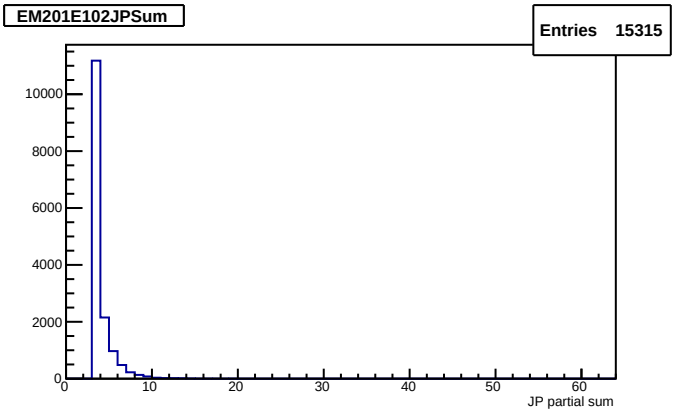

EM201E101JPId
Entries 0
simu
data

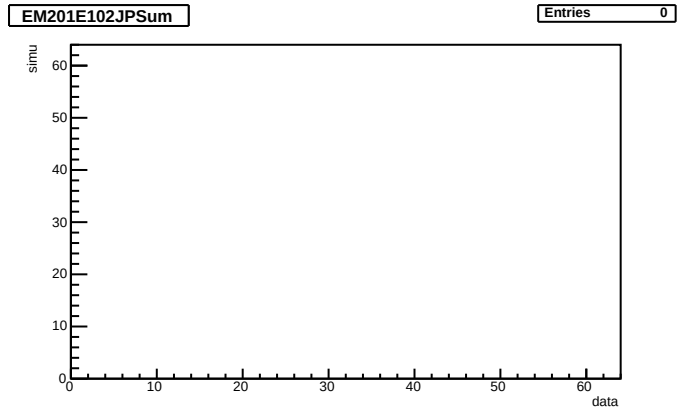

EM201E101Bits
Entries 76575
221
179
206
15094
15136
15109
15315
15315
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

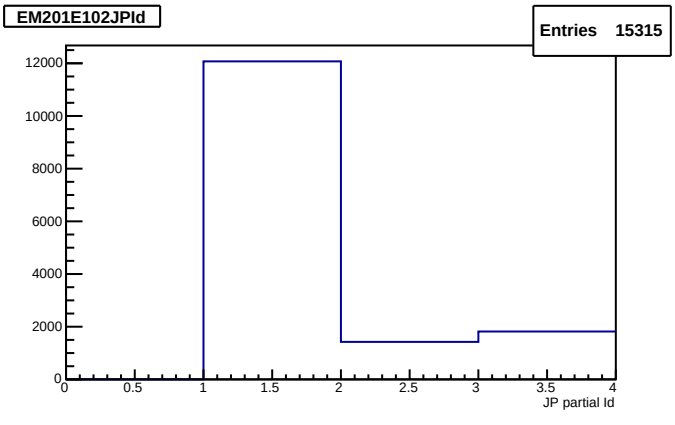

EM201E101Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

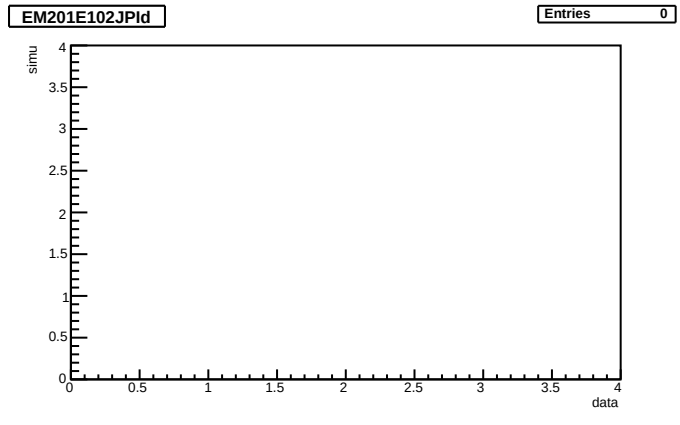

EM201E102JPThBits
Entries 15315
JP th bits

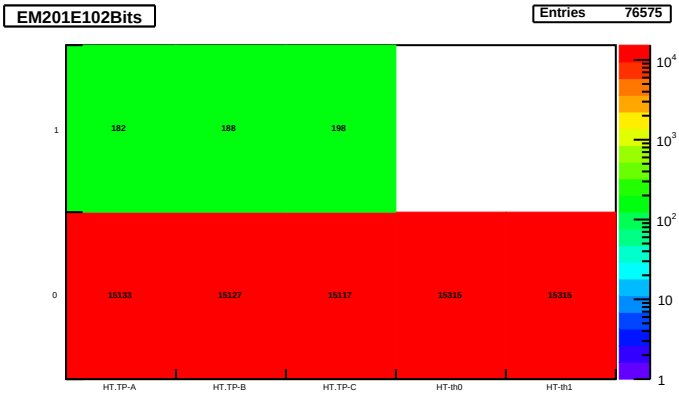

EM201E102JPThBits
Entries 0
simu
data

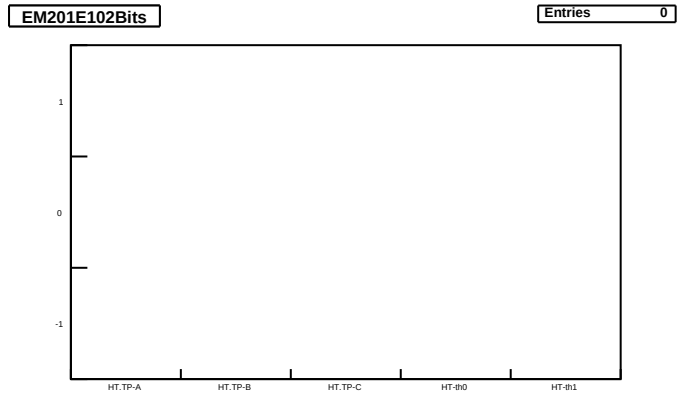

EM201E102JPSum
Entries 15315
JP partial sum

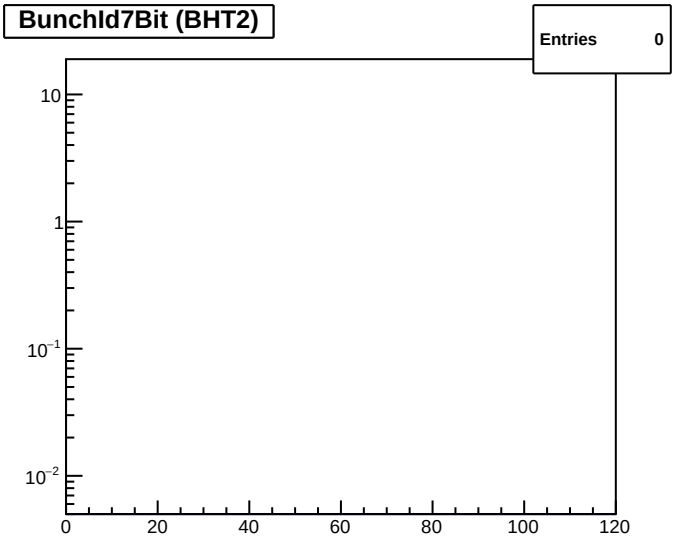

EM201E102JPSum
Entries 0
simu
data

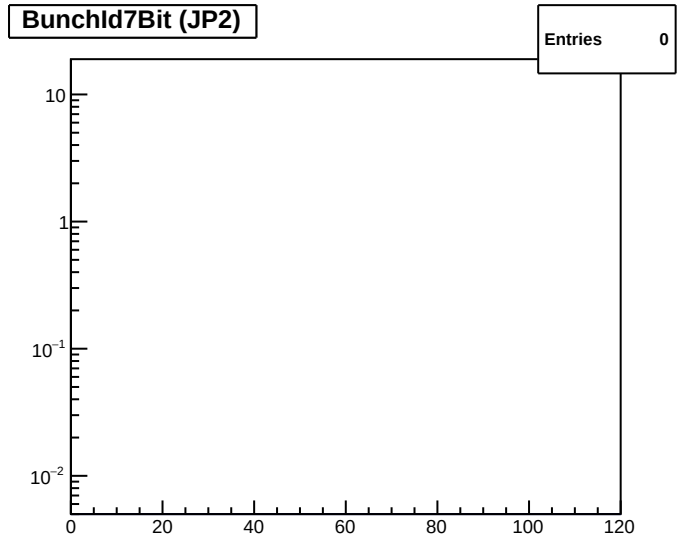

EM201E102JPId
Entries 15315
JP partial Id

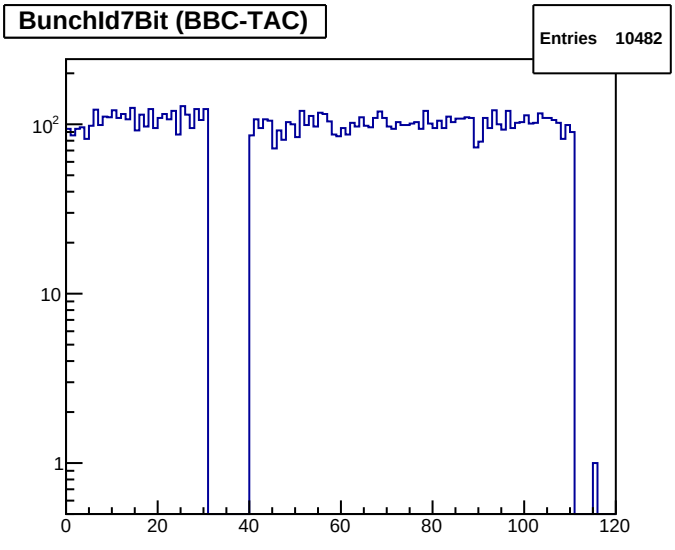

EM201E102JPId
Entries 0
simu
data

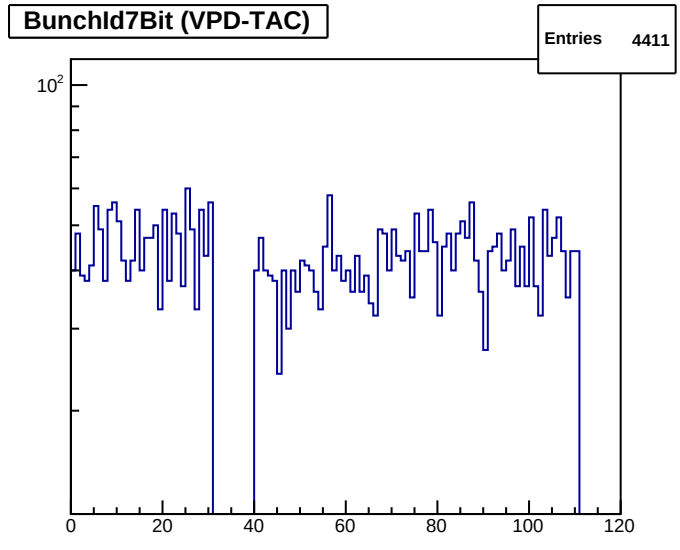

EM201E102Bits
Entries 76575
182
188
198
15133
15127
15117
15315
15315
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

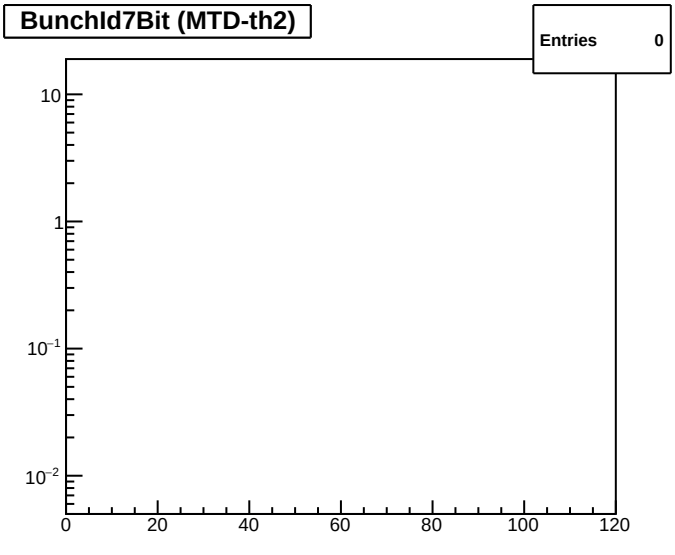

EM201E102Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

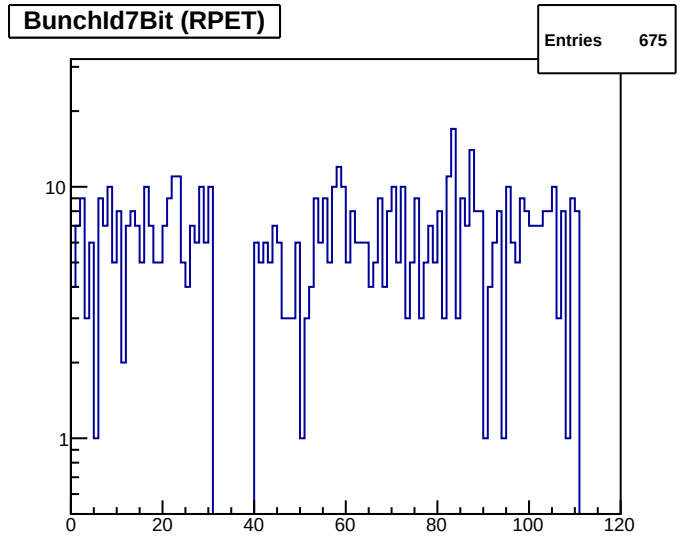

BunchId7Bit (BHT2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

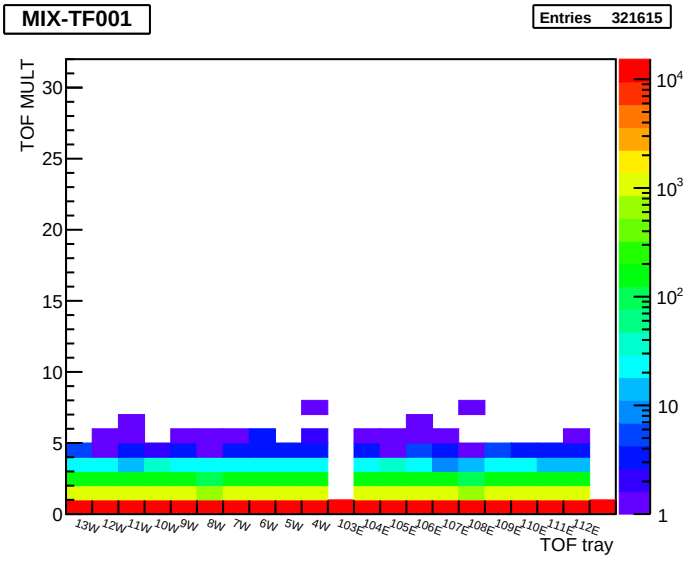

BunchId7Bit (JP2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

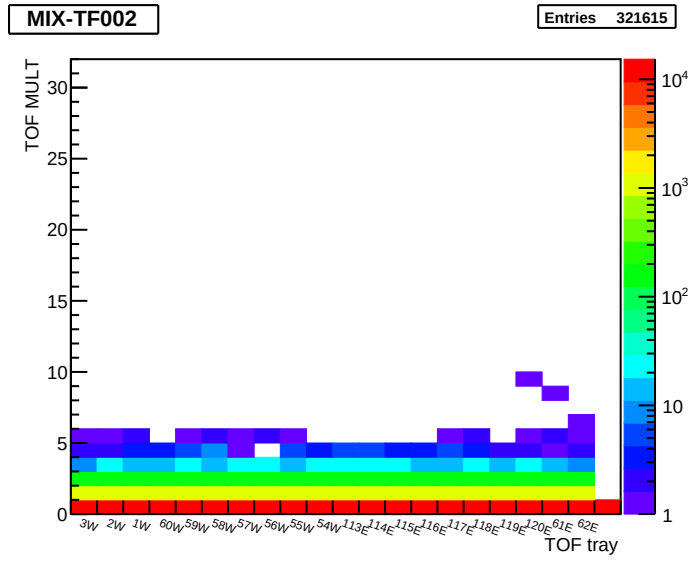

BunchId7Bit (BBC-TAC)
Entries 10482
10^2
10
1
0 20 40 60 80 100 120

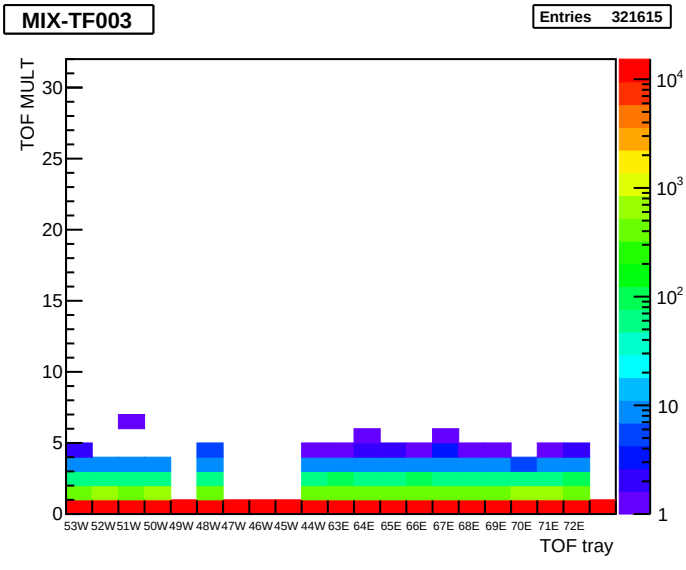

BunchId7Bit (VPD-TAC)
Entries 4411
10^2
0 20 40 60 80 100 120

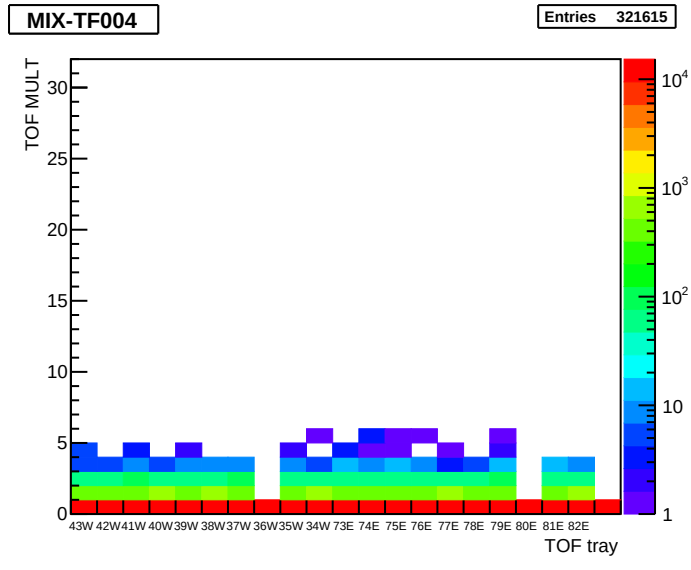

BunchId7Bit (MTD-th2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

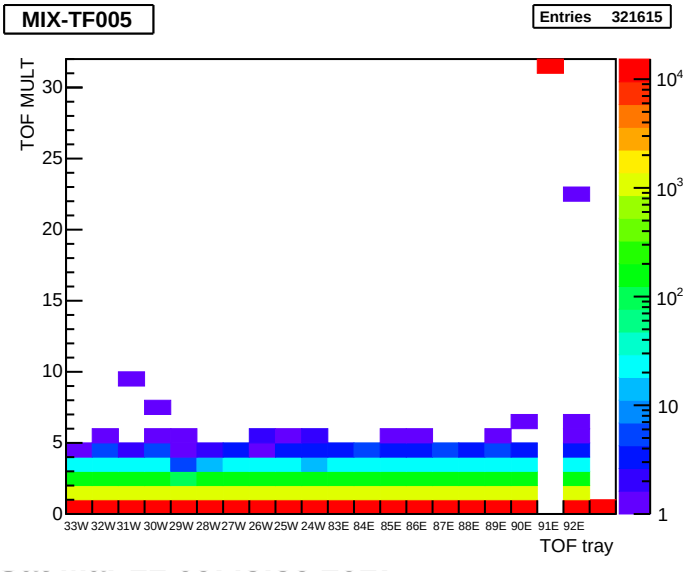

BunchId7Bit (RPET)
Entries 675
10
1
0 20 40 60 80 100 120

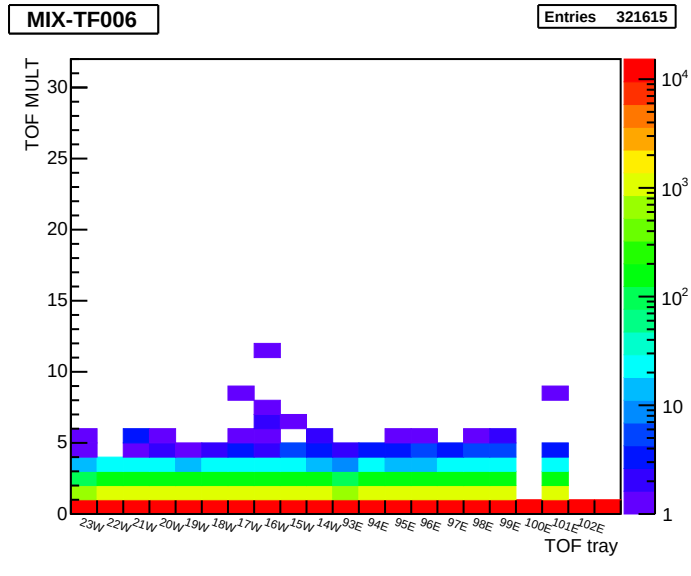

MIX-TF001
Entries 321615
TOF MULT
TOF tray

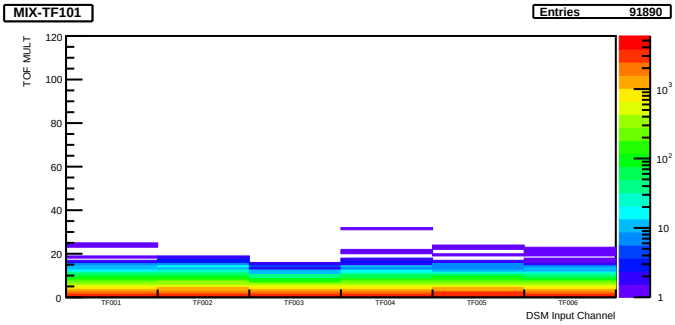

MIX-TF002
Entries 321615
TOF MULT
TOF tray

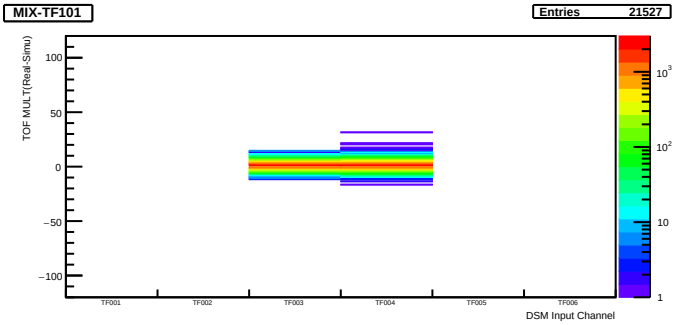

MIX-TF003
Entries 321615
TOF MULT
TOF tray

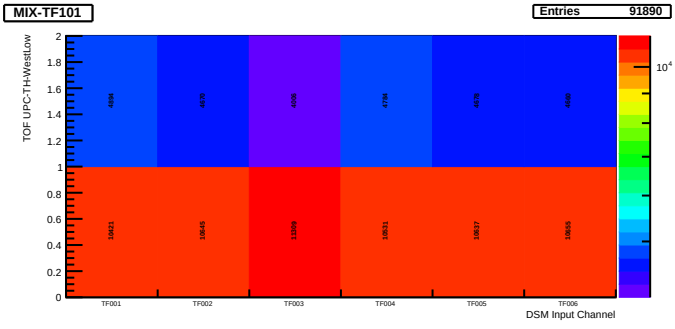

MIX-TF004
Entries 321615
TOF MULT
TOF tray

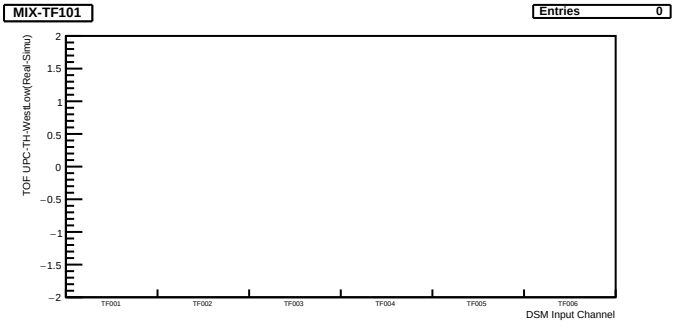

MIX-TF005
Entries 321615
TOF MULT
TOF tray

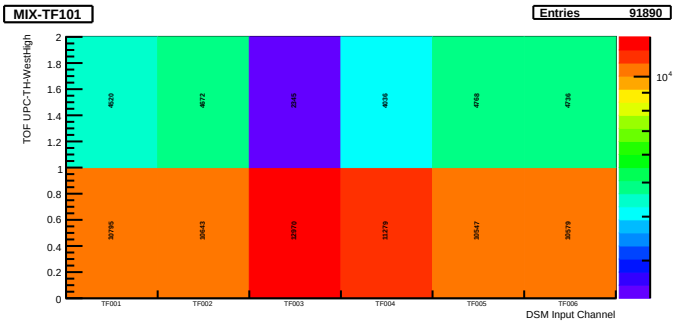

MIX-TF006
Entries 321615
TOF MULT
TOF tray

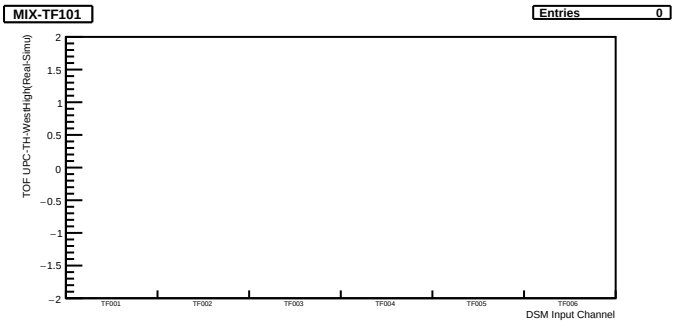

MIX-TF101
Entries 91890
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

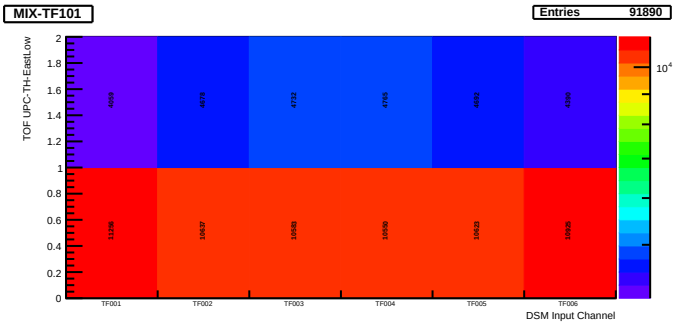

MIX-TF101
Entries 21527
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

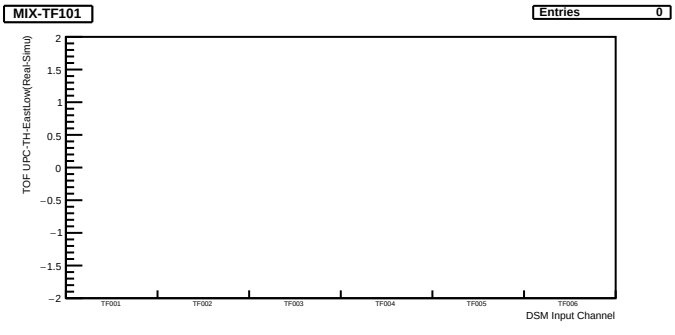

MIX-TF101
Entries 91890
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

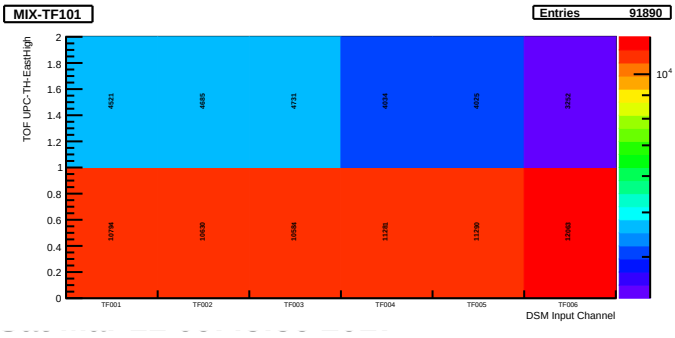

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

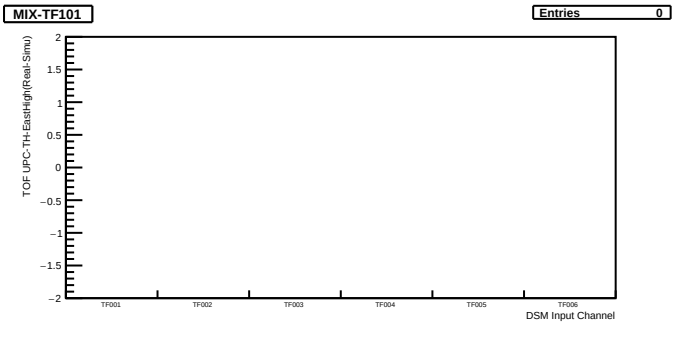

MIX-TF101
Entries 91890
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

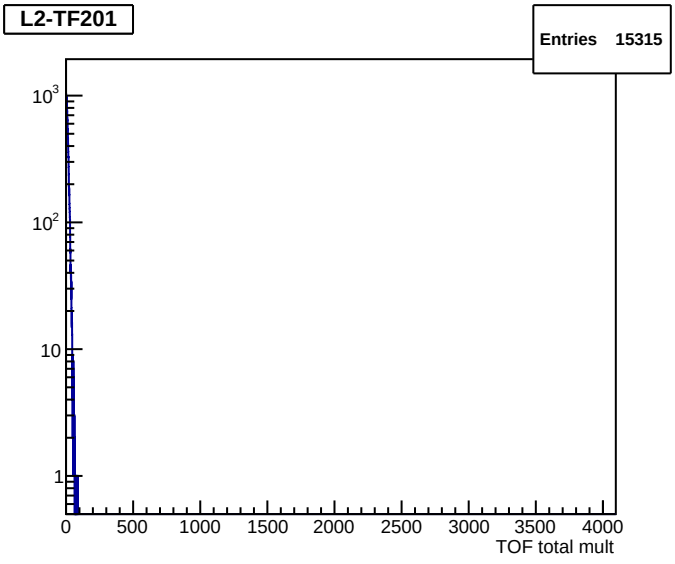

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

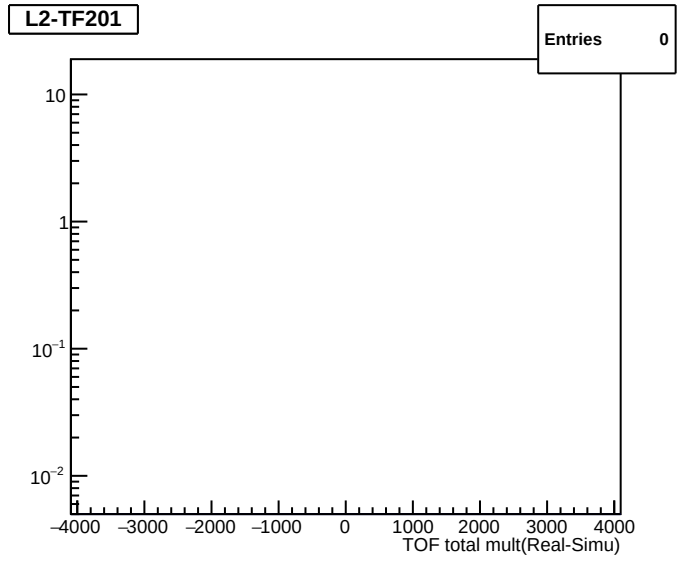

MIX-TF101
Entries 91890
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

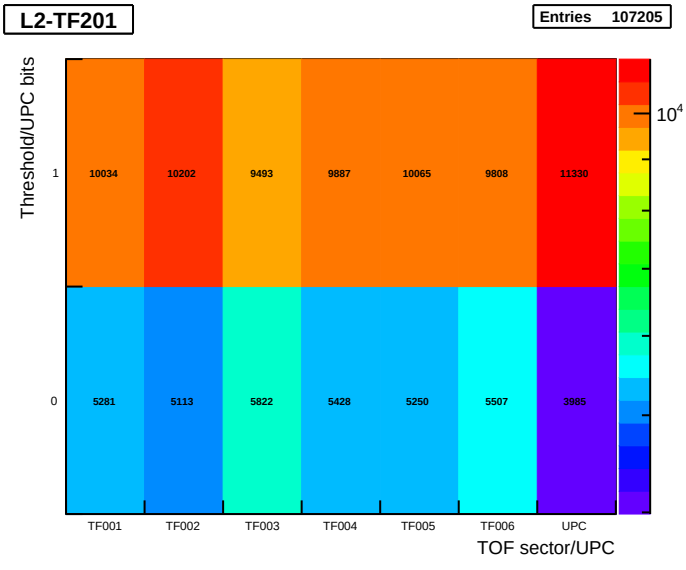

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

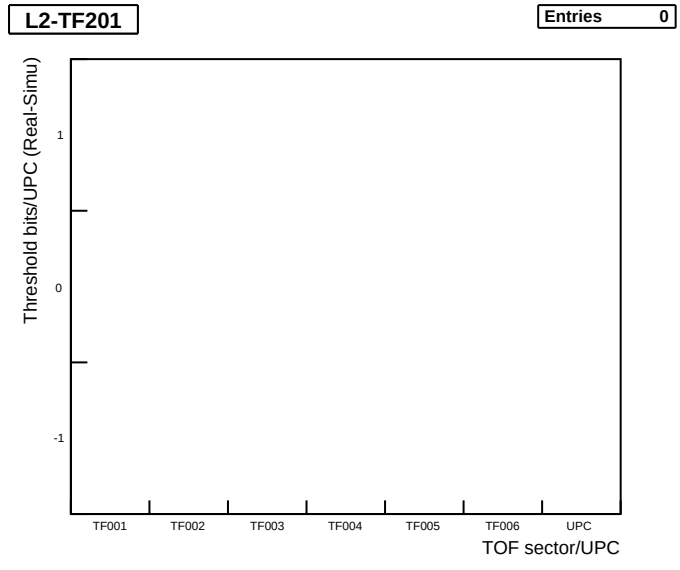

MIX-TF101
Entries 91890
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

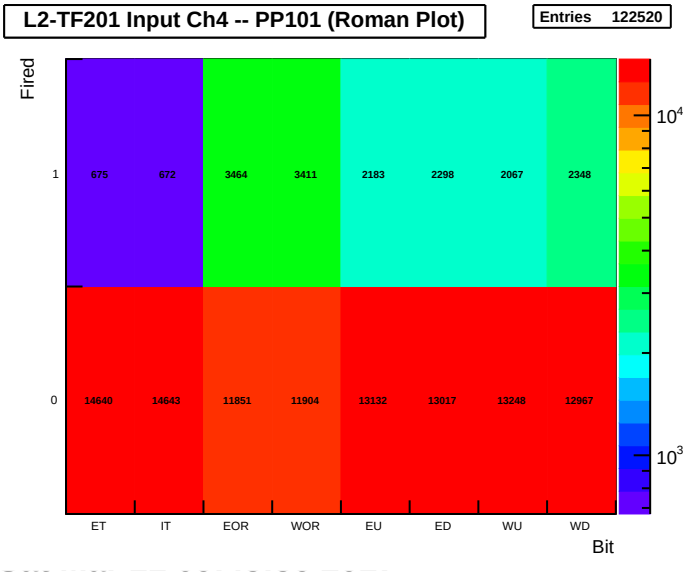

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

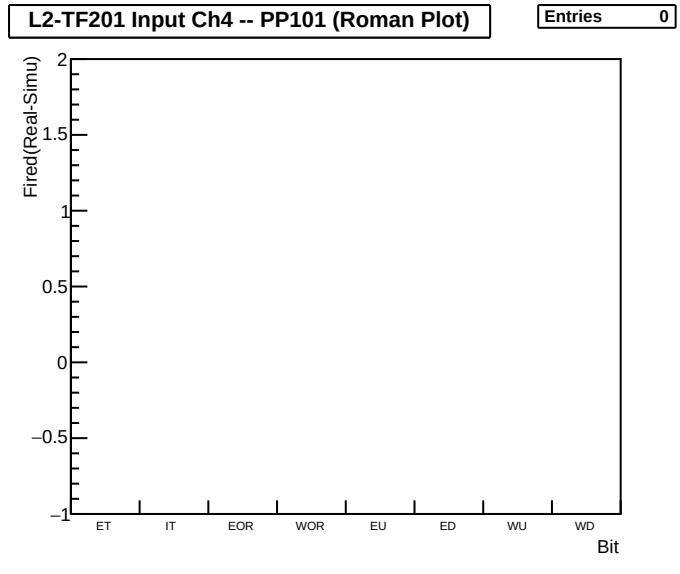

L2-TF201
Entries 15315
TOF total mult

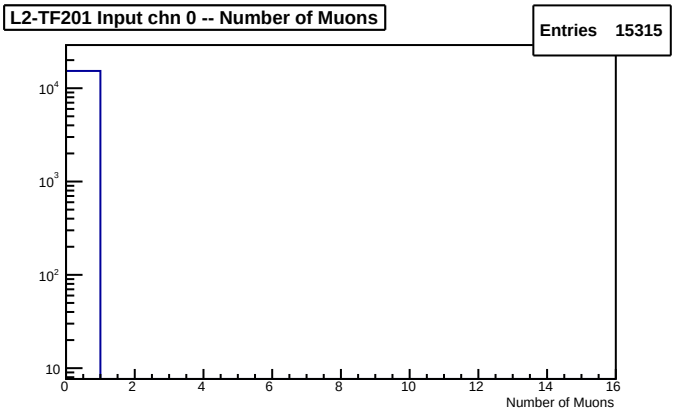

L2-TF201
Entries 0
TOF total mult(Real-Simu)

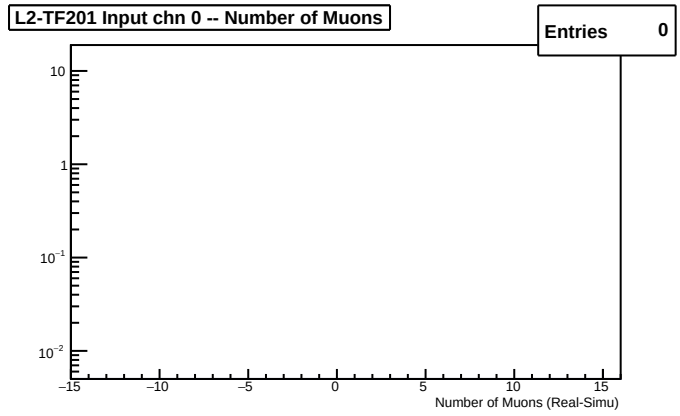

L2-TF201
Entries 107205
Threshold/UPC bits
10034
10202
9493
9887
10065
9808
11330
5281
5113
5822
5428
5250
5507
3985
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

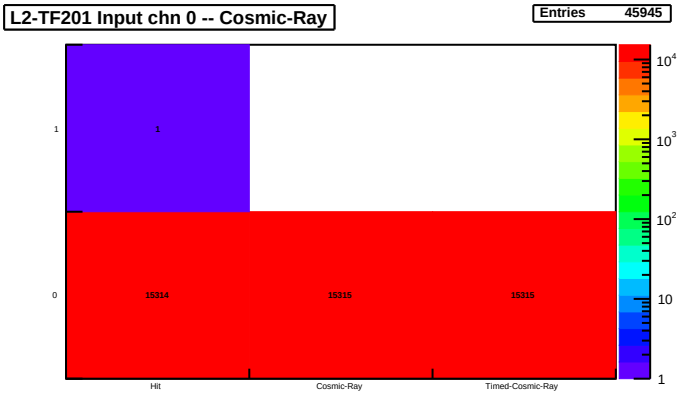

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

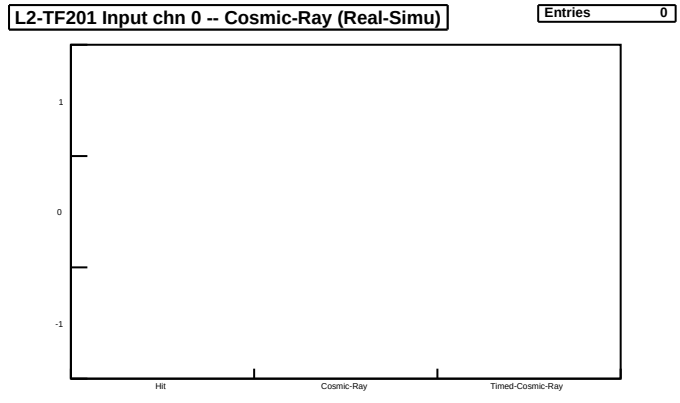

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 122520
Fired
675
672
3464
3411
2183
2298
2067
2348
14640
14643
11851
11904
13132
13017
13248
12967
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

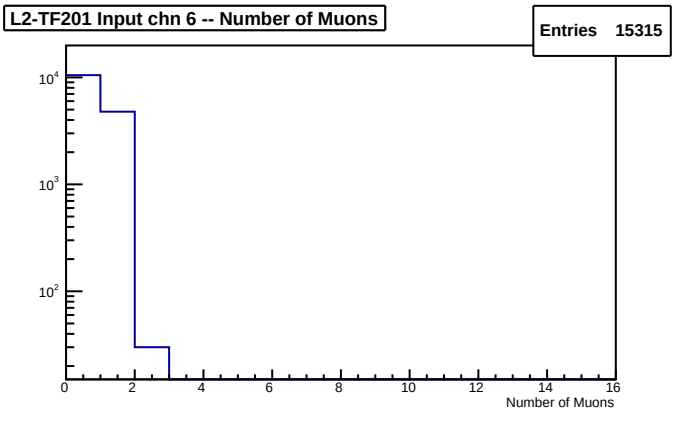

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 0
Fired(Real-Simu)
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

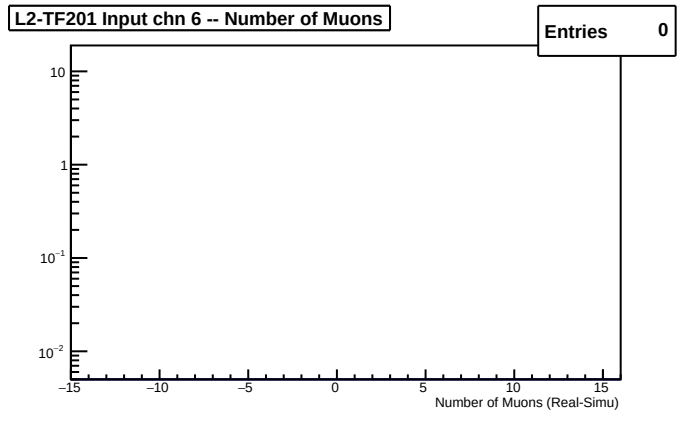

L2-TF201 Input chn 0 -- Number of Muons
Entries 15315
Number of Muons

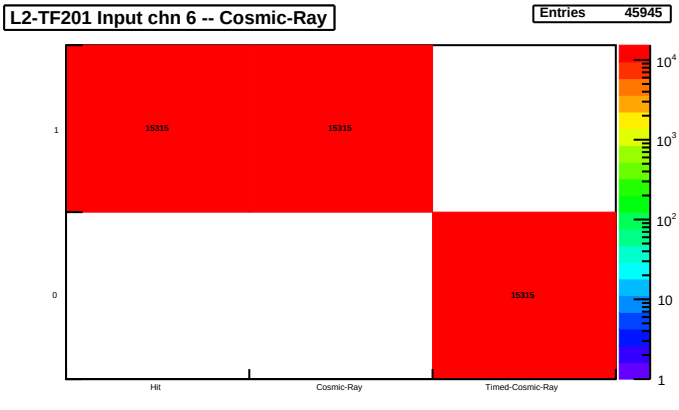

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

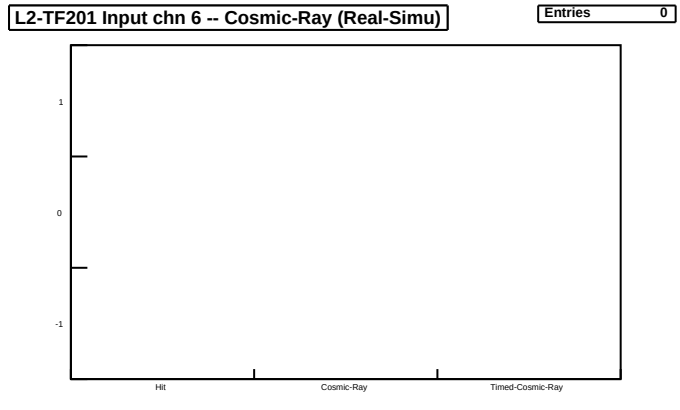

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 45945
1
15314
15315
15315
Hit
Cosmic-Ray
Timed-Cosmic-Ray

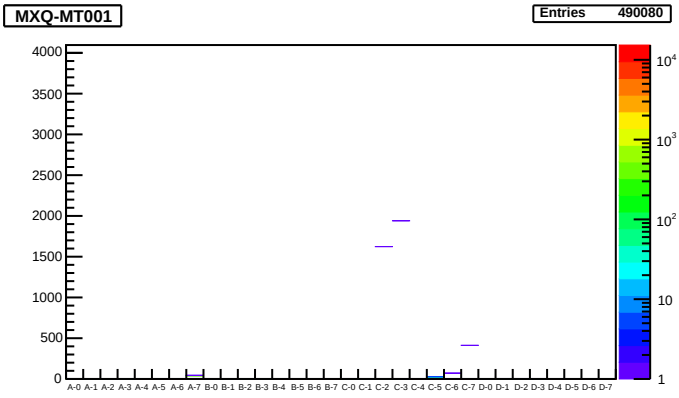

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

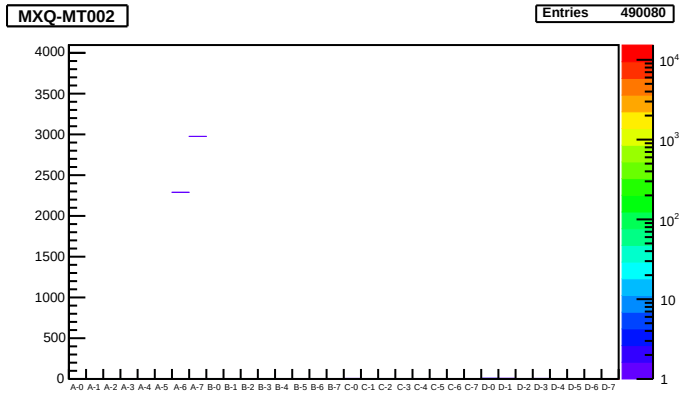

L2-TF201 Input chn 6 -- Number of Muons
Entries 15315
Number of Muons

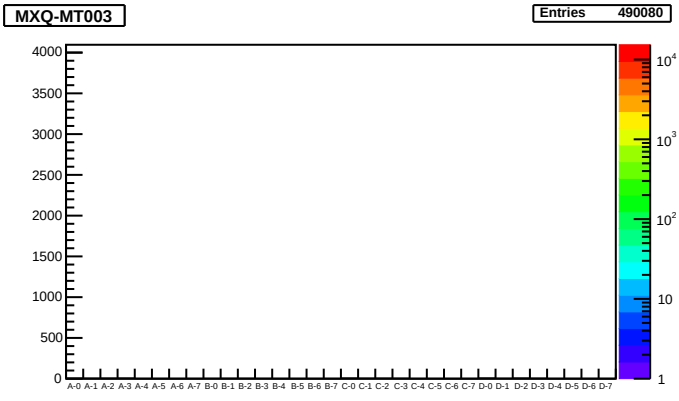

L2-TF201 Input chn 6 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

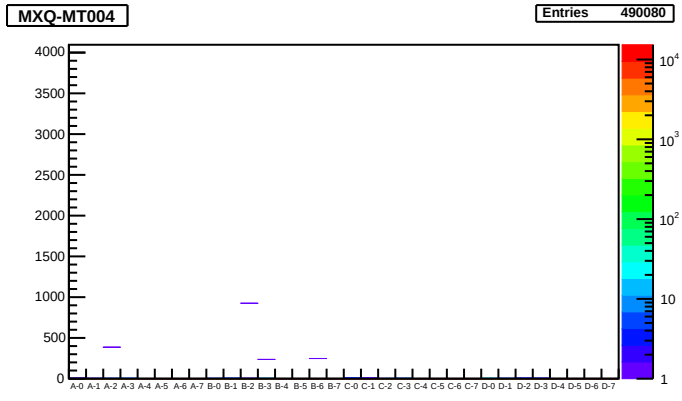

L2-TF201 Input chn 6 -- Cosmic-Ray
Entries 45945
15315
15315
15315
Hit
Cosmic-Ray
Timed-Cosmic-Ray

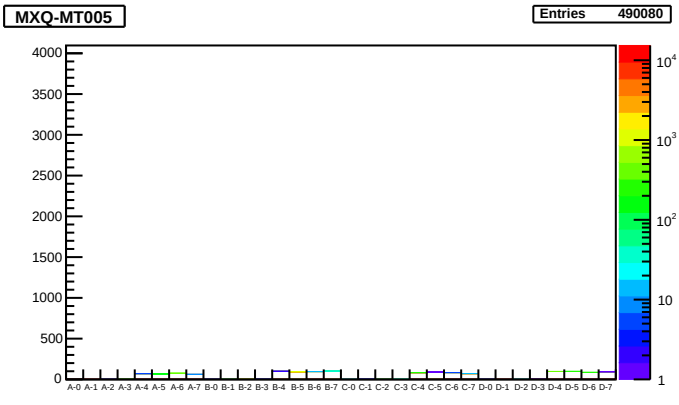

L2-TF201 Input chn 6 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

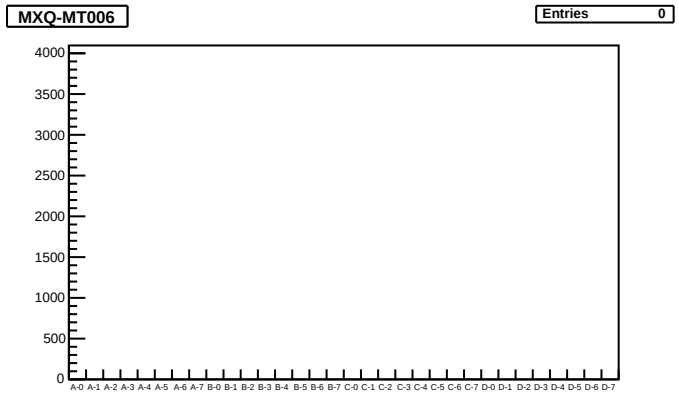

MXQ-MT001
Entries 490080

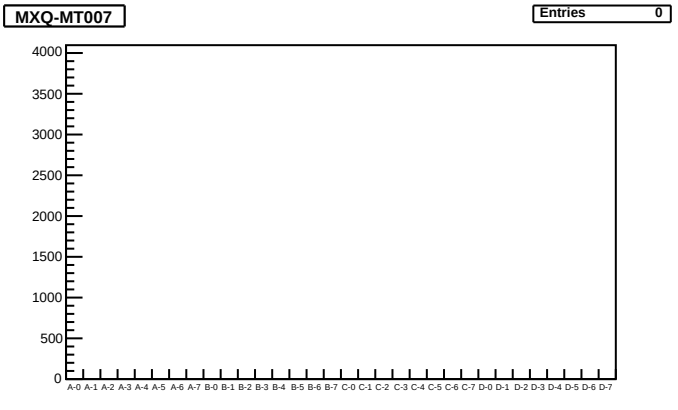

MXQ-MT002
Entries 490080

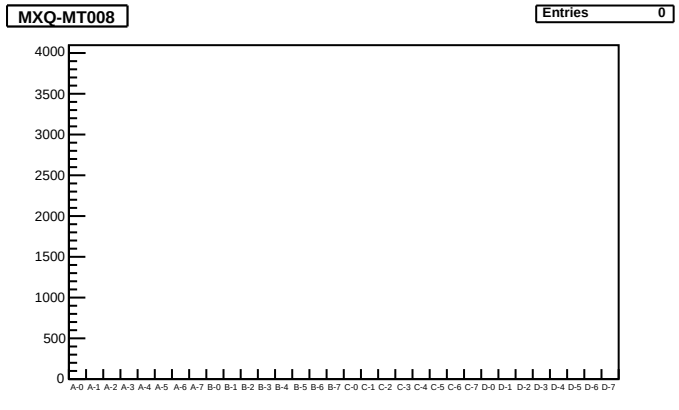

MXQ-MT003
Entries 490080

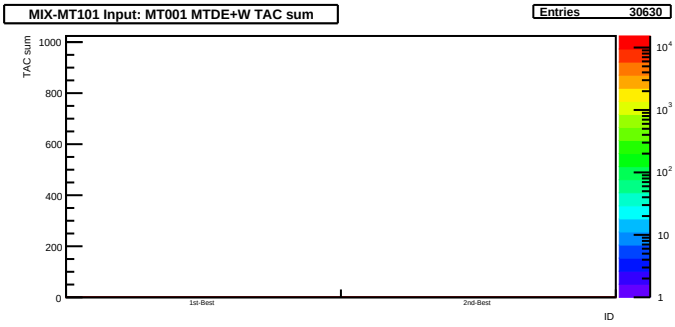

MXQ-MT004
Entries 490080

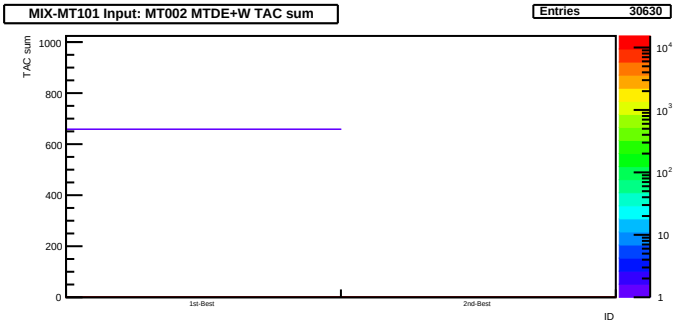

MXQ-MT005
Entries 490080

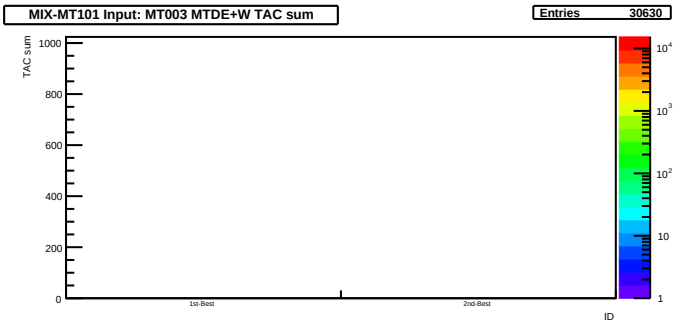

MXQ-MT006
Entries 0

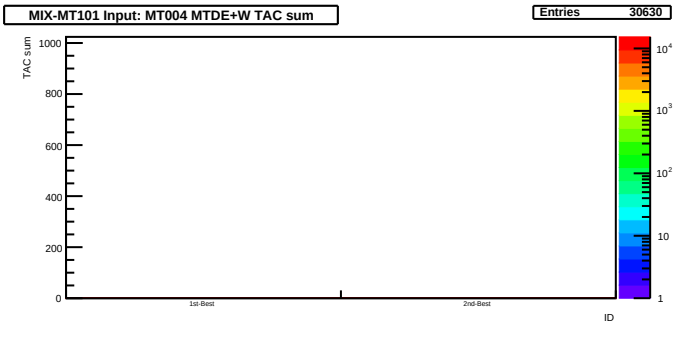

MXQ-MT007
Entries 0

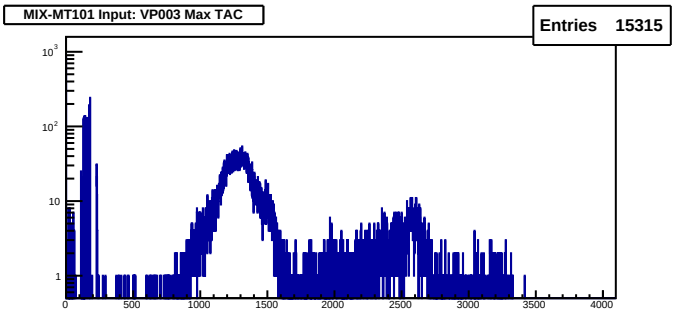

MXQ-MT008
Entries 0

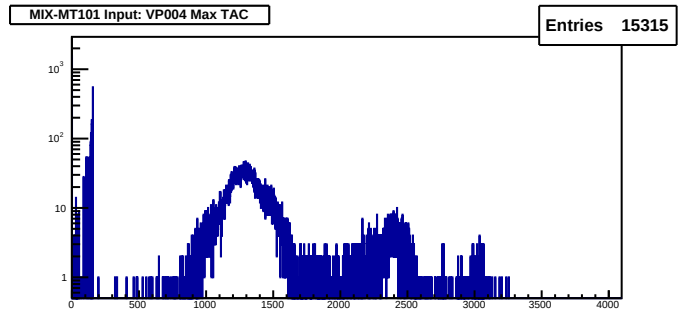

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 30630
TAC sum
1st-Best
2nd-Best
ID

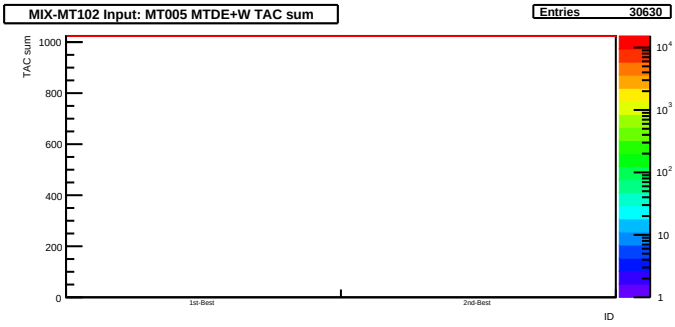

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 30630
TAC sum
1st-Best
2nd-Best
ID

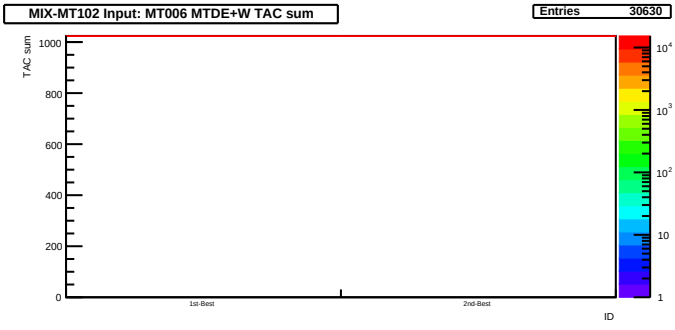

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 30630
TAC sum
1st-Best
2nd-Best
ID

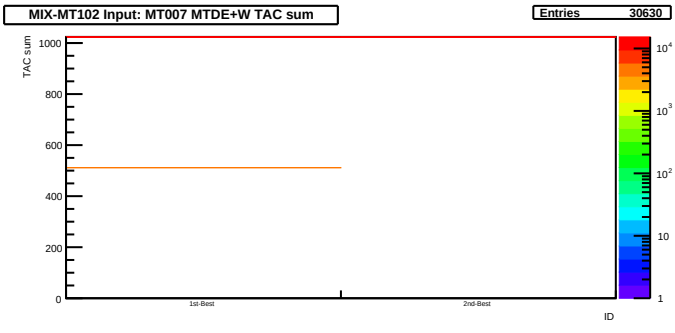

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 30630
TAC sum
1st-Best
2nd-Best
ID

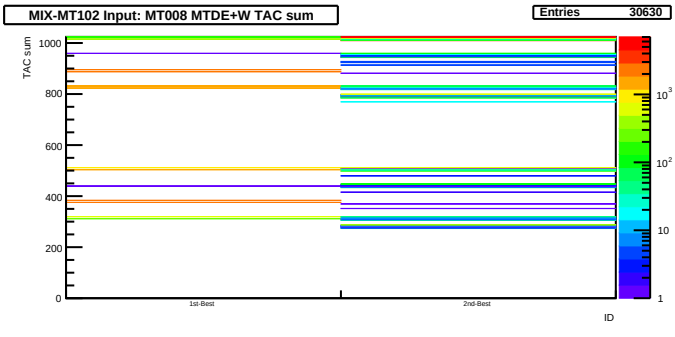

MIX-MT101 Input: VP003 Max TAC
Entries 15315

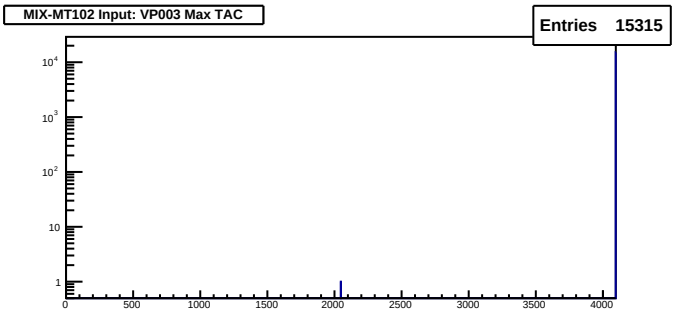

MIX-MT101 Input: VP004 Max TAC
Entries 15315

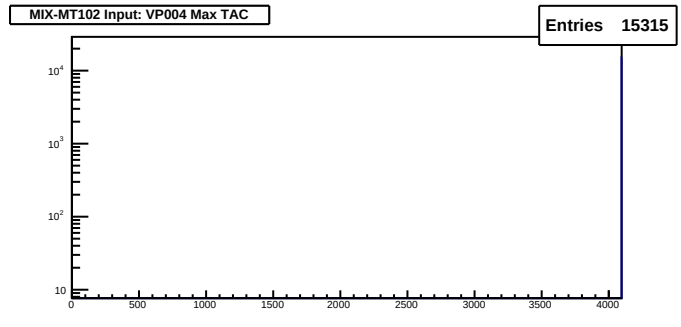

MIX-MT102 Input: MT005 MTDE+W TAC sum
Entries 30630
TAC sum
1st-Best
2nd-Best
ID

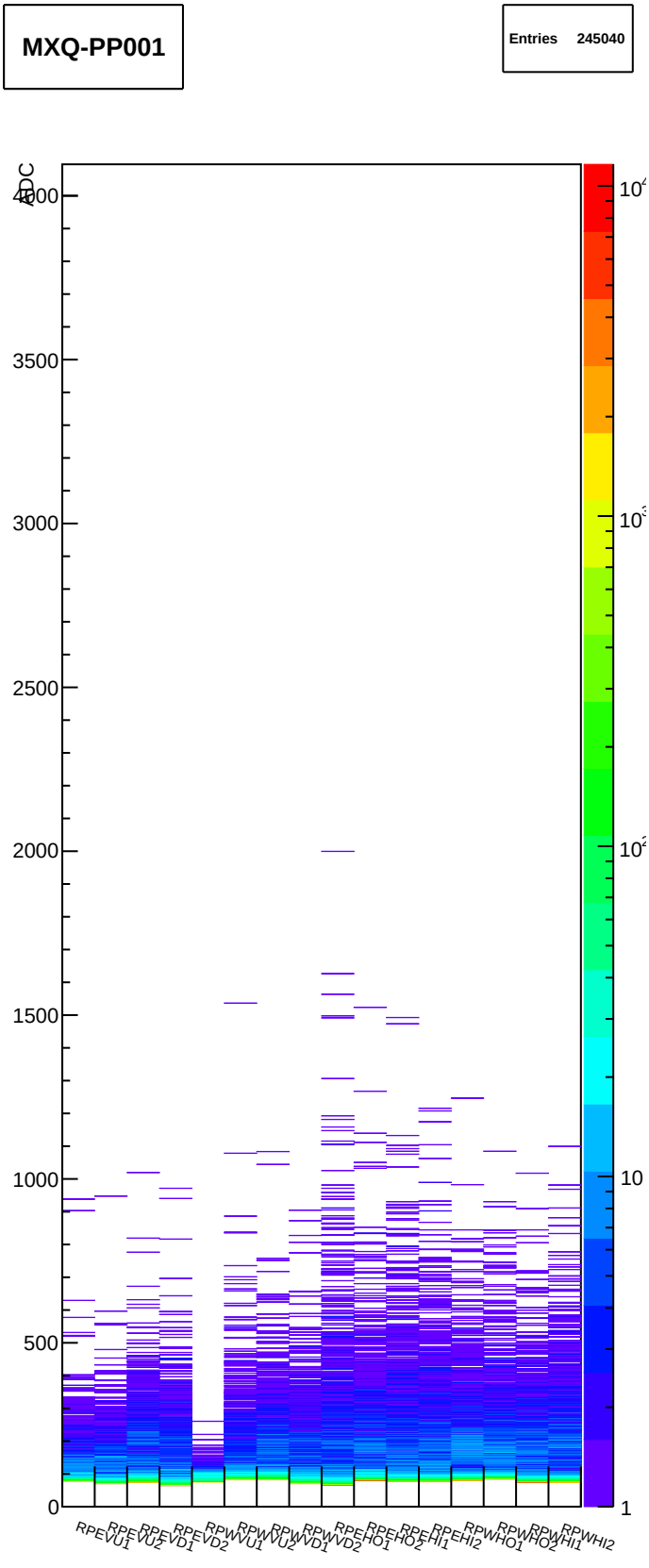

MIX-MT102 Input: MT006 MTDE+W TAC sum
Entries 30630
TAC sum
1st-Best
2nd-Best
ID

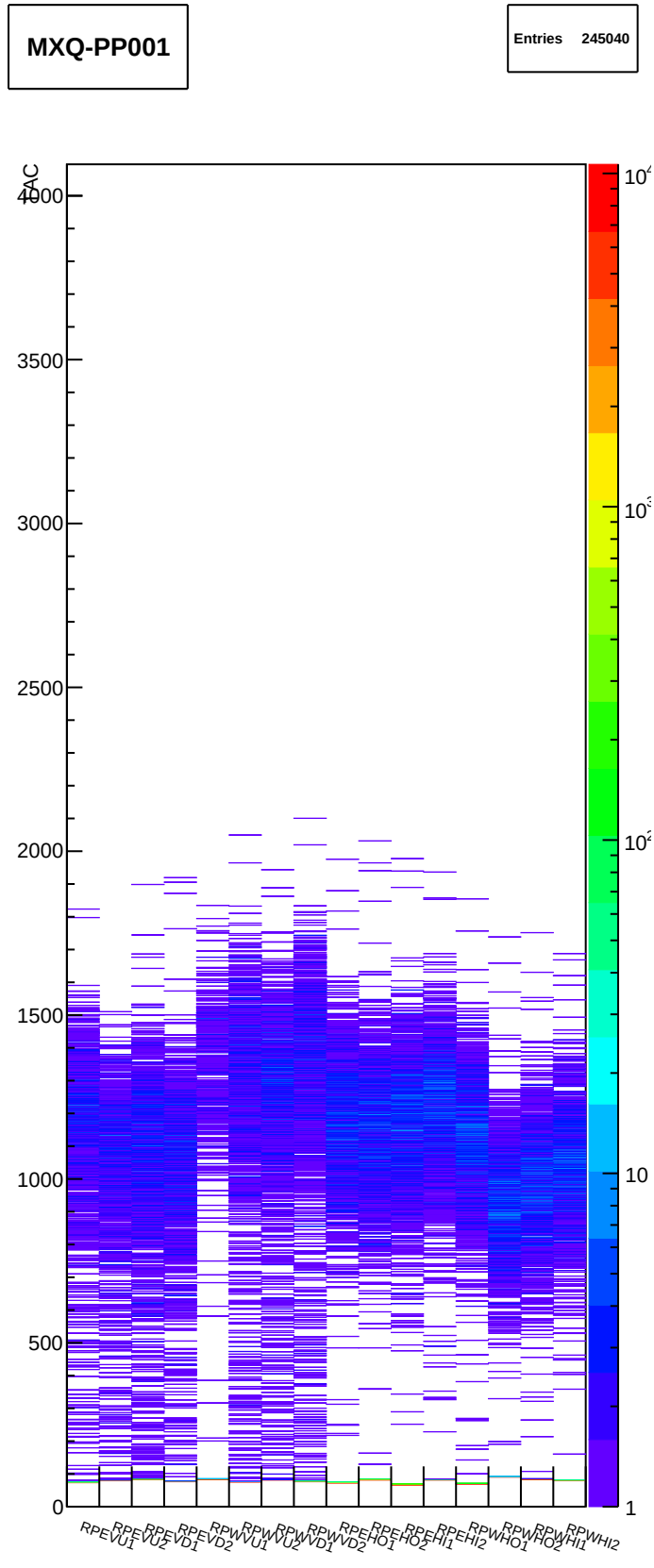

MIX-MT102 Input: MT007 MTDE+W TAC sum
Entries 30630
TAC sum
1st-Best
2nd-Best
ID

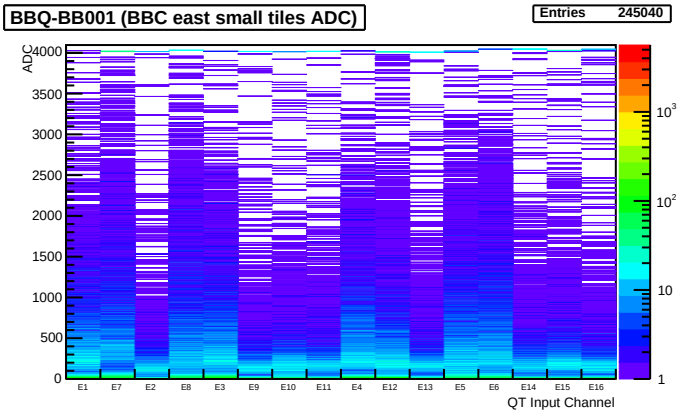

MIX-MT102 Input: MT008 MTDE+W TAC sum
Entries 30630
TAC sum
1st-Best
2nd-Best
ID

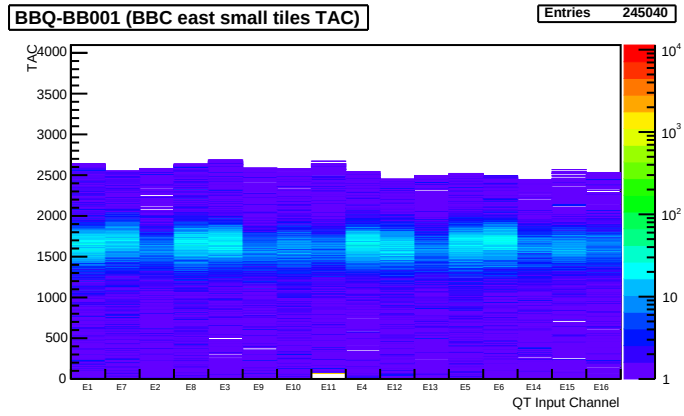

MIX-MT102 Input: VP003 Max TAC
Entries 15315

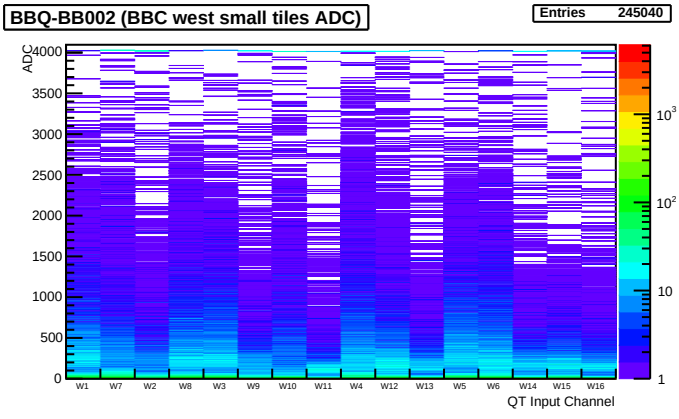

MIX-MT102 Input: VP004 Max TAC
Entries 15315

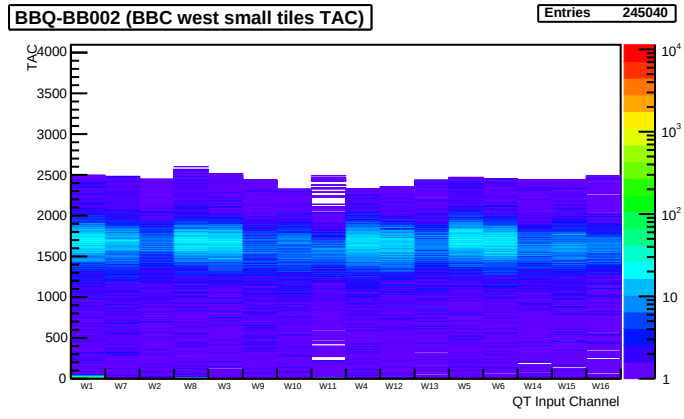

MXQ-PP001
Entries 245040
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
1
10

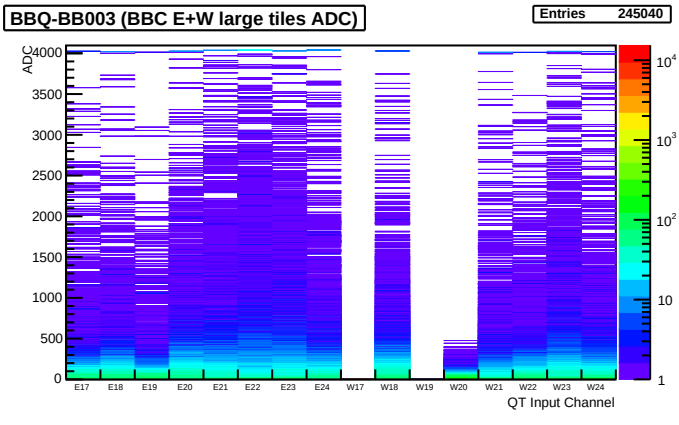

MXQ-PP001
Entries 245040
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
1
10

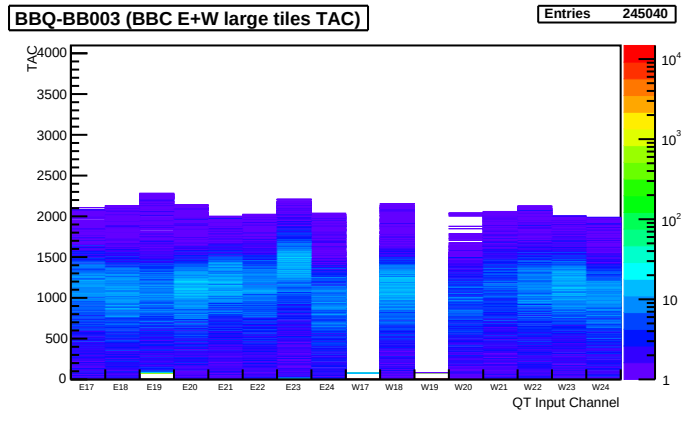

BBQ-BB001 (BBC east small tiles ADC)
Entries 245040
ADC
QT Input Channel

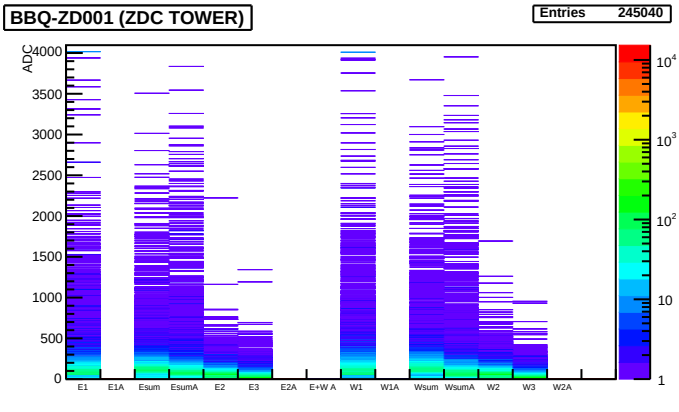

BBQ-BB001 (BBC east small tiles TAC)
Entries 245040
TAC
QT Input Channel

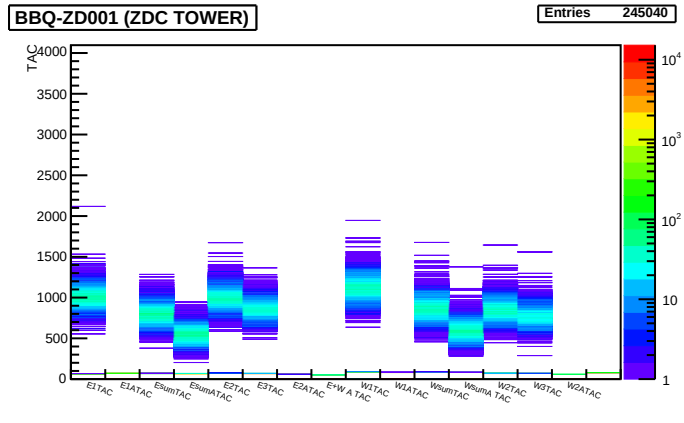

BBQ-BB002 (BBC west small tiles ADC)
Entries 245040
ADC
QT Input Channel

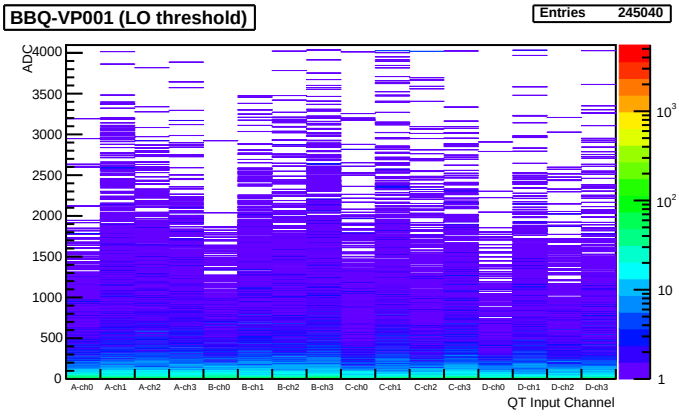

BBQ-BB002 (BBC west small tiles TAC)
Entries 245040
TAC
QT Input Channel

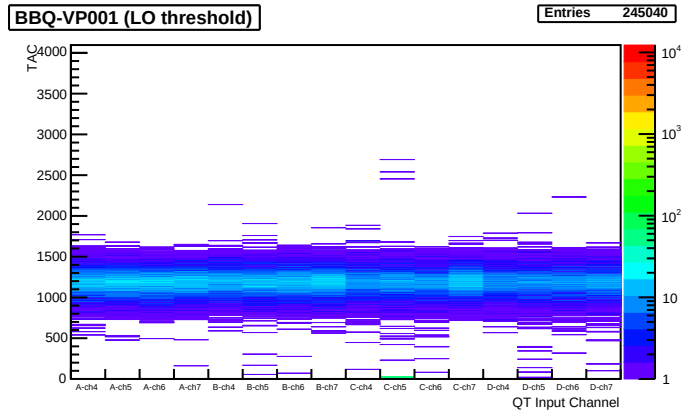

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 245040
ADC
QT Input Channel

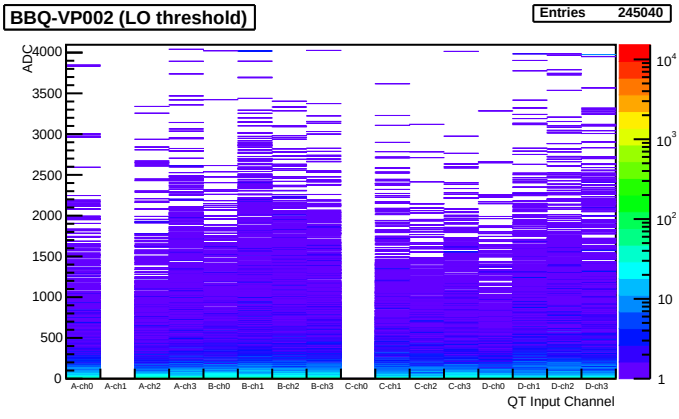

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 245040
TAC
QT Input Channel

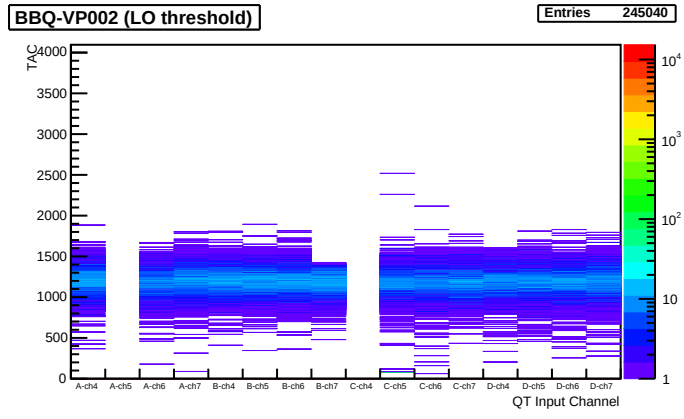

BBQ-ZD001 (ZDC TOWER)
Entries 245040
ADC

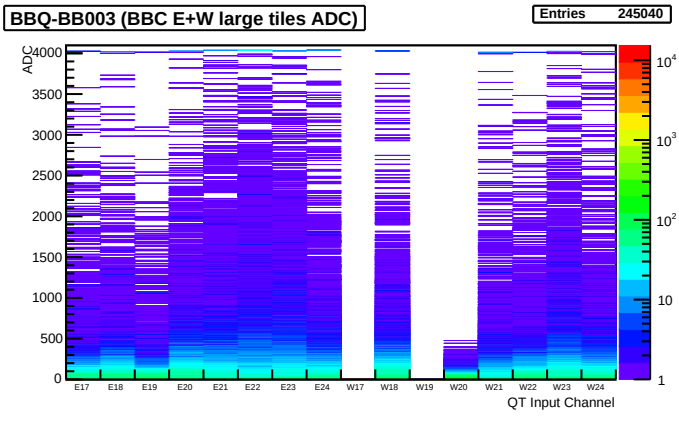

BBQ-ZD001 (ZDC TOWER)
Entries 245040
TAC

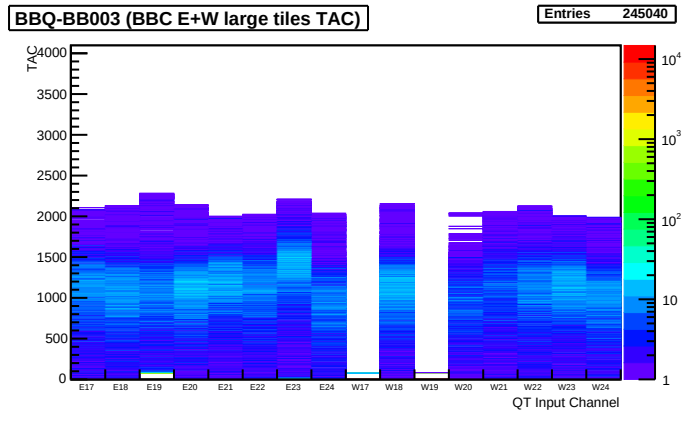

BBQ-VP001 (LO threshold)
Entries 245040
ADC
QT Input Channel

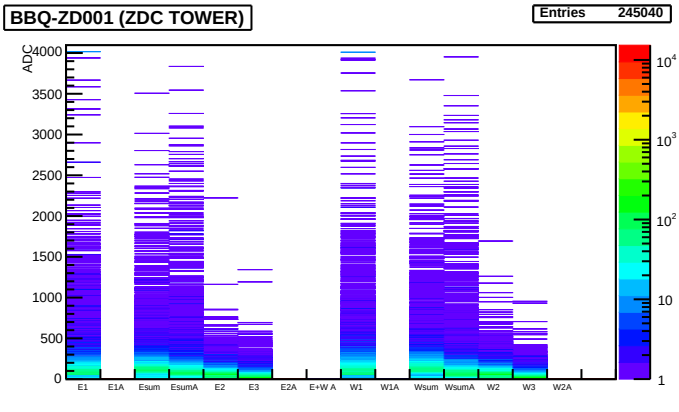

BBQ-VP001 (LO threshold)
Entries 245040
TAC
QT Input Channel

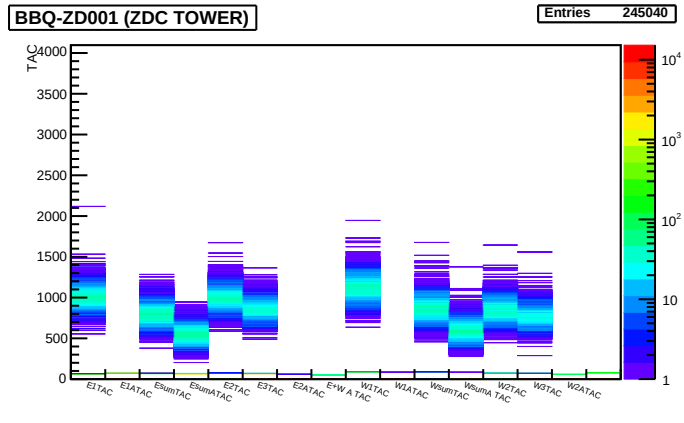

BBQ-VP002 (LO threshold)
Entries 245040
ADC
QT Input Channel

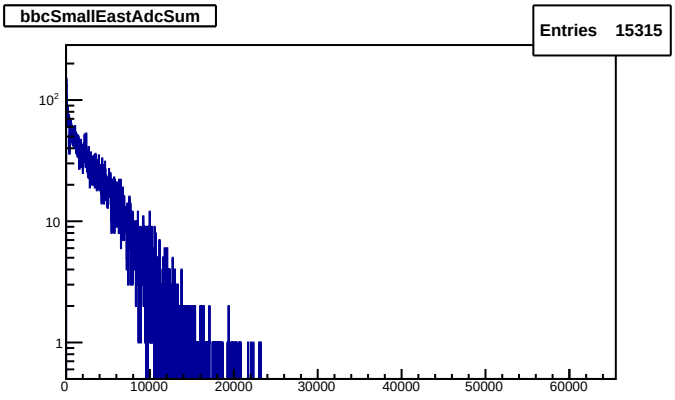

BBQ-VP002 (LO threshold)
Entries 245040
TAC
QT Input Channel

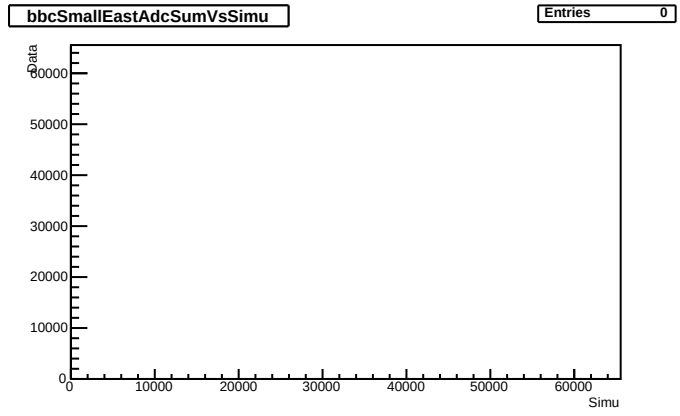

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 245040
ADC
QT Input Channel

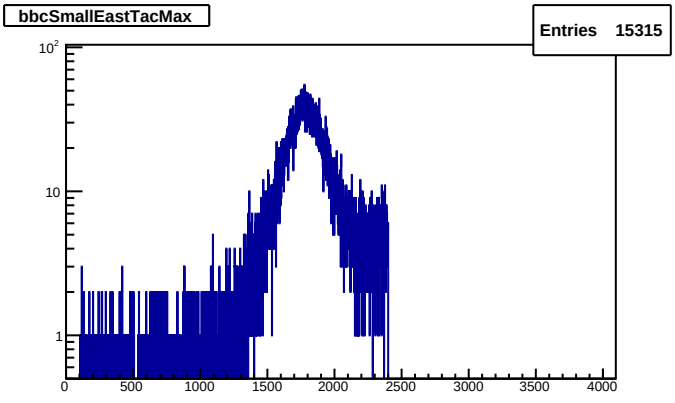

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 245040
TAC
QT Input Channel

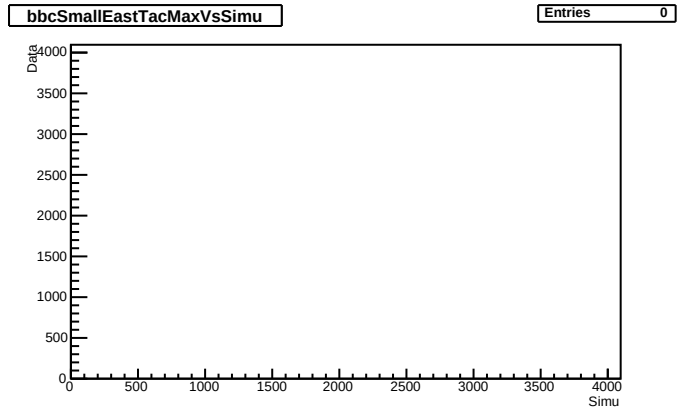

BBQ-ZD001 (ZDC TOWER)
Entries 245040
ADC

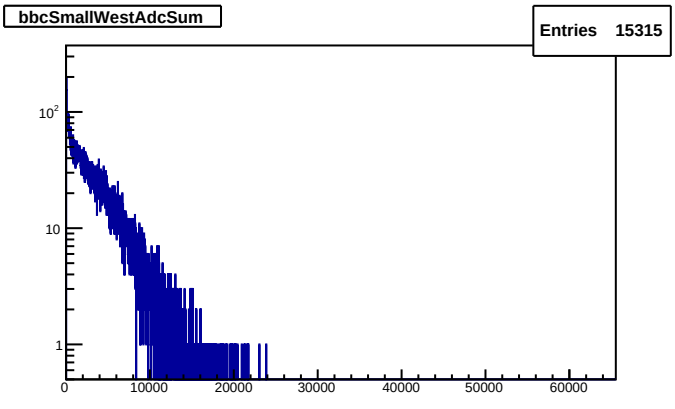

BBQ-ZD001 (ZDC TOWER)
Entries 245040
TAC

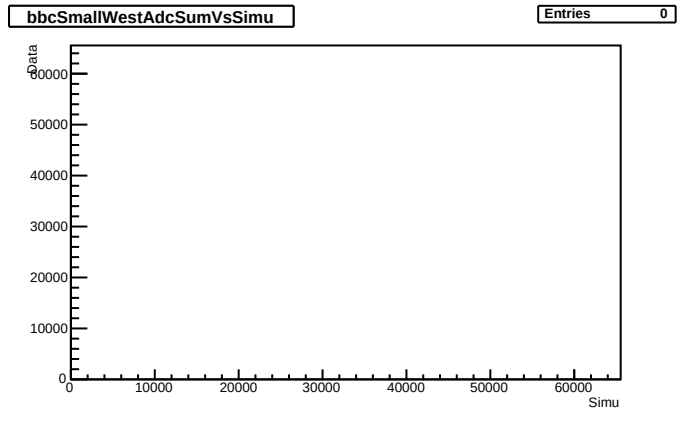

bbcSmallEastAdcSum
Entries 15315

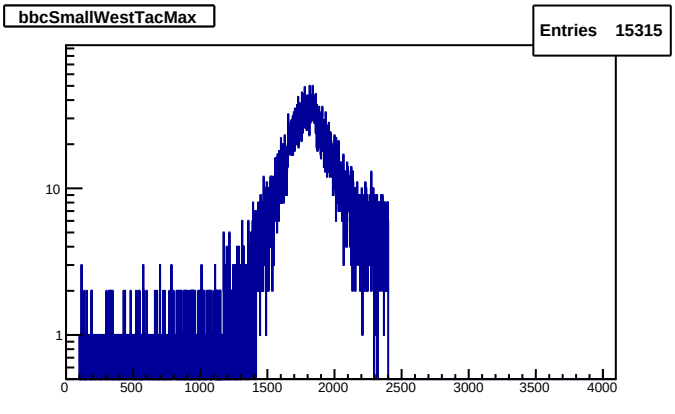

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

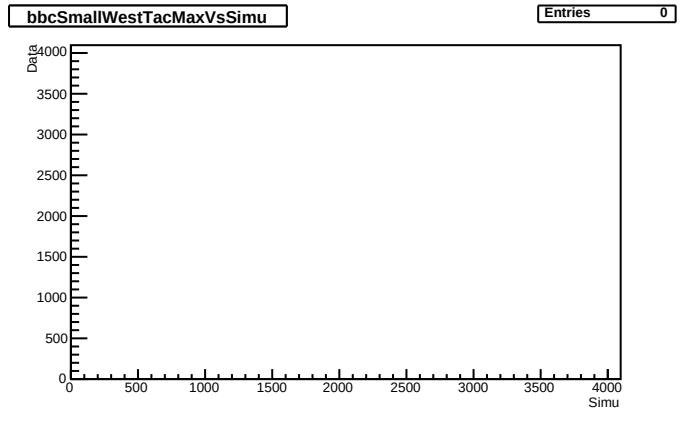

bbcSmallEastTacMax
Entries 15315

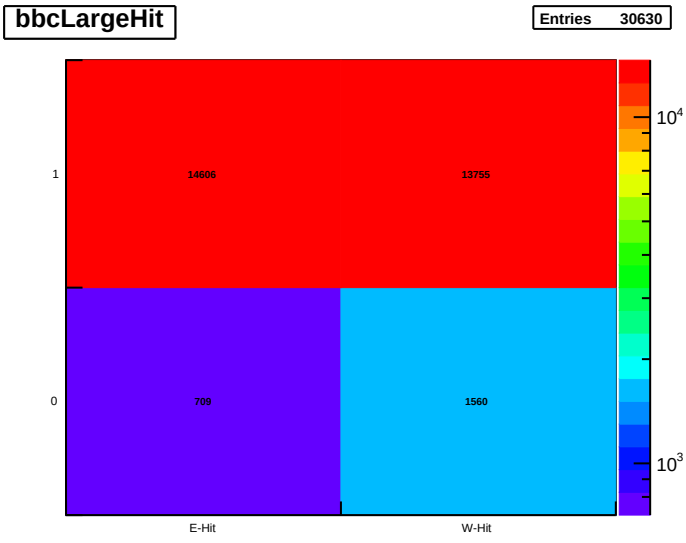

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

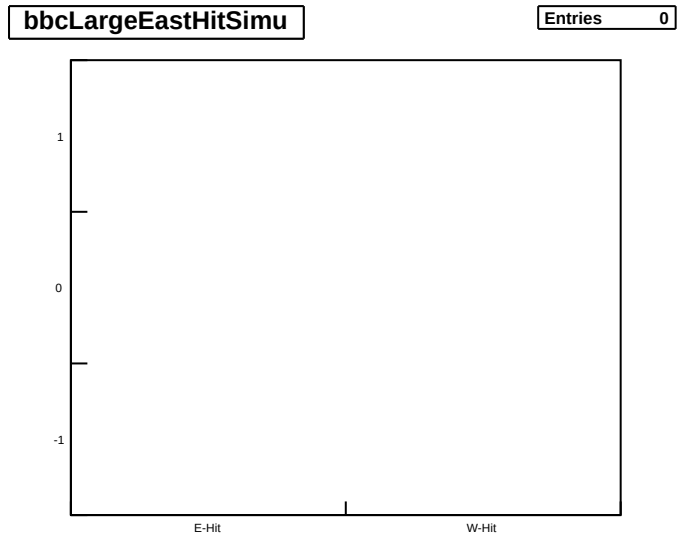

bbcSmallWestAdcSum
Entries 15315

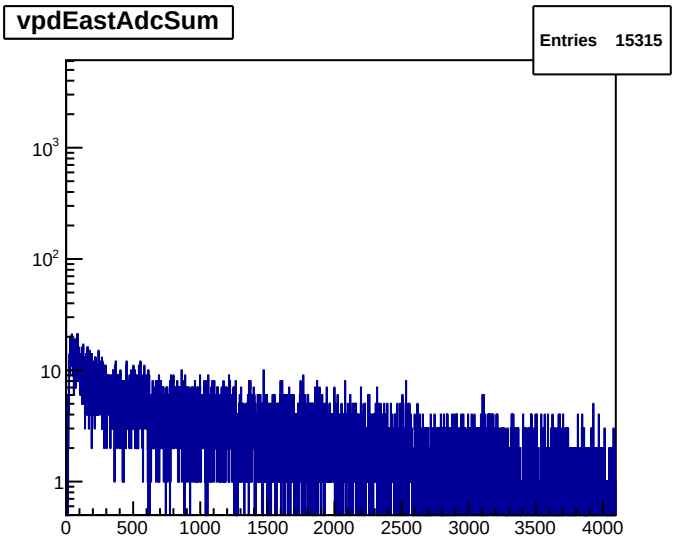

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

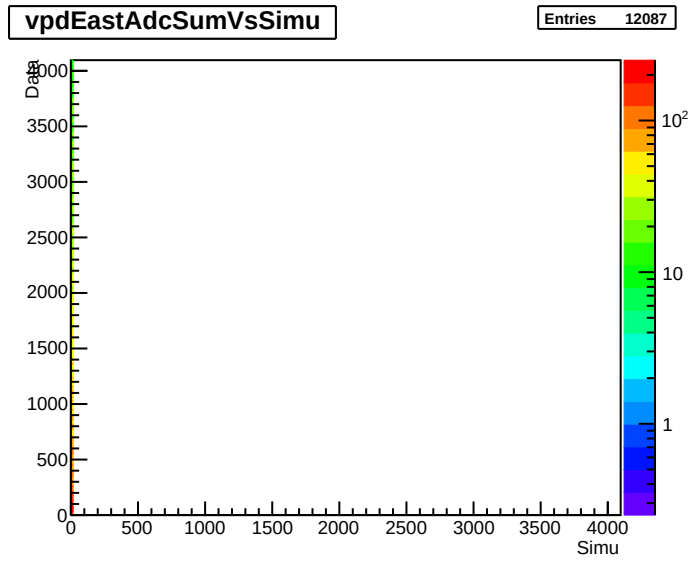

bbcSmallWestTacMax
Entries 15315

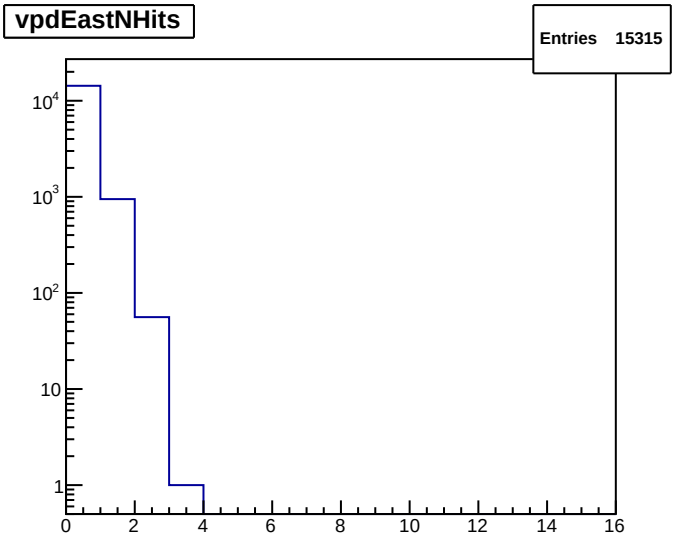

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

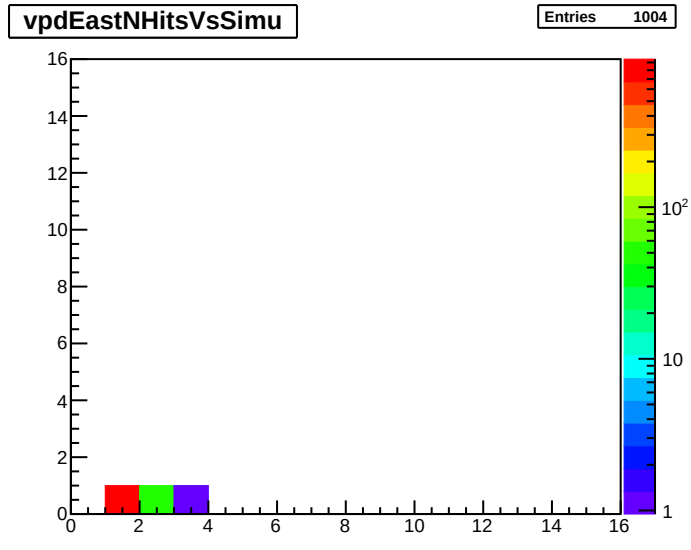

bbcLargeHit
Entries 30630
1
0
14606
13755
709
1560
E-Hit
W-Hit
10^4
10^3

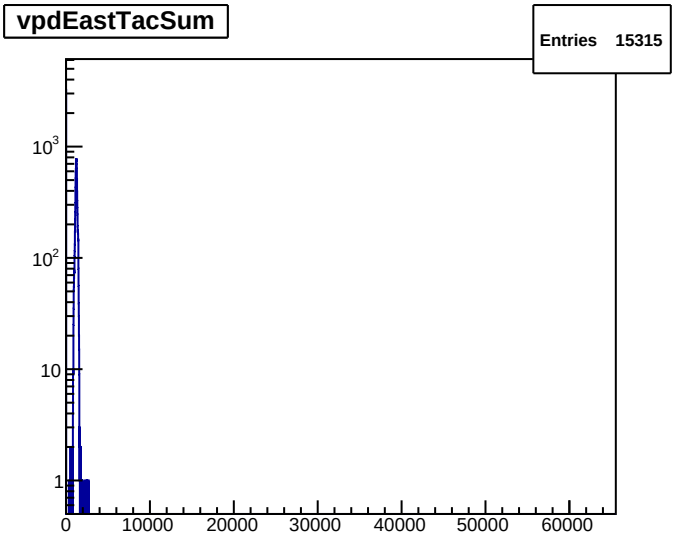

bbcLargeEastHitSimu
Entries 0
1
0
-1
E-Hit
W-Hit

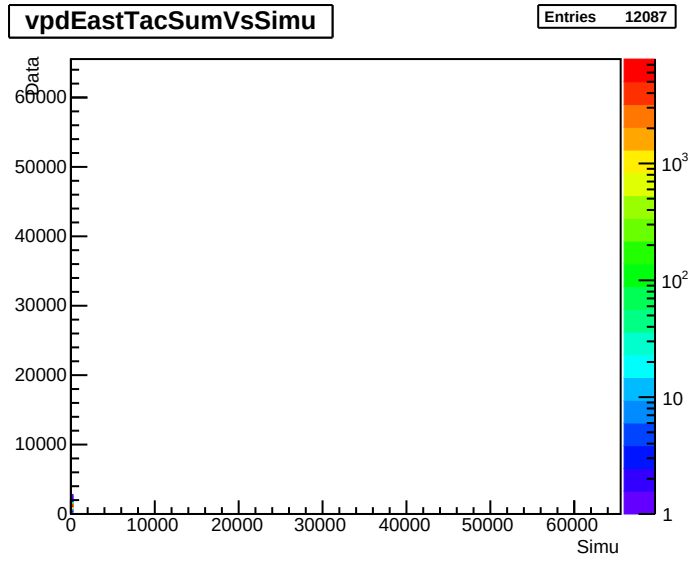

vpdEastAdcSum
Entries 15315

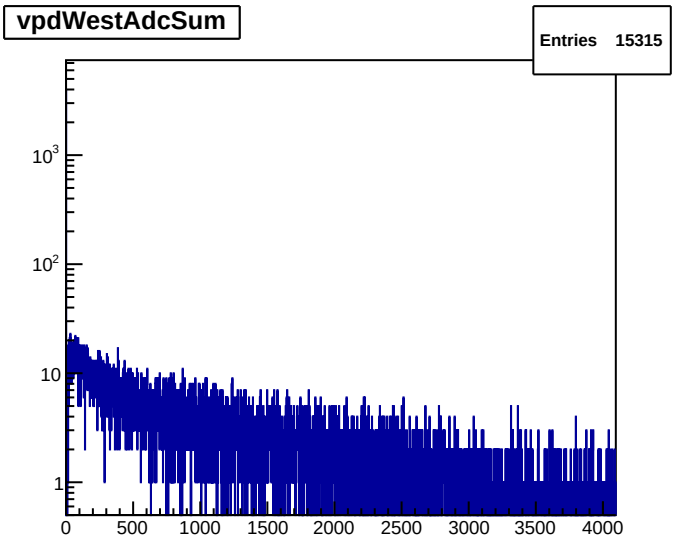

vpdEastAdcSumVsSimu
Entries 12087
Data
Simu

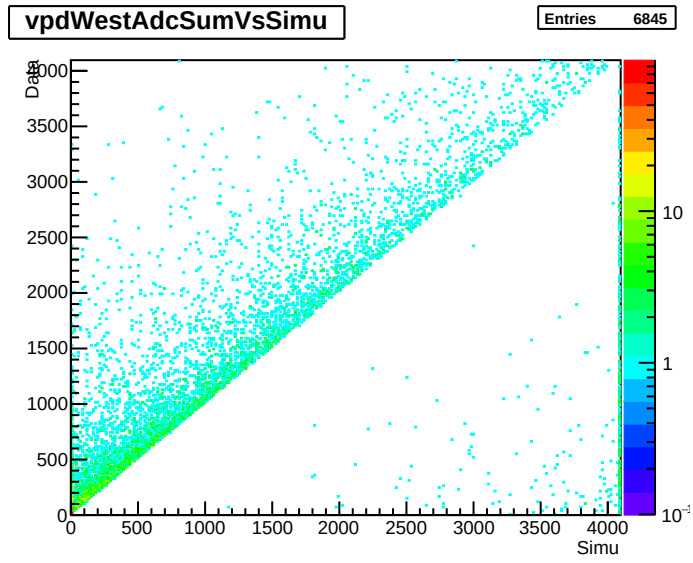

vpdEastNHits
Entries 15315

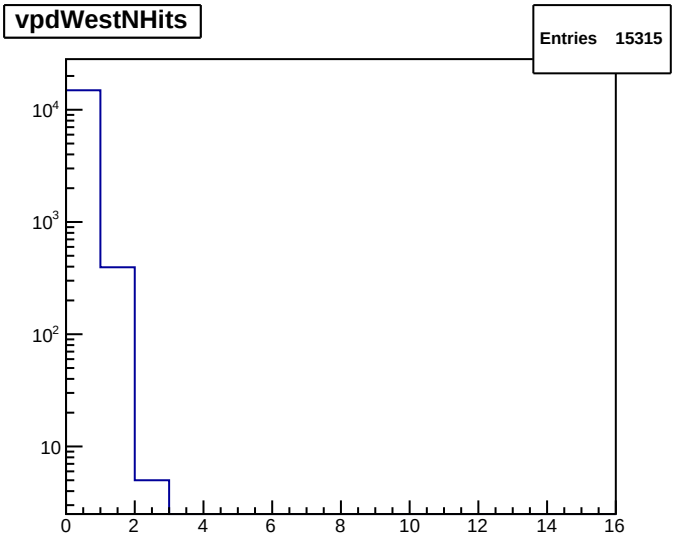

vpdEastNHitsVsSimu
Entries 1004

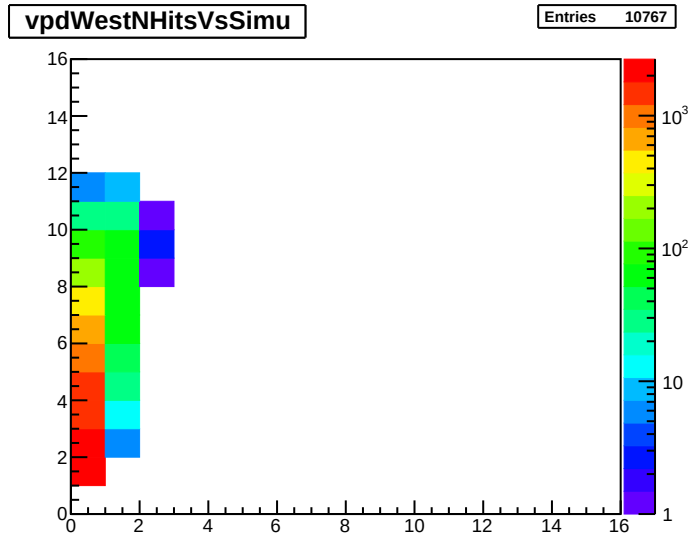

vpdEastTacSum
Entries 15315

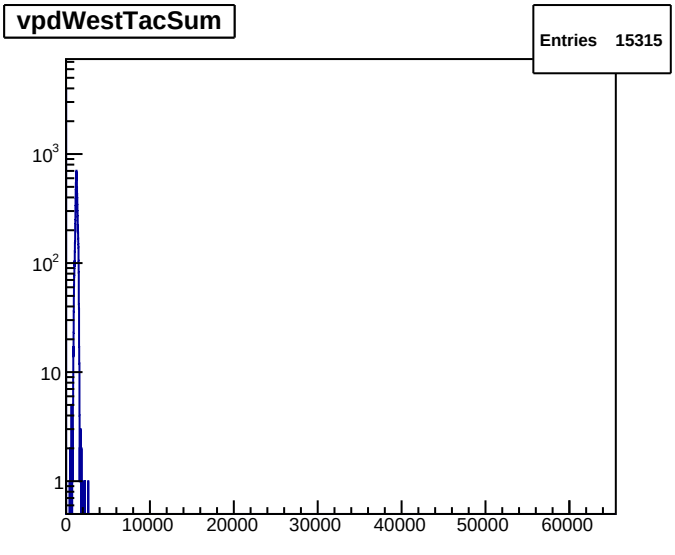

vpdEastTacSumVsSimu
Entries 12087
Data
Simu

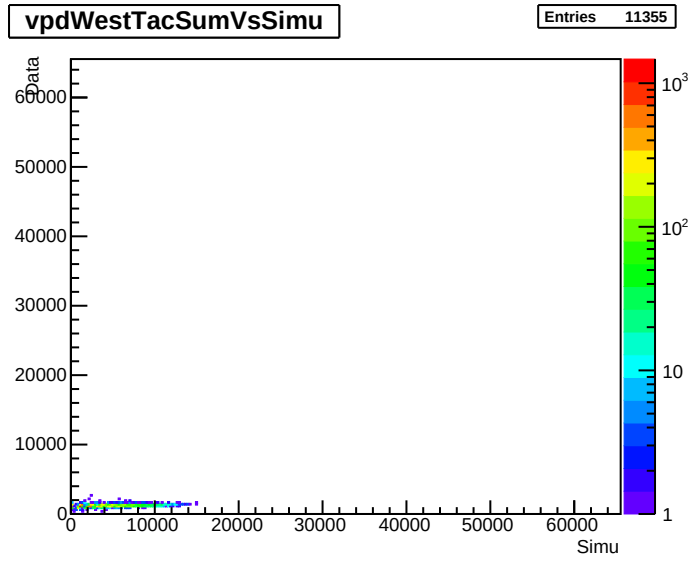

vpdWestAdcSum
Entries 15315

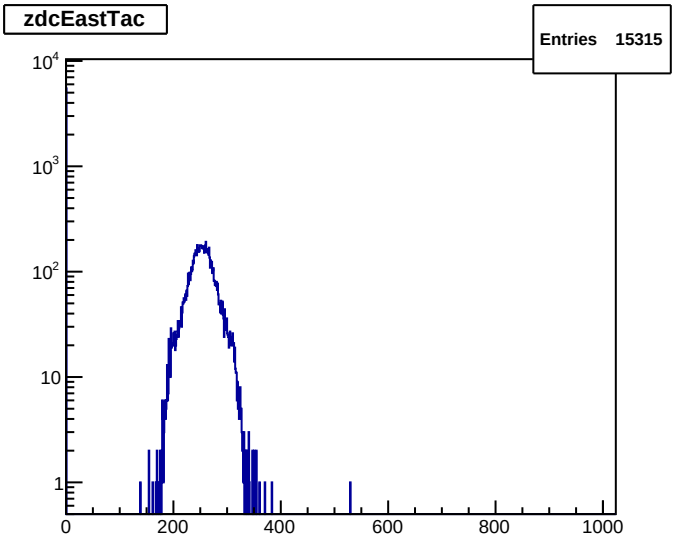

vpdWestAdcSumVsSimu
Entries 6845
Data
Simu

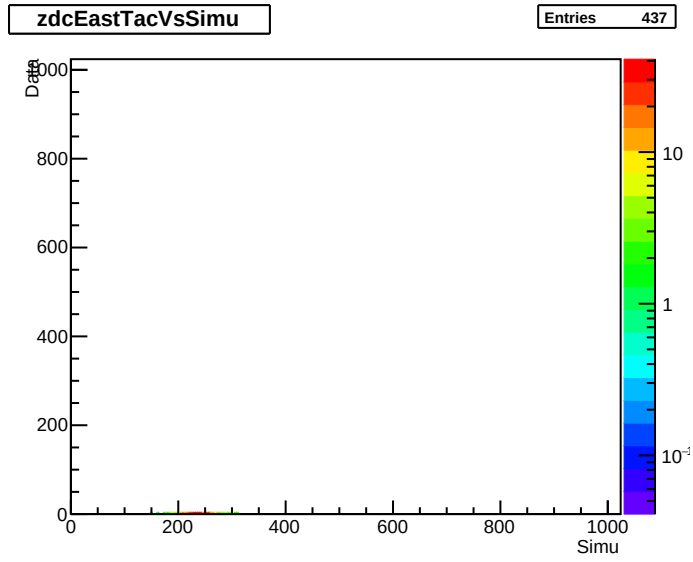

vpdWestNHits
Entries 15315

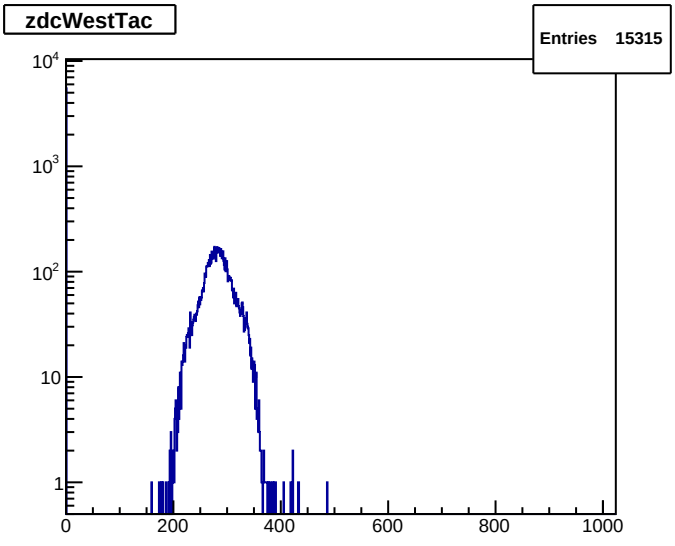

vpdWestNHitsVsSimu
Entries 10767

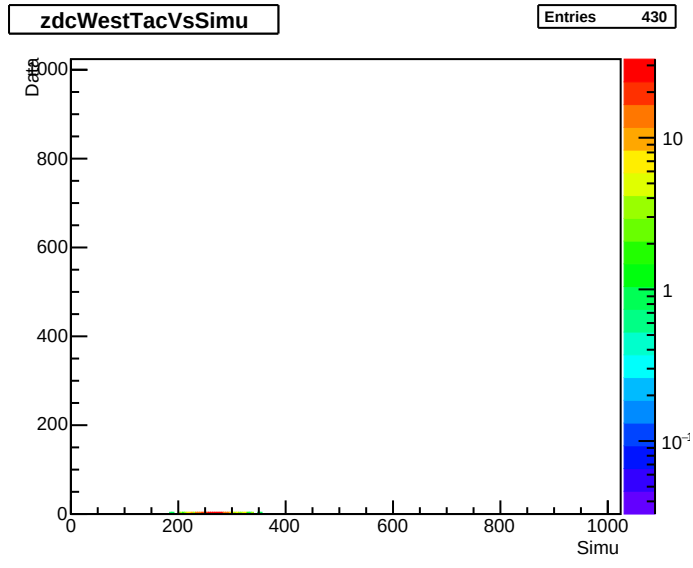

vpdWestTacSum
Entries 15315

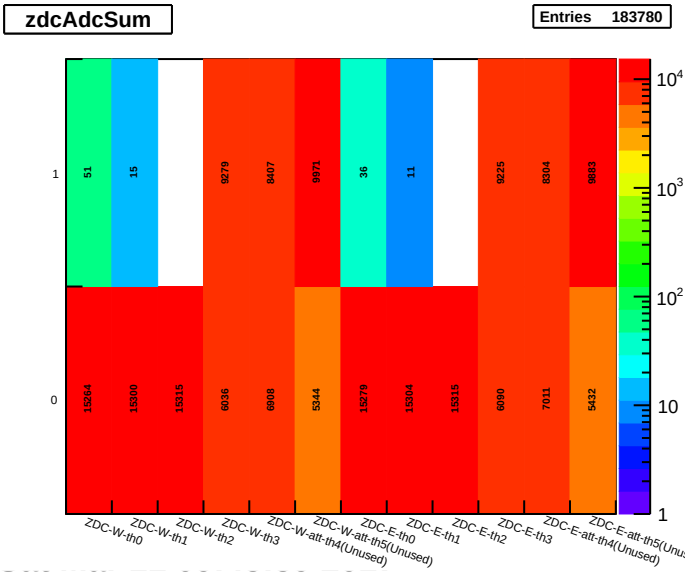

vpdWestTacSumVsSimu
Entries 11355
Data
Simu

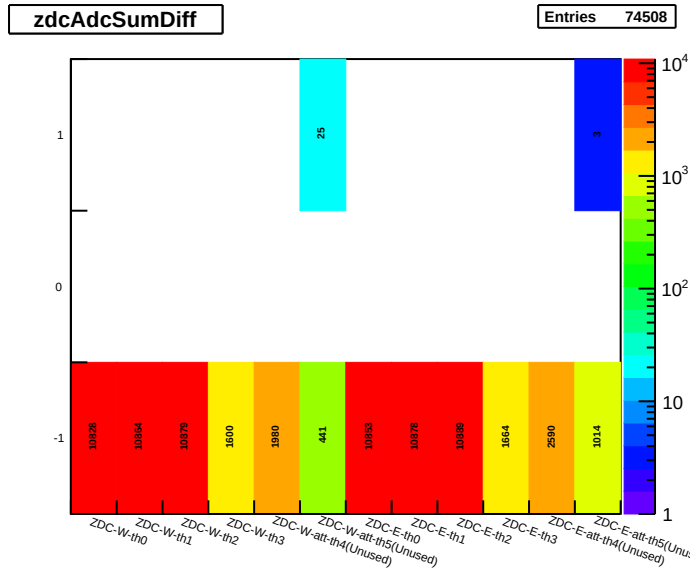

zdcEastTac
Entries 15315

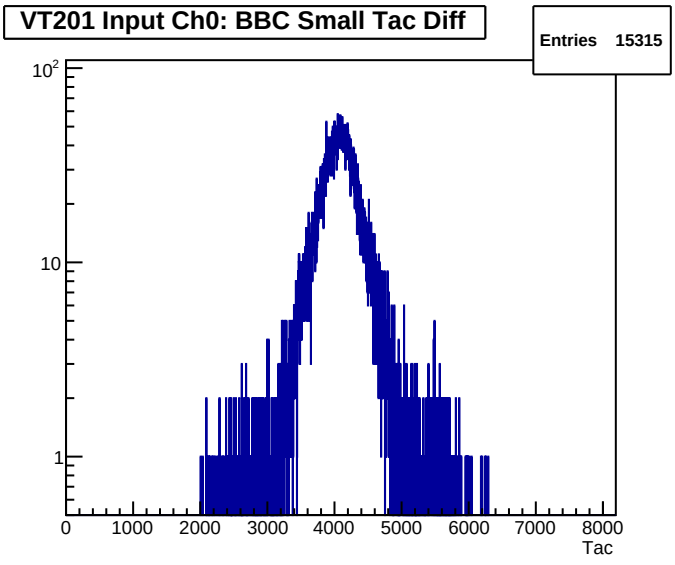

zdcEastTacVsSimu
Entries 437
Data
Simu

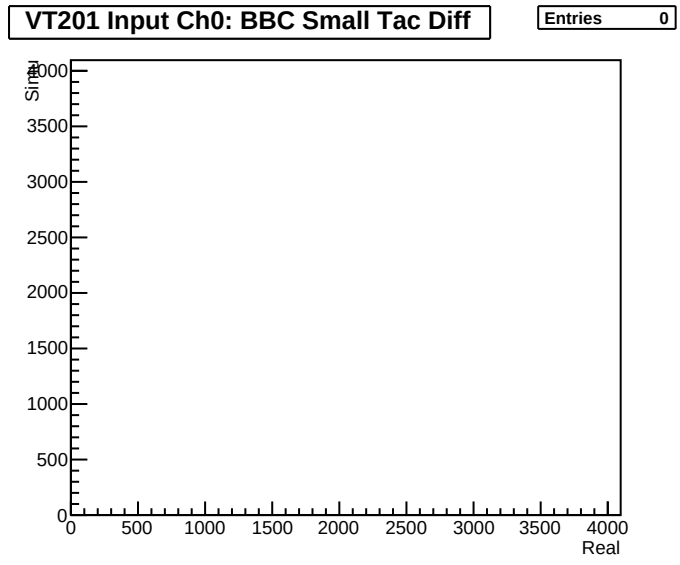

zdcWestTac
Entries 15315

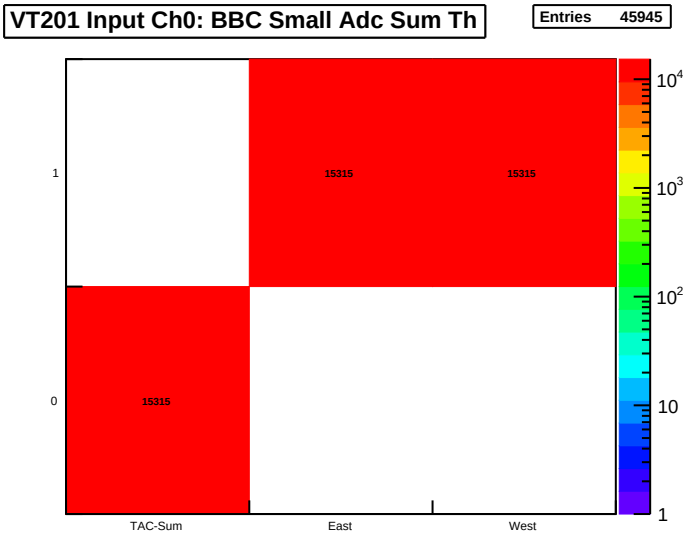

zdcWestTacVsSimu
Entries 430
Data
Simu

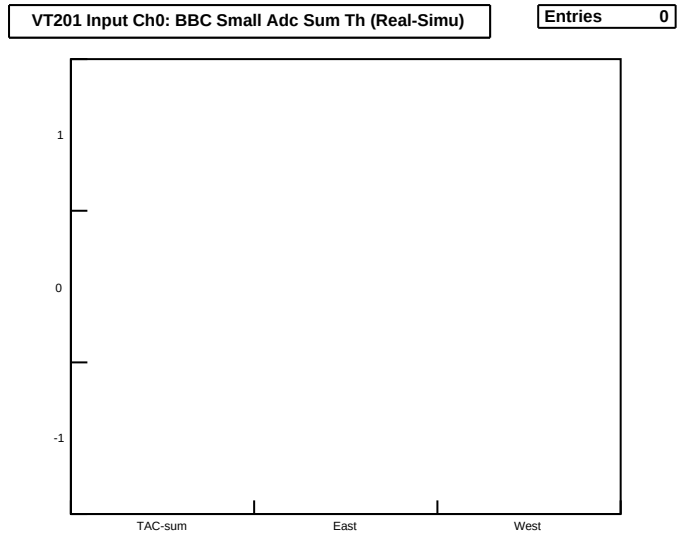

zdcAdcSum
Entries 183780
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

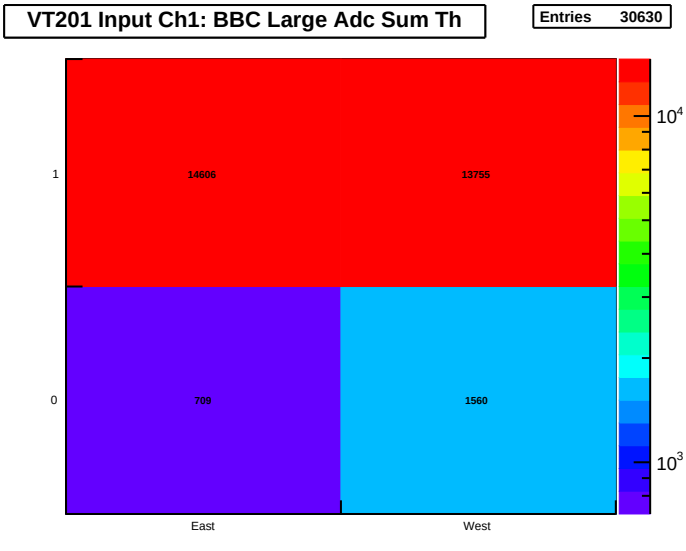

zdcAdcSumDiff
Entries 74508
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

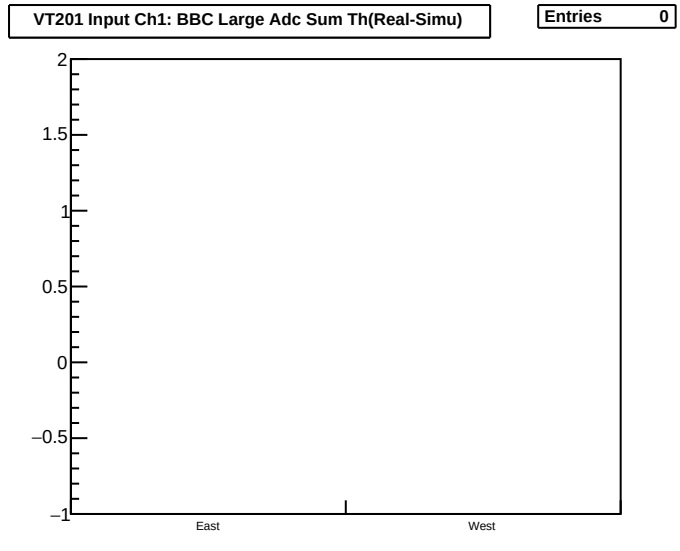

VT201 Input Ch0: BBC Small Tac Diff
Entries 15315
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

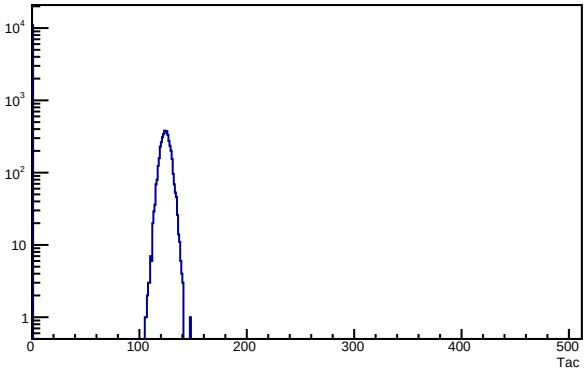

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 45945
15315
15315
15315
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

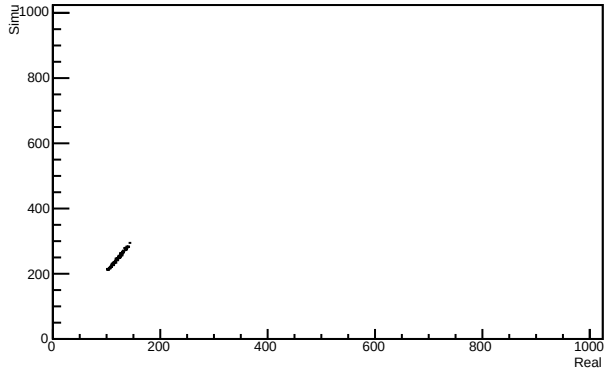

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 30630
14606
13755
709
1560
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

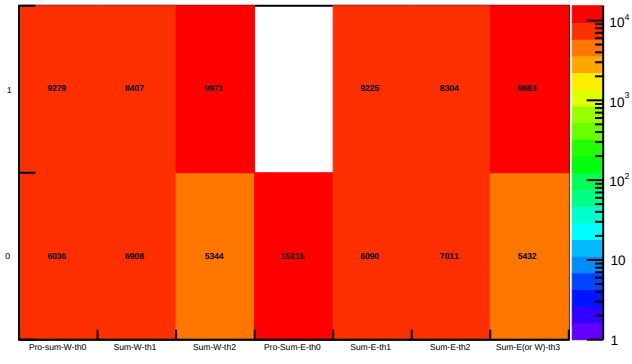
VT201 Input Ch2: ZDC Tac Diff



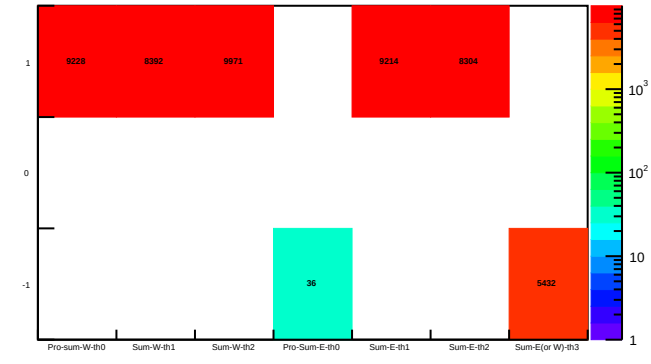
VT201 Input Ch2: ZDC Tac Diff



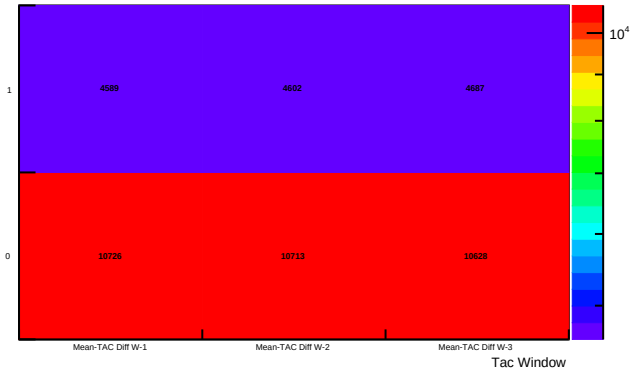
VT201 Input Ch2: ZDC Adc Sum Th



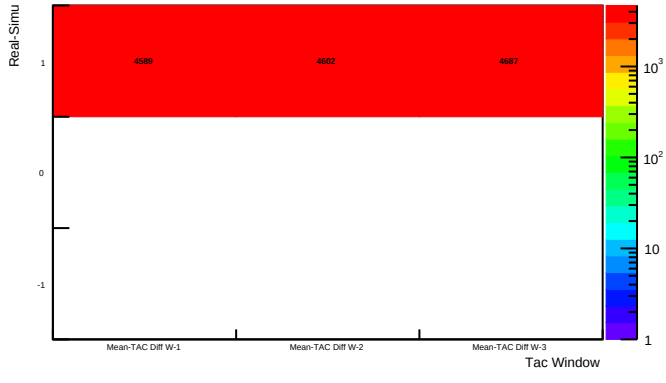
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



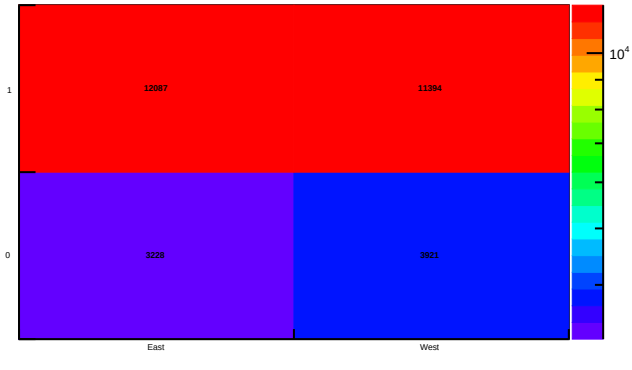
VT201 Input Ch4: VPD Tac Diff



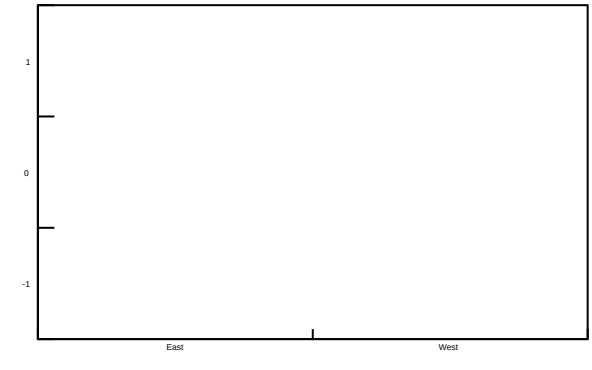
VT201 Input Ch4: VPD Tac Diff

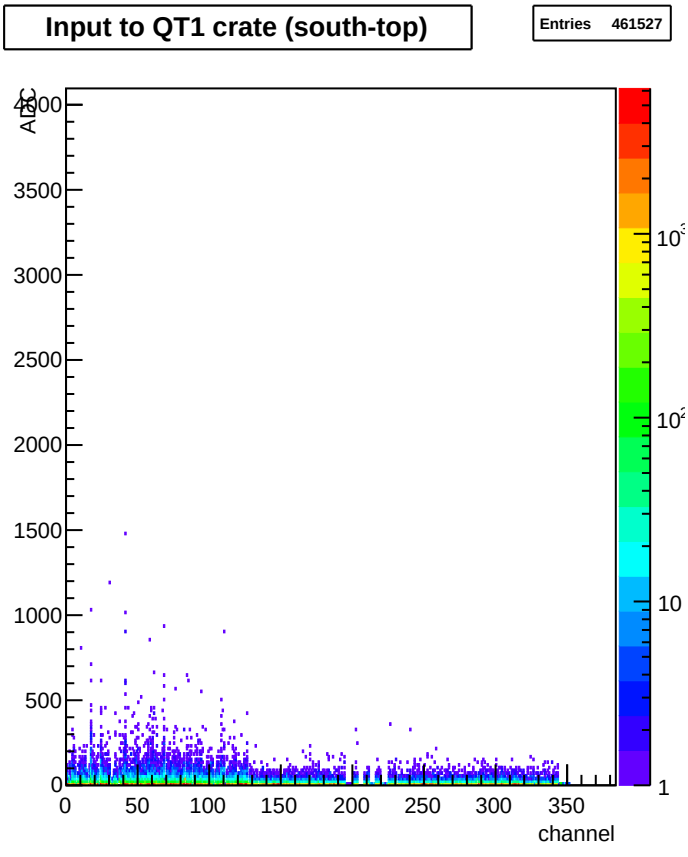


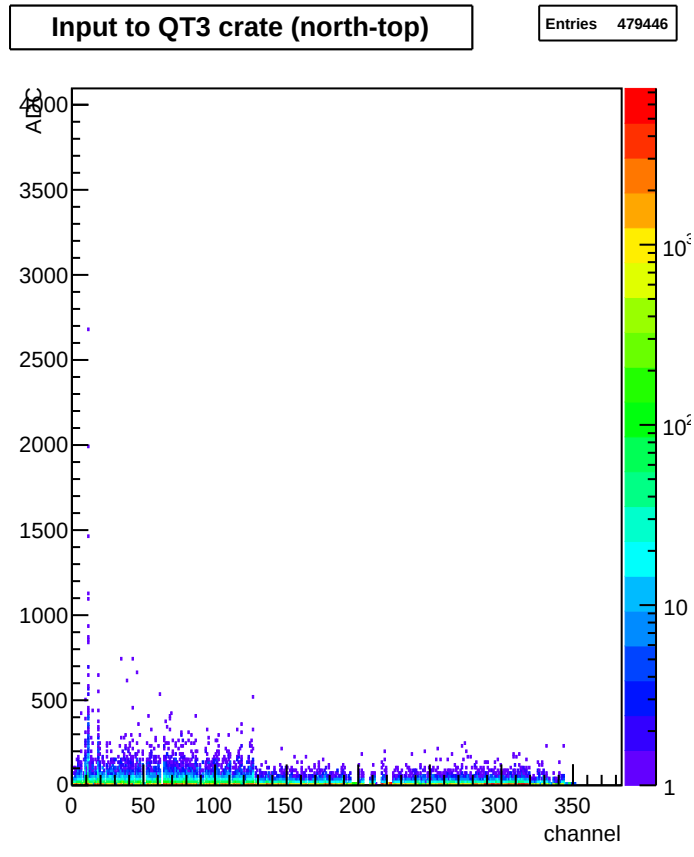
VT201 Input Ch4: VPD Adc Sum Th

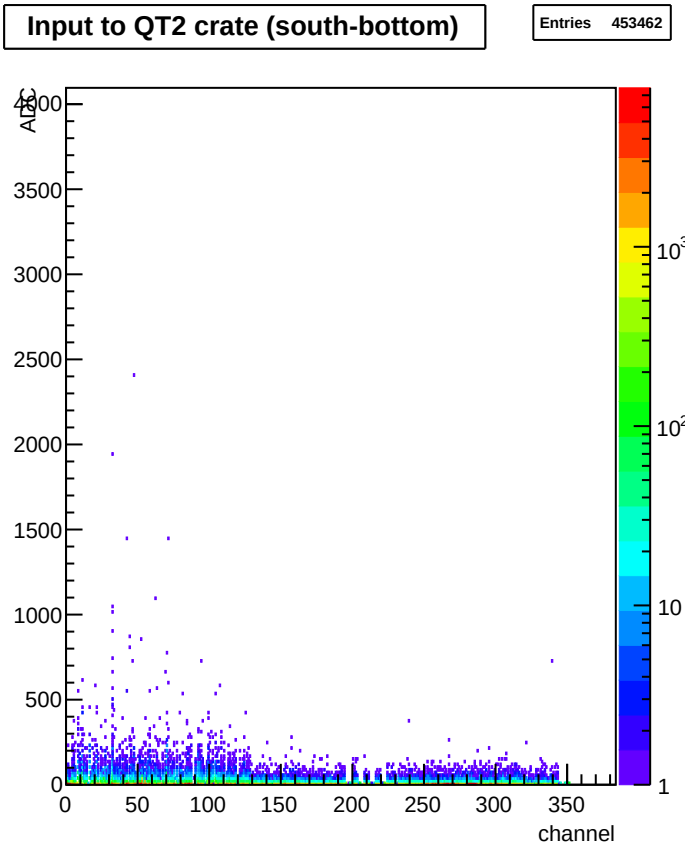


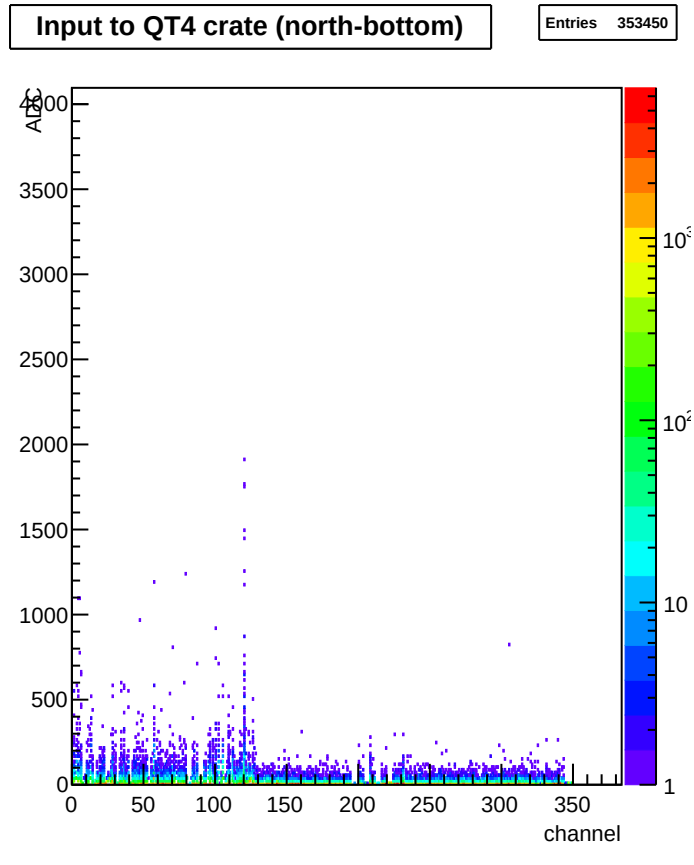
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

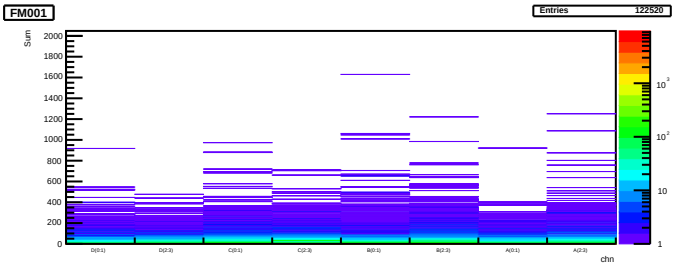


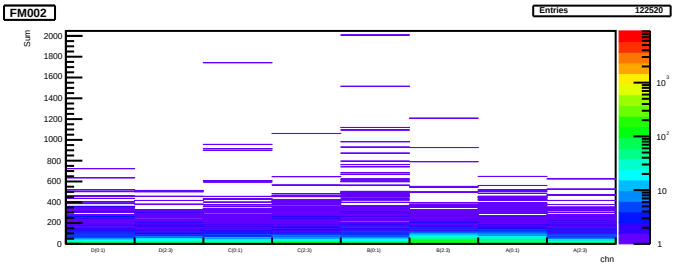

Input to QT1 crate (south-top)
Entries 461527
ADC
channel

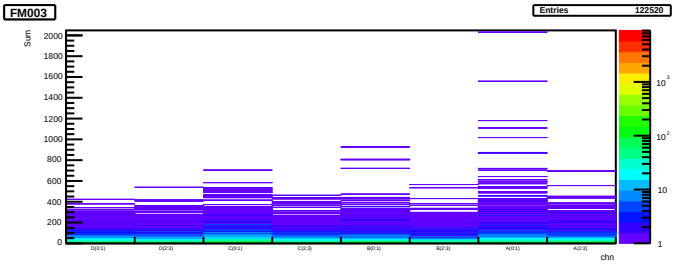

Input to QT3 crate (north-top)
Entries 479446
ADC
channel

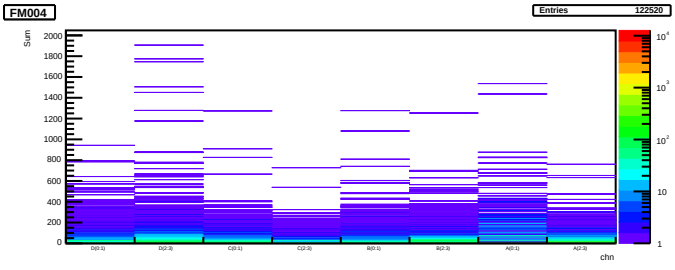

Input to QT2 crate (south-bottom)
Entries 453462
ADC
channel

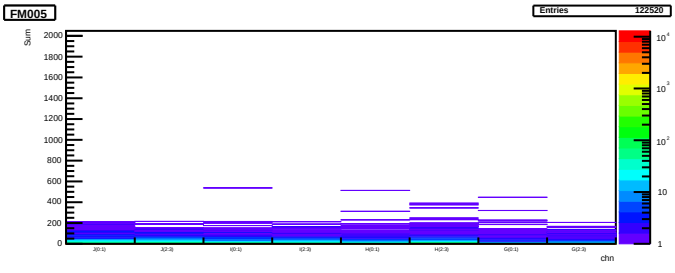

Input to QT4 crate (north-bottom)
Entries 353450
ADC
channel

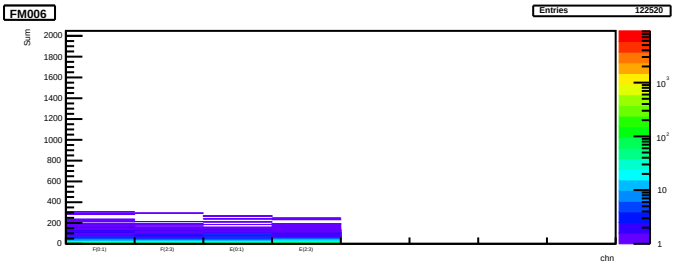

FM001
Entries 122520
Sum
chn

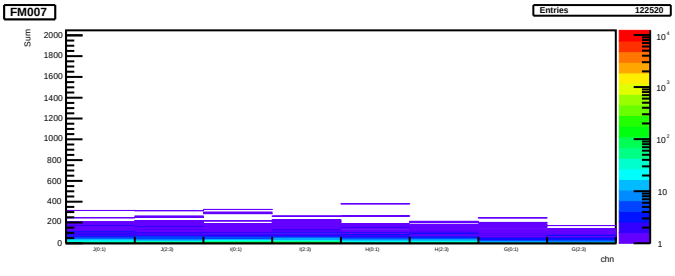

FM002
Entries 122520
Sum
chn

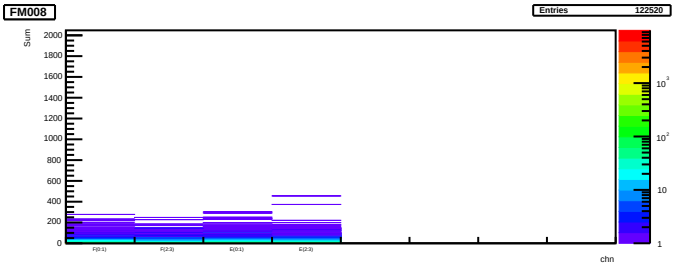

FM003
Entries 122520
Sum
chn

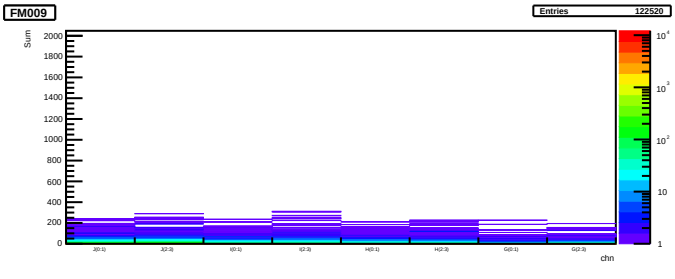

FM004
Entries 122520
Sum
chn

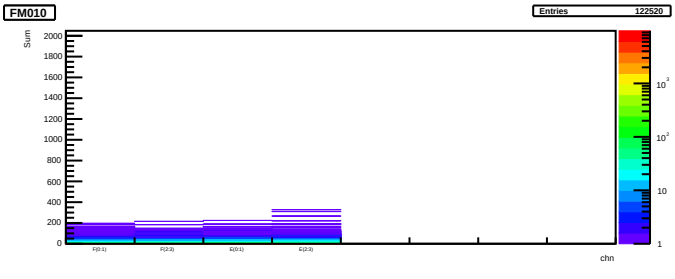

FM005
Entries 122520
Sum
chn

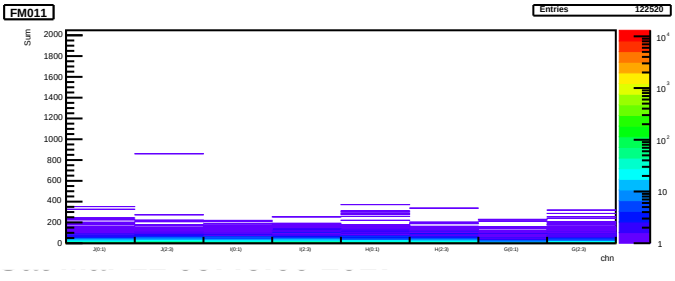

FM006
Entries 122520
Sum
chn

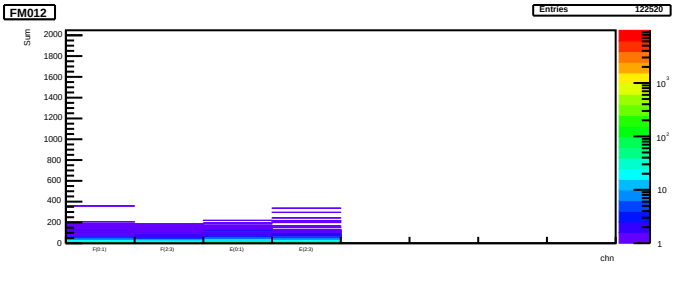

FM007
Entries 122520
Sum
chn

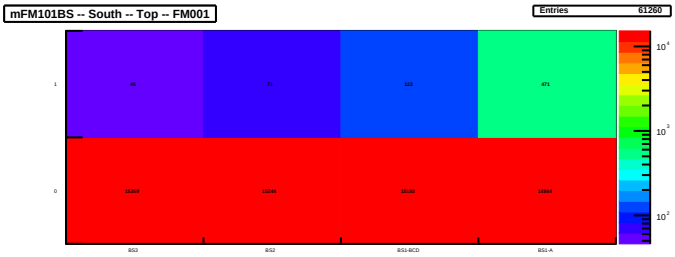

FM008
Entries 122520
Sum
chn

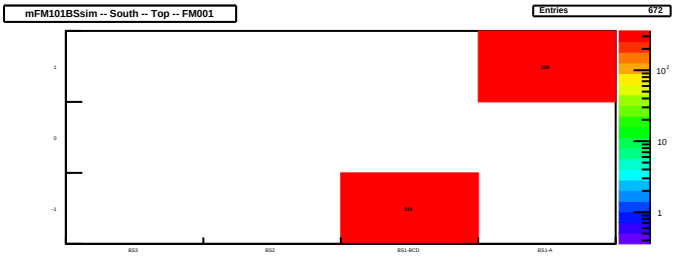

FM009
Entries 122520
Sum
chn

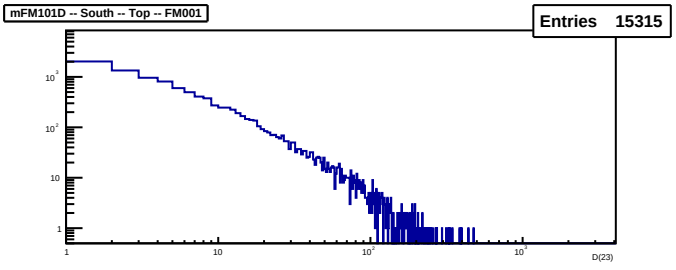

FM010
Entries 122520
Sum
chn

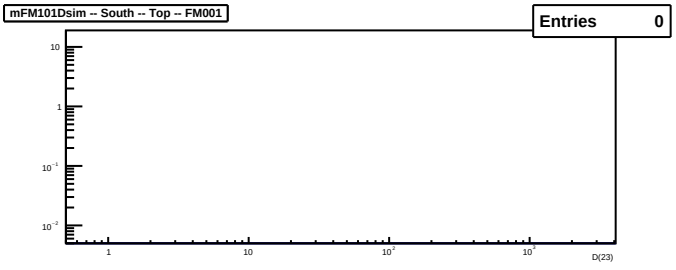

FM011
Entries 122520
Sum
chn

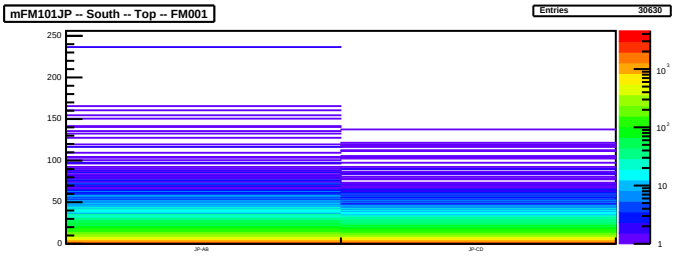

FM012
Entries 122520
Sum
chn


mFM101BS -- South -- Top -- FM001
Entries 61260

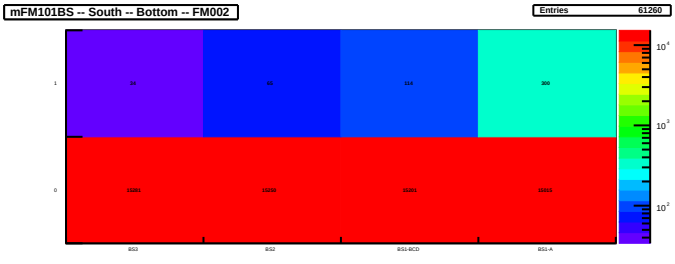

mFM101BSsim -- South -- Top -- FM001
Entries 672

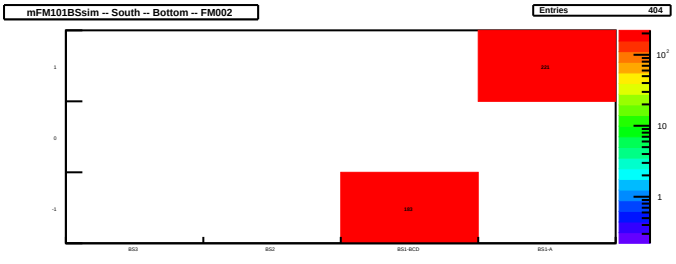

mFM101D -- South -- Top -- FM001
Entries 15315
D(23)

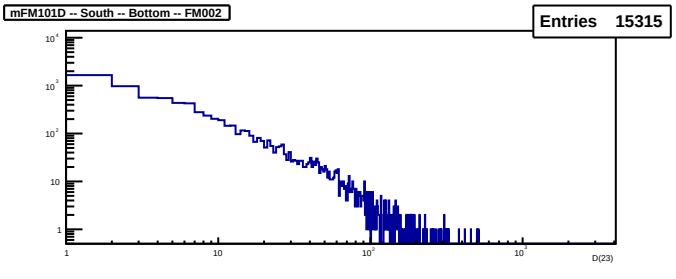

mFM101Dsim -- South -- Top -- FM001
Entries 0
D(23)

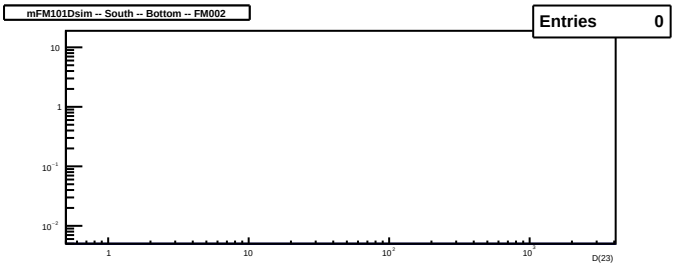

mFM101JP -- South -- Top -- FM001
Entries 30630

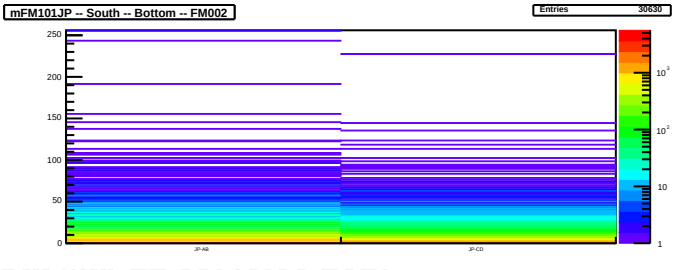
mFM101JPsim -- South -- Top -- FM001
Entries 0

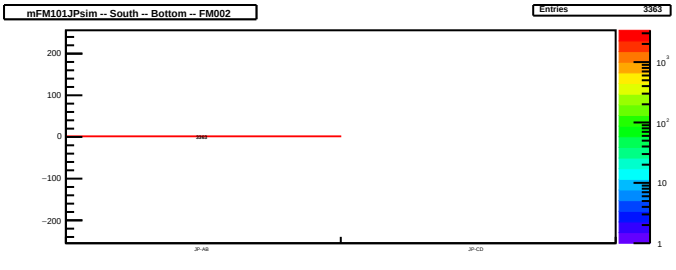

mFM101BS -- South -- Bottom -- FM002
Entries 61260

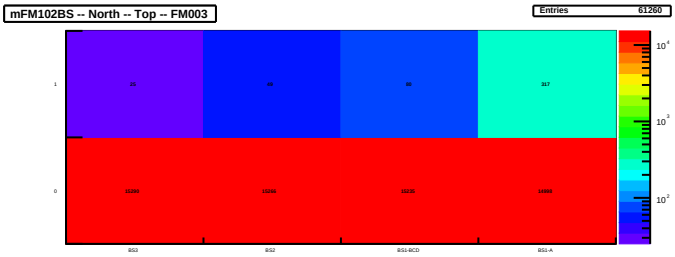

mFM101BSsim -- South -- Bottom -- FM002
Entries 404

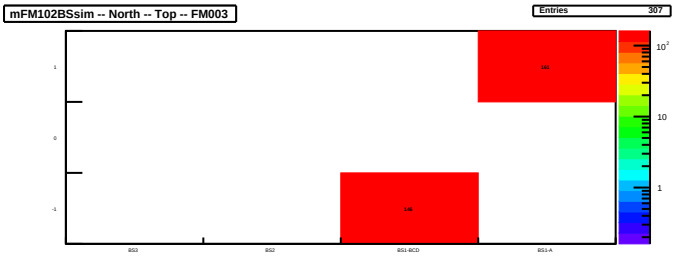

mFM101D -- South -- Bottom -- FM002
Entries 15315
D(23)

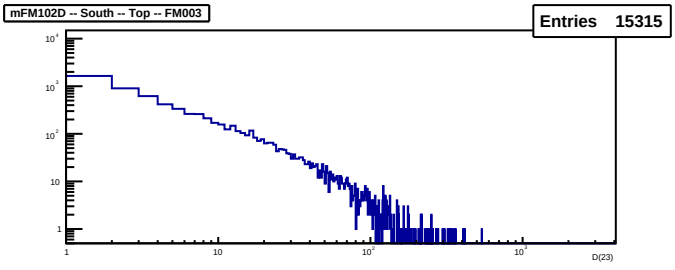

mFM101Dsim -- South -- Bottom -- FM002
Entries 0
D(23)

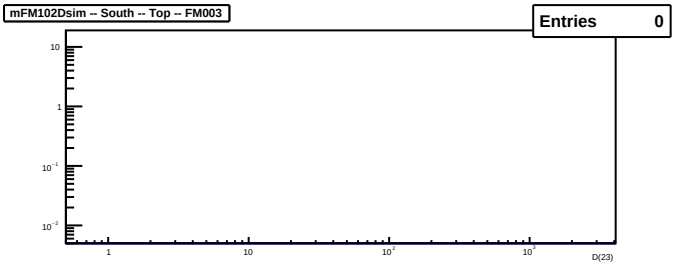

mFM101JP -- South -- Bottom -- FM002
Entries 30630

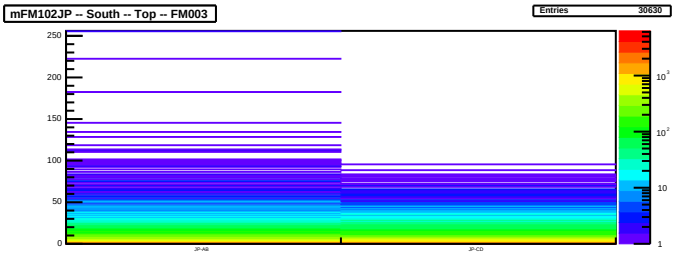

mFM101JPsim -- South -- Bottom -- FM002
Entries 3363


mFM102BS -- North -- Top -- FM003
Entries 61260
25
45
86
317
15290
15246
15235
14888
BS3
BS2
BS1-BCD
BS1-A

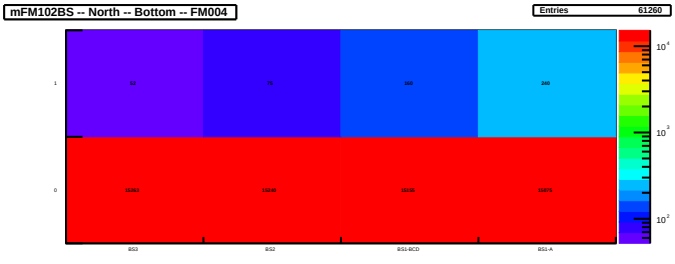

mFM102BSsim -- North -- Top -- FM003
Entries 307
161
146
BS3
BS2
BS1-BCD
BS1-A

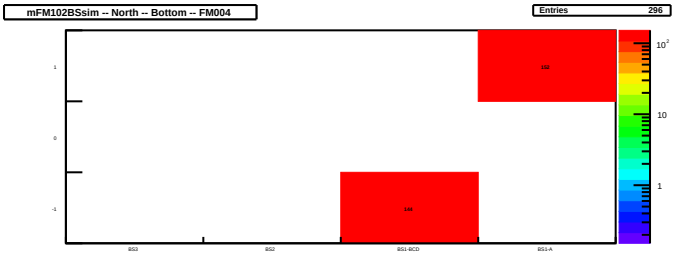

mFM102D -- South -- Top -- FM003
Entries 15315
D(23)

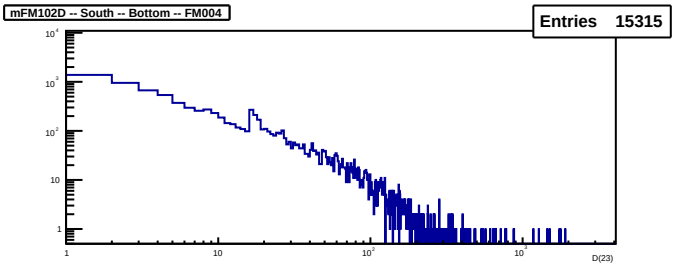

mFM102Dsim -- South -- Top -- FM003
Entries 0
D(23)

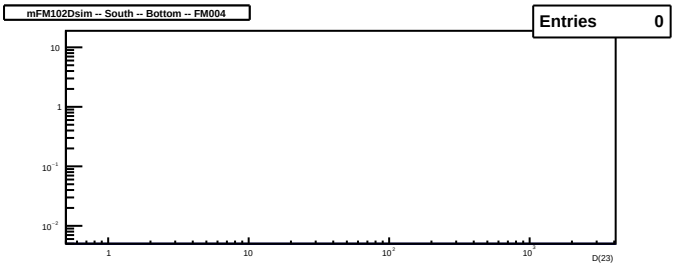

mFM102JP -- South -- Top -- FM003
Entries 30630
JP-AB
JP-CD

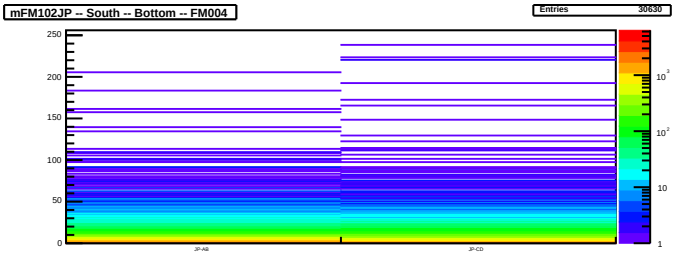
mFM102JPsim -- South -- Top -- FM003
Entries 0
JP-AB
JP-CD

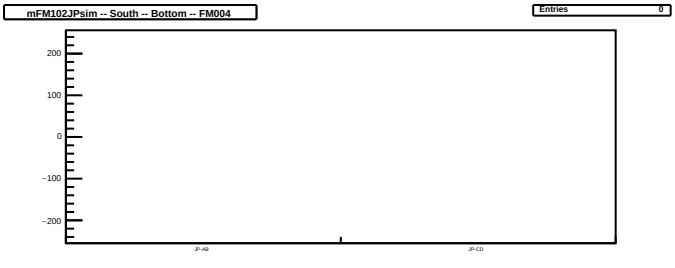

mFM102BS -- North -- Bottom -- FM004
Entries 61260
52
75
160
240
15263
15240
15155
15075
BS3
BS2
BS1-BCD
BS1-A

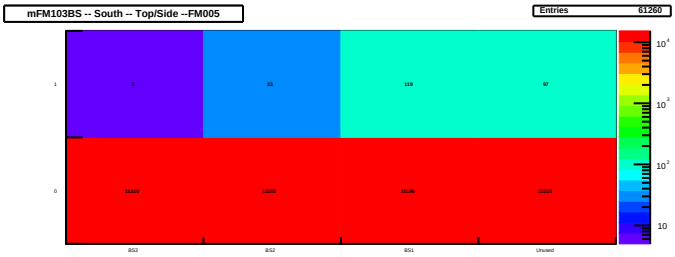

mFM102BSsim -- North -- Bottom -- FM004
Entries 296
152
144
BS3
BS2
BS1-BCD
BS1-A

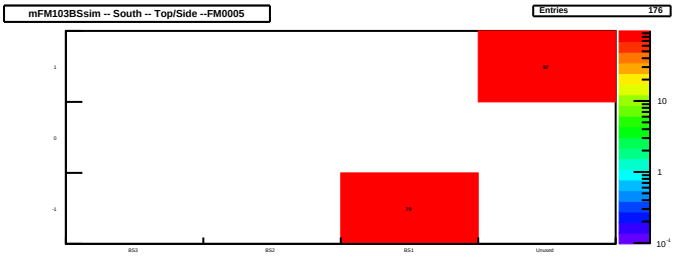

mFM102D -- South -- Bottom -- FM004
Entries 15315
D(23)

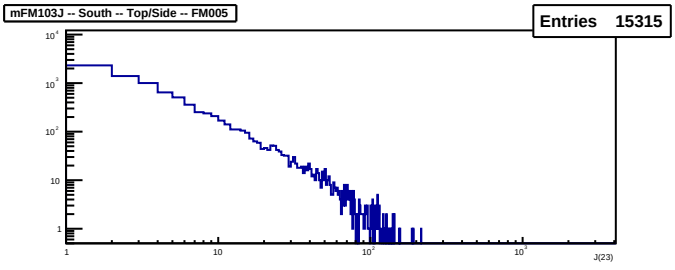

mFM102Dsim -- South -- Bottom -- FM004
Entries 0
D(23)

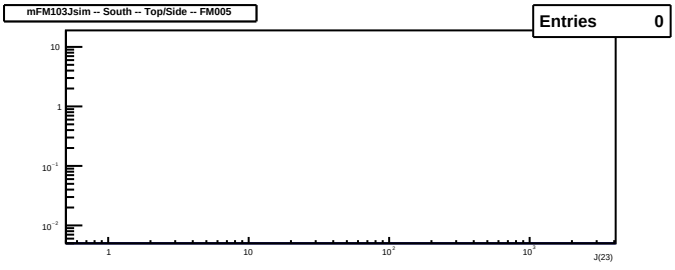

mFM102JP -- South -- Bottom -- FM004
Entries 30630
JP-AB
JP-CD

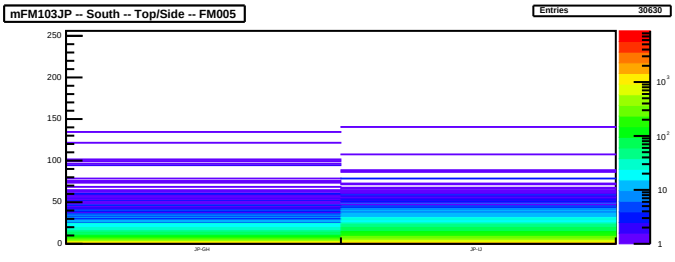

mFM102JPsim -- South -- Bottom -- FM004
Entries 0
JP-AB
JP-CD

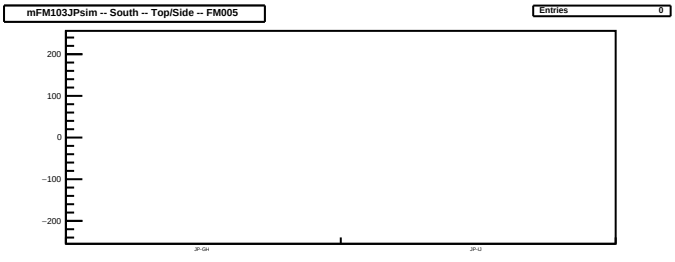

mFM103BS -- South -- Top/Side --FM005
Entries 61260
BS3
BS2
BS1
Unused

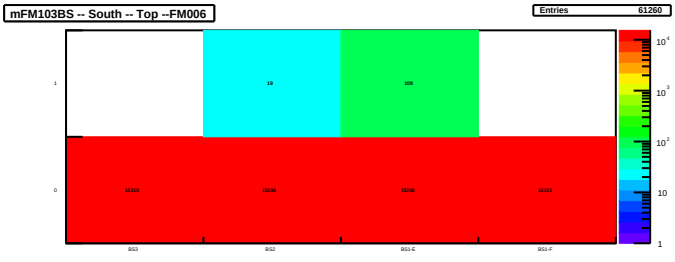

mFM103BSsim -- South -- Top/Side --FM0005
Entries 176
BS3
BS2
BS1
Unused

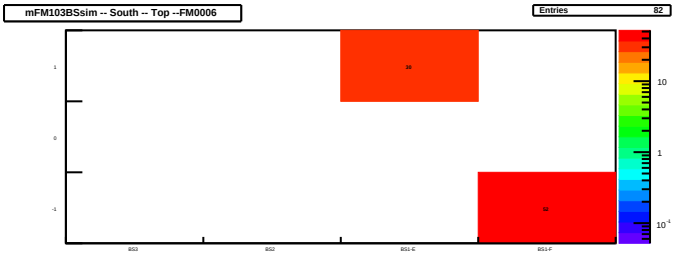

mFM103J -- South -- Top/Side -- FM005
Entries 15315
J(23)

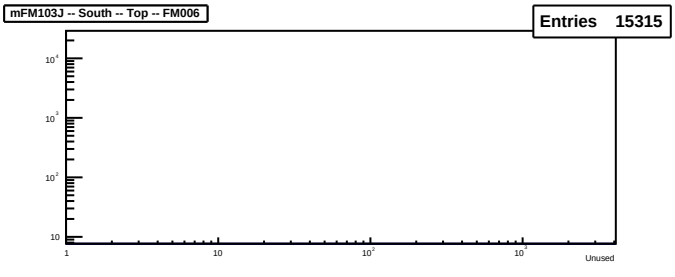

mFM103Jsim -- South -- Top/Side -- FM005
Entries 0
J(23)

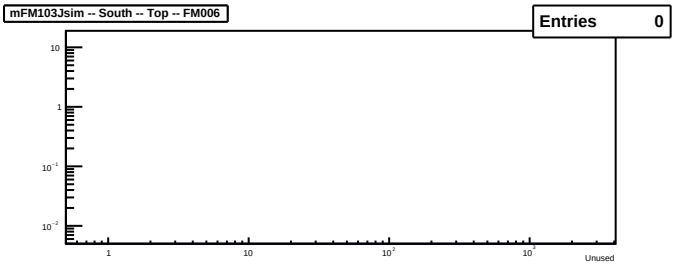

mFM103JP -- South -- Top/Side -- FM005
Entries 30630
JP-GH
JP-IJ

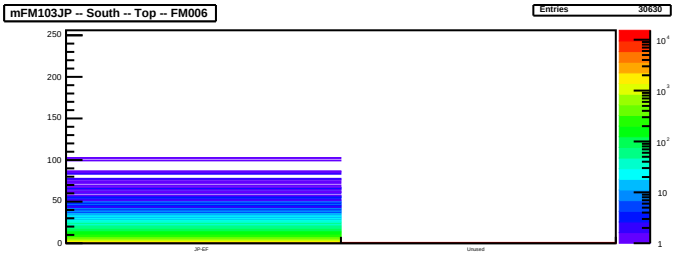

mFM103JPsim -- South -- Top/Side -- FM005
Entries 0
JP-GH
JP-IJ

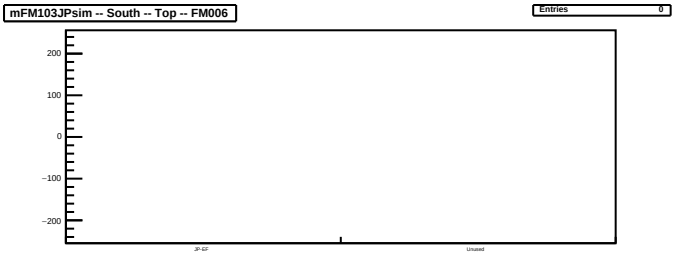

mFM103BS -- South -- Top --FM006
Entries 61260
BS3
BS2
BS1-E
BS1-F

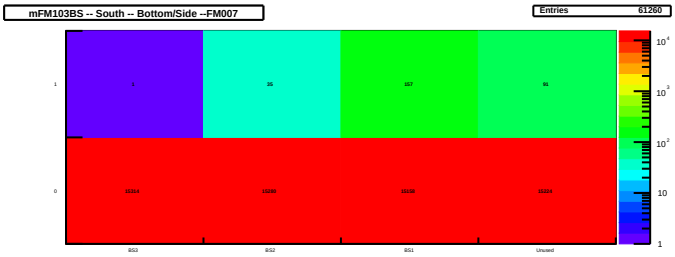

mFM103BSsim -- South -- Top --FM0006
Entries 82
BS3
BS2
BS1-E
BS1-F

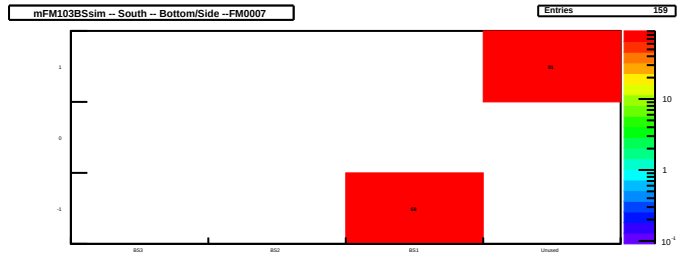

mFM103J -- South -- Top -- FM006
Entries 15315
Unused

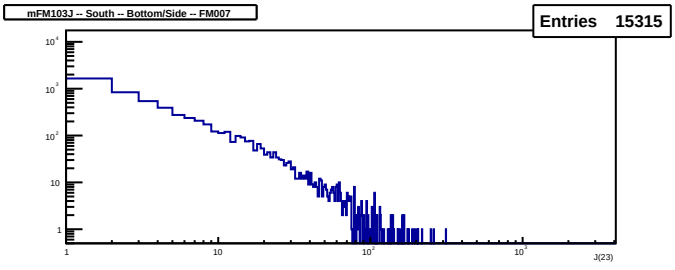

mFM103Jsim -- South -- Top -- FM006
Entries 0
Unused

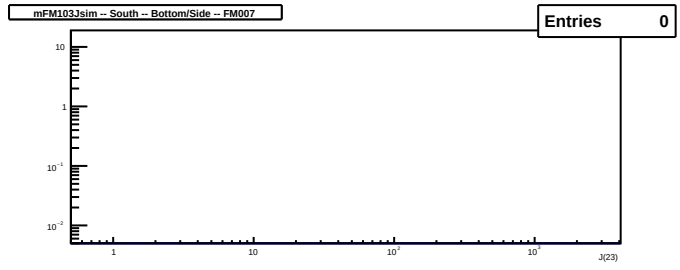

mFM103JP -- South -- Top -- FM006
Entries 30630
JP-EF
Unused

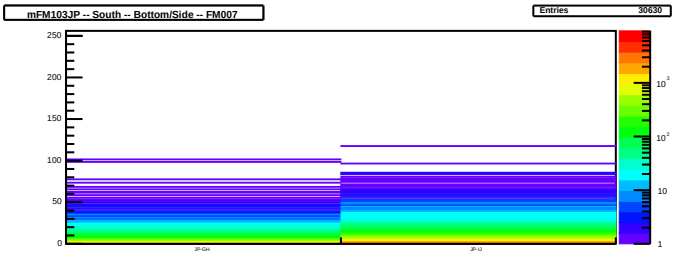

mFM103JPsim -- South -- Top -- FM006
Entries 0
JP-EF
Unused

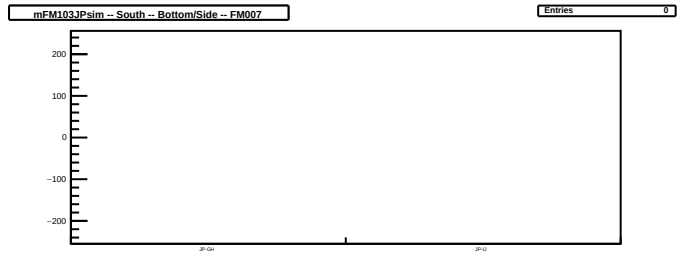

mFM103BS -- South -- Bottom/Side --FM007
Entries 61260

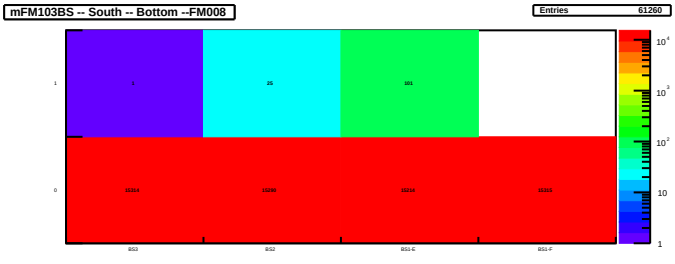

mFM103BSsim -- South -- Bottom/Side --FM0007
Entries 159

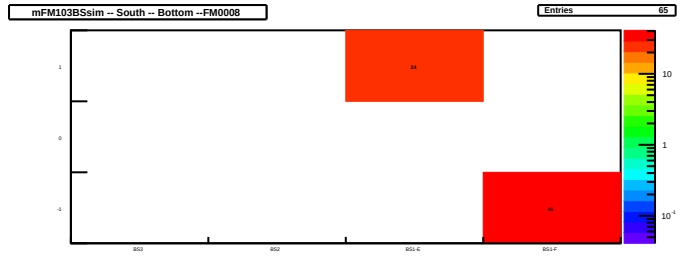

mFM103J -- South -- Bottom/Side -- FM007
Entries 15315
J(23)

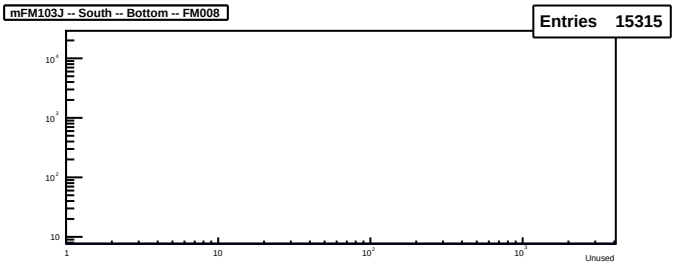

mFM103Jsim -- South -- Bottom/Side -- FM007
Entries 0
J(23)

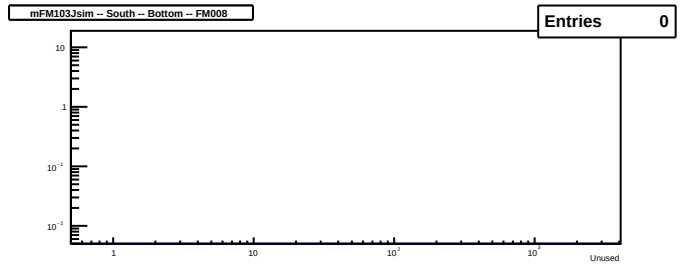

mFM103JP -- South -- Bottom/Side -- FM007
Entries 30630

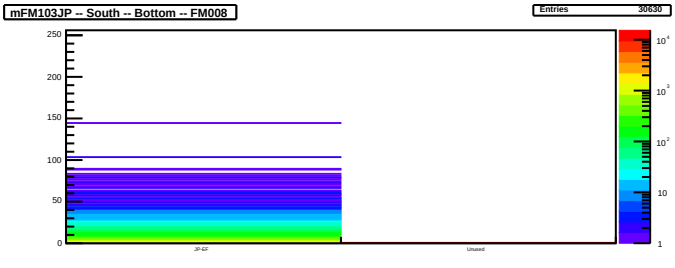

mFM103JPsim -- South -- Bottom/Side -- FM007
Entries 0

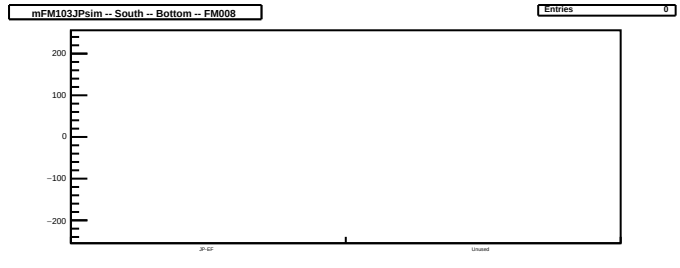

mFM103BS -- South -- Bottom --FM008
Entries 61260

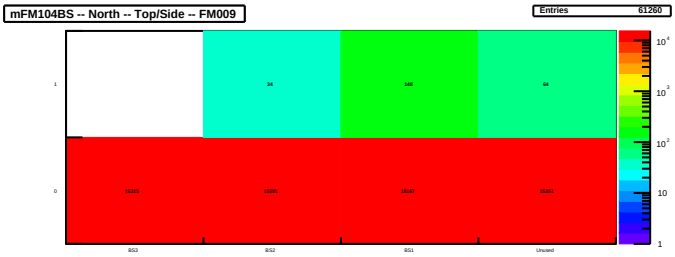

mFM103BSsim -- South -- Bottom --FM0008
Entries 65

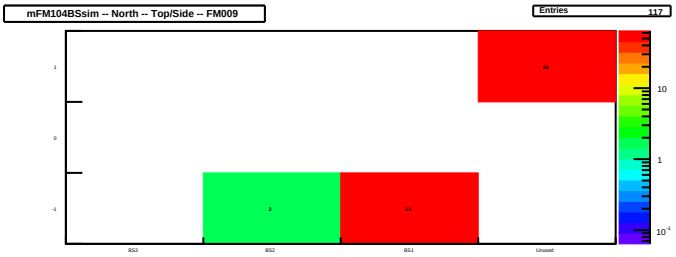

mFM103J -- South -- Bottom -- FM008
Entries 15315
Unused

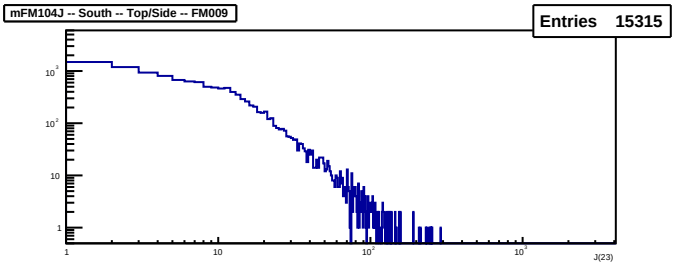

mFM103Jsim -- South -- Bottom -- FM008
Entries 0
Unused

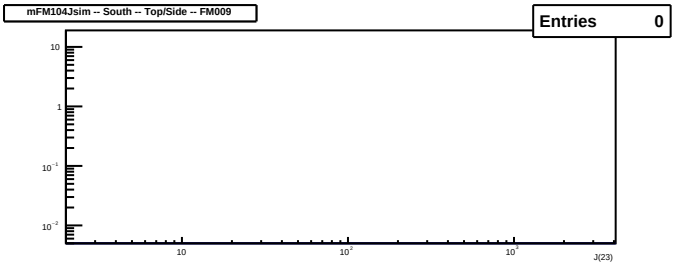

mFM103JP -- South -- Bottom -- FM008
Entries 30630

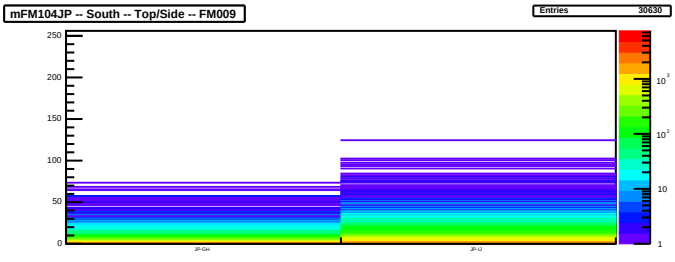

mFM103JPsim -- South -- Bottom -- FM008
Entries 0

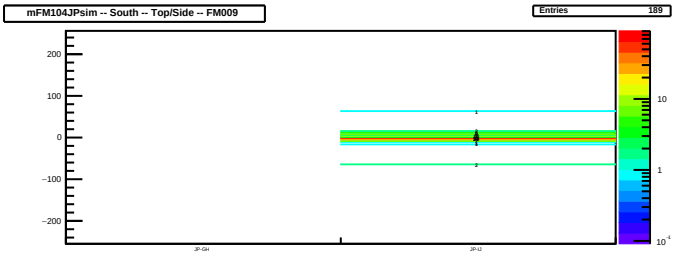

mFM104BS -- North -- Top/Side -- FM009
Entries 61260

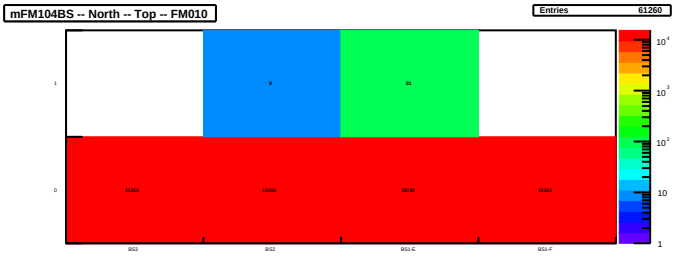

mFM104BSsim -- North -- Top/Side -- FM009
Entries 117

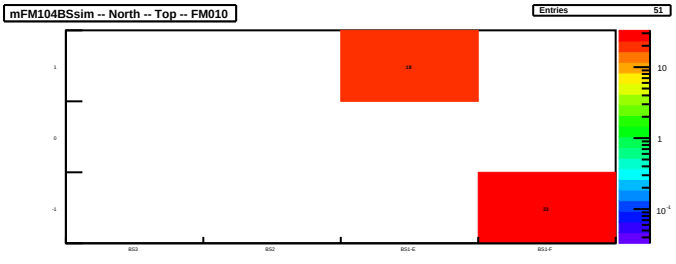

mFM104J -- South -- Top/Side -- FM009
Entries 15315
J(23)

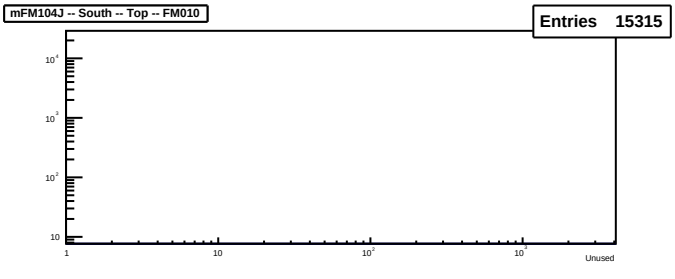

mFM104Jsim -- South -- Top/Side -- FM009
Entries 0
J(23)

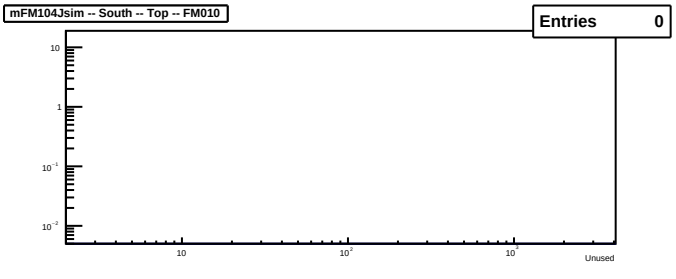

mFM104JP -- South -- Top/Side -- FM009
Entries 30630

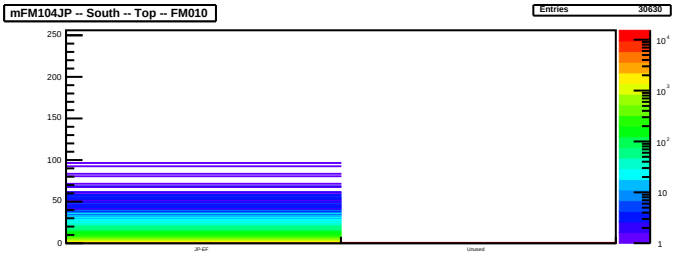

mFM104JPsim -- South -- Top/Side -- FM009
Entries 189

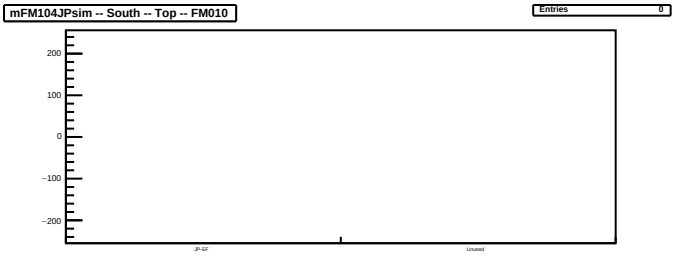

mFM104BS -- North -- Top -- FM010
Entries 61260

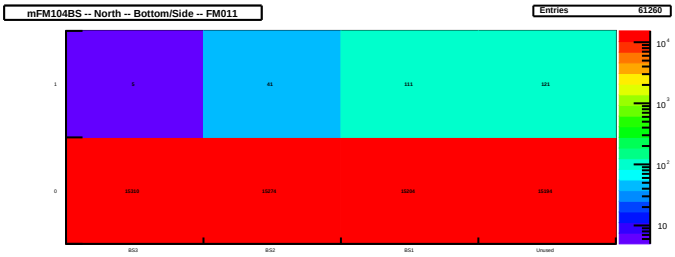

mFM104BSsim -- North -- Top -- FM010
Entries 51

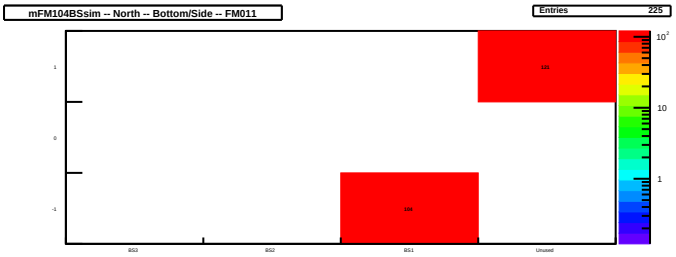

mFM104J -- South -- Top -- FM010
Entries 15315
Unused

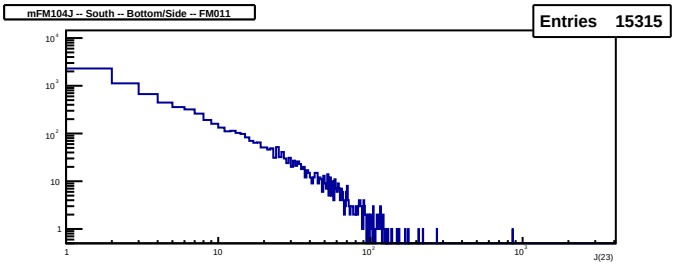

mFM104Jsim -- South -- Top -- FM010
Entries 0
Unused

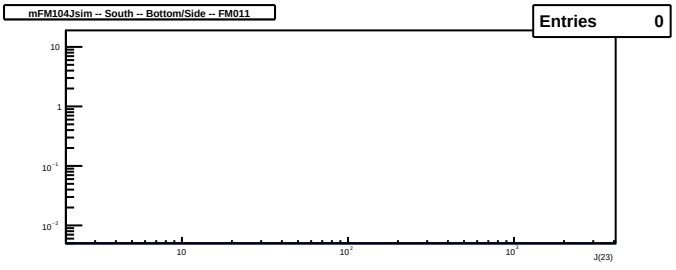

mFM104JP -- South -- Top -- FM010
Entries 30630

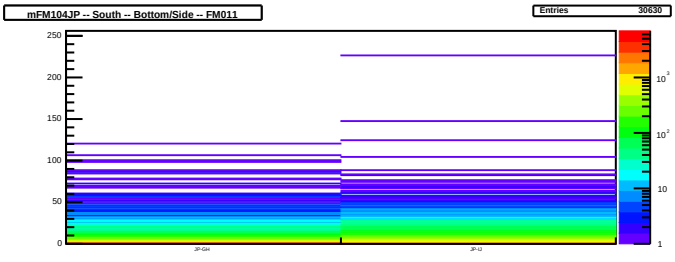

mFM104JPsim -- South -- Top -- FM010
Entries 0

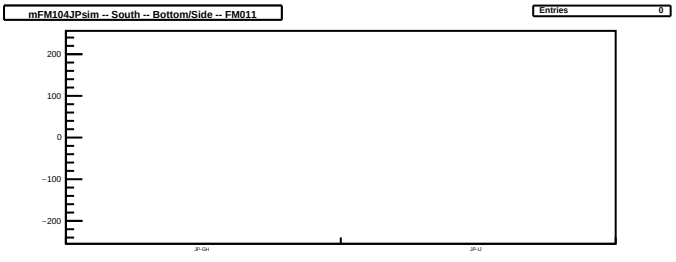

mFM104BS -- North -- Bottom/Side -- FM011
Entries 61260

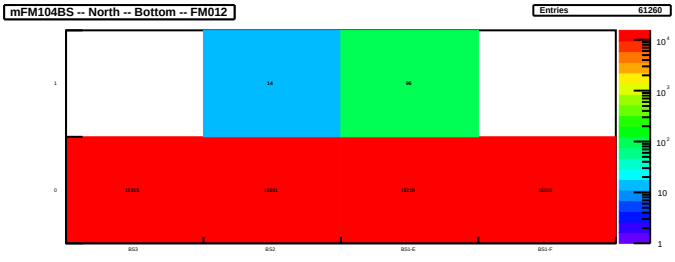

mFM104BSsim -- North -- Bottom/Side -- FM011
Entries 225

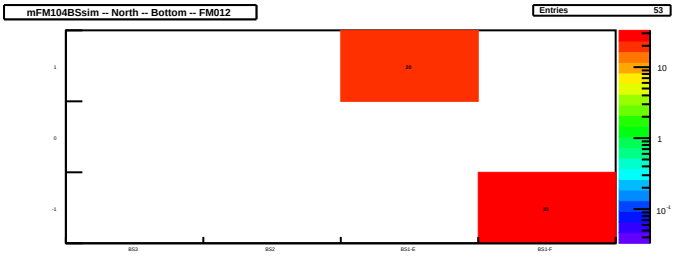

mFM104J -- South -- Bottom/Side -- FM011
Entries 15315
J(23)

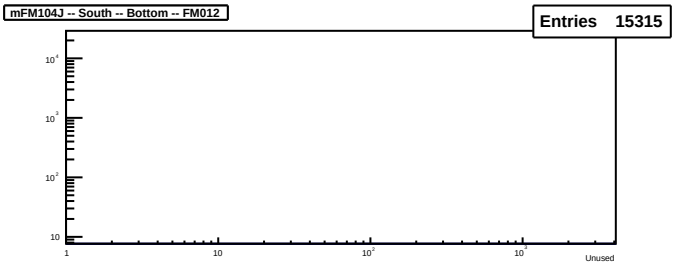

mFM104Jsim -- South -- Bottom/Side -- FM011
Entries 0
J(23)

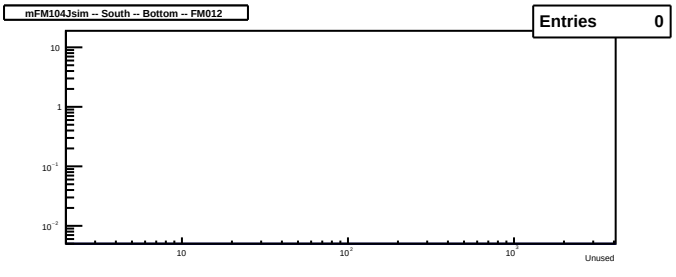

mFM104JP -- South -- Bottom/Side -- FM011
Entries 30630

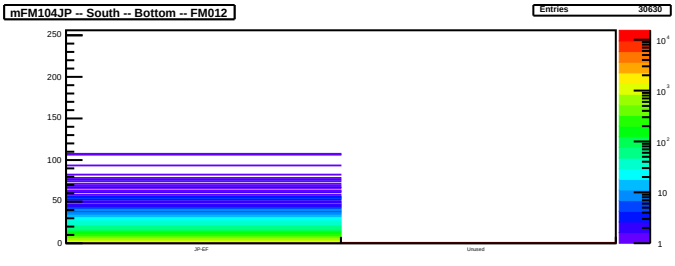

mFM104JPsim -- South -- Bottom/Side -- FM011
Entries 0


mFM104BS -- North -- Bottom -- FM012
Entries 61260

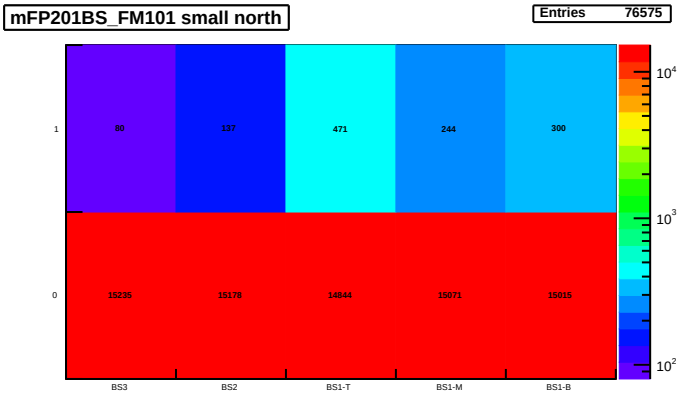

mFM104BSsim -- North -- Bottom -- FM012
Entries 53

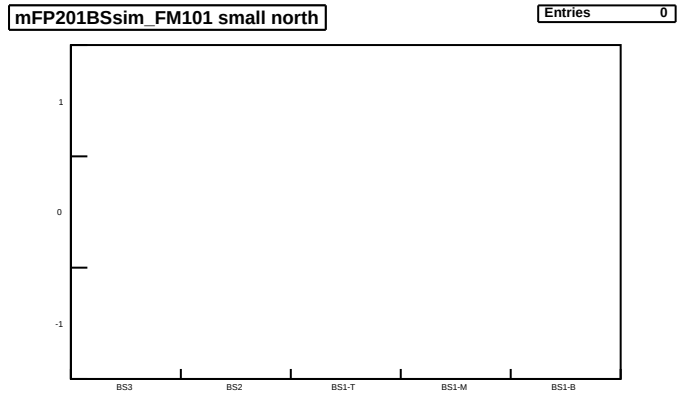

mFM104J -- South -- Bottom -- FM012
Entries 15315
Unused

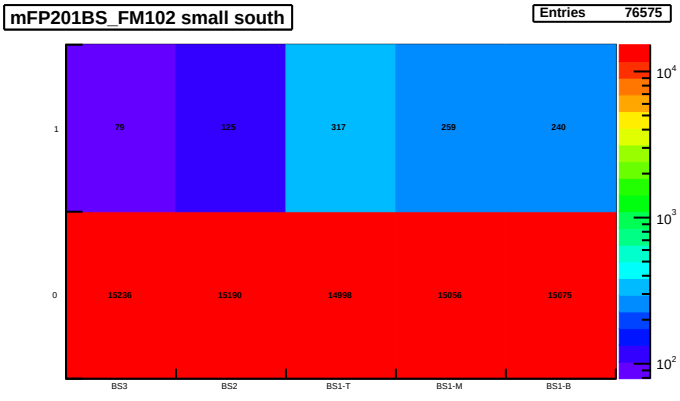

mFM104Jsim -- South -- Bottom -- FM012
Entries 0
Unused

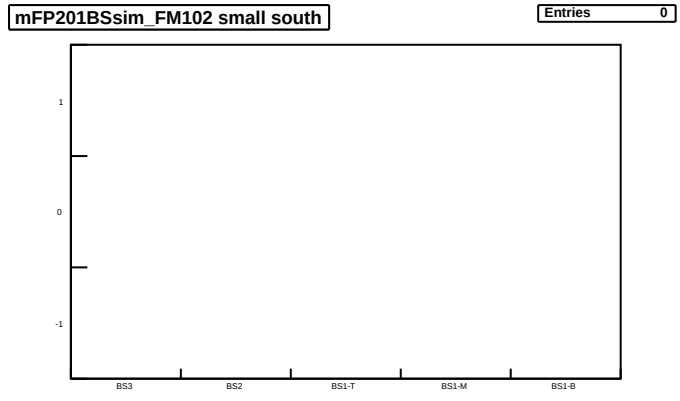

mFM104JP -- South -- Bottom -- FM012
Entries 30630

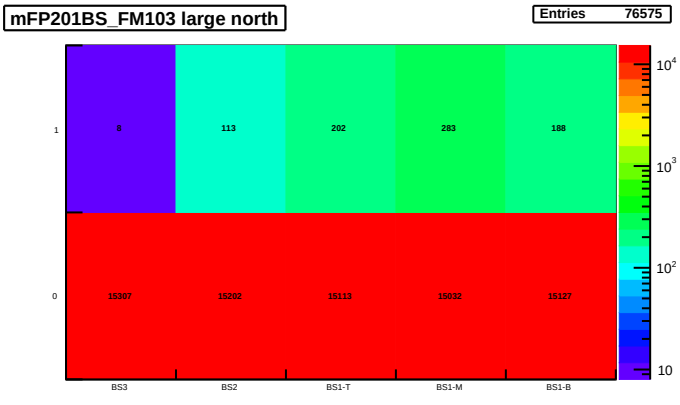
mFM104JPsim -- South -- Bottom -- FM012
Entries 0

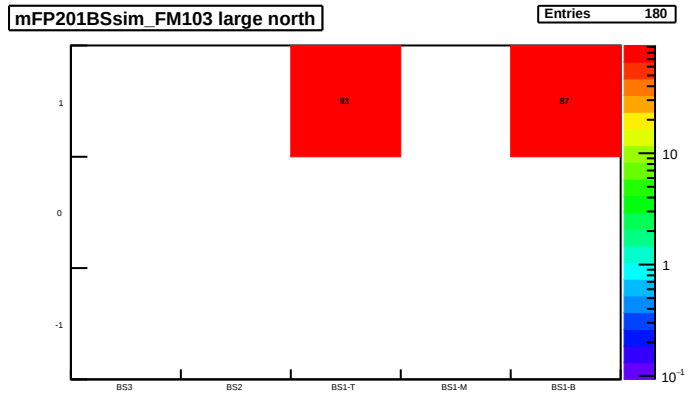

mFP201BS_FM101 small north
Entries 76575
80
137
471
244
300
15235
15178
14844
15071
15015
BS3
BS2
BS1-T
BS1-M
BS1-B

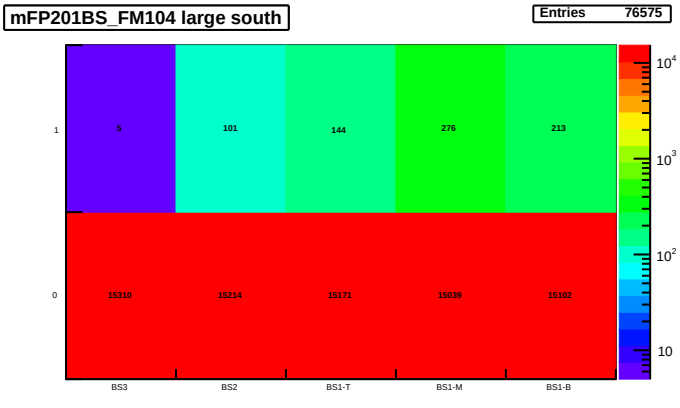

mFP201BSsim_FM101 small north
Entries 0
BS3
BS2
BS1-T
BS1-M
BS1-B

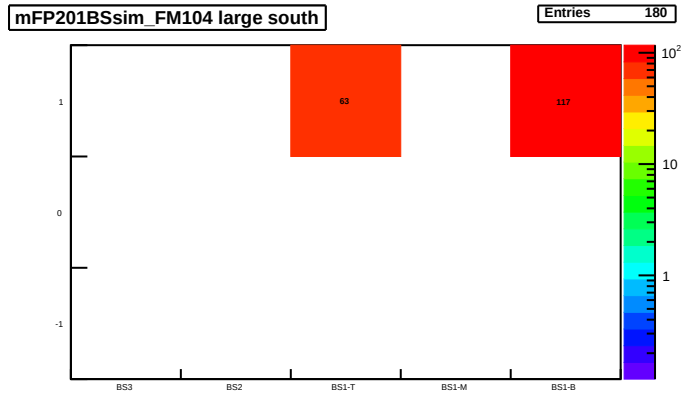

mFP201BS_FM102 small south
Entries 76575
79
125
317
259
240
15236
15190
14998
15056
15075
BS3
BS2
BS1-T
BS1-M
BS1-B

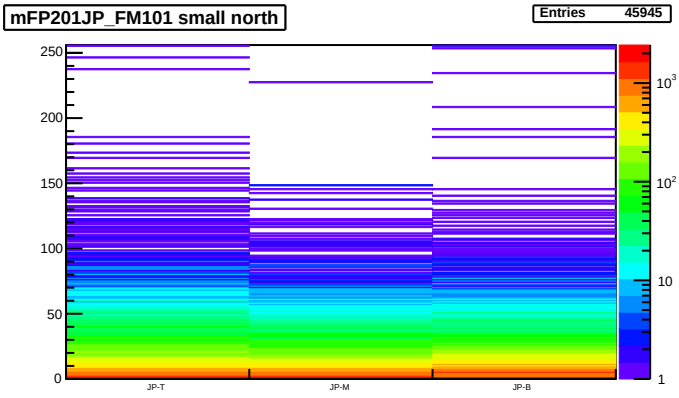

mFP201BSsim_FM102 small south
Entries 0
BS3
BS2
BS1-T
BS1-M
BS1-B

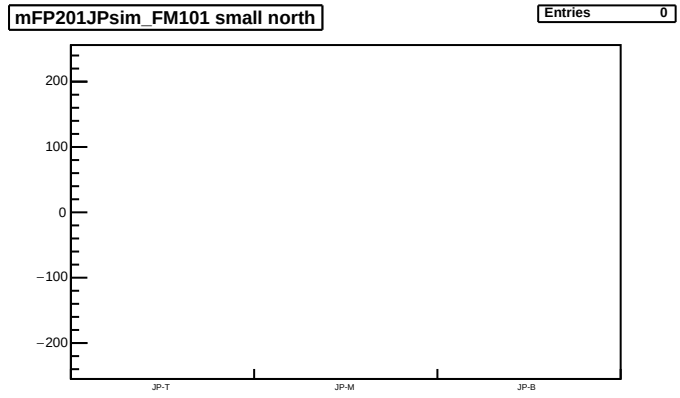

mFP201BS_FM103 large north
Entries 76575
8
113
202
283
188
15307
15202
15113
15032
15127
BS3
BS2
BS1-T
BS1-M
BS1-B

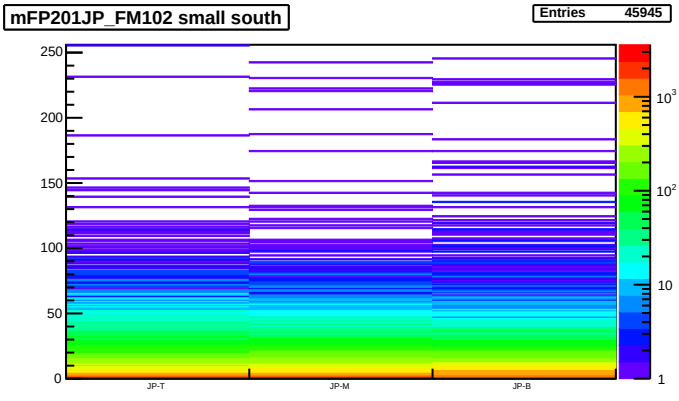

mFP201BSsim_FM103 large north
Entries 180
93
87
BS3
BS2
BS1-T
BS1-M
BS1-B

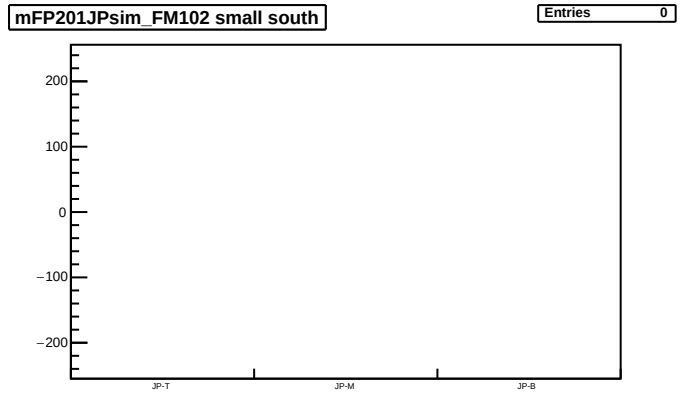

mFP201BS_FM104 large south
Entries 76575
5
101
144
276
213
15310
15214
15171
15039
15102
BS3
BS2
BS1-T
BS1-M
BS1-B

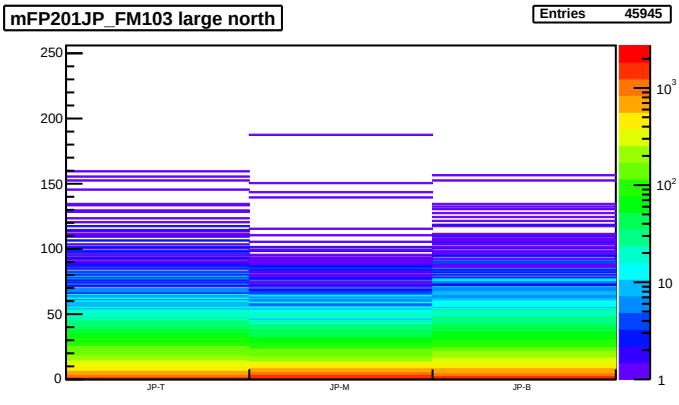

mFP201BSsim_FM104 large south
Entries 180
63
117
BS3
BS2
BS1-T
BS1-M
BS1-B

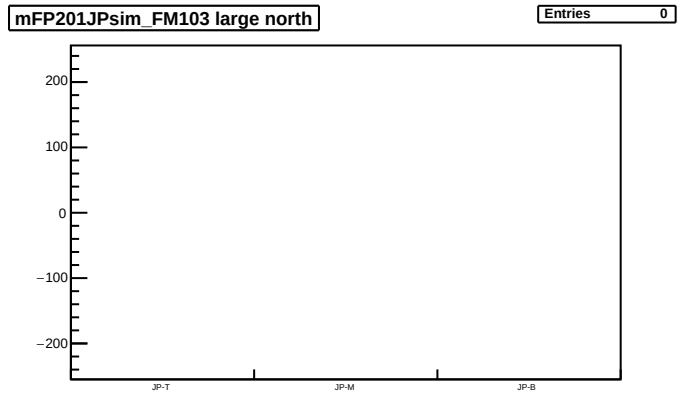

mFP201JP_FM101 small north
Entries 45945
250
200
150
100
50
0
JP-T
JP-M
JP-B
10^3
10^2
10
1

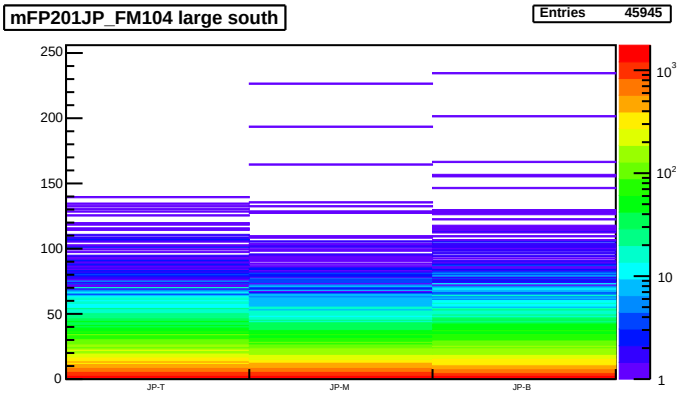

mFP201JPsim_FM101 small north
Entries 0
200
100
0
−100
−200
JP-T
JP-M
JP-B

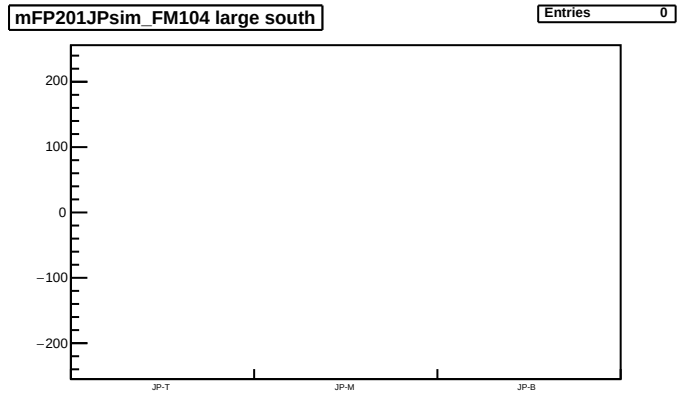

mFP201JP_FM102 small south
Entries 45945
250
200
150
100
50
0
JP-T
JP-M
JP-B
10^3
10^2
10
1

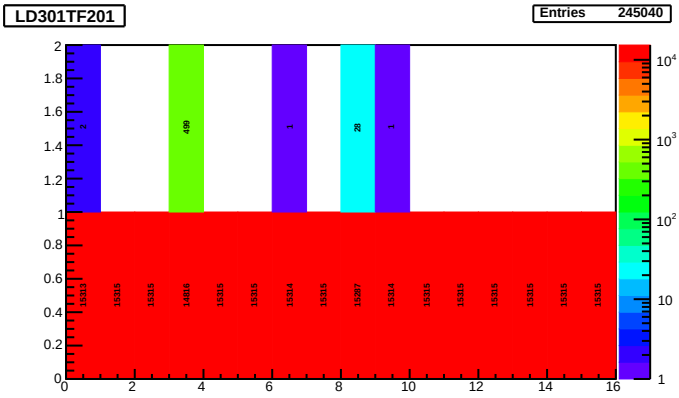

mFP201JPsim_FM102 small south
Entries 0
200
100
0
−100
−200
JP-T
JP-M
JP-B

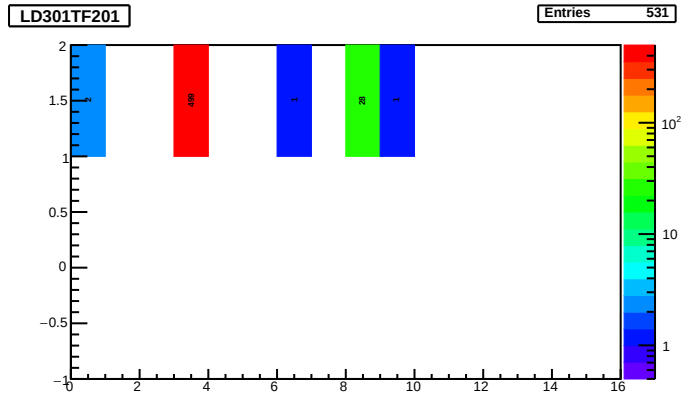

mFP201JP_FM103 large north
Entries 45945
250
200
150
100
50
0
JP-T
JP-M
JP-B
10^3
10^2
10
1

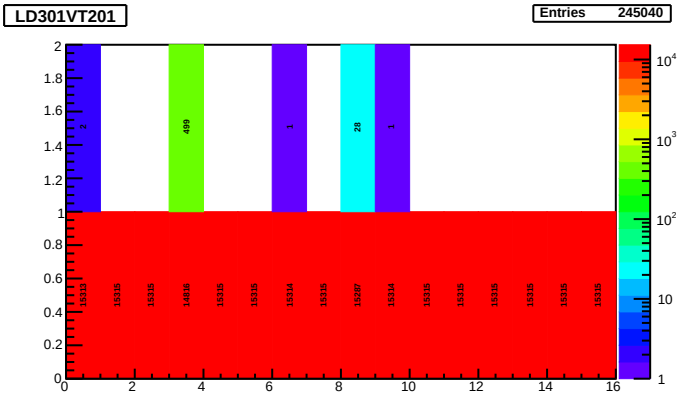

mFP201JPsim_FM103 large north
Entries 0
200
100
0
−100
−200
JP-T
JP-M
JP-B

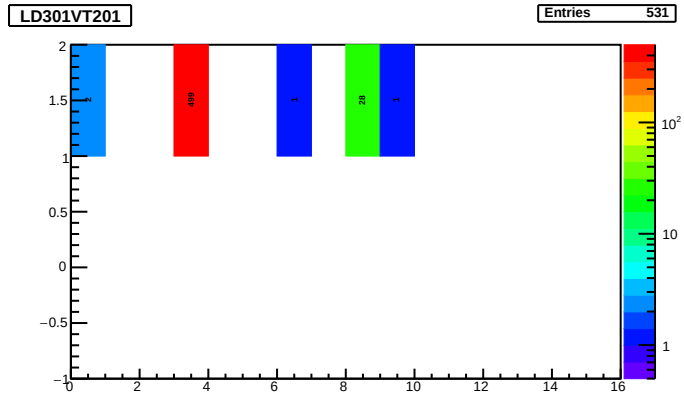

mFP201JP_FM104 large south
Entries 45945
250
200
150
100
50
0
JP-T
JP-M
JP-B
10^3
10^2
10
1

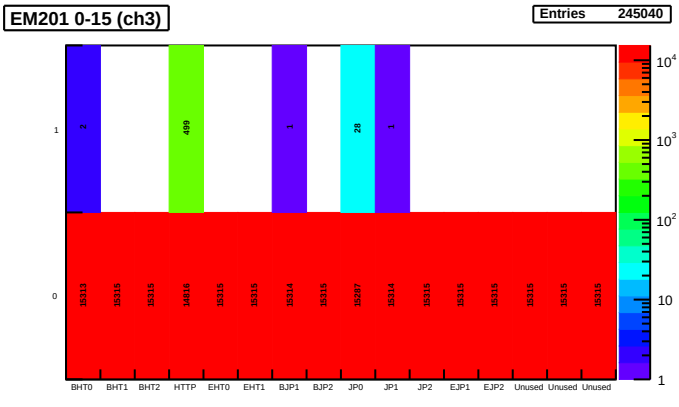

mFP201JPsim_FM104 large south
Entries 0
200
100
0
−100
−200
JP-T
JP-M
JP-B

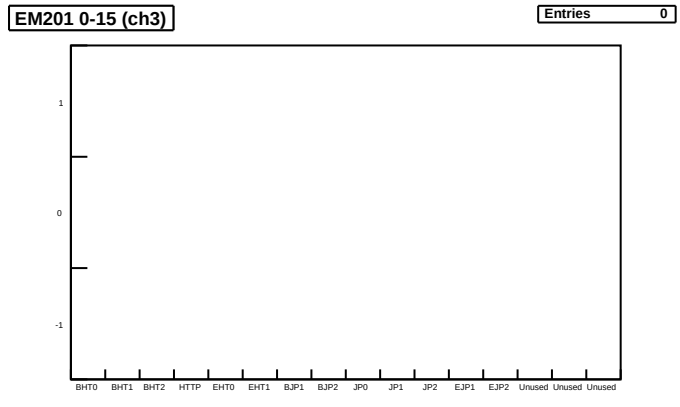

LD301TF201
Entries 245040

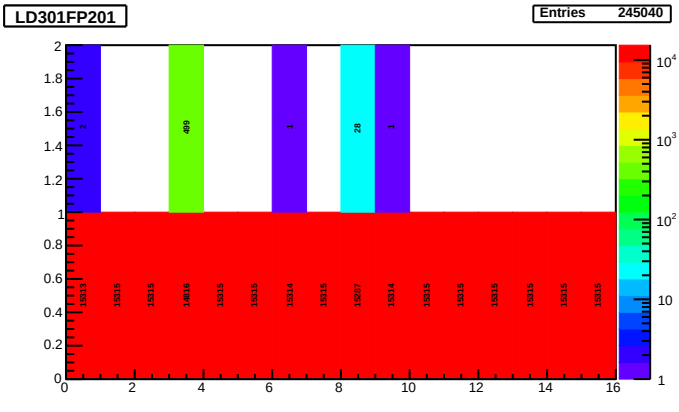

LD301TF201
Entries 531

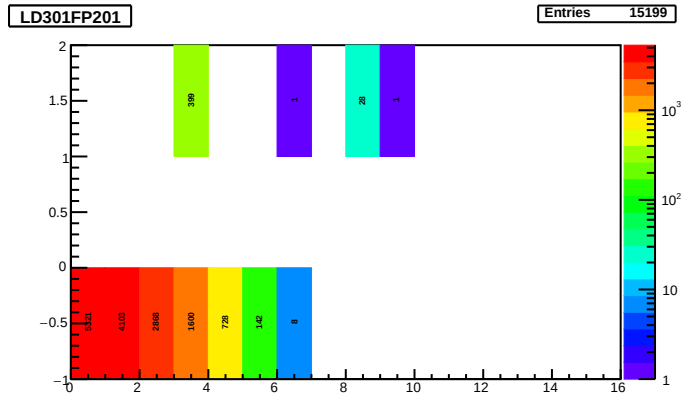

LD301VT201
Entries 245040


LD301VT201
Entries 531


EM201 0-15 (ch3)
Entries 245040
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


LD301FP201
Entries 245040


LD301FP201
Entries 15199