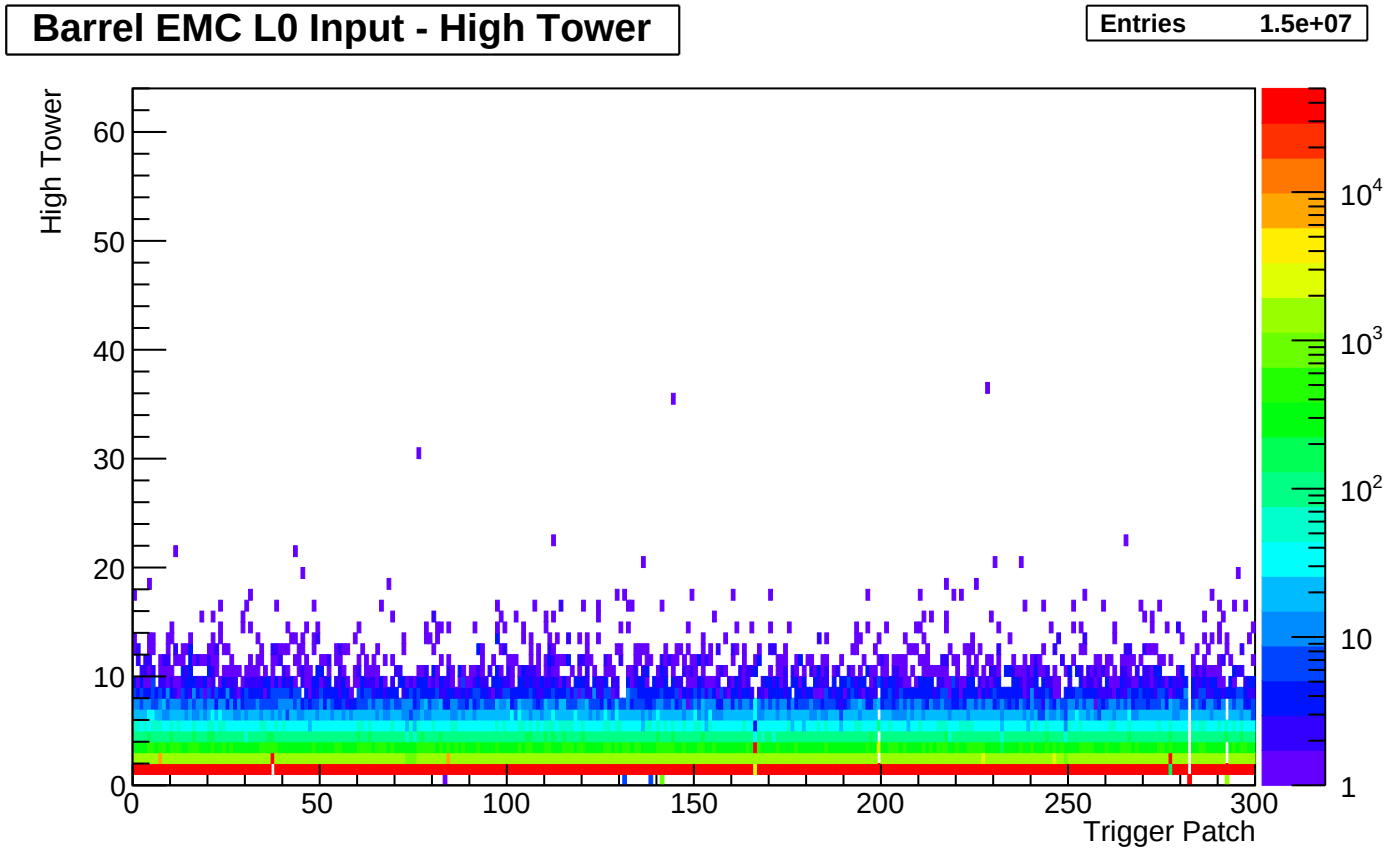
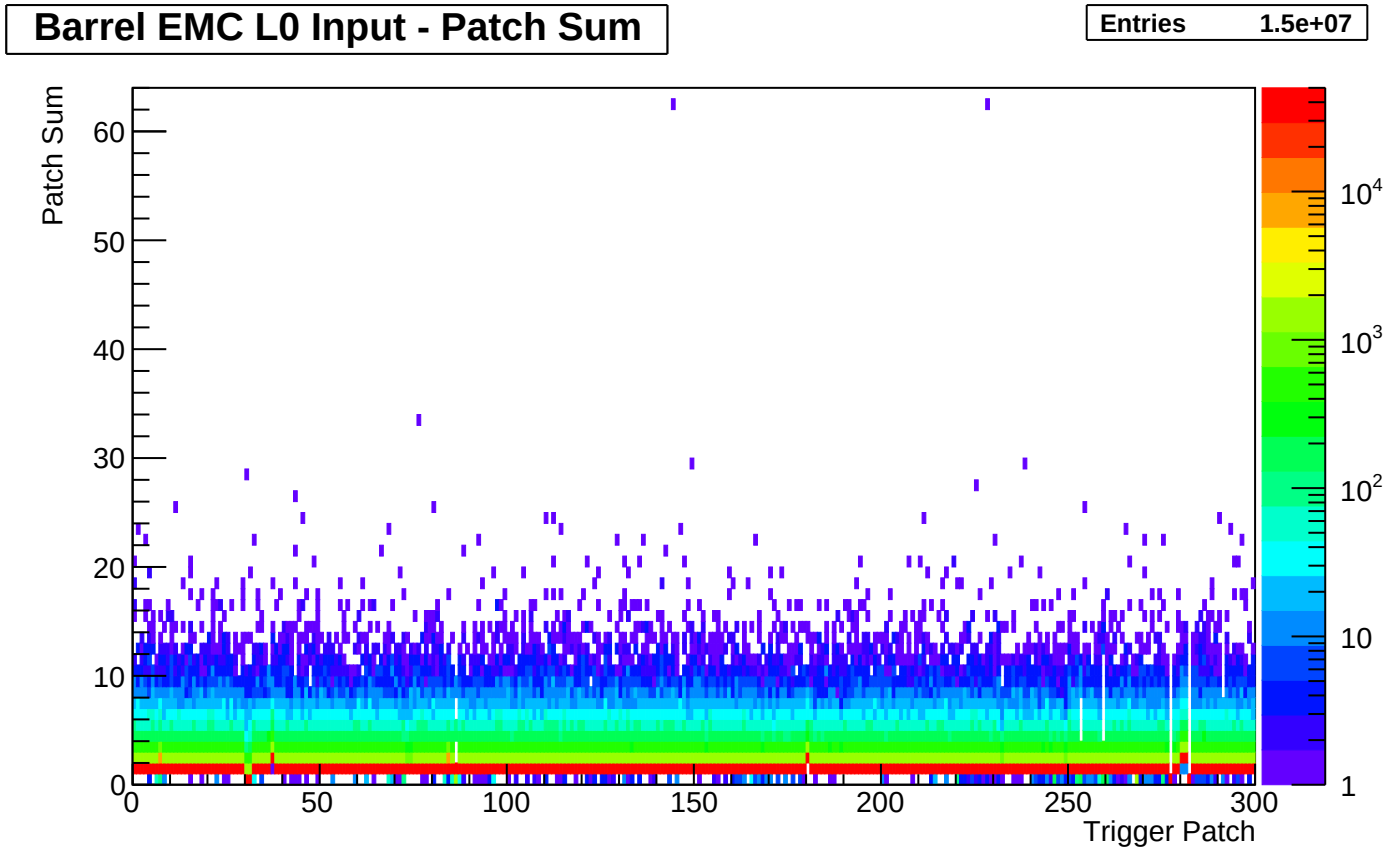
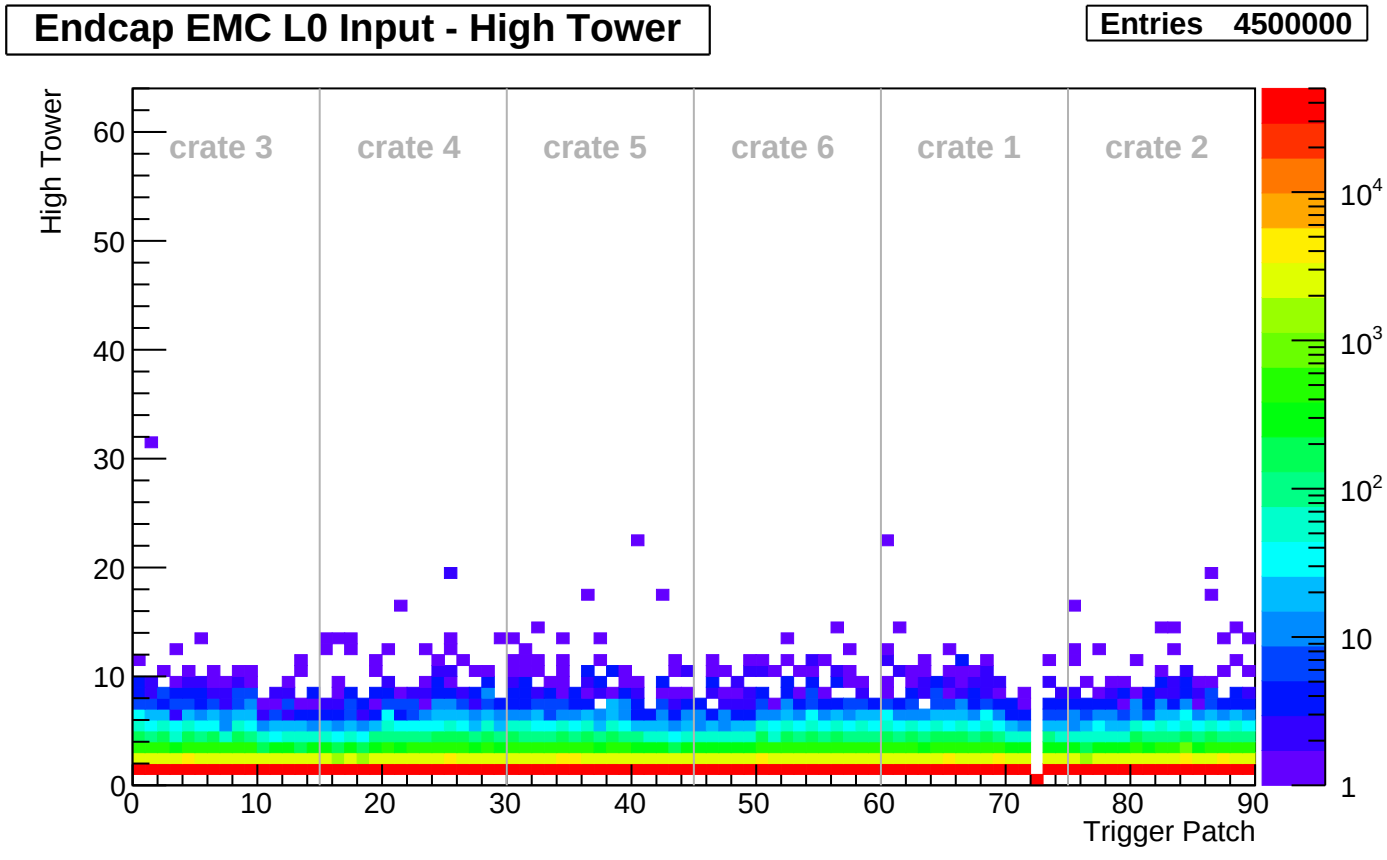
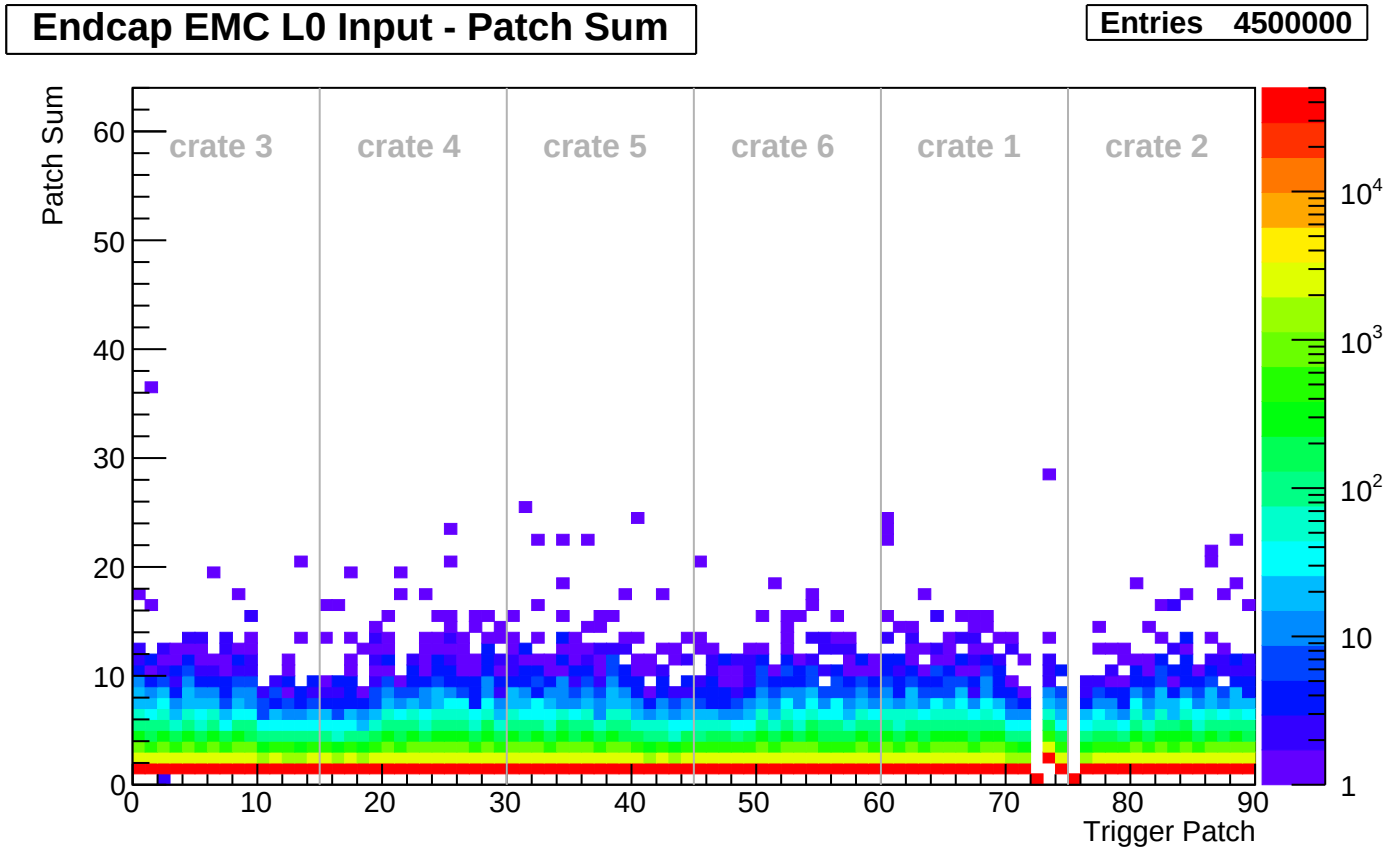
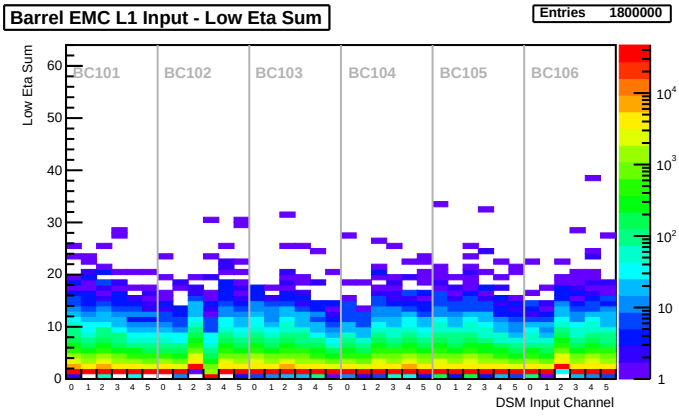
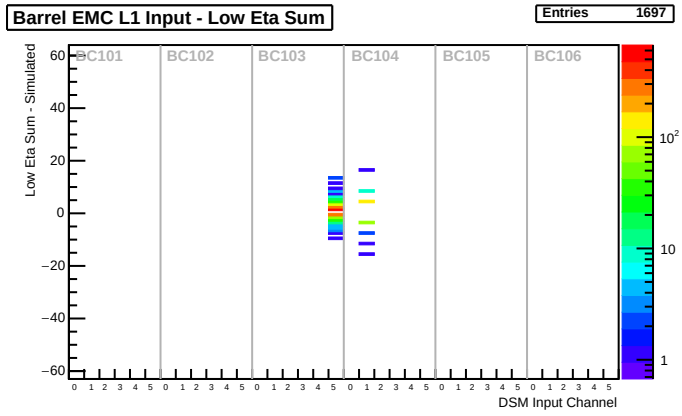

Barrel EMC L0 Input - High Tower
Entries 1.5e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

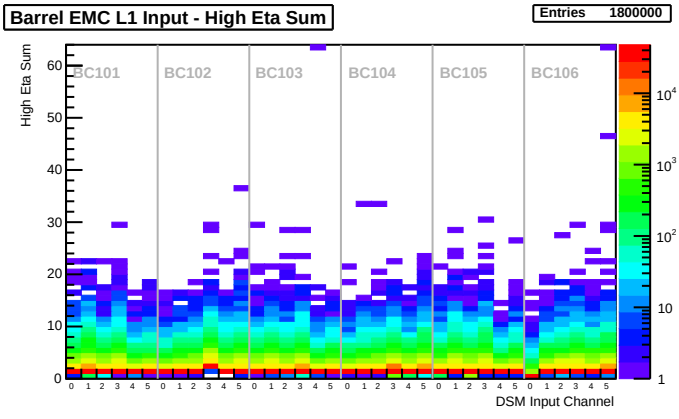

Barrel EMC L0 Input - Patch Sum
Entries 1.5e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

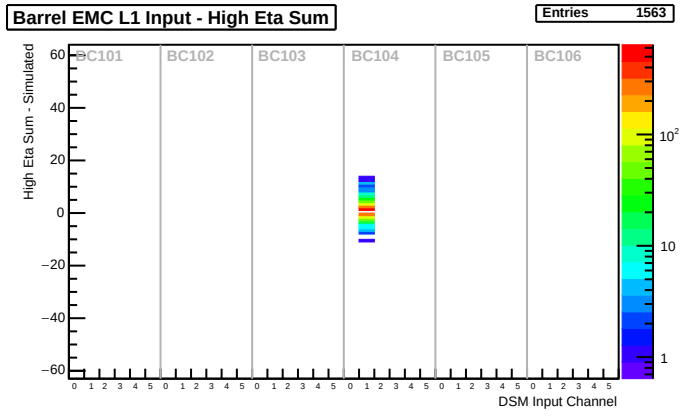

Endcap EMC L0 Input - High Tower
Entries 4500000
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
$10^2$
$10^3$
$10^4$

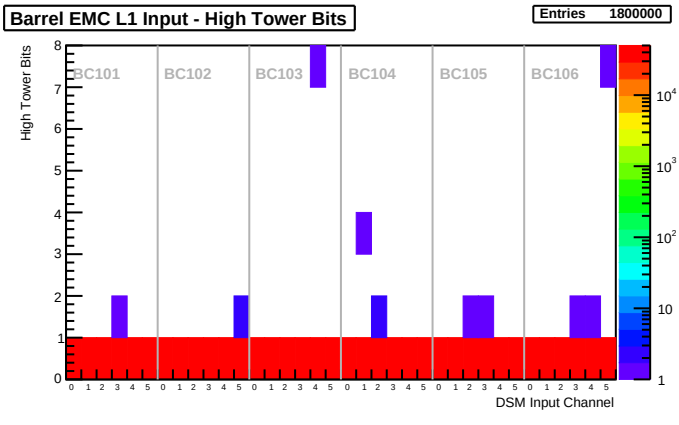

Endcap EMC L0 Input - Patch Sum
Entries 4500000
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
$10^2$
$10^3$
$10^4$

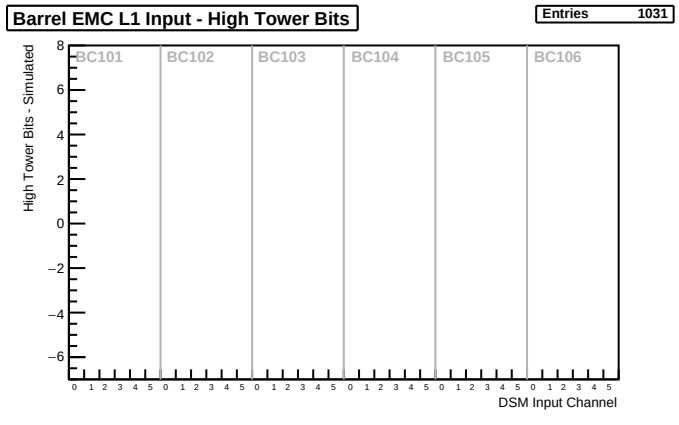

Barrel EMC L1 Input - Low Eta Sum
Entries 1800000
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

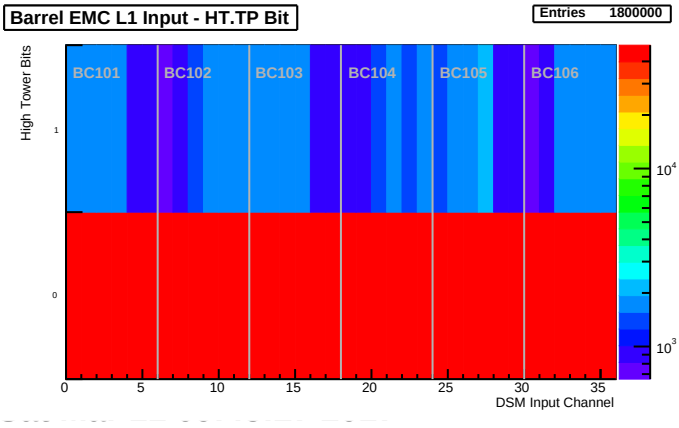

Barrel EMC L1 Input - Low Eta Sum
Entries 1697
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

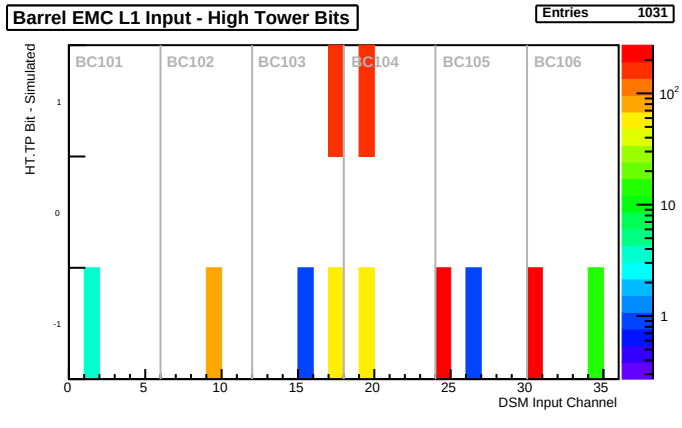

Barrel EMC L1 Input - High Eta Sum
Entries 1800000
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

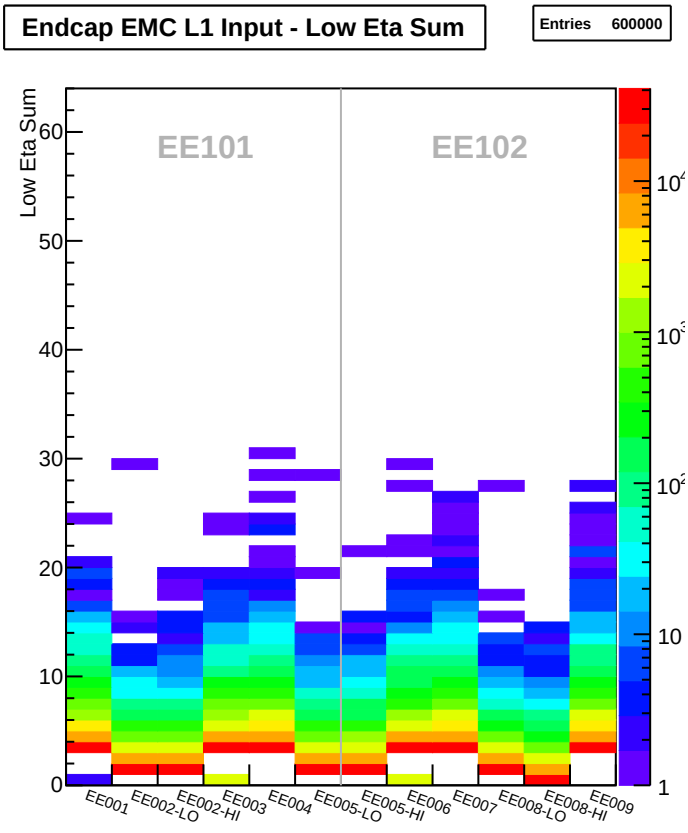

Barrel EMC L1 Input - High Eta Sum
Entries 1563
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

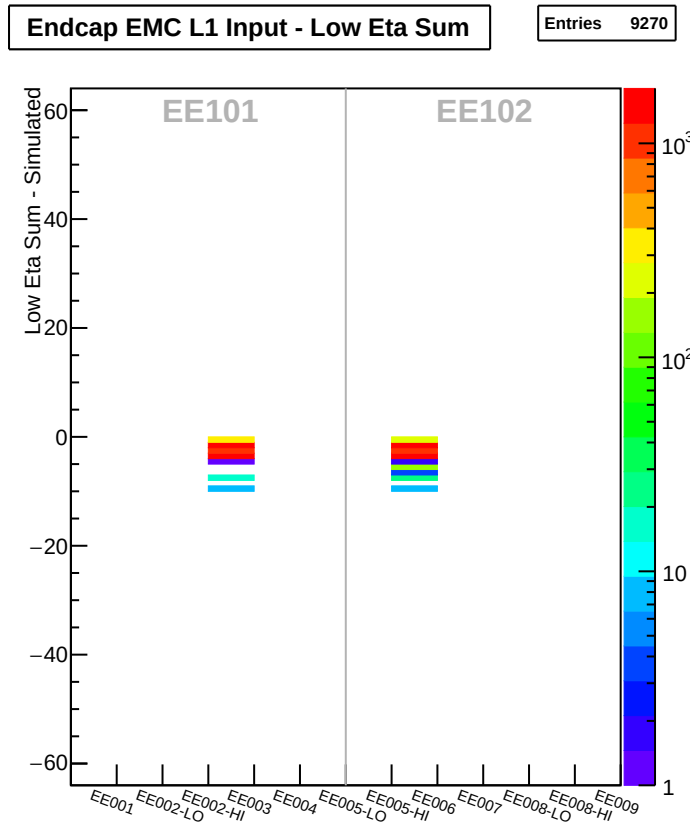

Barrel EMC L1 Input - High Tower Bits
Entries 1800000
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

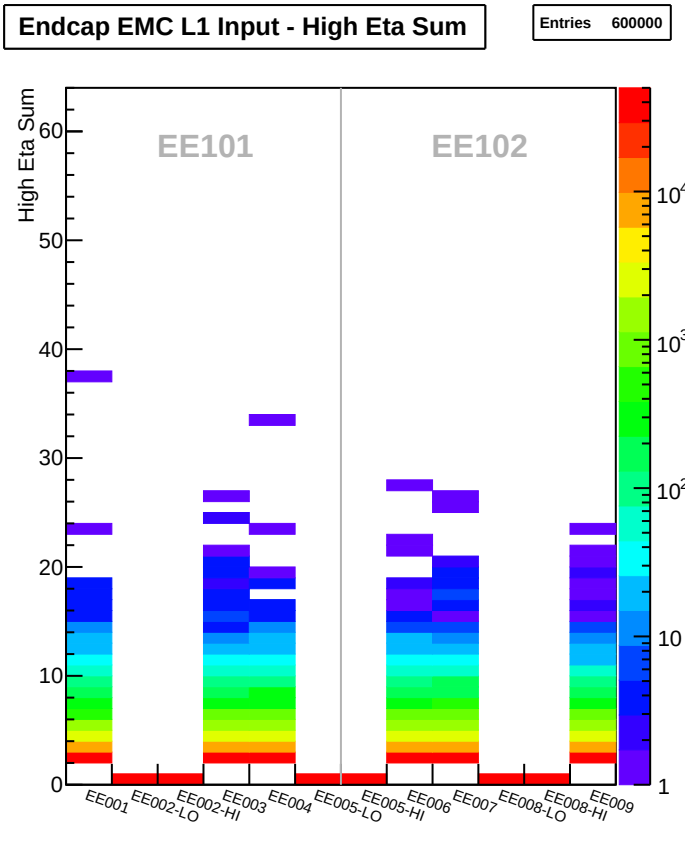

Barrel EMC L1 Input - High Tower Bits
Entries 1031
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

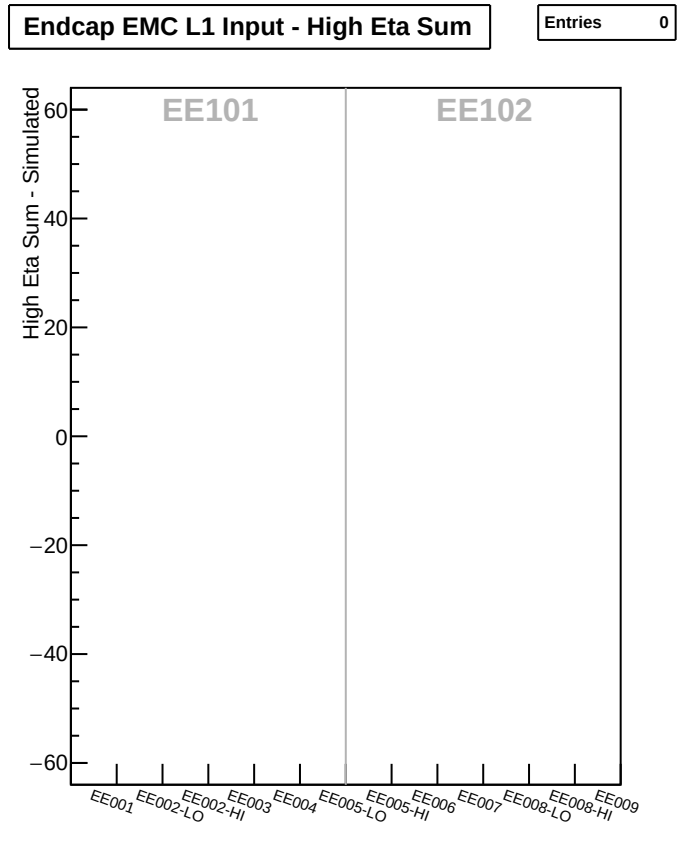

Barrel EMC L1 Input - HT.TP Bit
Entries 1800000
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel


Barrel EMC L1 Input - High Tower Bits
Entries 1031
HT.TP Bit - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel


Endcap EMC L1 Input - Low Eta Sum
Entries 600000
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

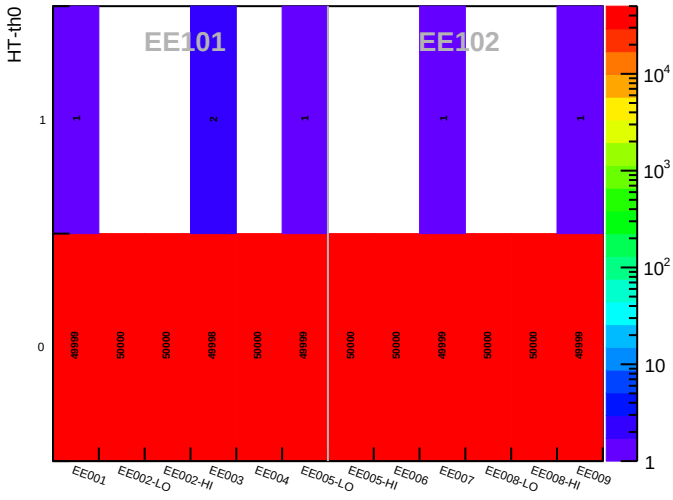

Endcap EMC L1 Input - Low Eta Sum
Entries 9270
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009


Endcap EMC L1 Input - High Eta Sum
Entries 600000
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009


Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

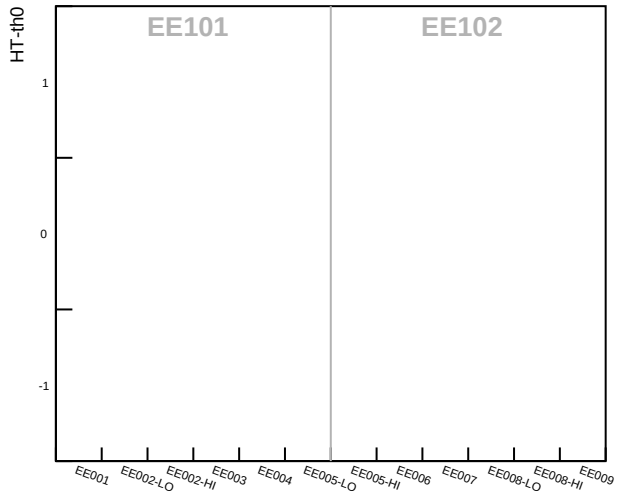
## Endcap EMC L1 Input - HT-th0

Entries 600000



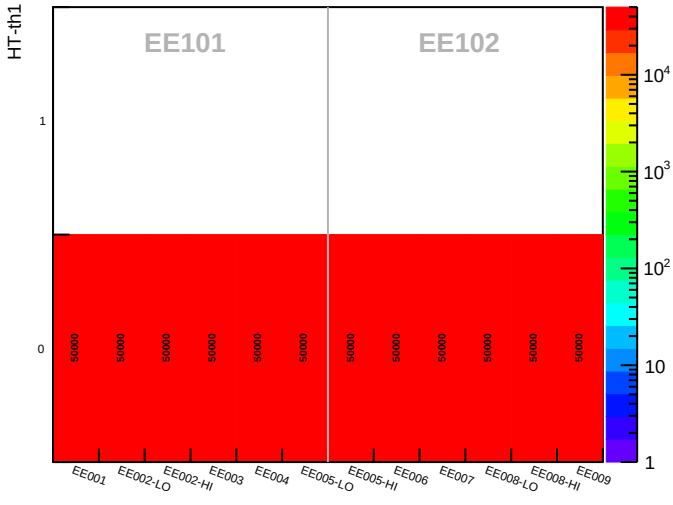

## Endcap EMC L1 Input - HT-th0 Diff

Entries 0



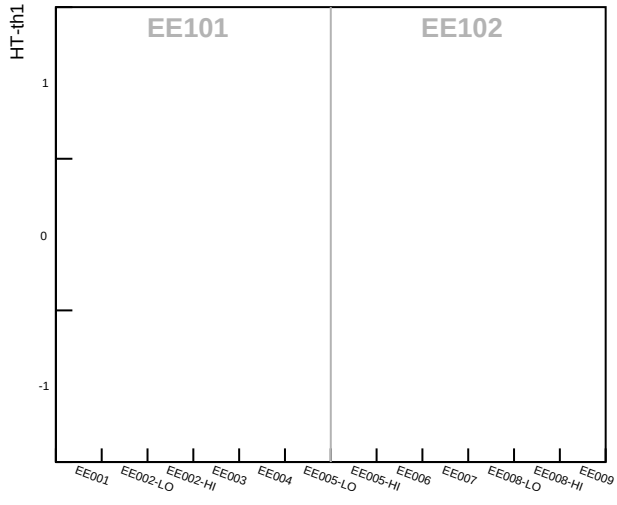

## Endcap EMC L1 Input - HT-th1

Entries 600000



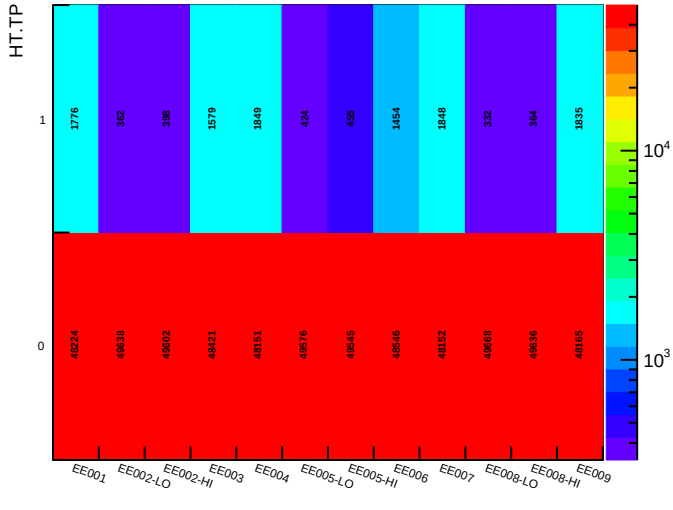

## Endcap EMC L1 Input - HT-th1 Diff

Entries 0



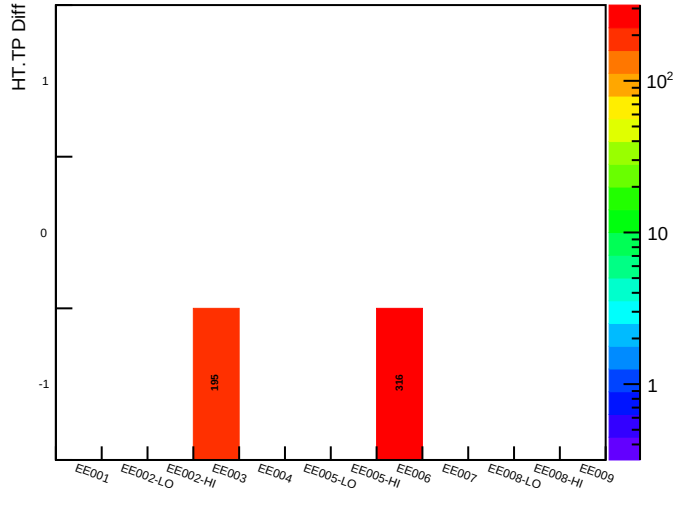

## Endcap EMC L1 Input - HT.TP

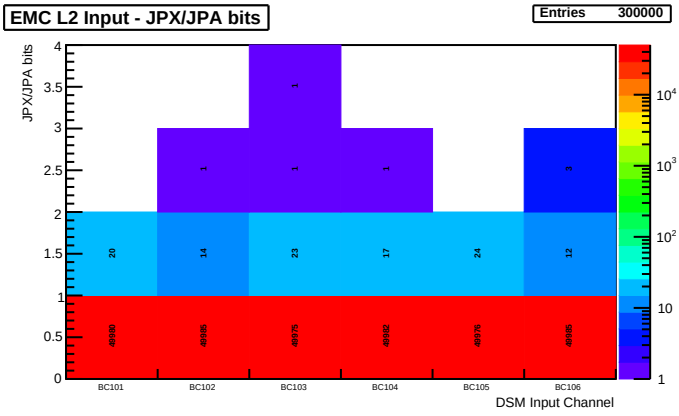
Entries 600000

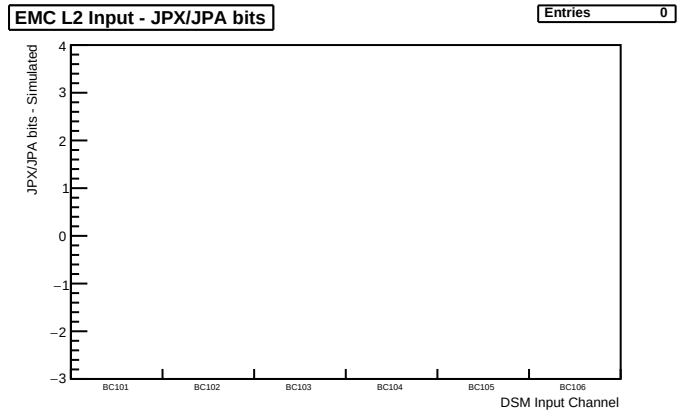


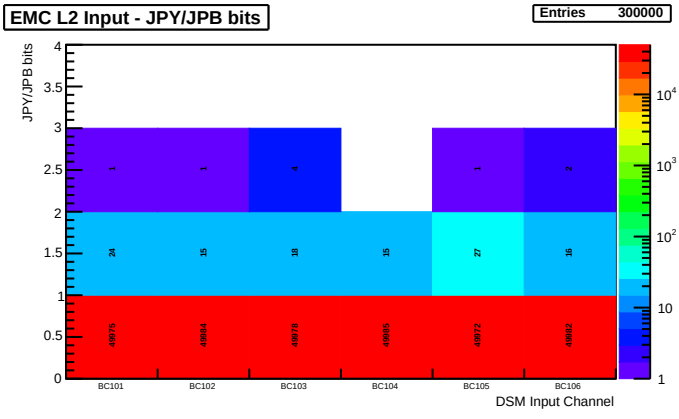

## Endcap EMC L1 Input - HT.TP Diff

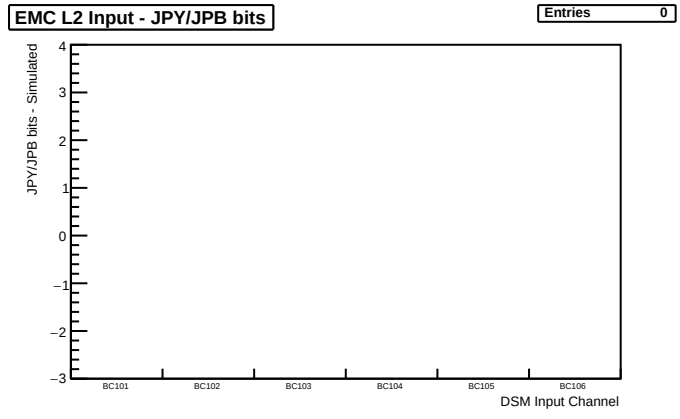
Entries 511

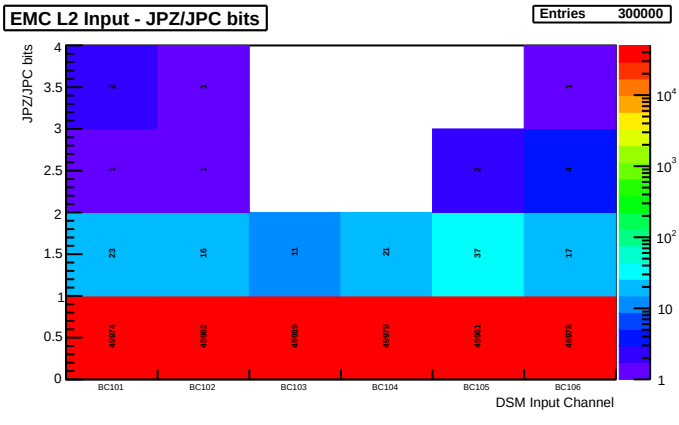


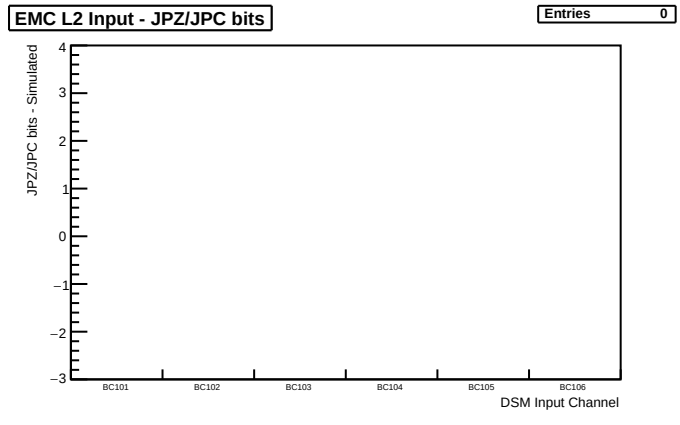

EMC L2 Input - JPX/JPA bits
Entries 300000
JPX/JPA bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

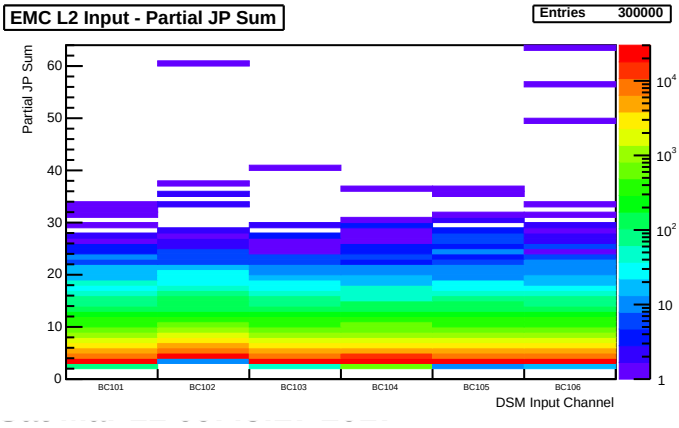

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

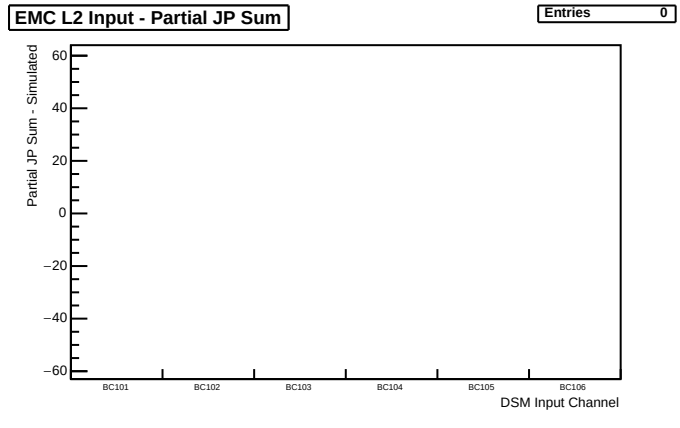

EMC L2 Input - JPY/JPB bits
Entries 300000
JPY/JPB bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

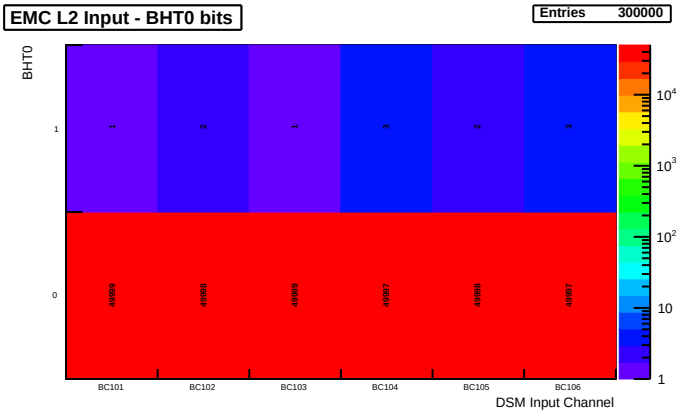

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

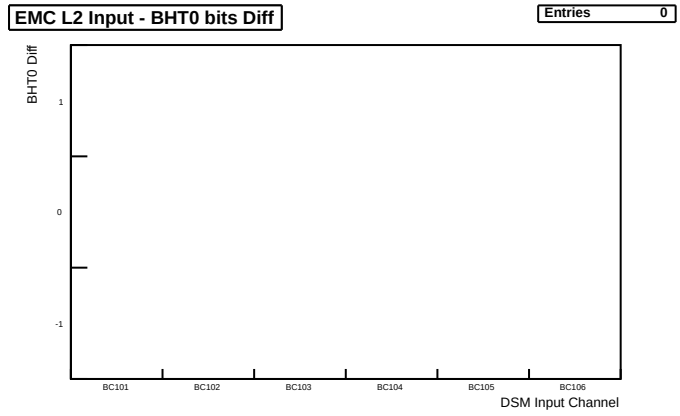

EMC L2 Input - JPZ/JPC bits
Entries 300000
JPZ/JPC bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

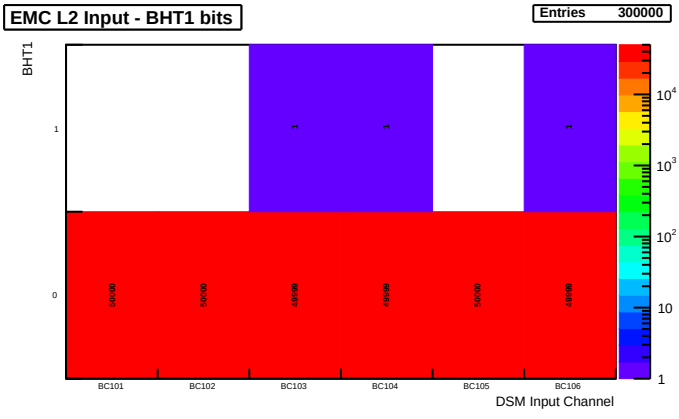

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

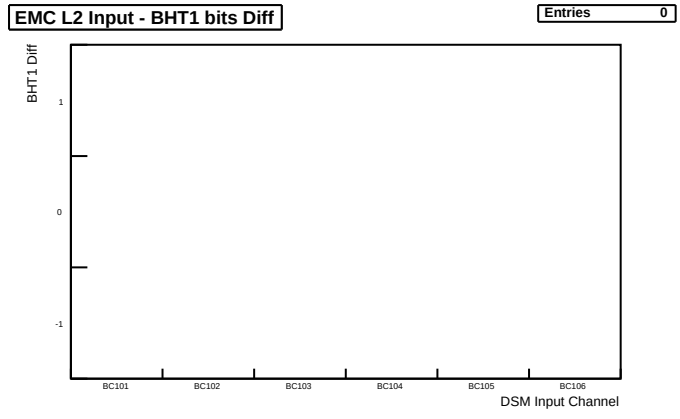

EMC L2 Input - Partial JP Sum
Entries 300000
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

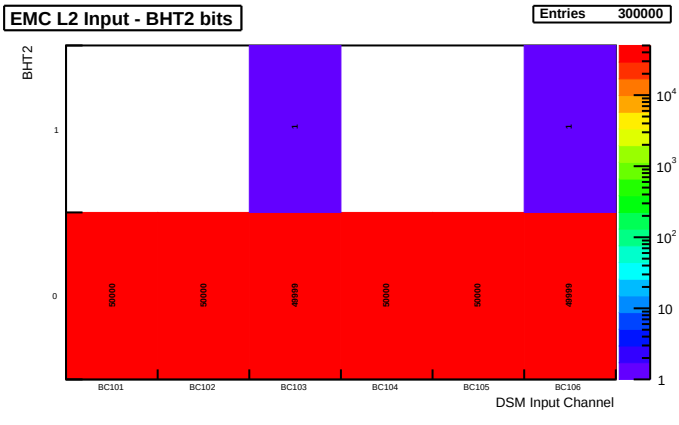

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

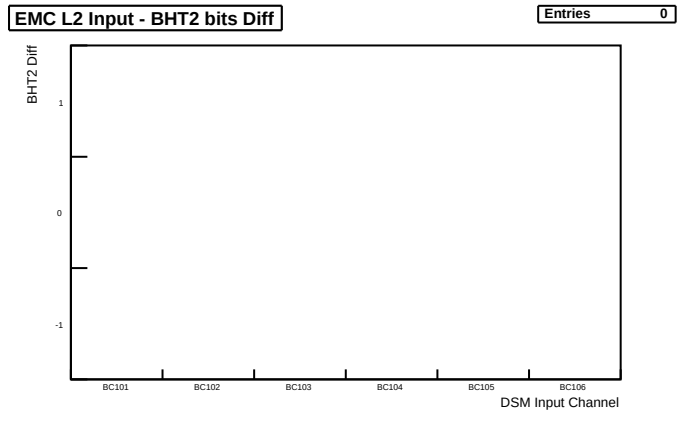

EMC L2 Input - BHT0 bits
Entries 300000
BHT0
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

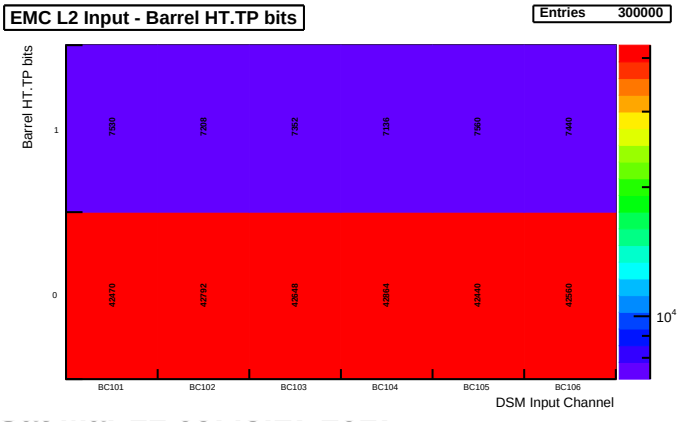

EMC L2 Input - BHT0 bits Diff
Entries 0
BHT0 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

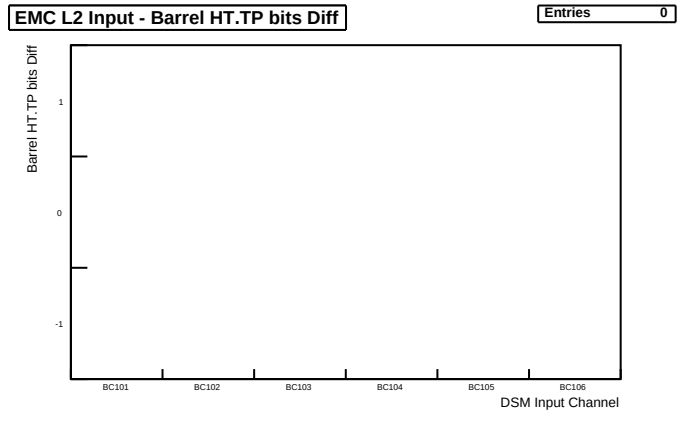

EMC L2 Input - BHT1 bits
Entries 300000
BHT1
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

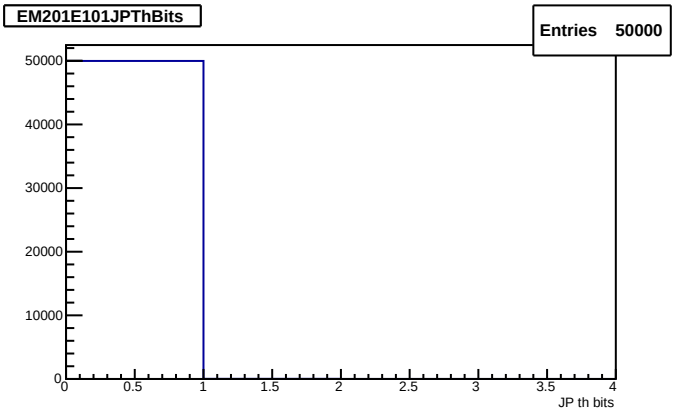

EMC L2 Input - BHT1 bits Diff
Entries 0
BHT1 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

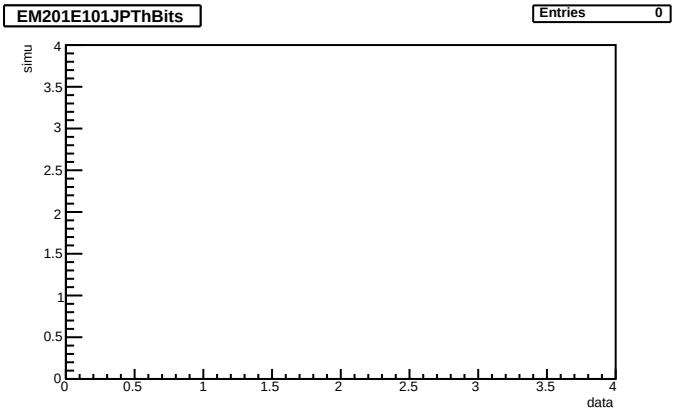

EMC L2 Input - BHT2 bits
Entries 300000
BHT2
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

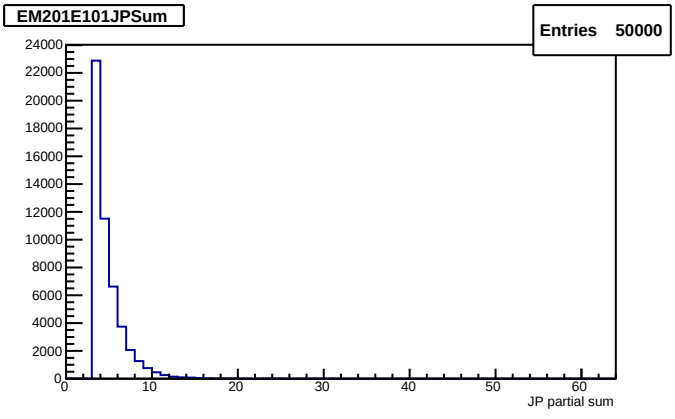

EMC L2 Input - BHT2 bits Diff
Entries 0
BHT2 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

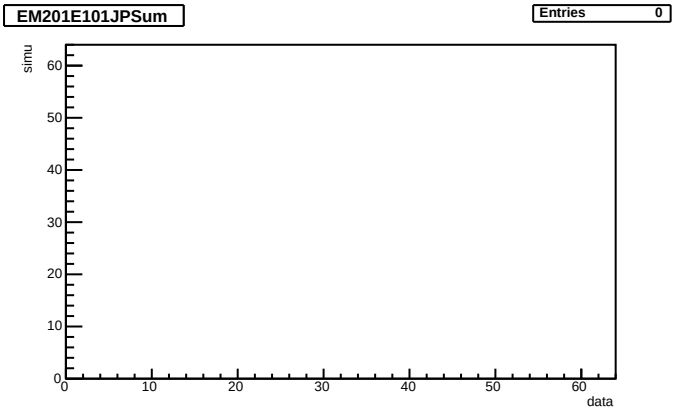

EMC L2 Input - Barrel HT.TP bits
Entries 300000
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

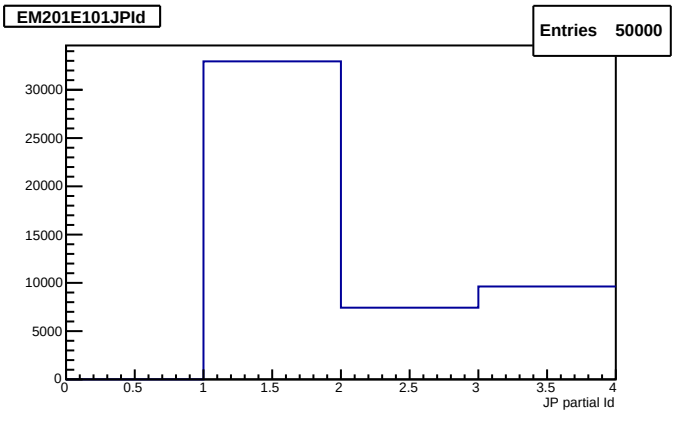

EMC L2 Input - Barrel HT.TP bits Diff
Entries 0
Barrel HT.TP bits Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

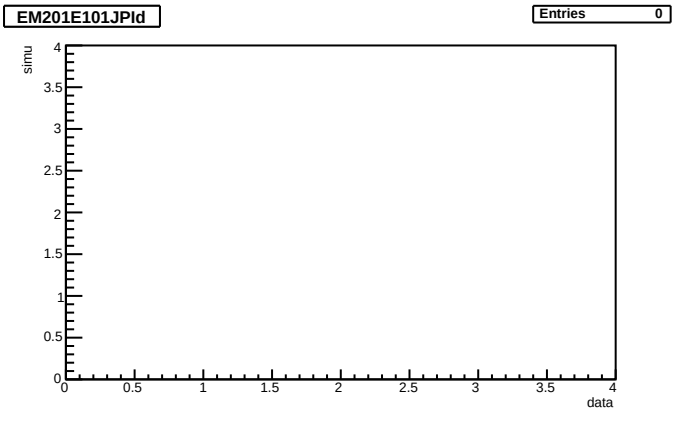

EM201E101JPThBits
Entries 50000
JP th bits

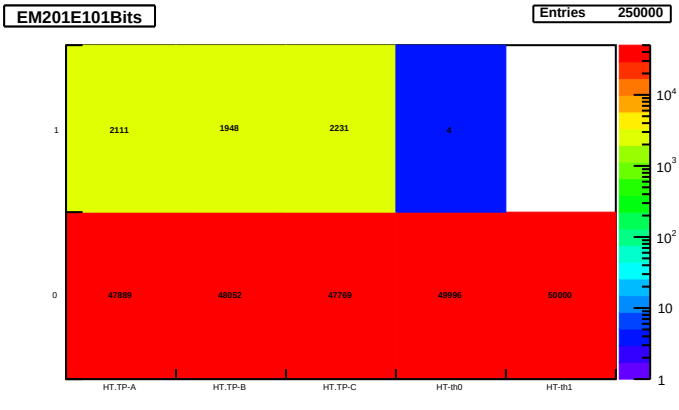

EM201E101JPThBits
Entries 0
simu
data

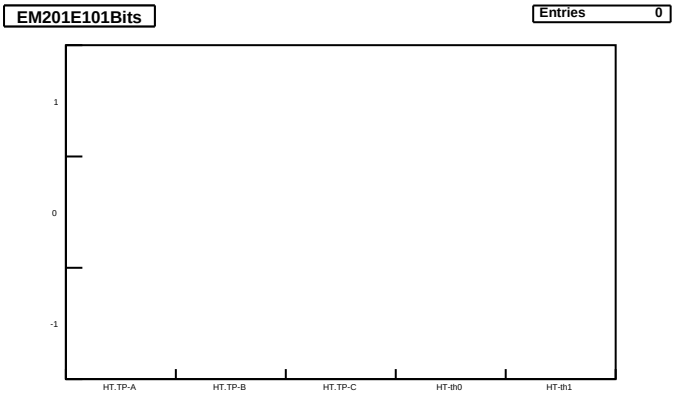

EM201E101JPSum
Entries 50000
JP partial sum

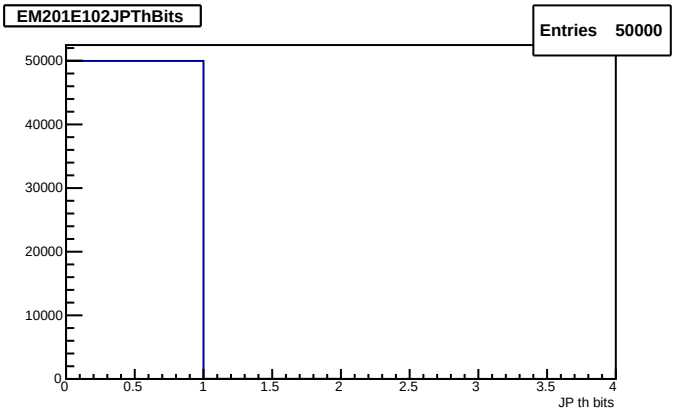

EM201E101JPSum
Entries 0
simu
data

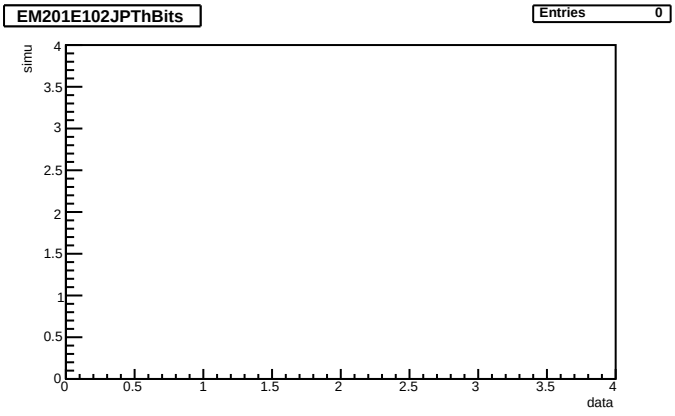

EM201E101JPId
Entries 50000
JP partial Id

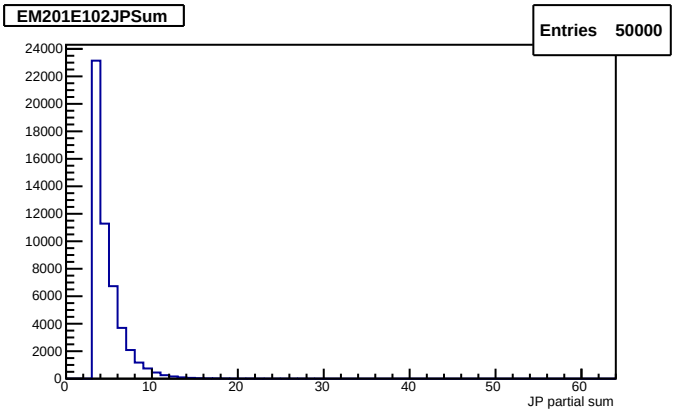

EM201E101JPId
Entries 0
simu
data

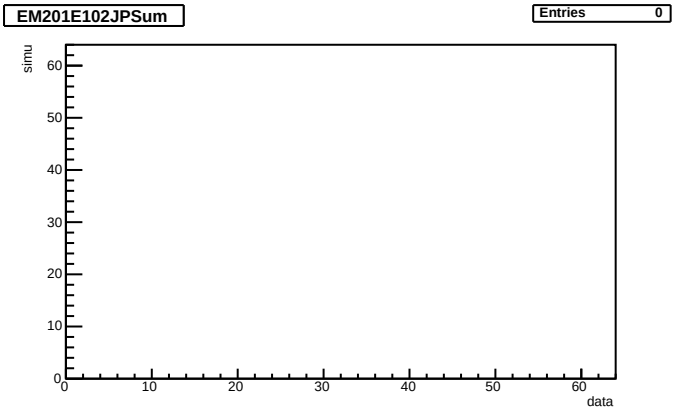

EM201E101Bits
Entries 250000
2111
1948
2231
4
47889
48052
47769
49996
50000
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

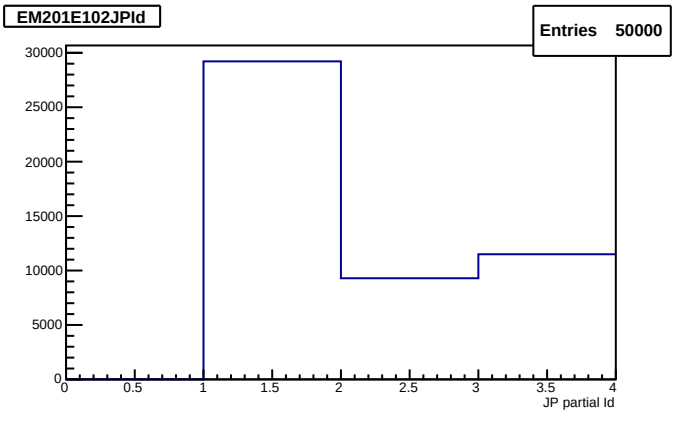

EM201E101Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

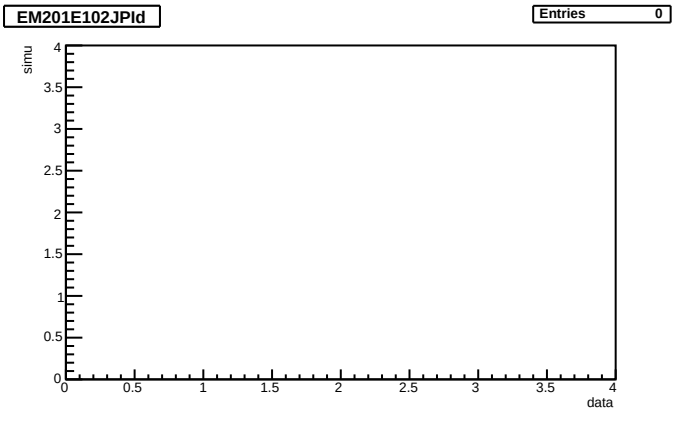

EM201E102JPThBits
Entries 50000
JP th bits

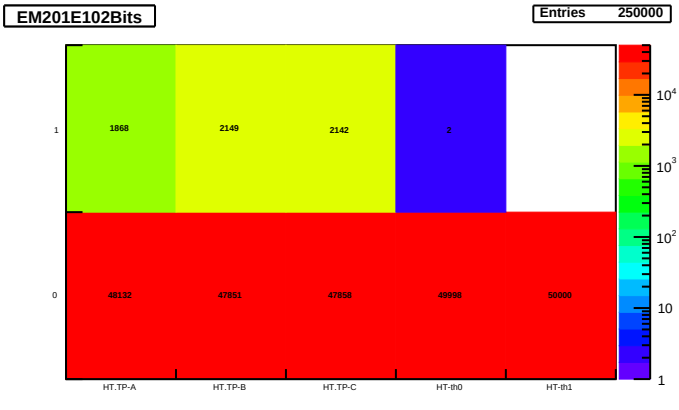

EM201E102JPThBits
Entries 0
simu
data

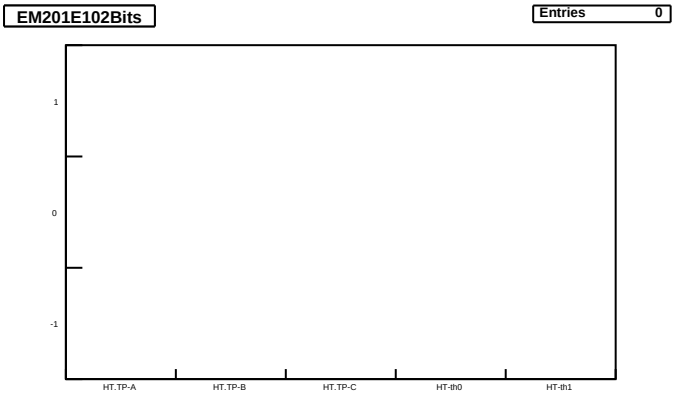

EM201E102JPSum
Entries 50000
JP partial sum

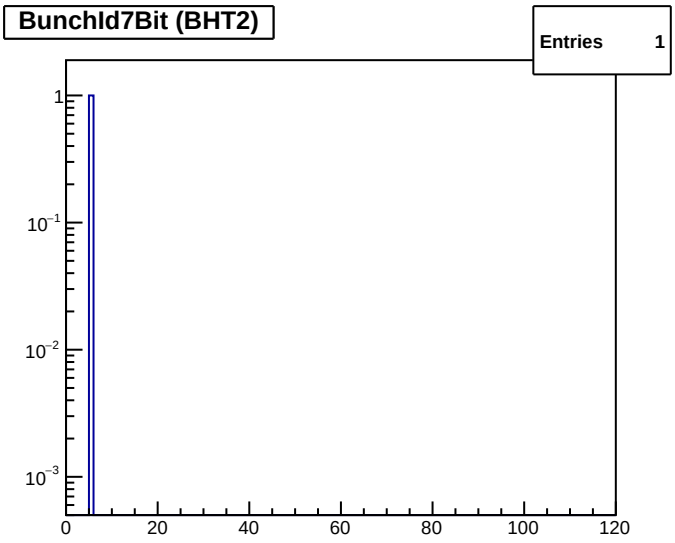

EM201E102JPSum
Entries 0
simu
data

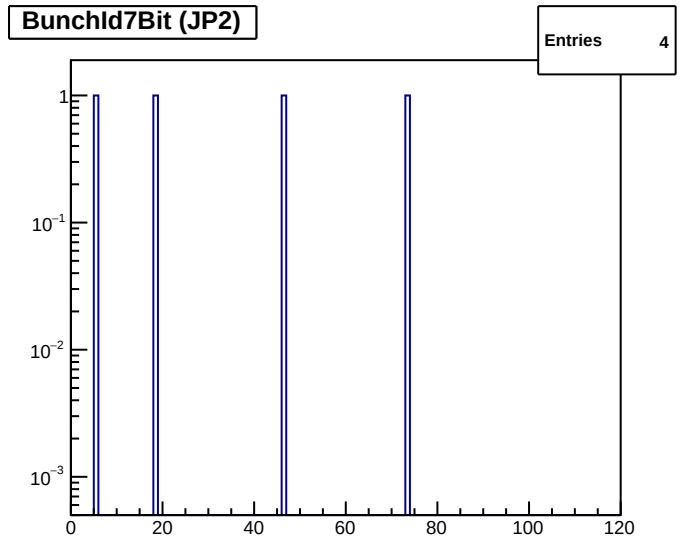

EM201E102JPId
Entries 50000
JP partial Id

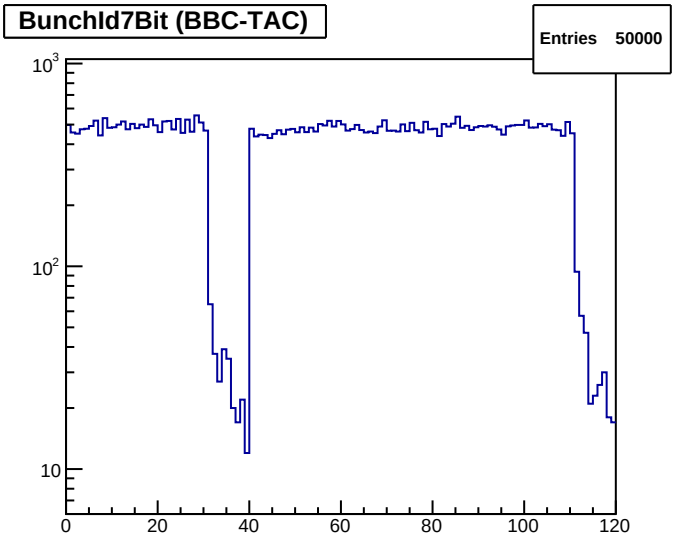

EM201E102JPId
Entries 0
simu
data

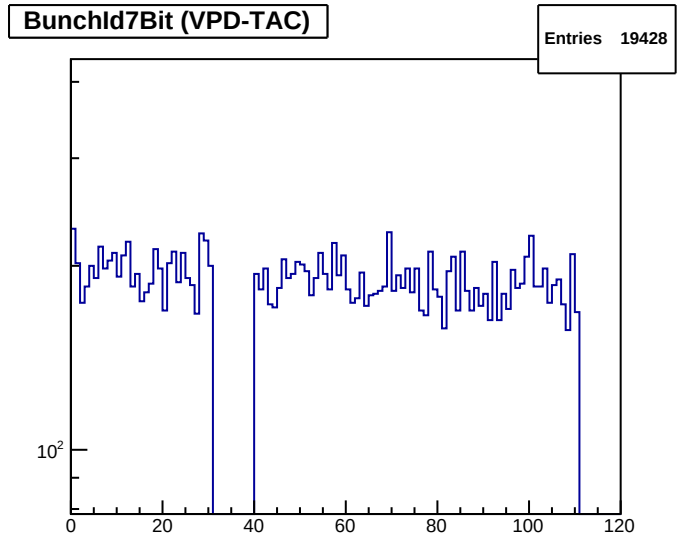

EM201E102Bits
Entries 250000
1868
2149
2142
2
48132
47851
47858
49998
50000
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

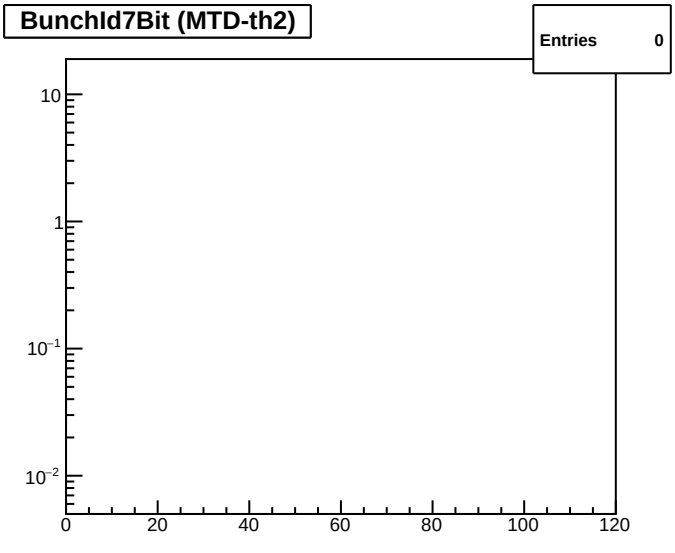

EM201E102Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

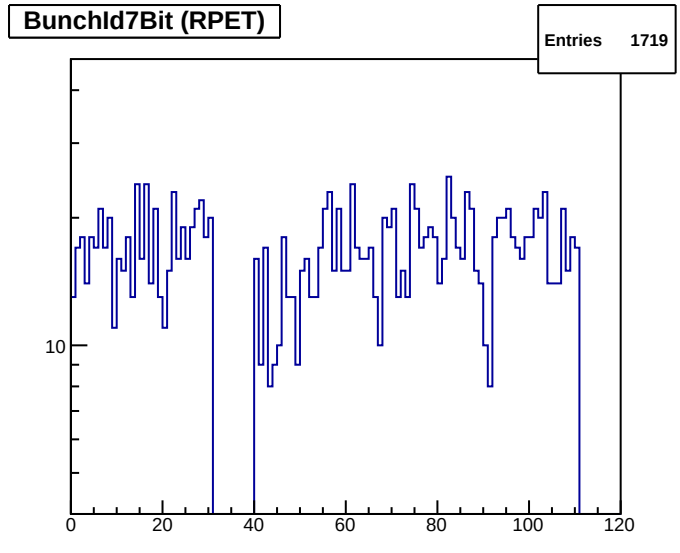

BunchId7Bit (BHT2)
Entries 1

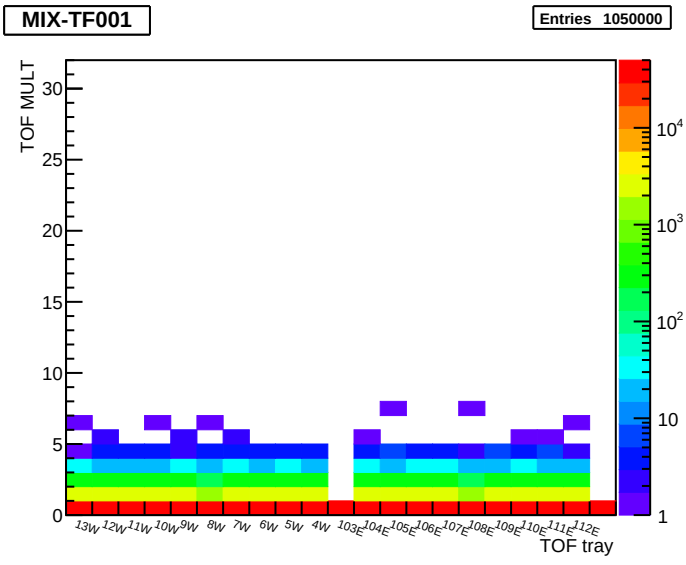

BunchId7Bit (JP2)
Entries 4

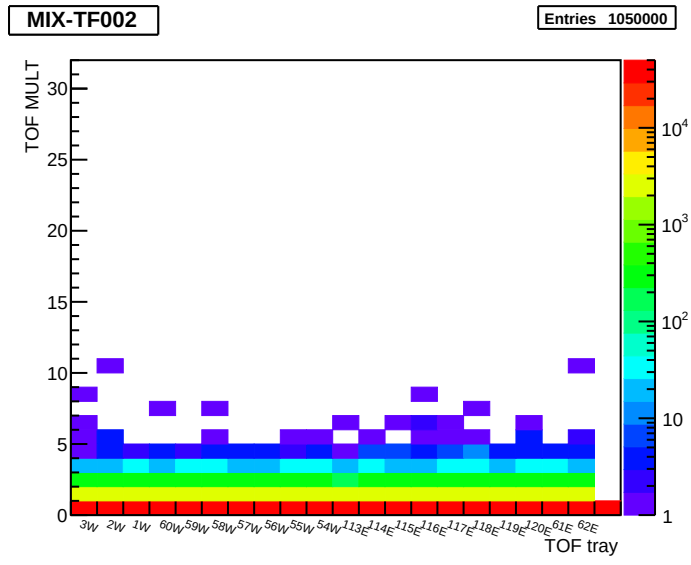

BunchId7Bit (BBC-TAC)
Entries 50000

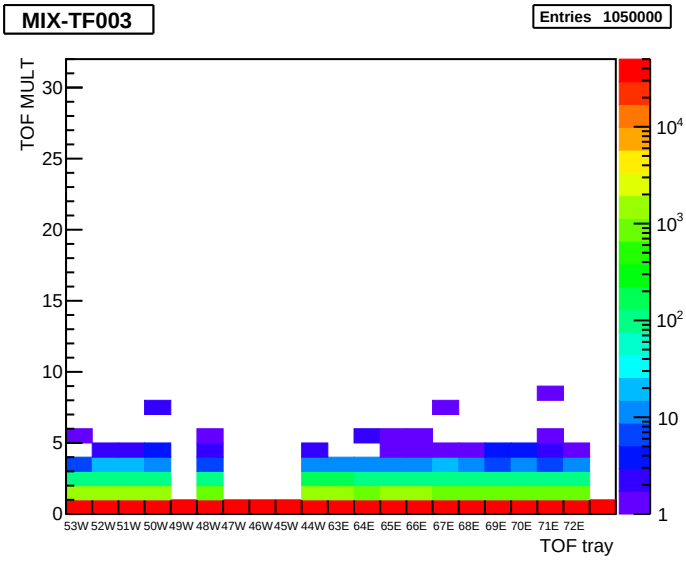

BunchId7Bit (VPD-TAC)
Entries 19428

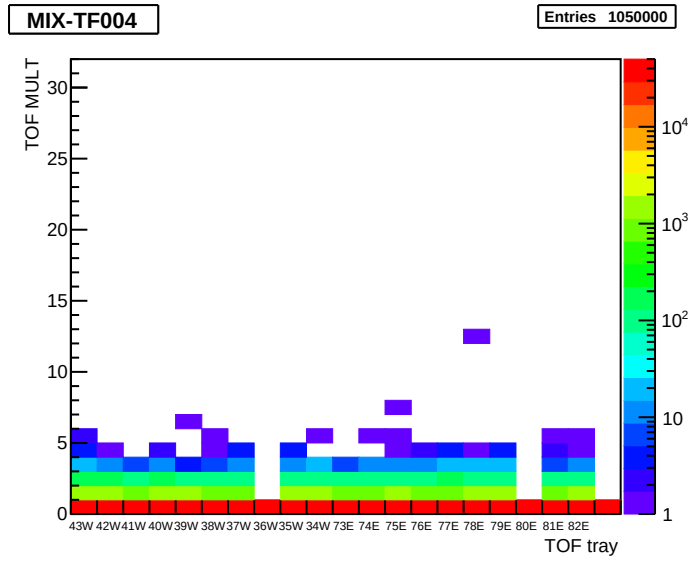

BunchId7Bit (MTD-th2)
Entries 0

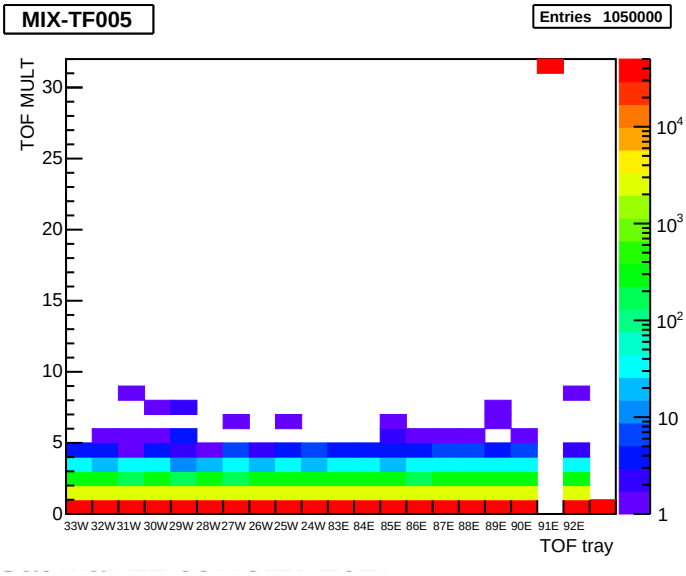

BunchId7Bit (RPET)
Entries 1719

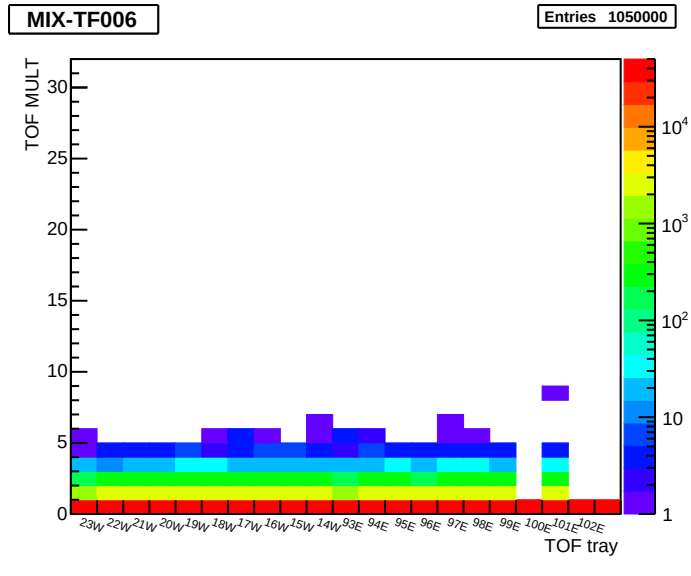

MIX-TF001
Entries 1050000
TOF MULT
TOF tray

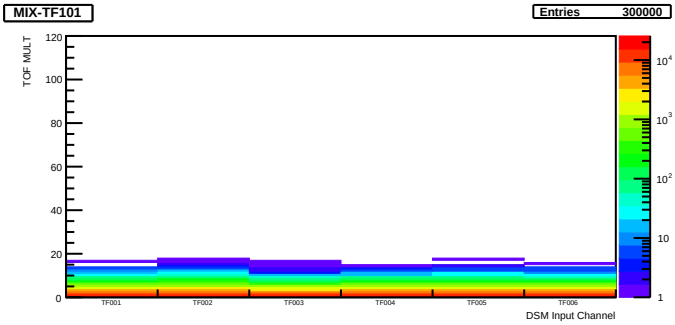

MIX-TF002
Entries 1050000
TOF MULT
TOF tray

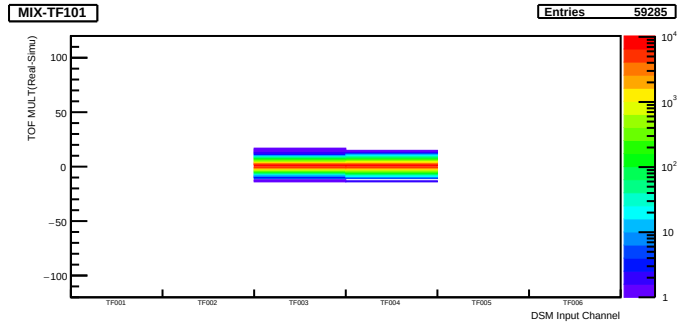

MIX-TF003
Entries 1050000
TOF MULT
TOF tray

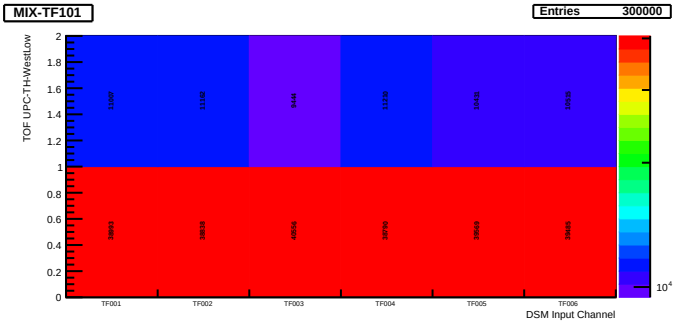

MIX-TF004
Entries 1050000
TOF MULT
TOF tray

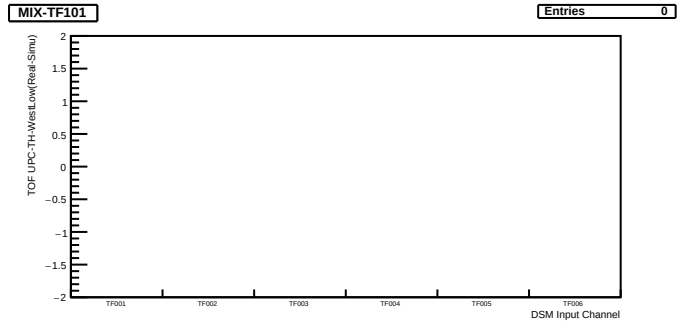

MIX-TF005
Entries 1050000
TOF MULT
TOF tray

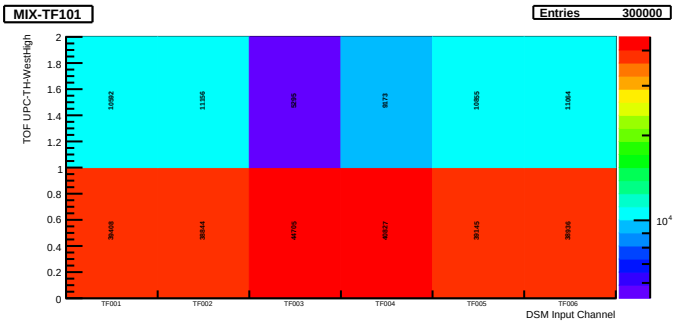

MIX-TF006
Entries 1050000
TOF MULT
TOF tray

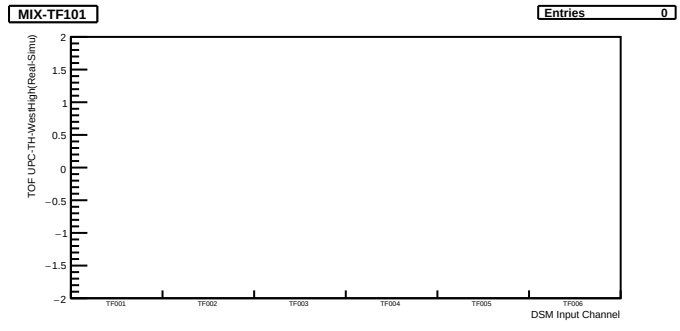

MIX-TF101
Entries 300000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

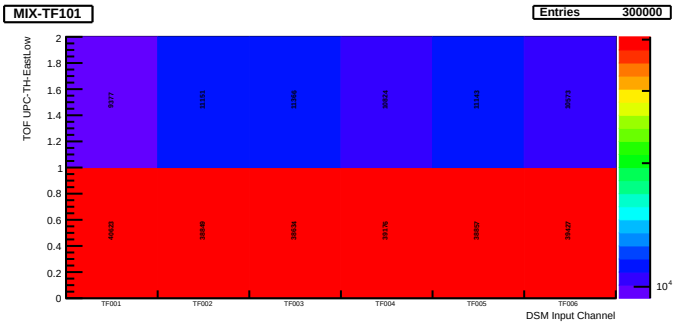

MIX-TF101
Entries 59285
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

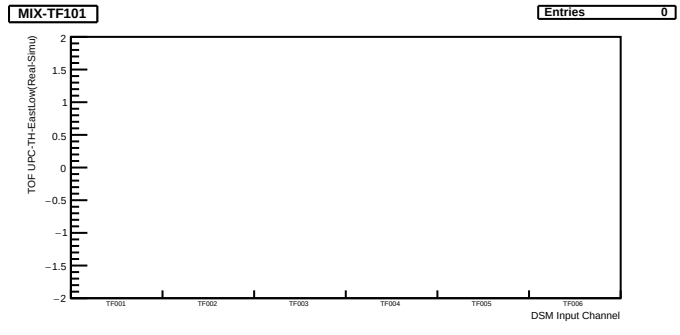

MIX-TF101
Entries 300000
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

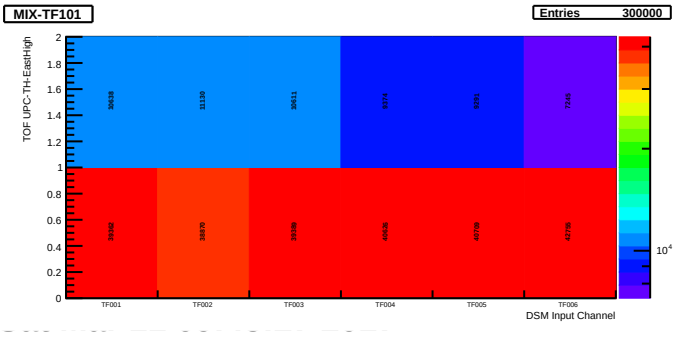

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

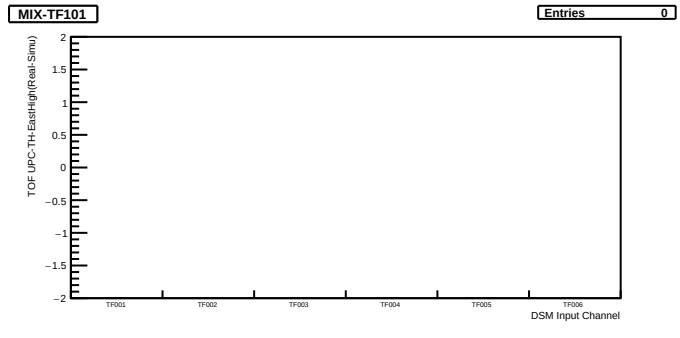

MIX-TF101
Entries 300000
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

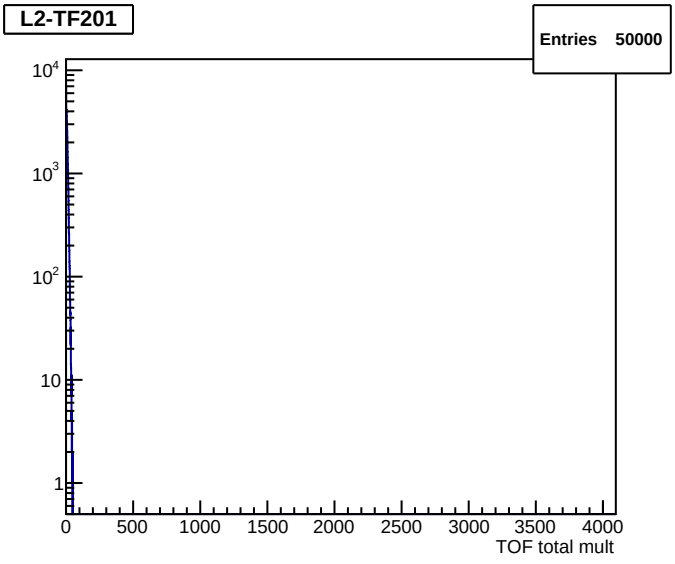

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

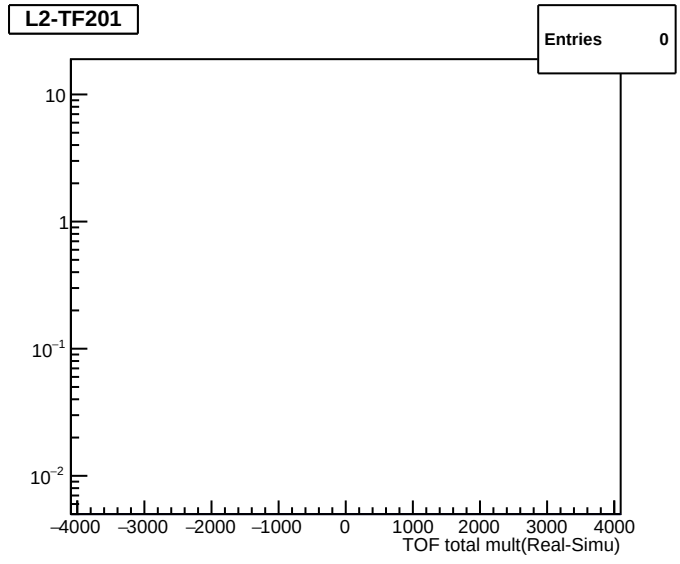

MIX-TF101
Entries 300000
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

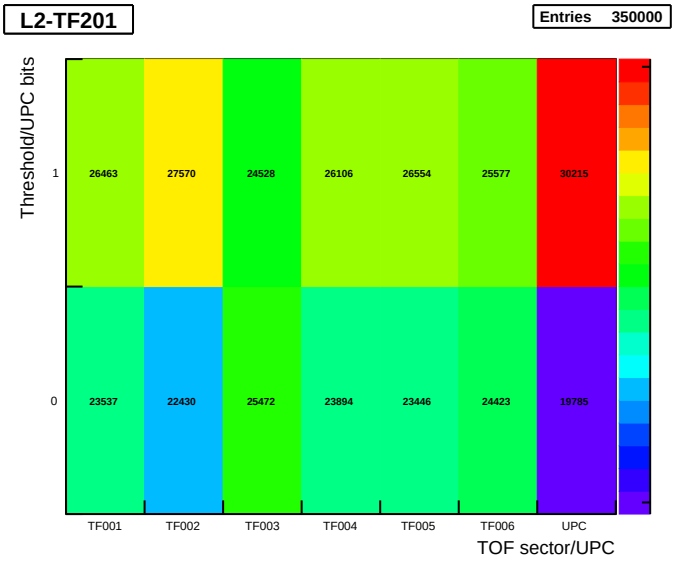

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

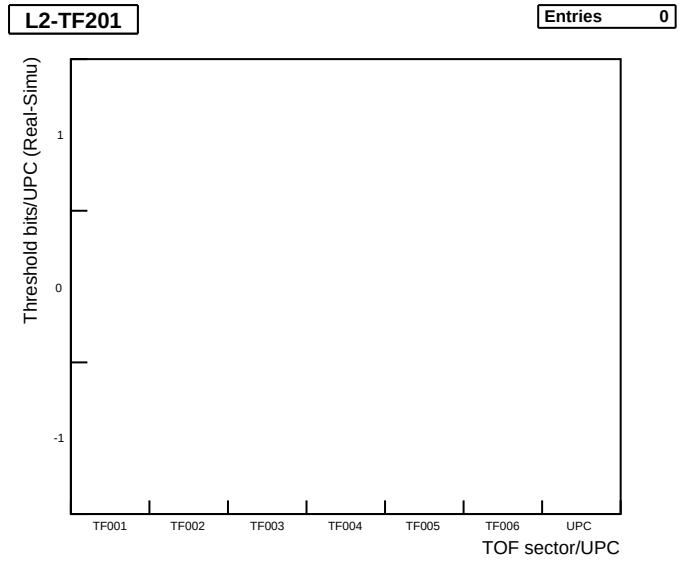

MIX-TF101
Entries 300000
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

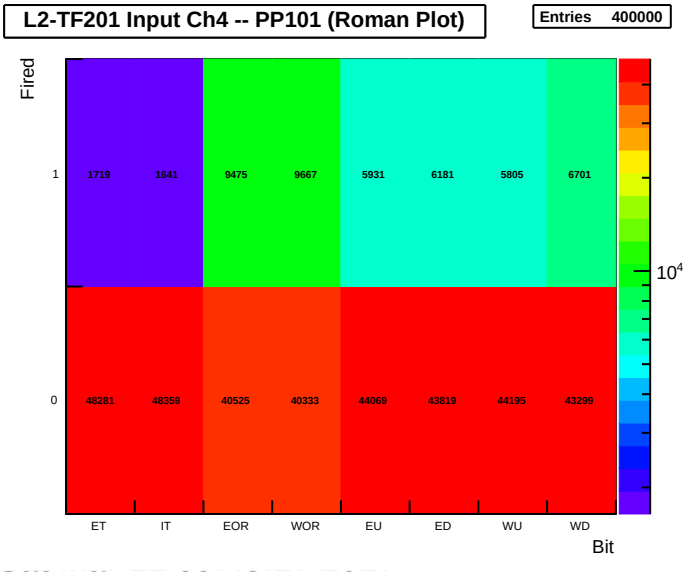

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

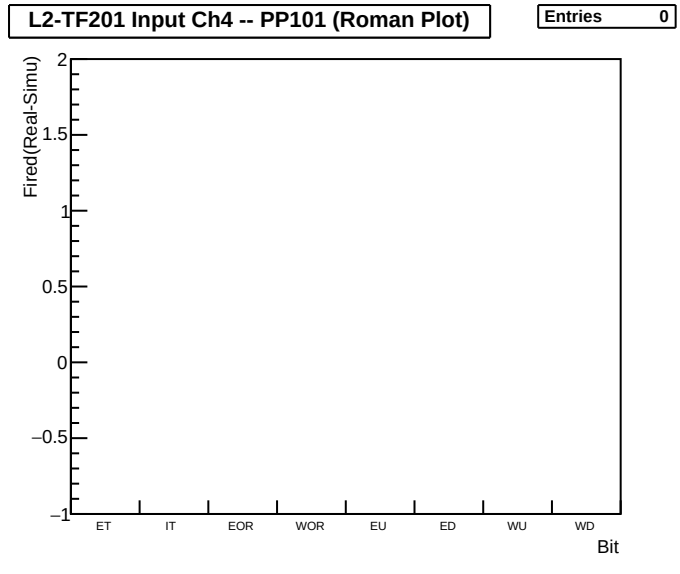

L2-TF201
Entries 50000
TOF total mult

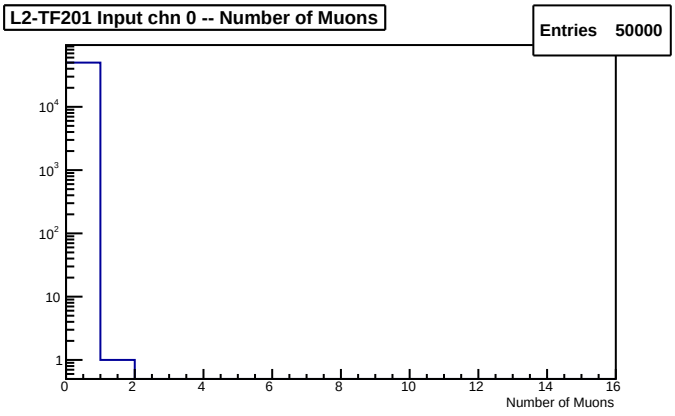

L2-TF201
Entries 0
TOF total mult(Real-Simu)

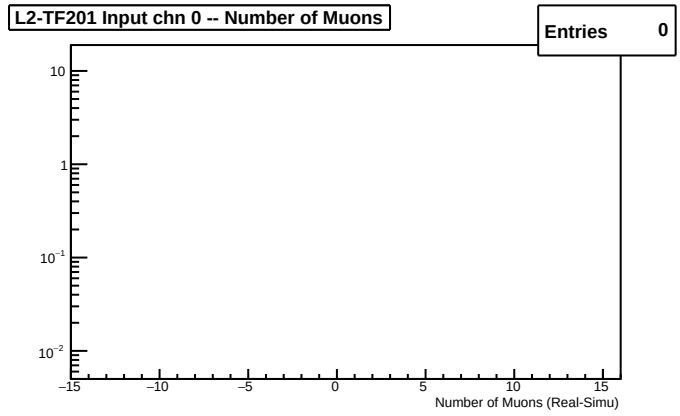

L2-TF201
Entries 350000
Threshold/UPC bits
26463 27570 24528 26106 26554 25577 30215
23537 22430 25472 23894 23446 24423 19785
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

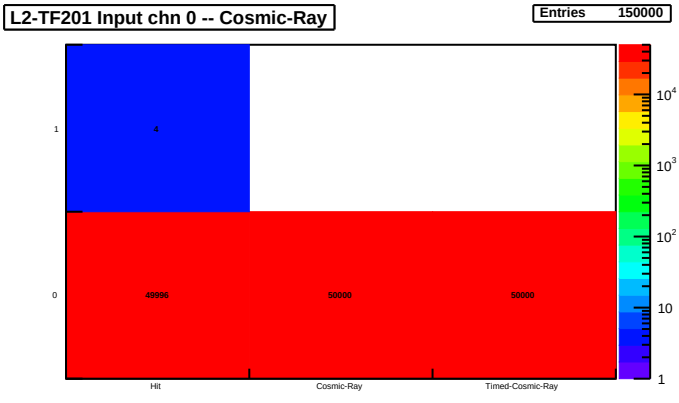

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

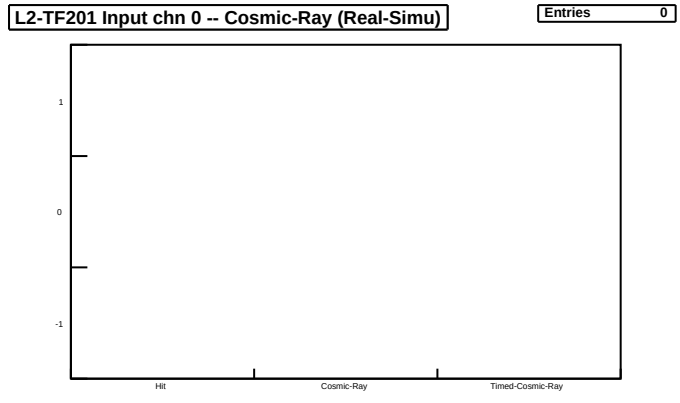

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 400000
Fired
1719 1641 9475 9667 5931 6181 5805 6701
48281 48359 40525 40333 44069 43819 44195 43299
ET IT EOR WOR EU ED WU WD
Bit

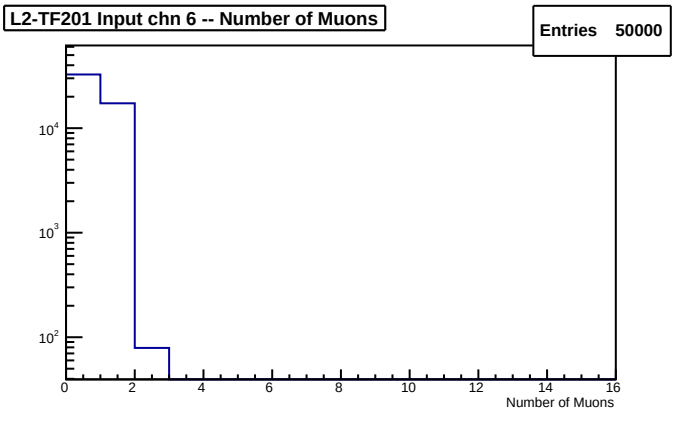

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 0
Fired(Real-Simu)
ET IT EOR WOR EU ED WU WD
Bit

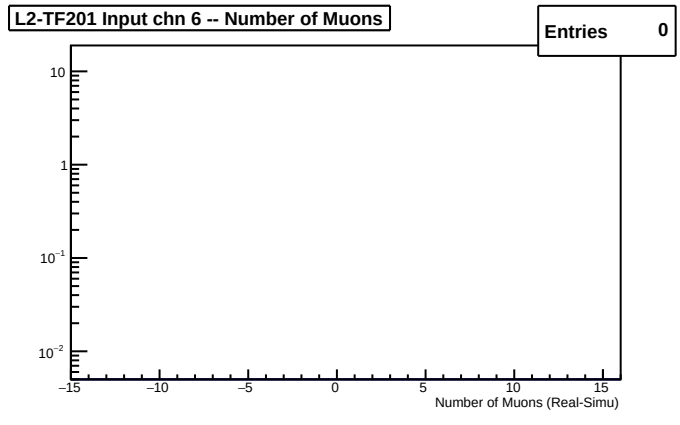

L2-TF201 Input chn 0 -- Number of Muons
Entries 50000
Number of Muons

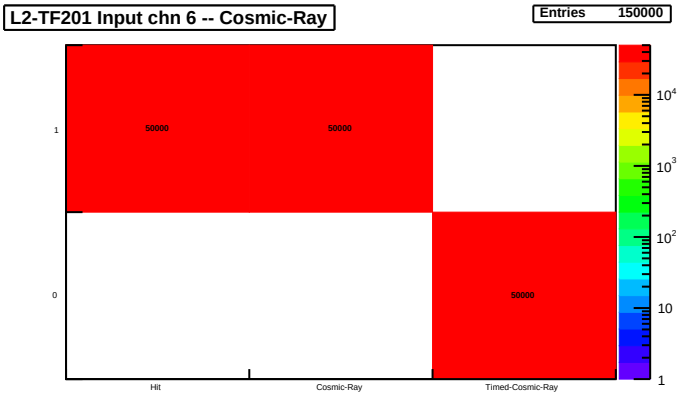

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

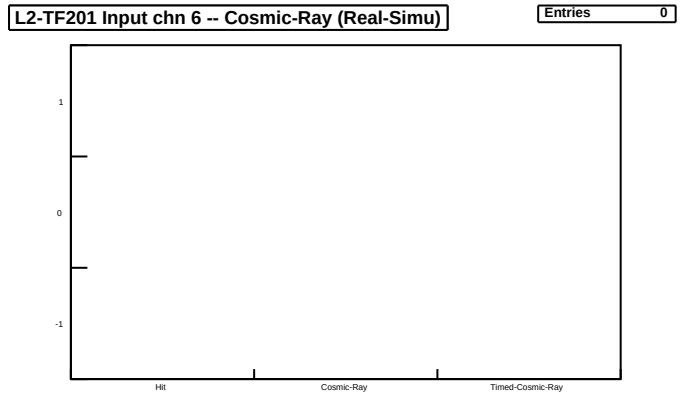

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 150000
4
49996
50000
50000
Hit
Cosmic-Ray
Timed-Cosmic-Ray

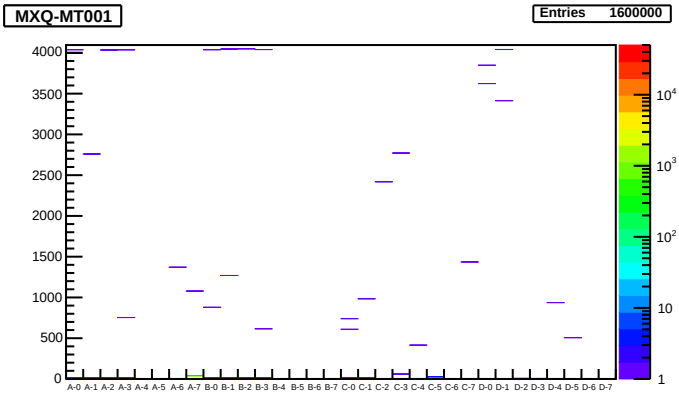

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

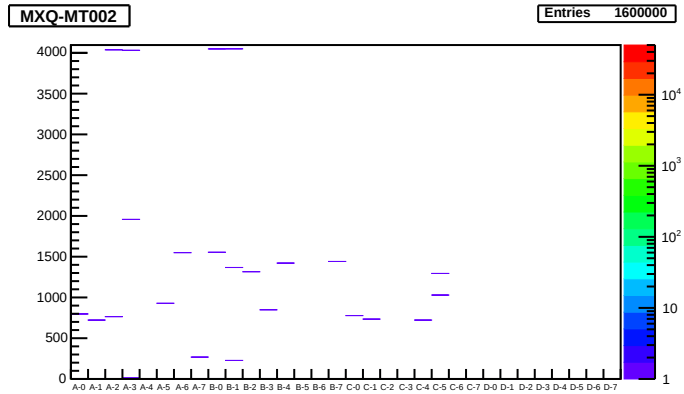

L2-TF201 Input chn 6 -- Number of Muons
Entries 50000
Number of Muons

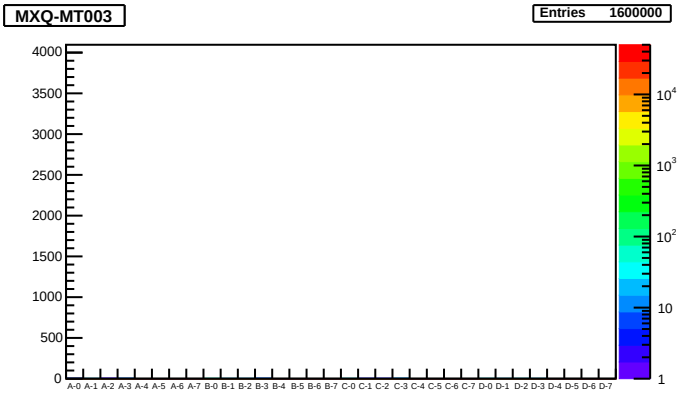

L2-TF201 Input chn 6 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

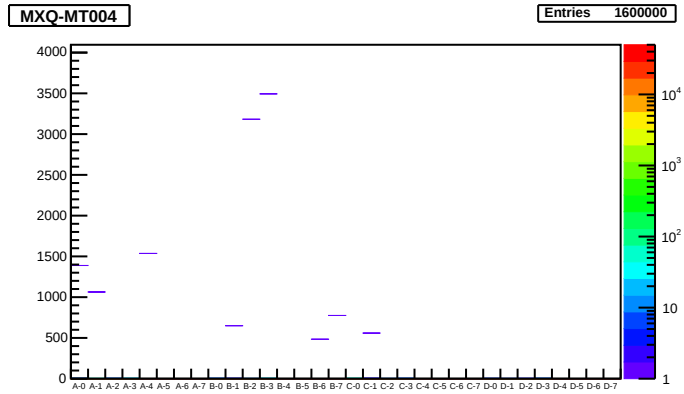

L2-TF201 Input chn 6 -- Cosmic-Ray
Entries 150000
50000
50000
50000
Hit
Cosmic-Ray
Timed-Cosmic-Ray

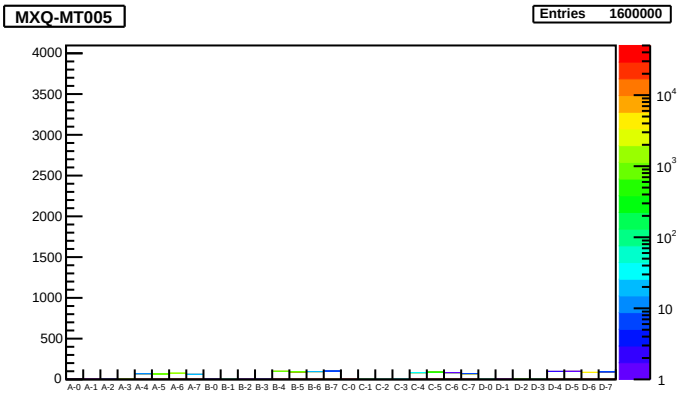

L2-TF201 Input chn 6 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

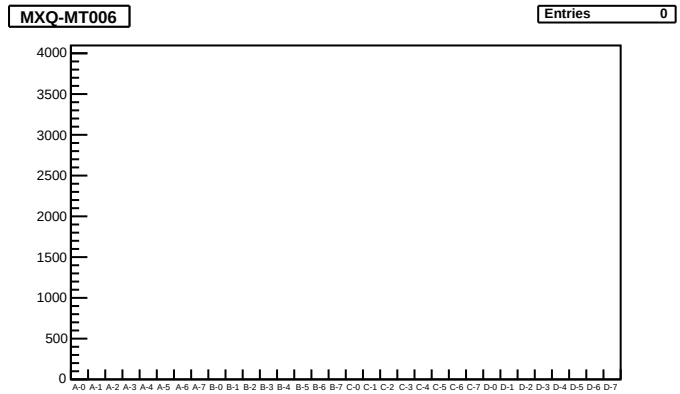

MXQ-MT001
Entries 1600000

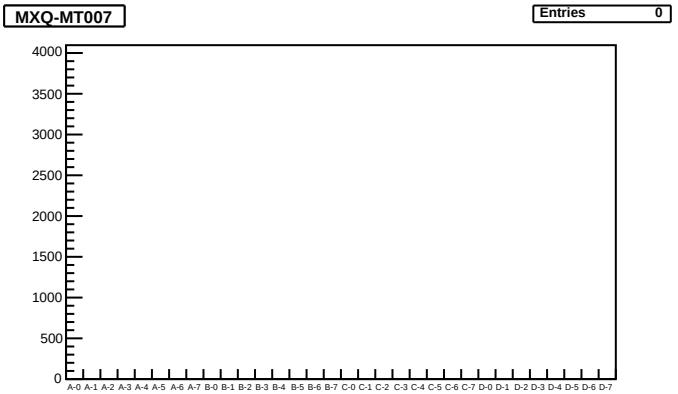

MXQ-MT002
Entries 1600000

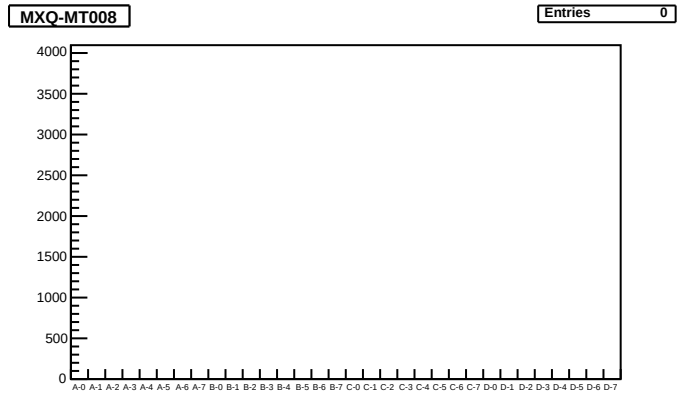

MXQ-MT003
Entries 1600000

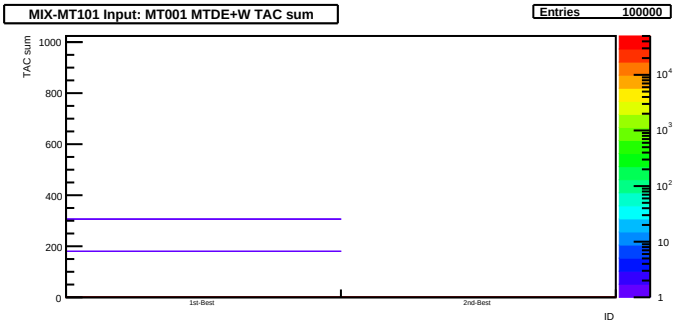

MXQ-MT004
Entries 1600000

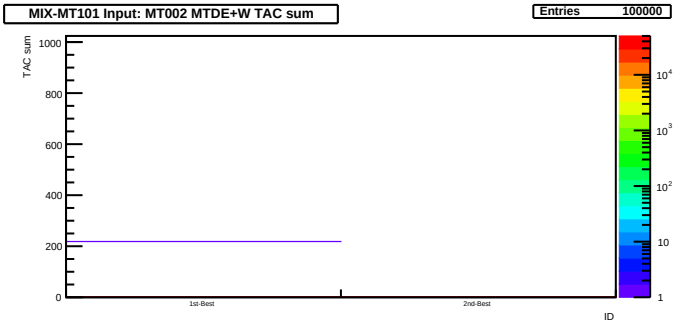

MXQ-MT005
Entries 1600000

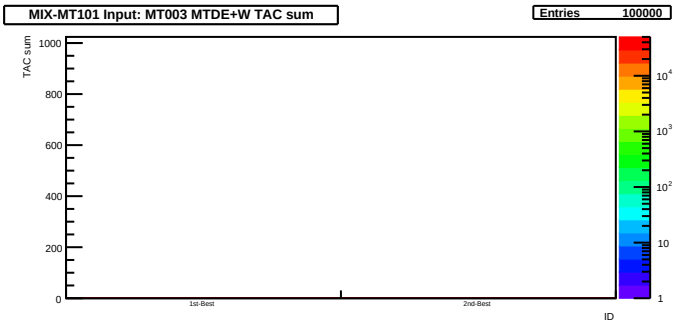

MXQ-MT006
Entries 0

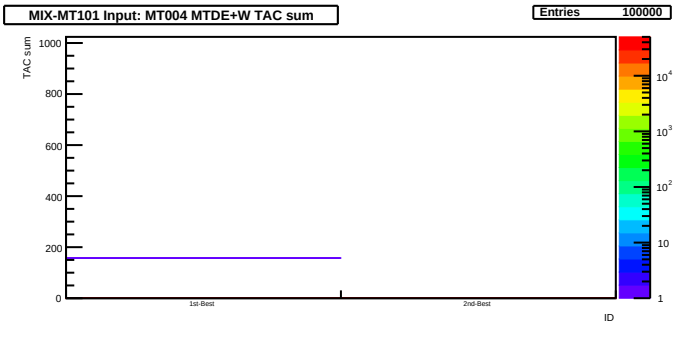

MXQ-MT007
Entries 0

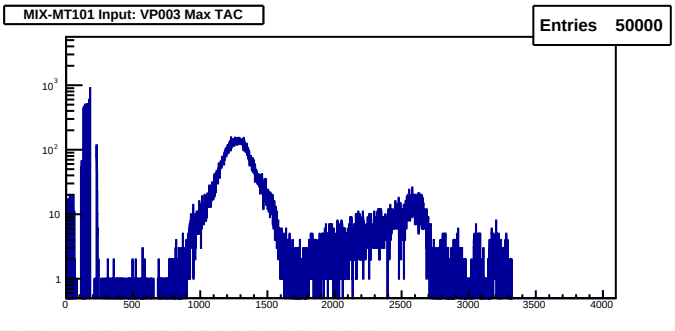

MXQ-MT008
Entries 0

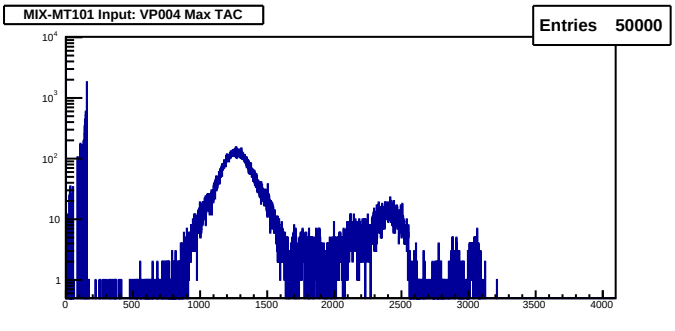

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 100000
TAC sum
1st-Best
2nd-Best
ID

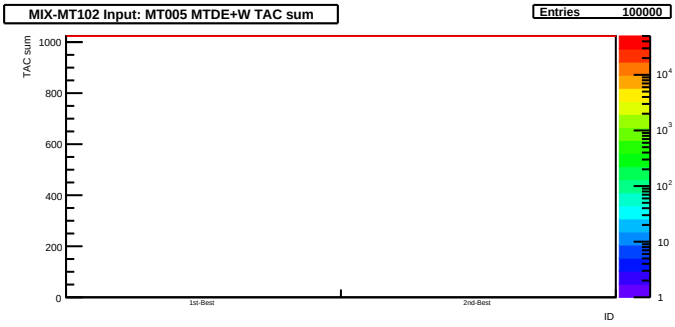

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 100000
TAC sum
1st-Best
2nd-Best
ID

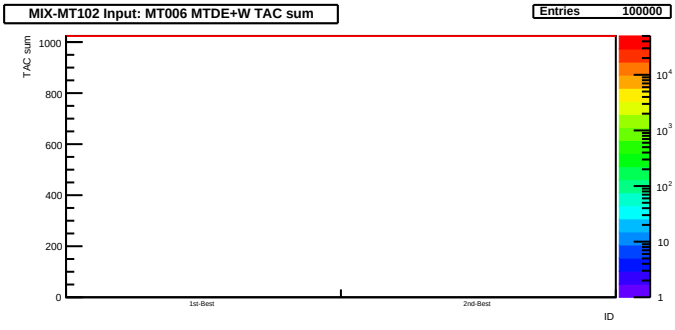

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 100000
TAC sum
1st-Best
2nd-Best
ID

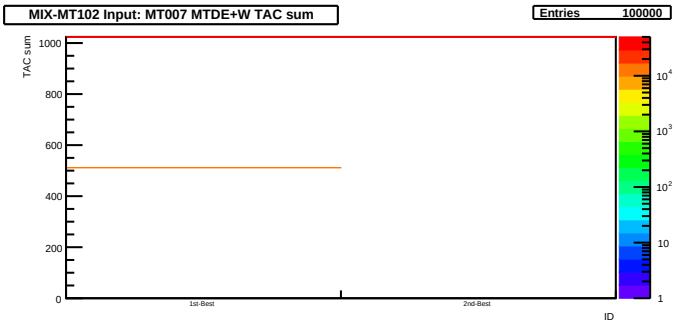

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 100000
TAC sum
1st-Best
2nd-Best
ID

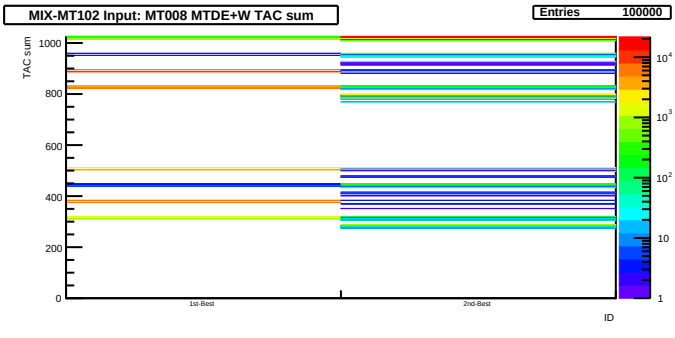

MIX-MT101 Input: VP003 Max TAC
Entries 50000

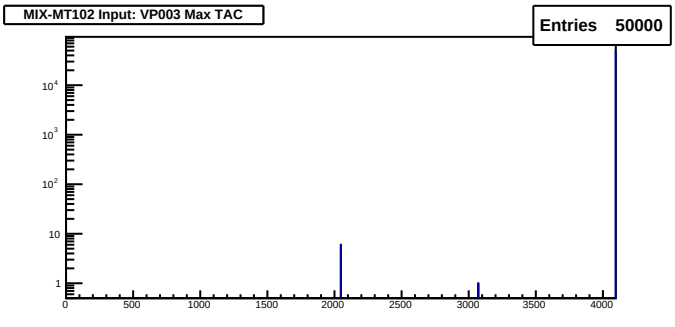

MIX-MT101 Input: VP004 Max TAC
Entries 50000

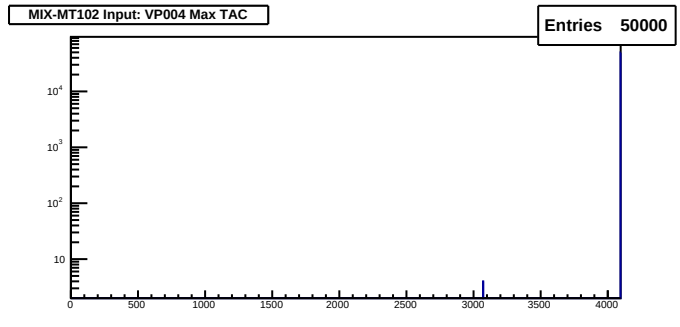

MIX-MT102 Input: MT005 MTDE+W TAC sum
Entries 100000
TAC sum
1st-Best
2nd-Best
ID

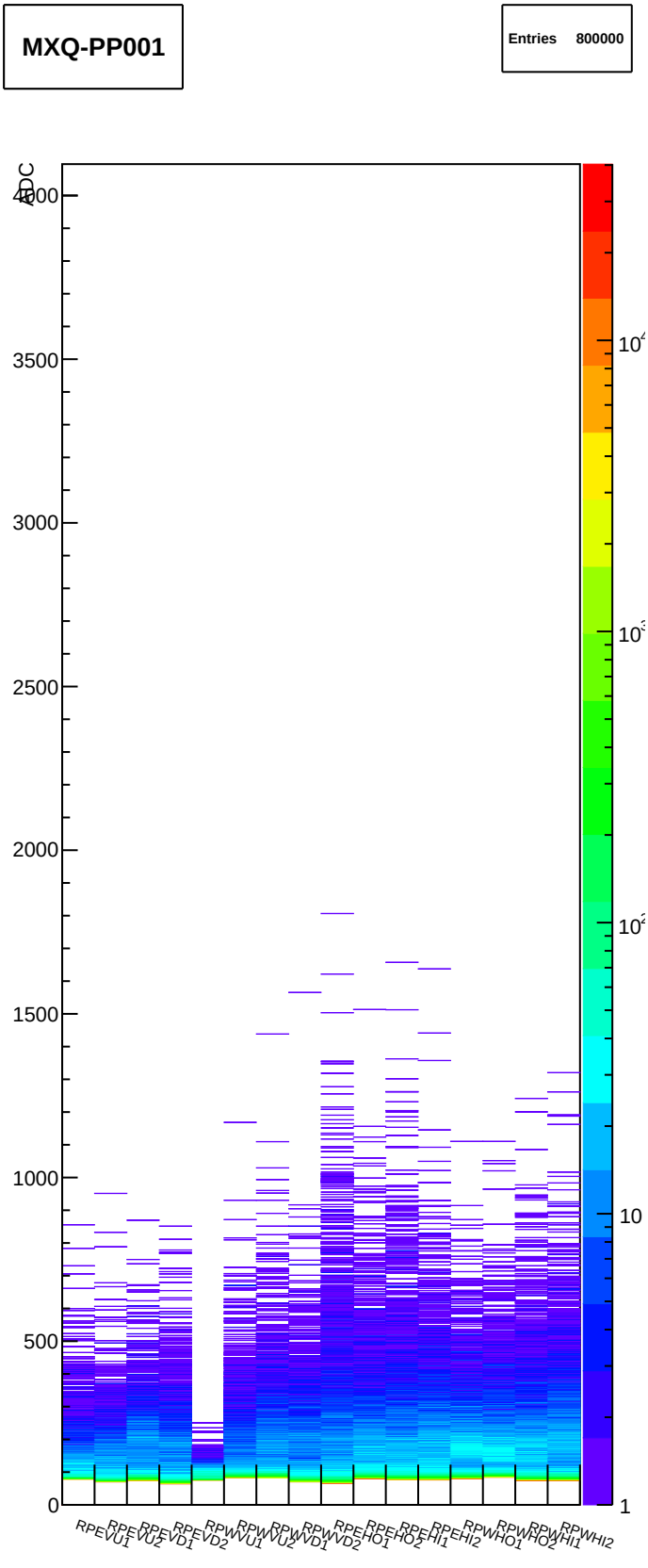

MIX-MT102 Input: MT006 MTDE+W TAC sum
Entries 100000
TAC sum
1st-Best
2nd-Best
ID

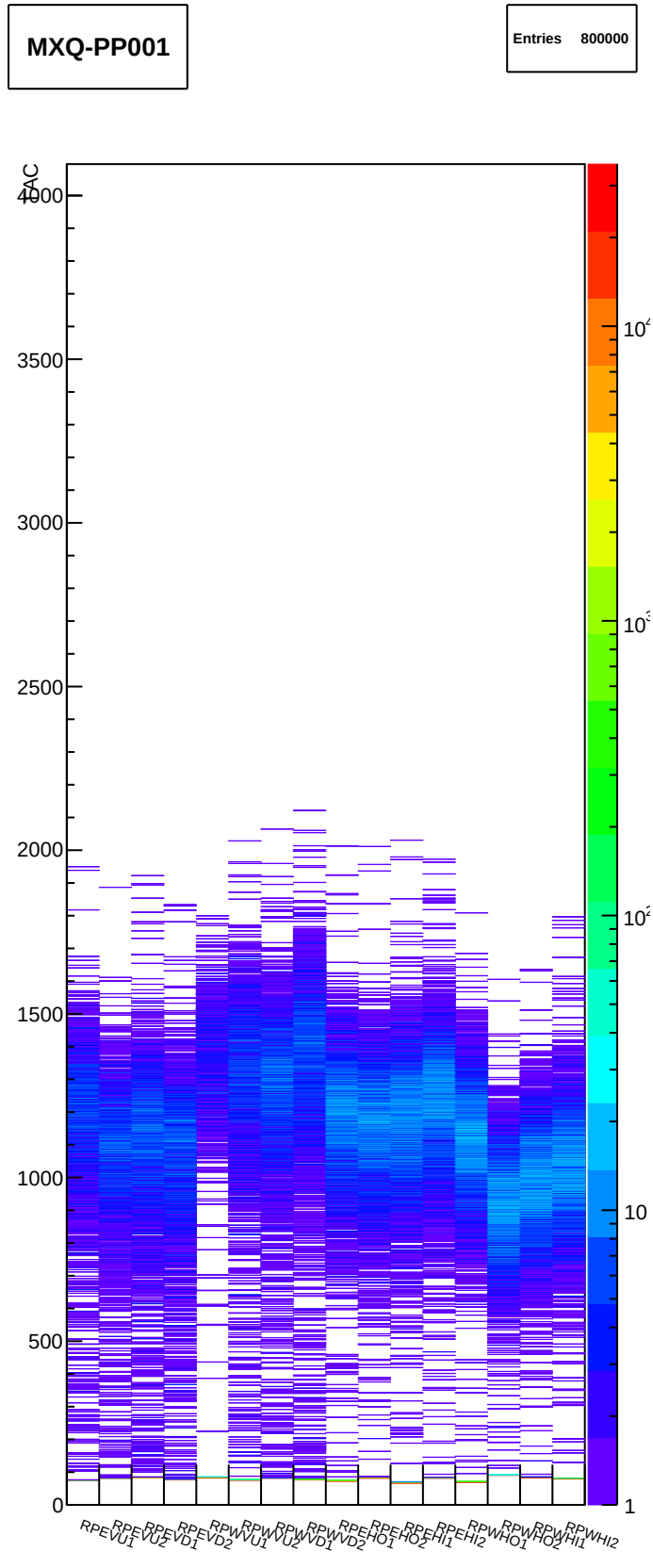

MIX-MT102 Input: MT007 MTDE+W TAC sum
Entries 100000
TAC sum
1st-Best
2nd-Best
ID

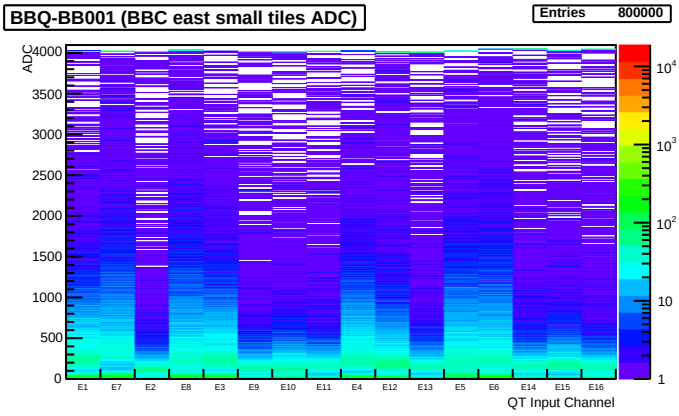

MIX-MT102 Input: MT008 MTDE+W TAC sum
Entries 100000
TAC sum
1st-Best
2nd-Best
ID

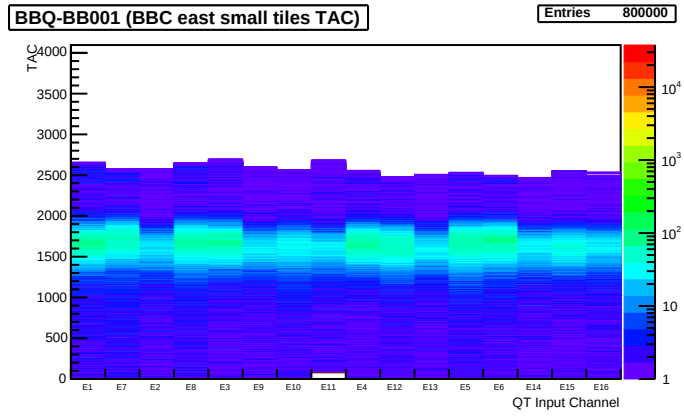

MIX-MT102 Input: VP003 Max TAC
Entries 50000

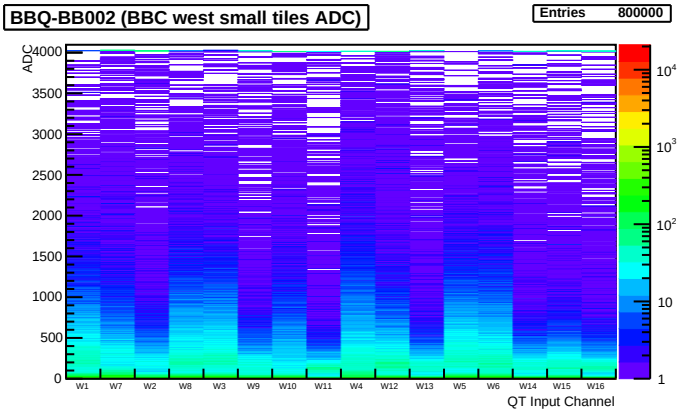

MIX-MT102 Input: VP004 Max TAC
Entries 50000

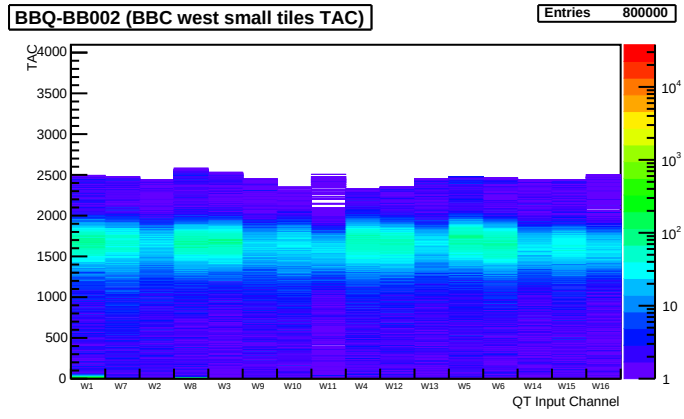

MXQ-PP001
Entries 800000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

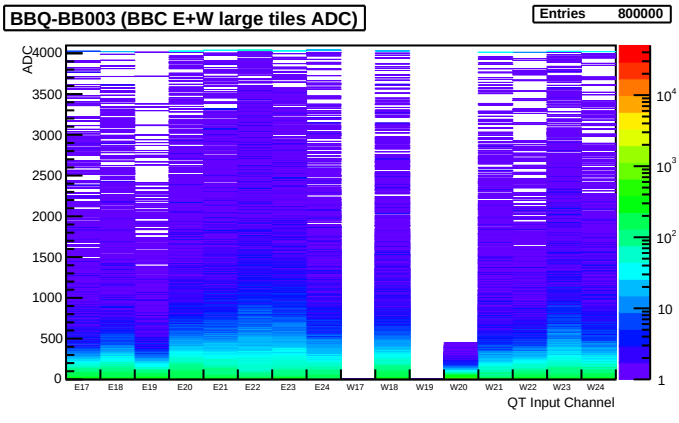

MXQ-PP001
Entries 800000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

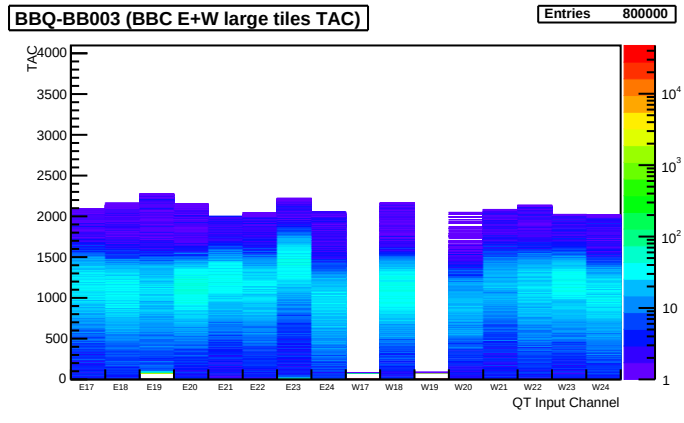

BBQ-BB001 (BBC east small tiles ADC)
Entries 800000
ADC
QT Input Channel

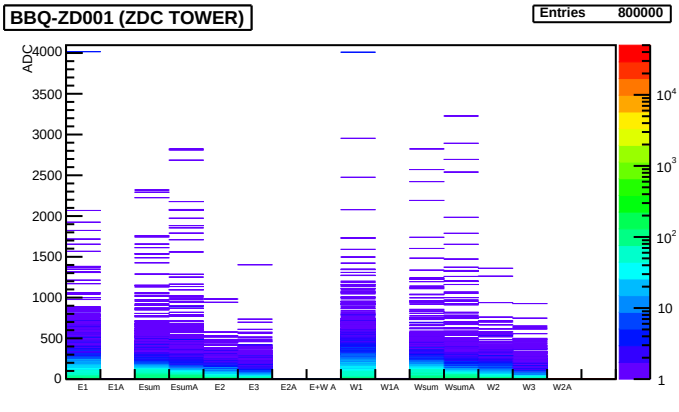

BBQ-BB001 (BBC east small tiles TAC)
Entries 800000
TAC
QT Input Channel

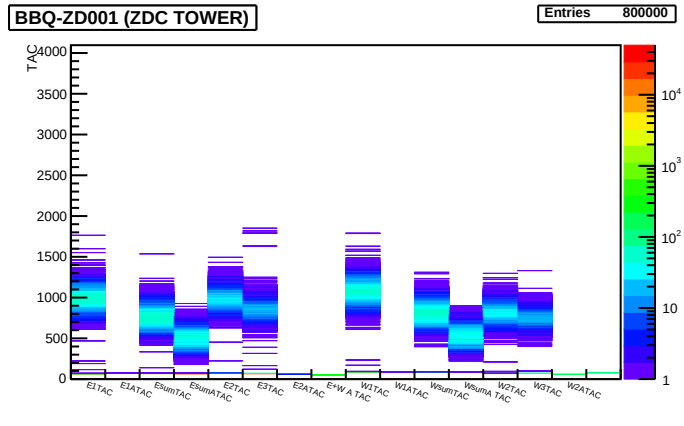

BBQ-BB002 (BBC west small tiles ADC)
Entries 800000
ADC
QT Input Channel

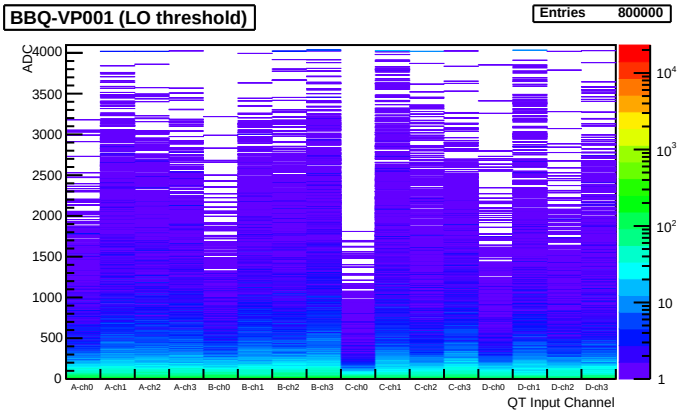

BBQ-BB002 (BBC west small tiles TAC)
Entries 800000
TAC
QT Input Channel

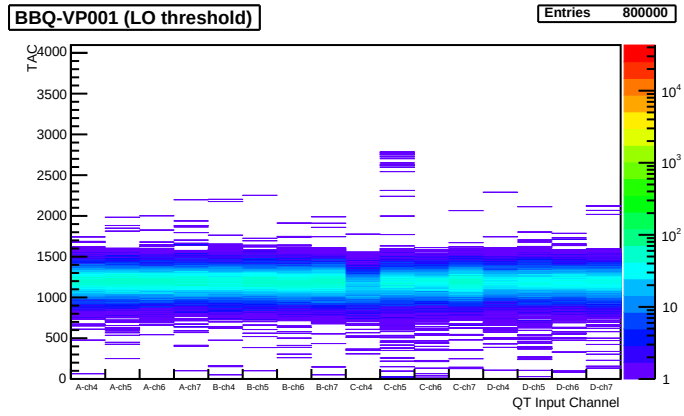

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 800000
ADC
QT Input Channel

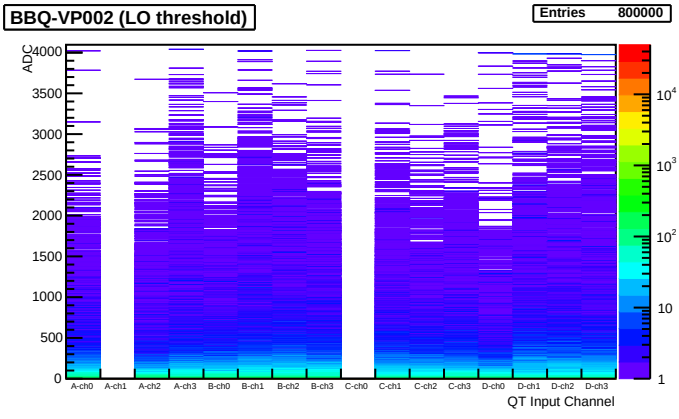

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 800000
TAC
QT Input Channel

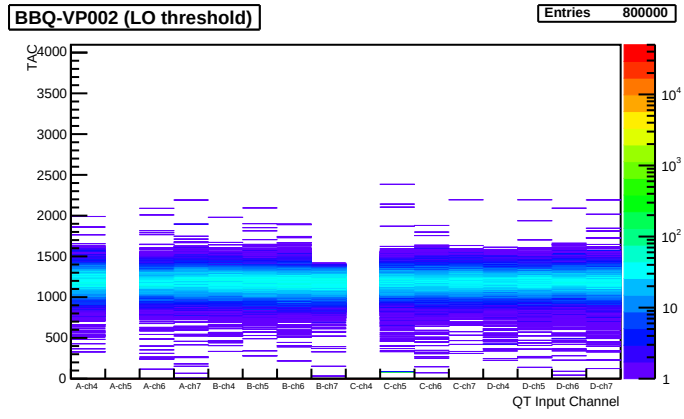

BBQ-ZD001 (ZDC TOWER)
Entries 800000
ADC

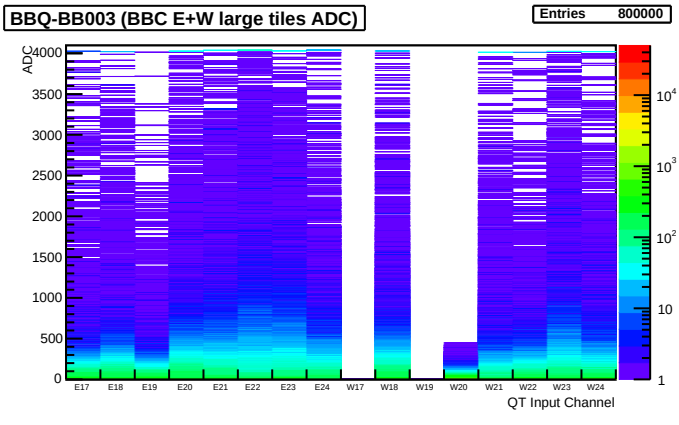

BBQ-ZD001 (ZDC TOWER)
Entries 800000
TAC

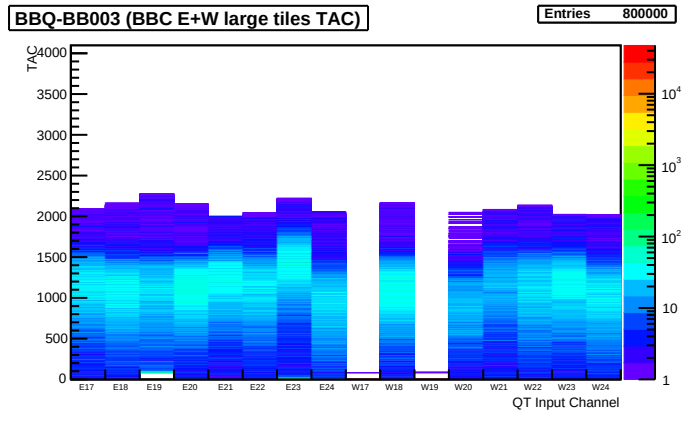

BBQ-VP001 (LO threshold)
Entries 800000
ADC
QT Input Channel

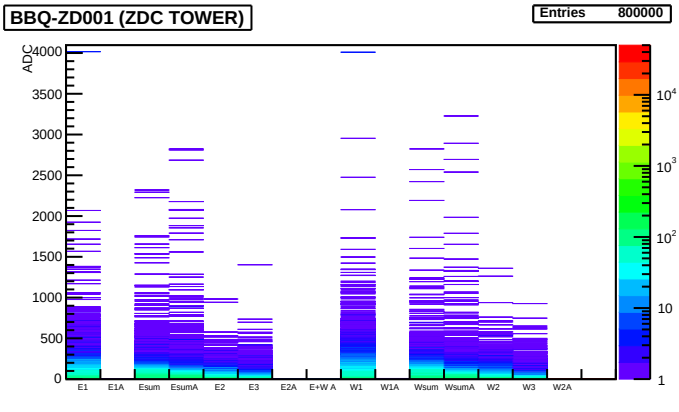

BBQ-VP001 (LO threshold)
Entries 800000
TAC
QT Input Channel

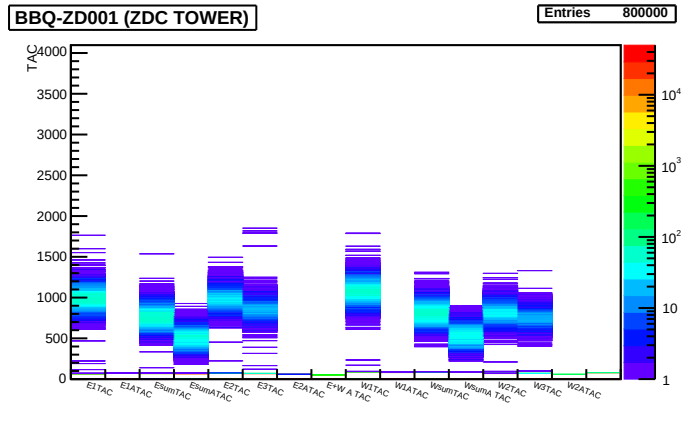

BBQ-VP002 (LO threshold)
Entries 800000
ADC
QT Input Channel

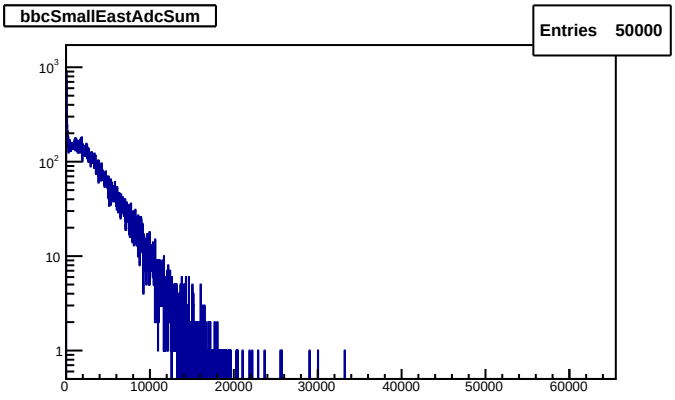

BBQ-VP002 (LO threshold)
Entries 800000
TAC
QT Input Channel

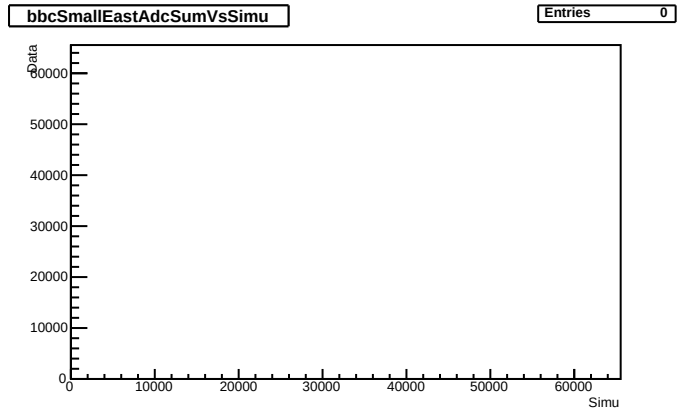

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 800000
ADC
QT Input Channel

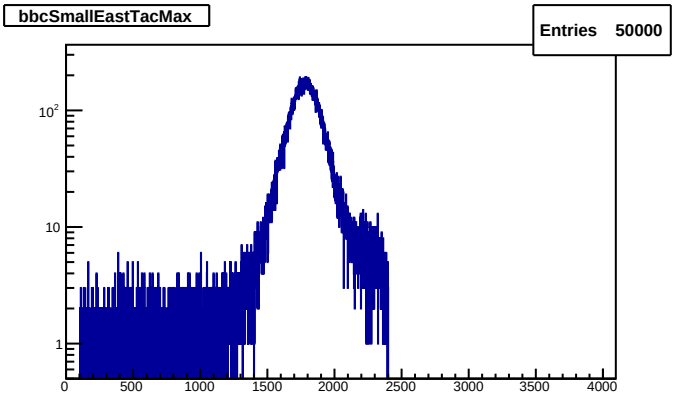

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 800000
TAC
QT Input Channel

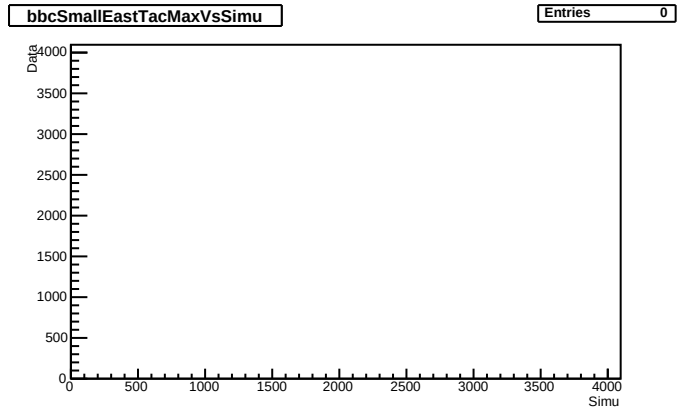

BBQ-ZD001 (ZDC TOWER)
Entries 800000
ADC

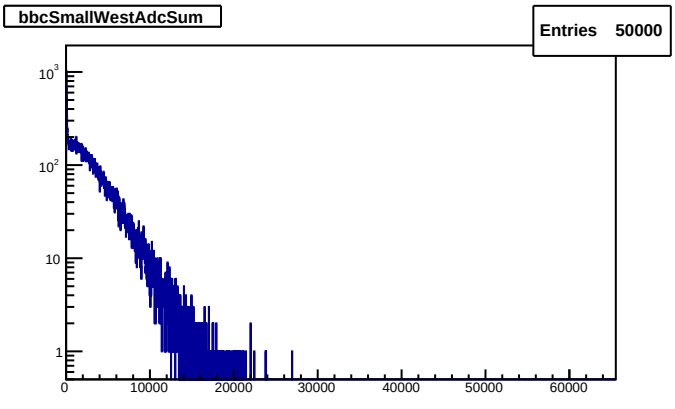

BBQ-ZD001 (ZDC TOWER)
Entries 800000
TAC

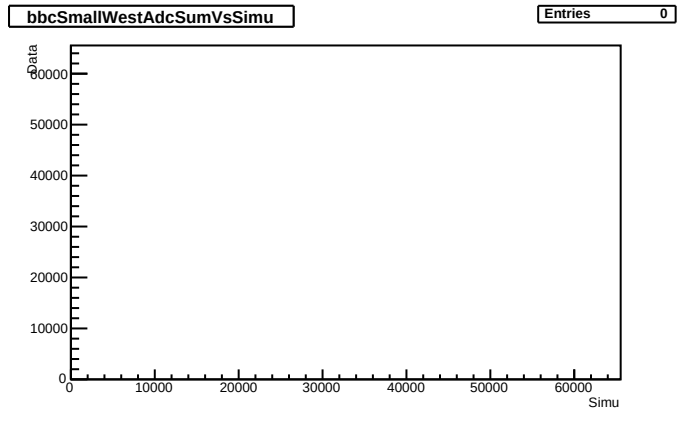

bbcSmallEastAdcSum
Entries 50000

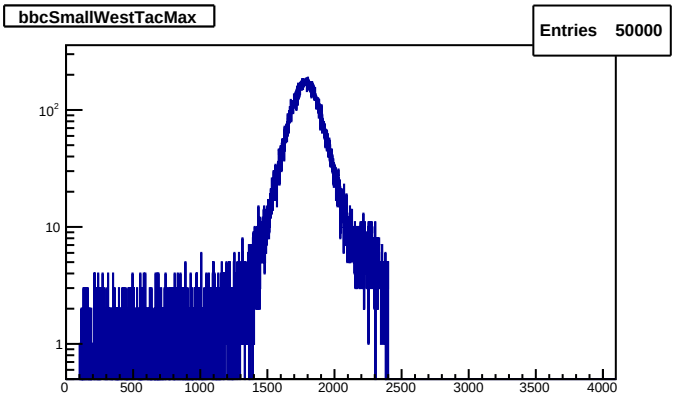

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

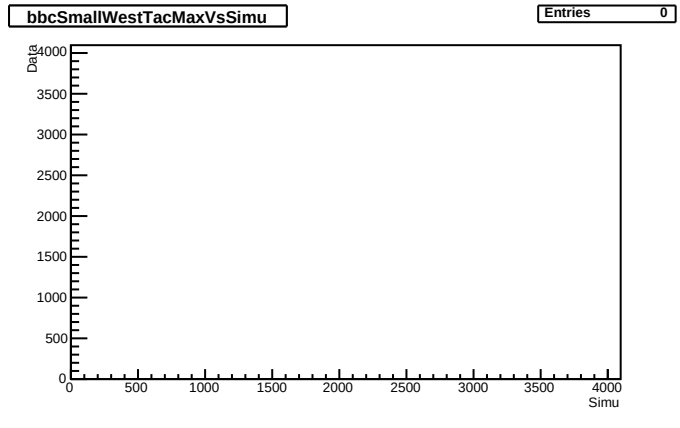

bbcSmallEastTacMax
Entries 50000

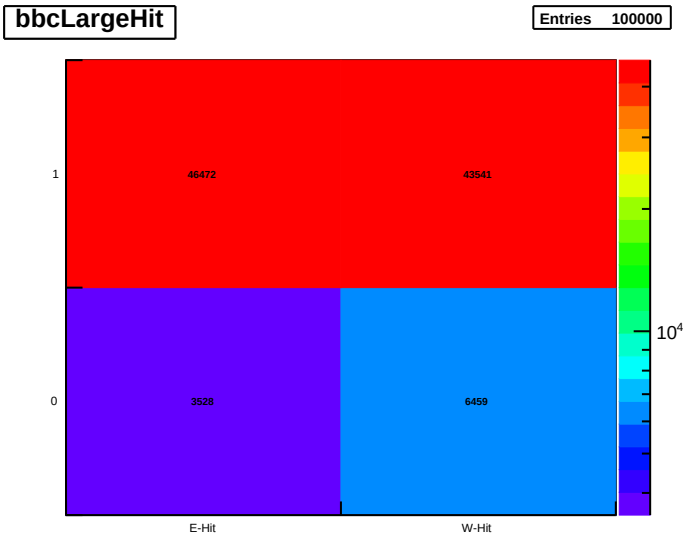

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

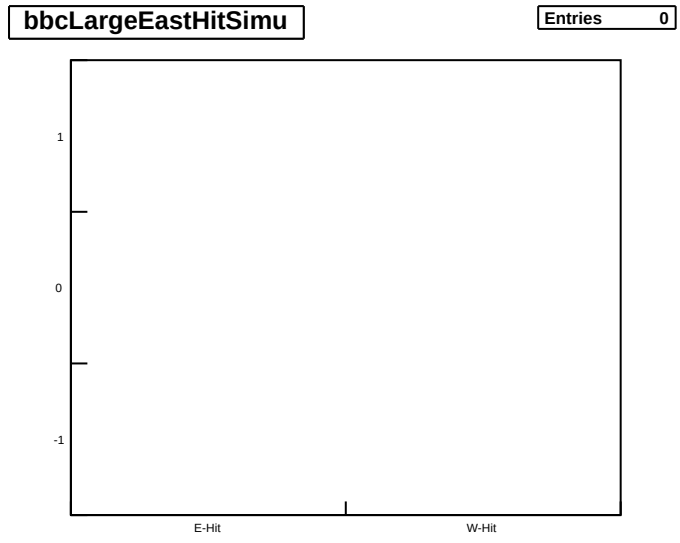

bbcSmallWestAdcSum
Entries 50000

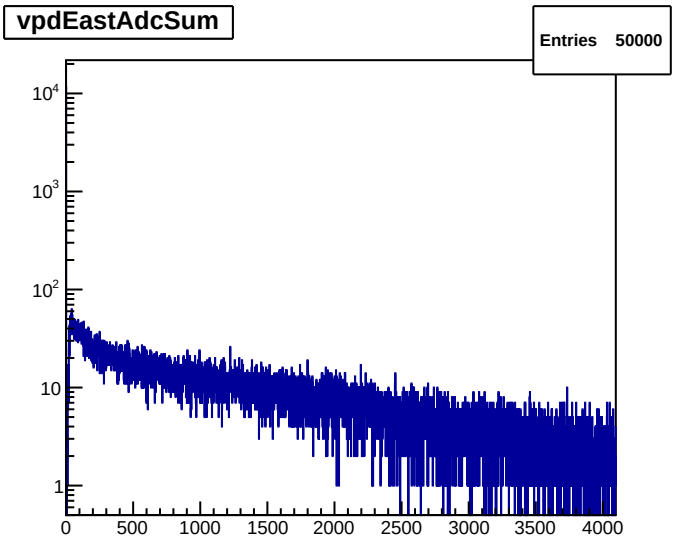

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

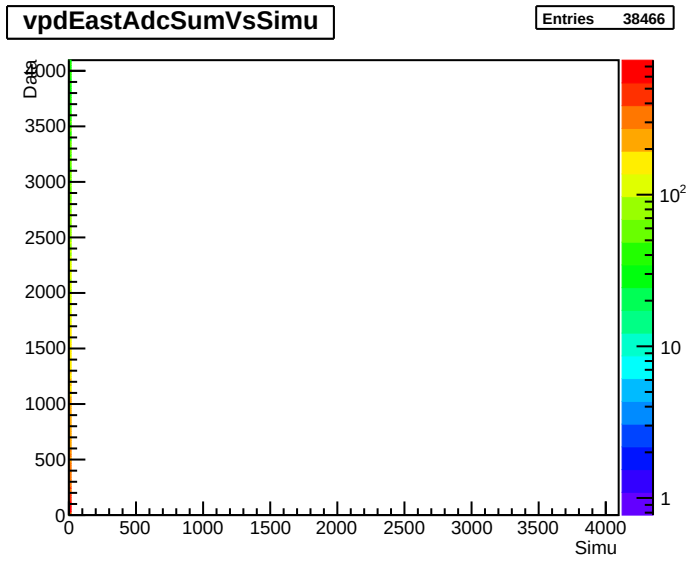

bbcSmallWestTacMax
Entries 50000

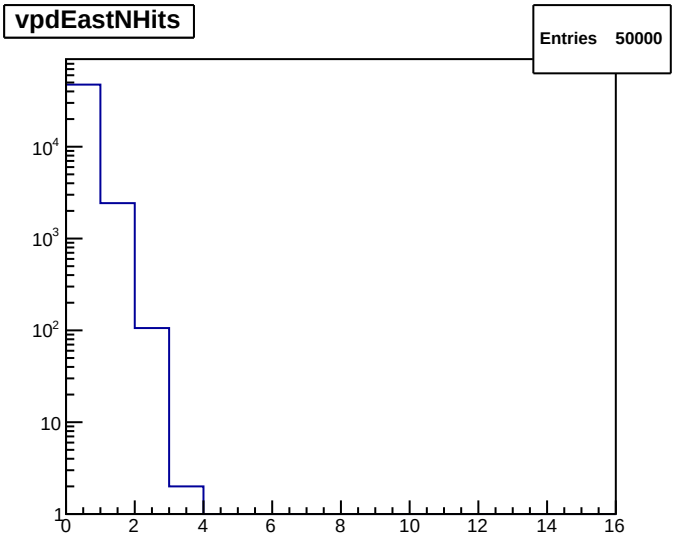

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

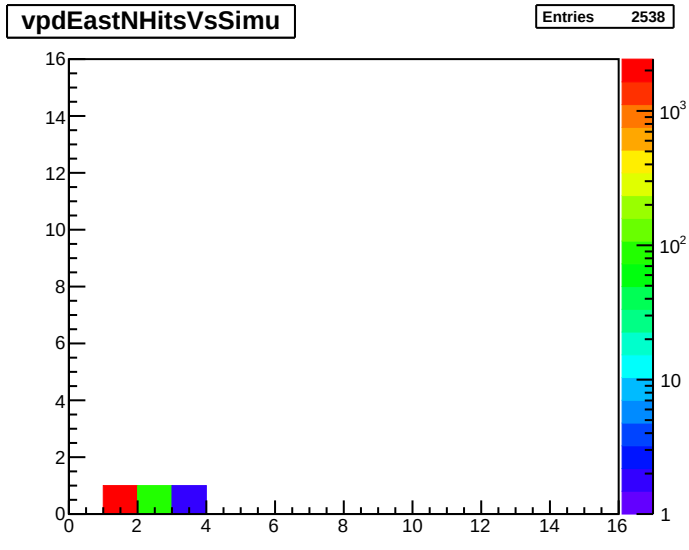

bbcLargeHit
Entries 100000
1
0
46472
43541
3528
6459
$10^4$
E-Hit
W-Hit

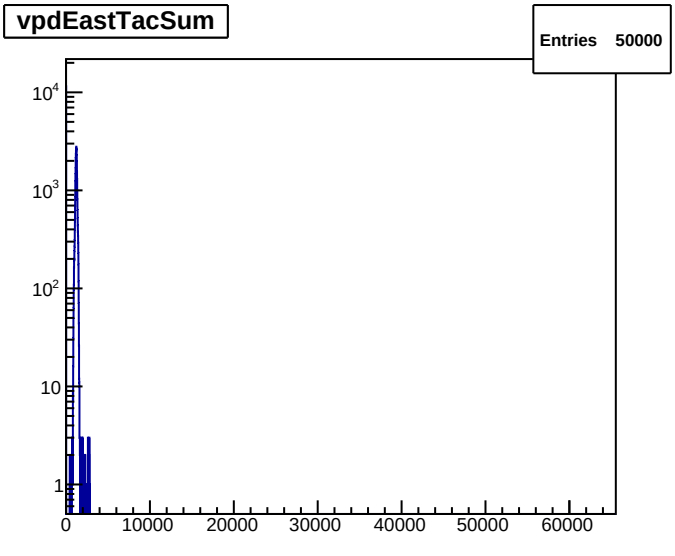

bbcLargeEastHitSimu
Entries 0
1
0
-1
E-Hit
W-Hit

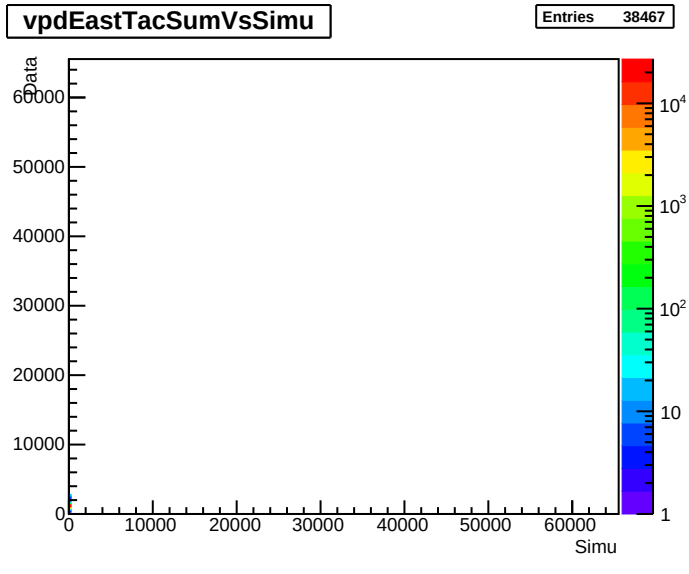

vpdEastAdcSum
Entries 50000

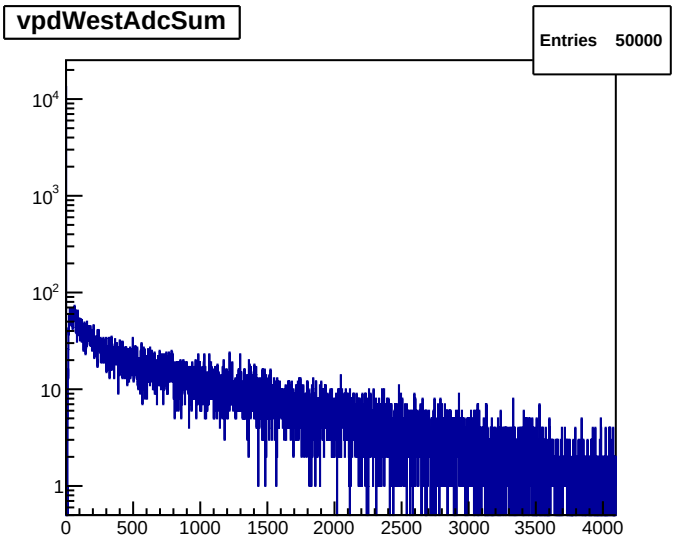

vpdEastAdcSumVsSimu
Entries 38466
Data
Simu

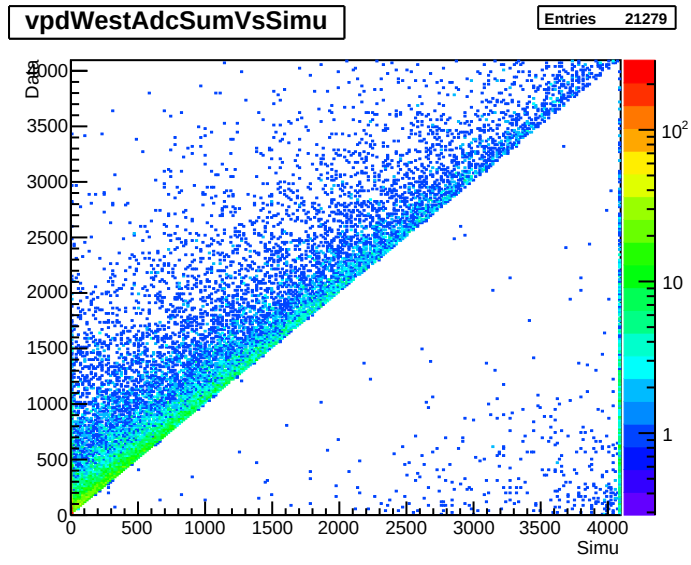

vpdEastNHits
Entries 50000

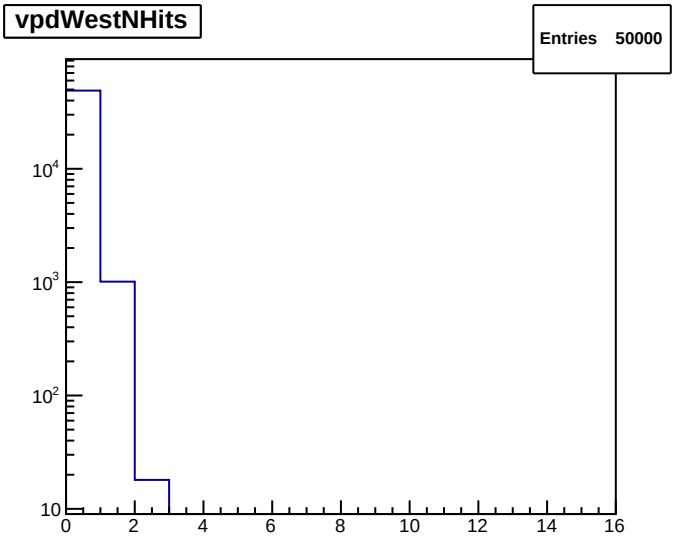

vpdEastNHitsVsSimu
Entries 2538

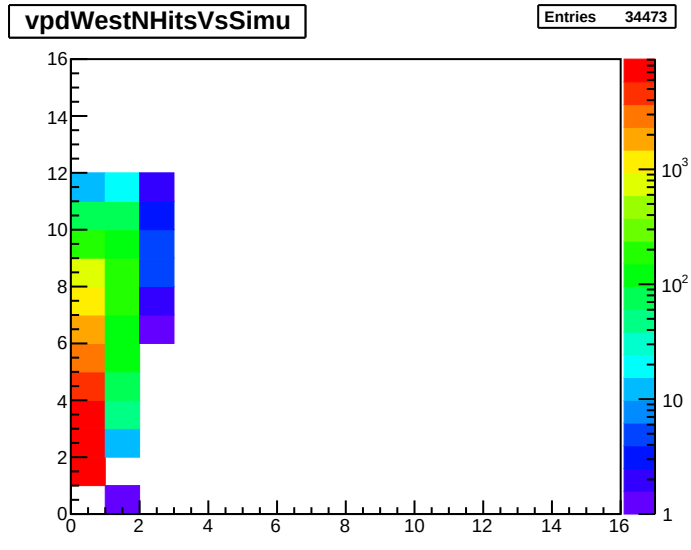

vpdEastTacSum
Entries 50000

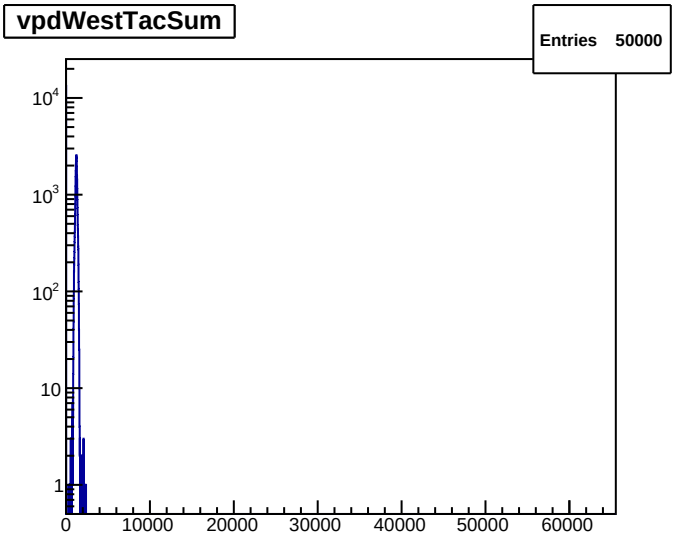

vpdEastTacSumVsSimu
Entries 38467
Data
Simu

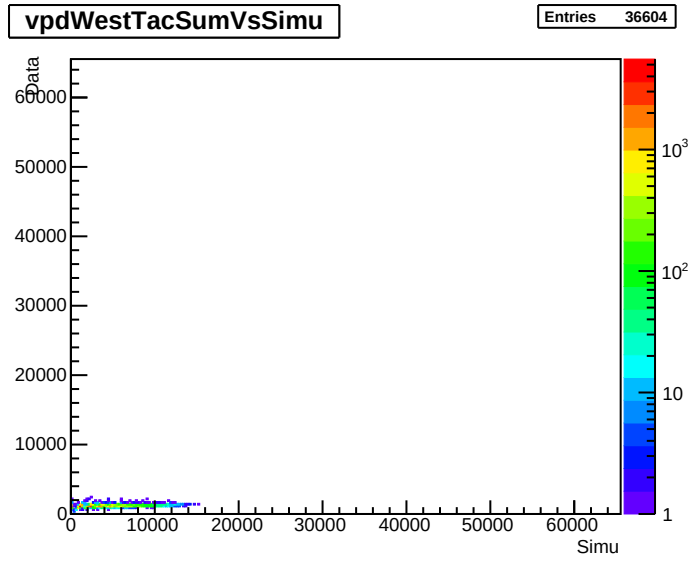

vpdWestAdcSum
Entries 50000

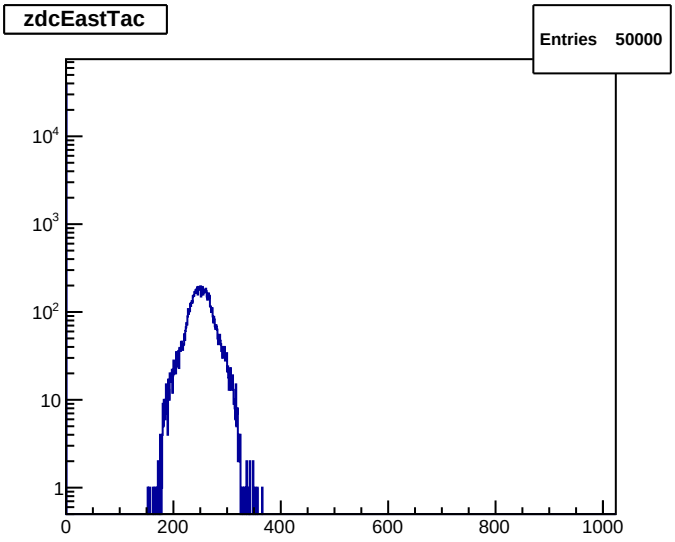

vpdWestAdcSumVsSimu
Entries 21279
Data
Simu

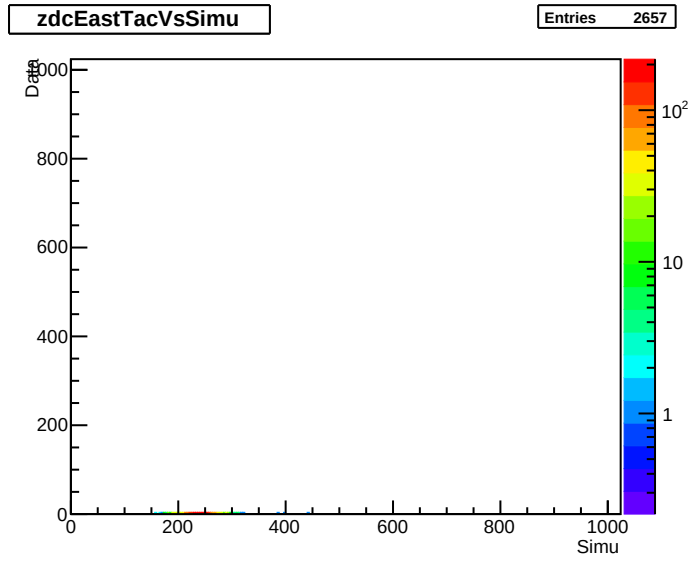

vpdWestNHits
Entries 50000

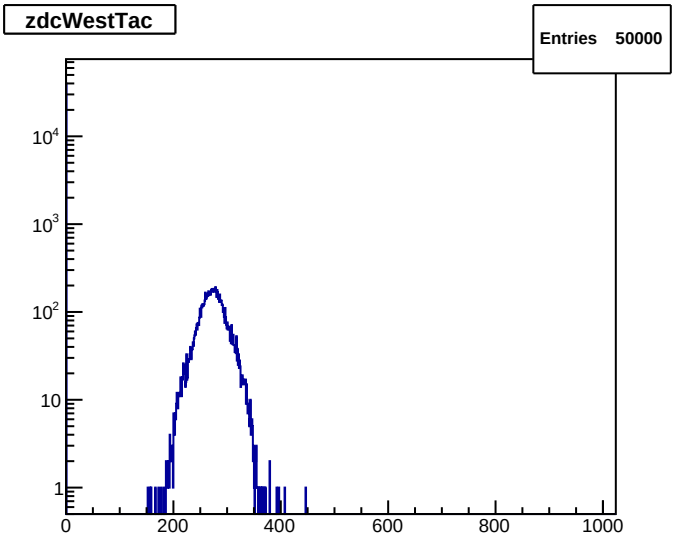

vpdWestNHitsVsSimu
Entries 34473

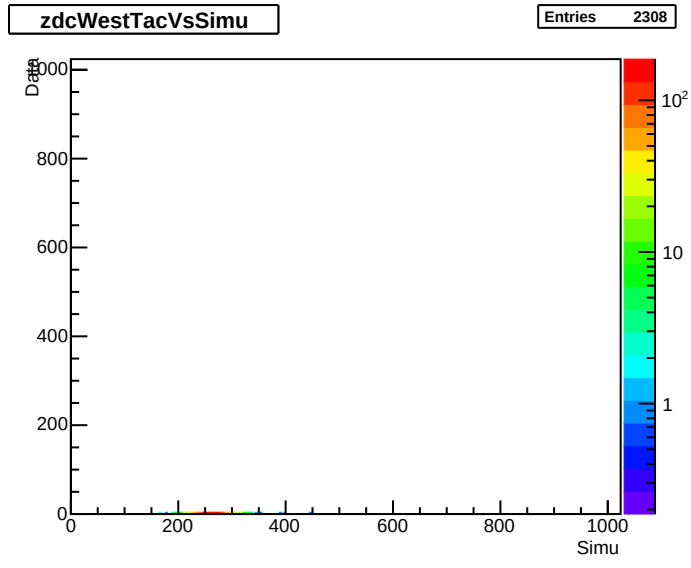

vpdWestTacSum
Entries 50000

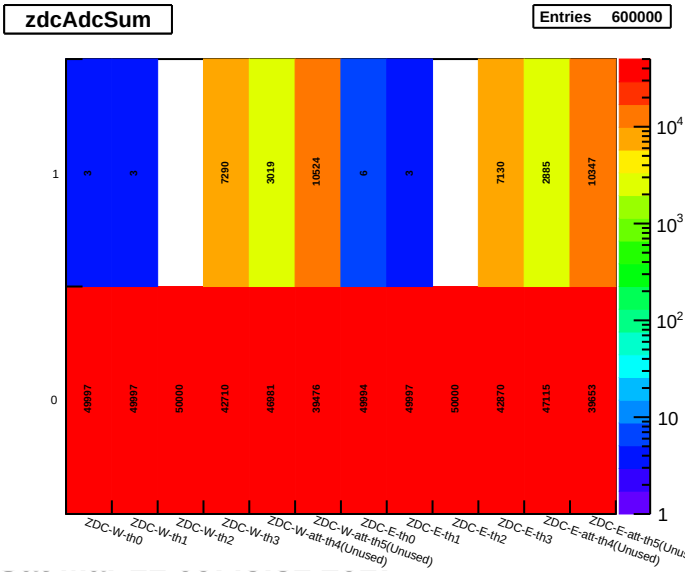

vpdWestTacSumVsSimu
Entries 36604
Data
Simu

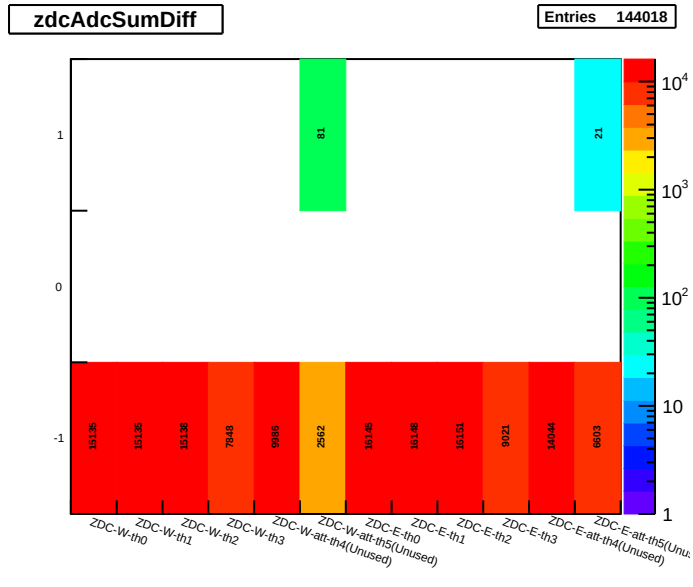

zdcEastTac
Entries 50000

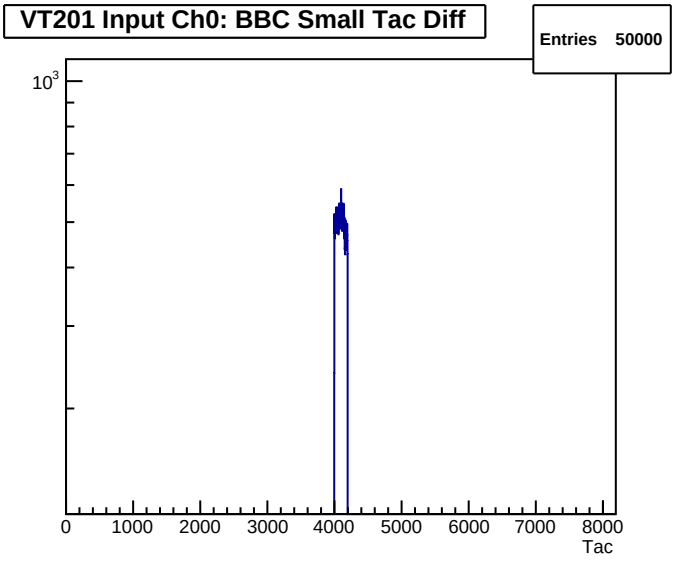

zdcEastTacVsSimu
Entries 2657
Data
Simu

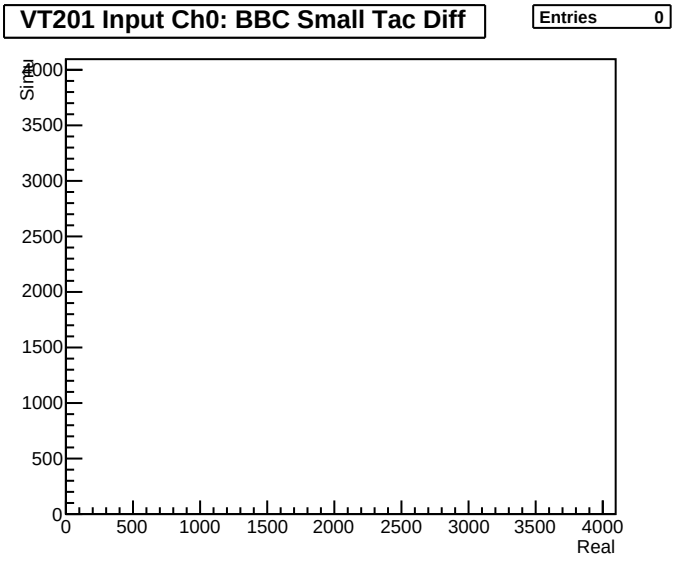

zdcWestTac
Entries 50000

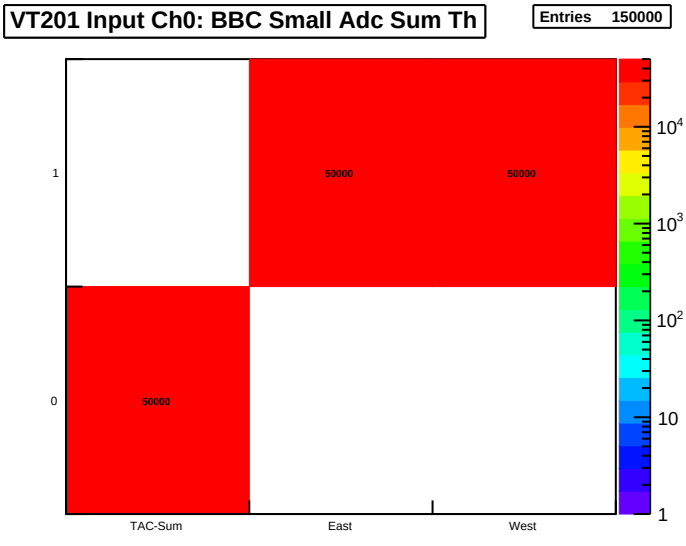

zdcWestTacVsSimu
Entries 2308
Data
Simu

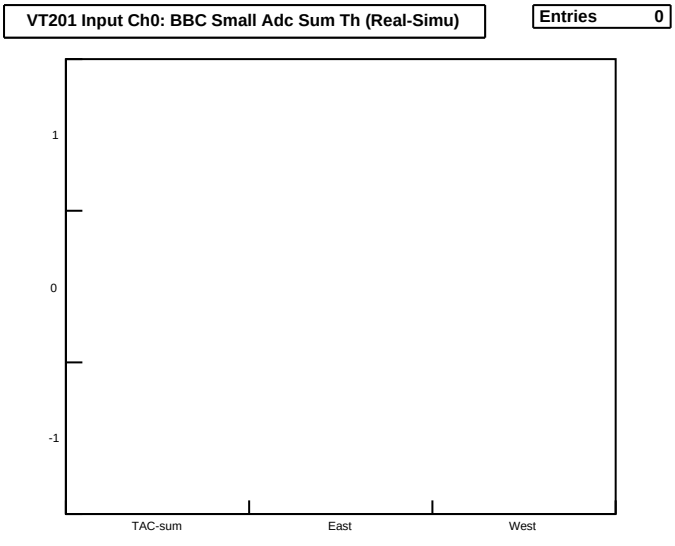

zdcAdcSum
Entries 600000
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

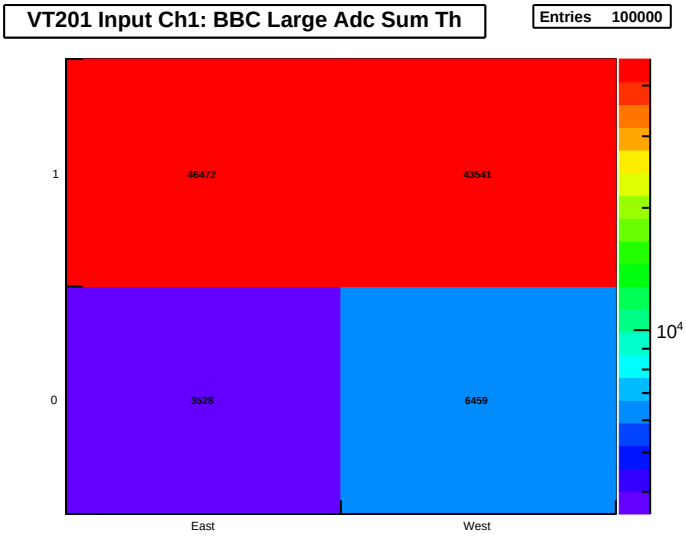

zdcAdcSumDiff
Entries 144018
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

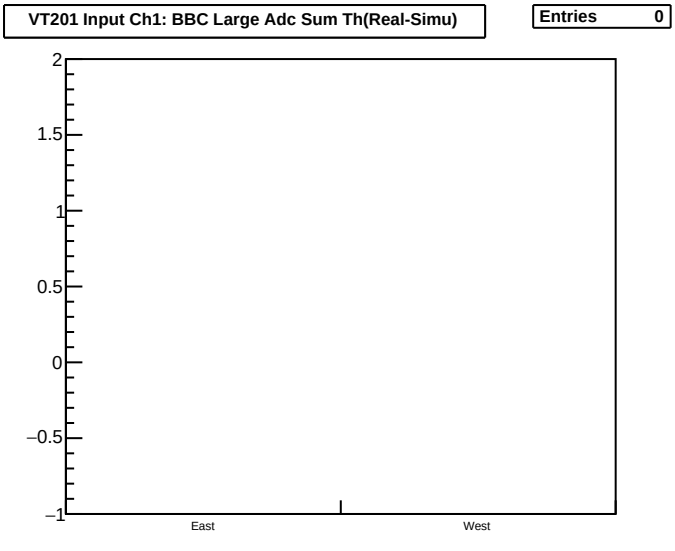

VT201 Input Ch0: BBC Small Tac Diff
Entries 50000
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

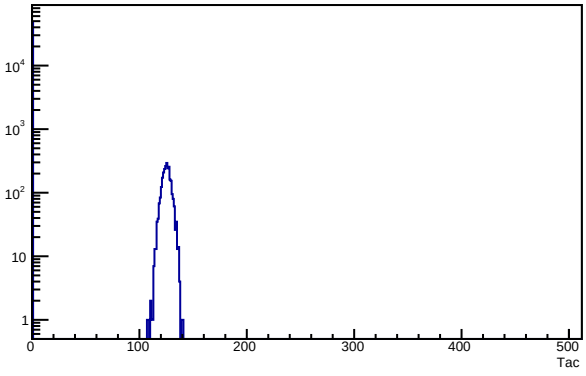

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 150000
50000
50000
50000
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

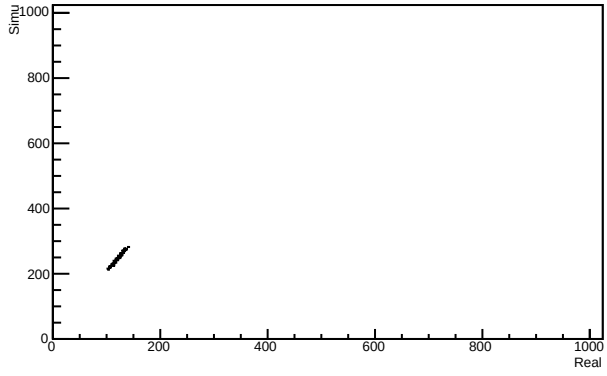

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 100000
46472
43541
3528
6459
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

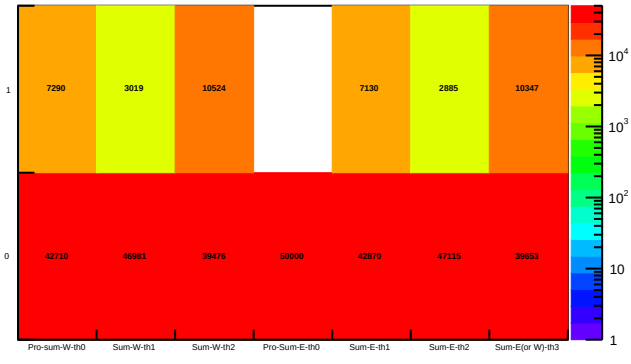
**VT201 Input Ch2: ZDC Tac Diff**



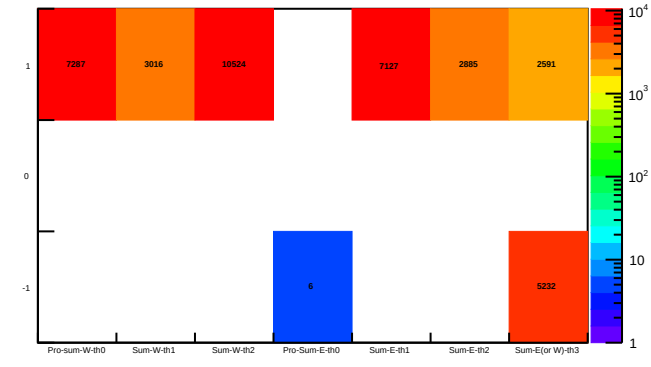

**VT201 Input Ch2: ZDC Tac Diff**



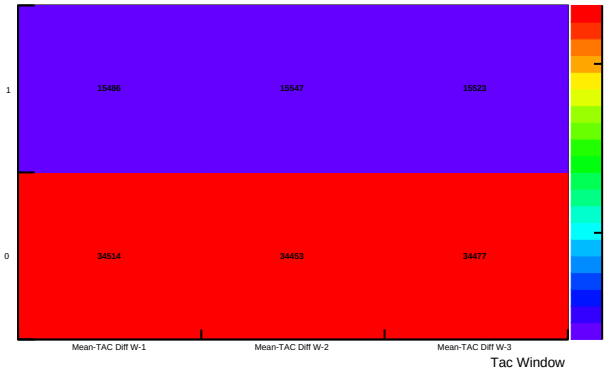

**VT201 Input Ch2: ZDC Adc Sum Th**



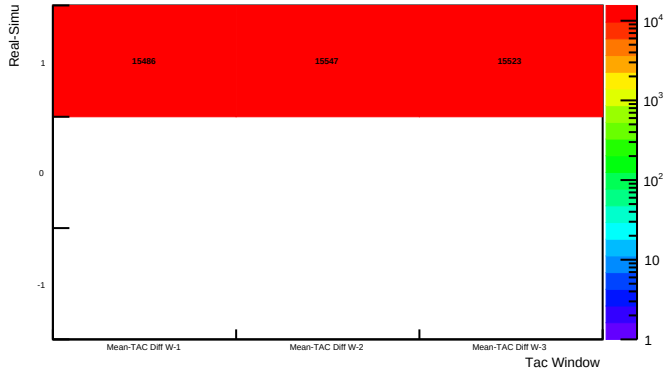

**VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)**



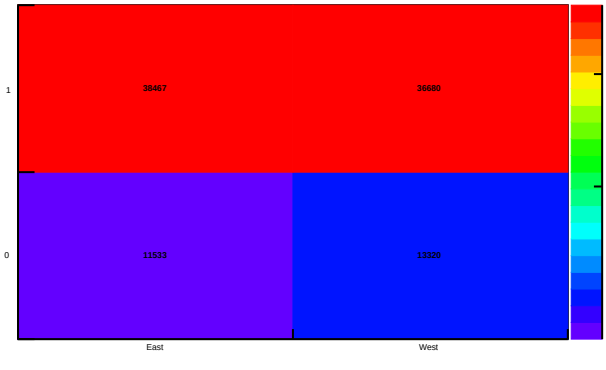

**VT201 Input Ch4: VPD Tac Diff**



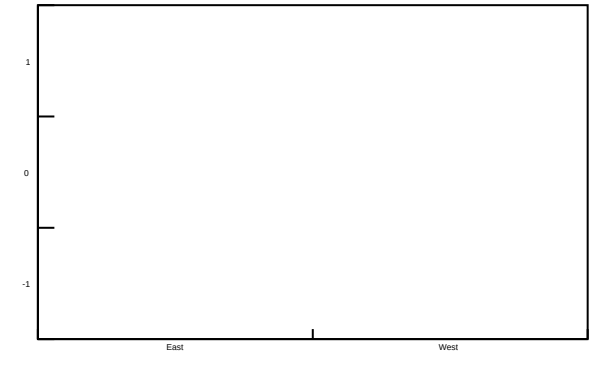

**VT201 Input Ch4: VPD Tac Diff**

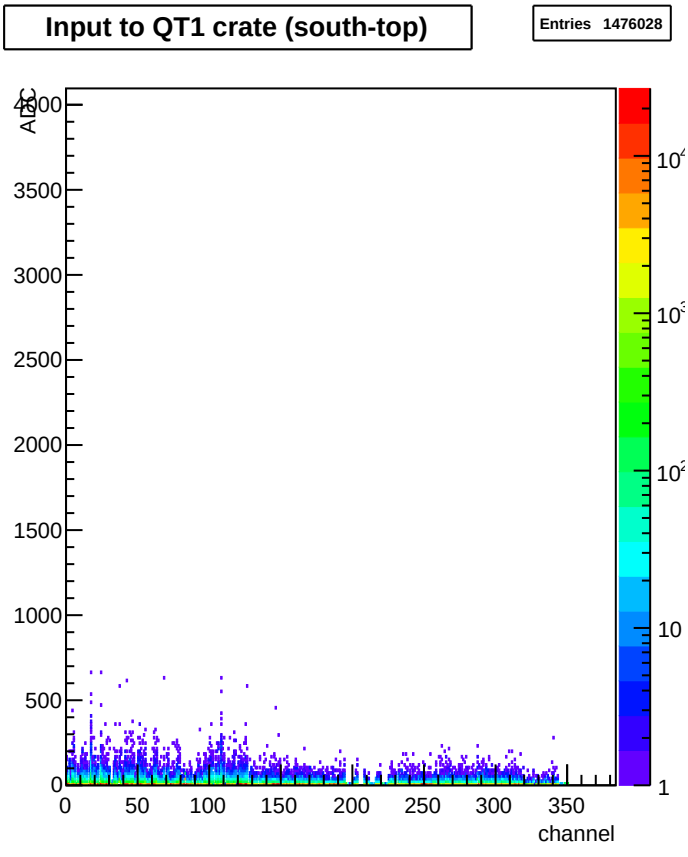


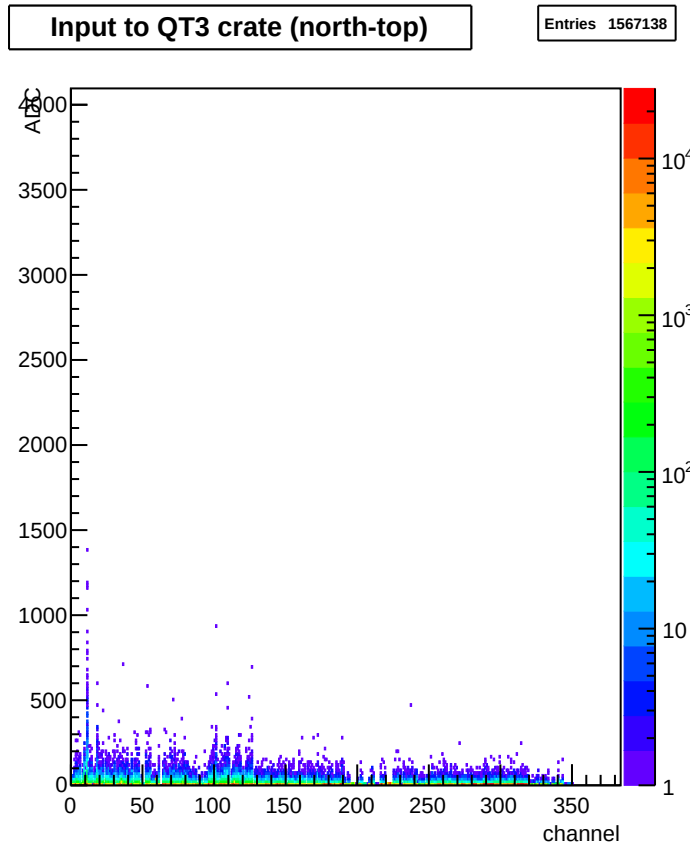

**VT201 Input Ch4: VPD Adc Sum Th**

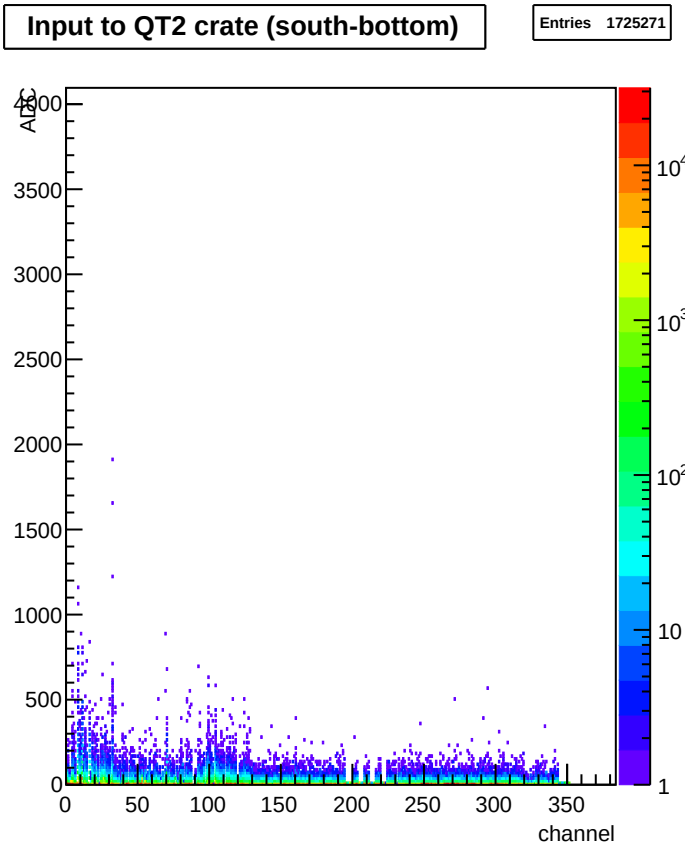


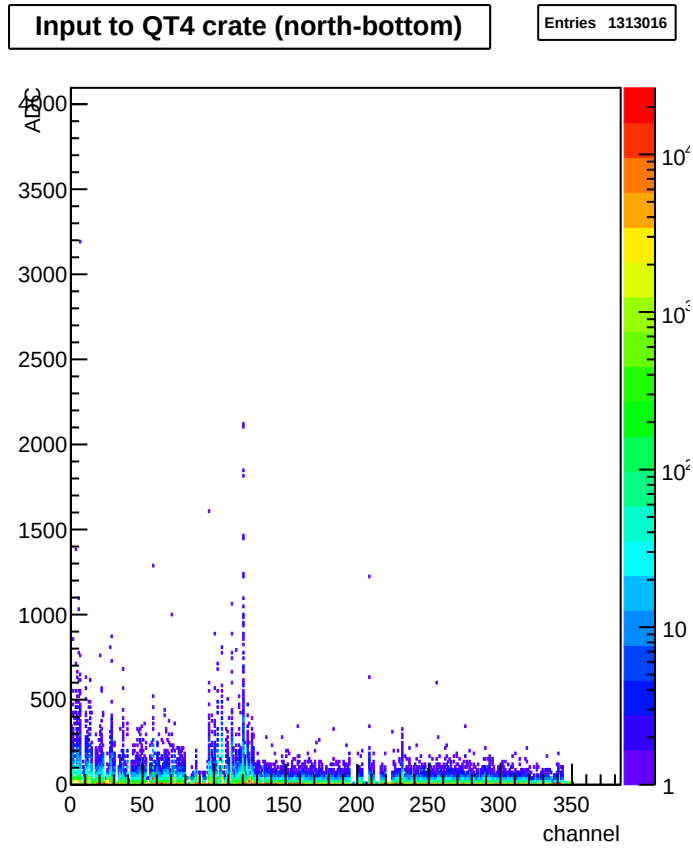

**VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)**

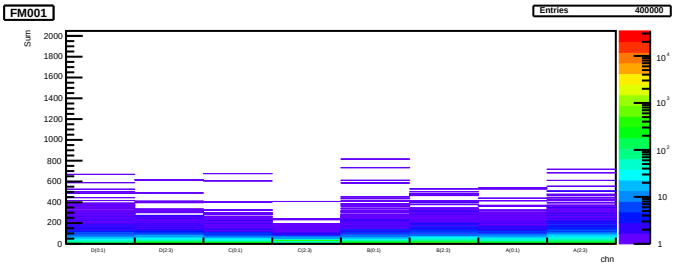


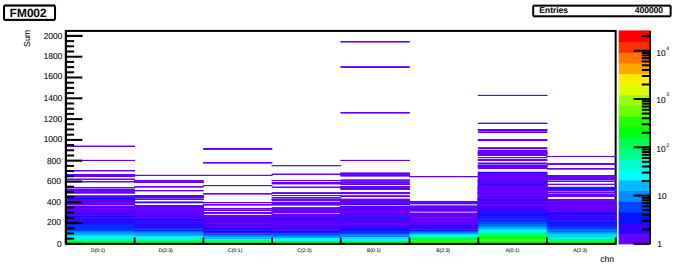

Input to QT1 crate (south-top)
Entries 1476028
ADC
channel

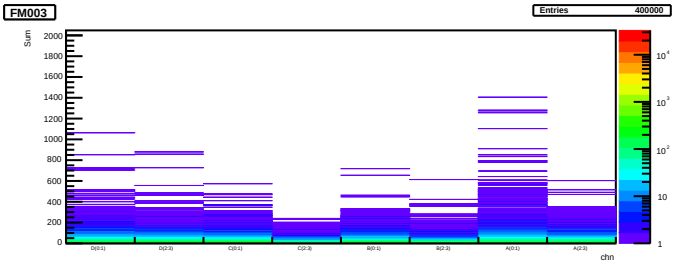

Input to QT3 crate (north-top)
Entries 1567138
ADC
channel

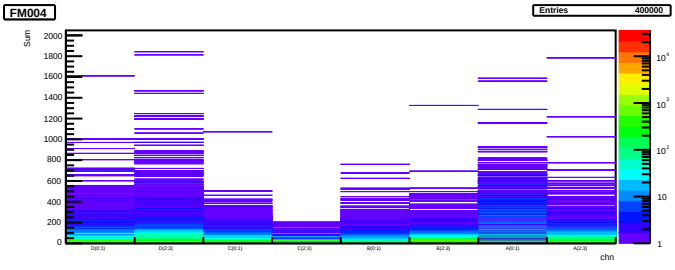

Input to QT2 crate (south-bottom)
Entries 1725271
ADC
channel

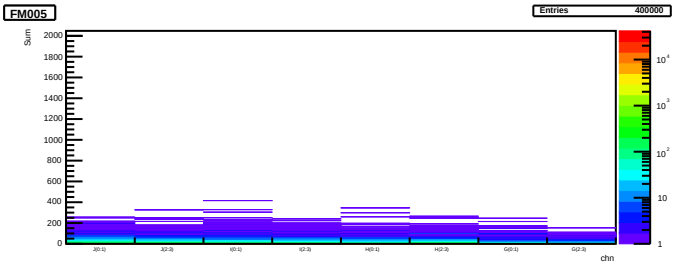

Input to QT4 crate (north-bottom)
Entries 1313016
ADC
channel

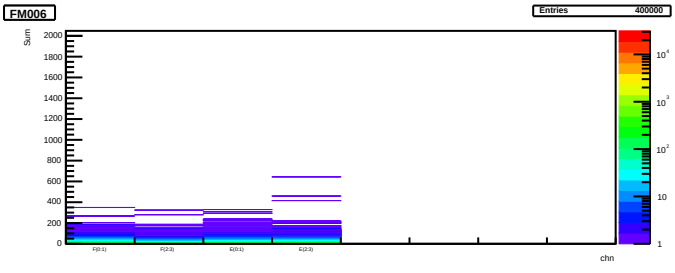

FM001
Entries 400000
Sum
chn

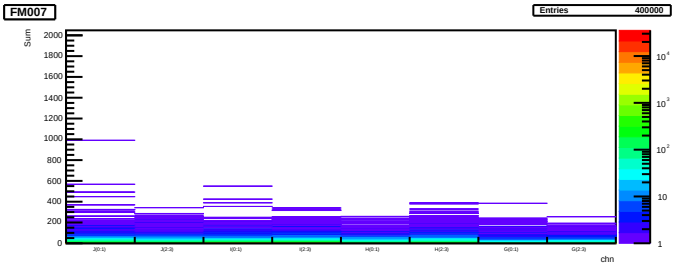

FM002
Entries 400000
Sum
chn

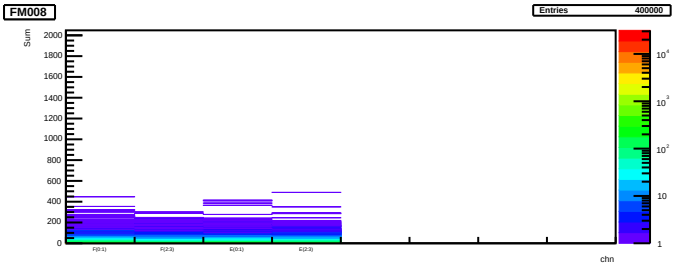

FM003
Entries 400000
Sum
chn

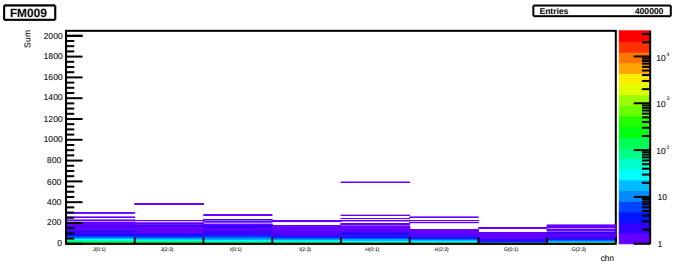

FM004
Entries 400000
Sum
chn

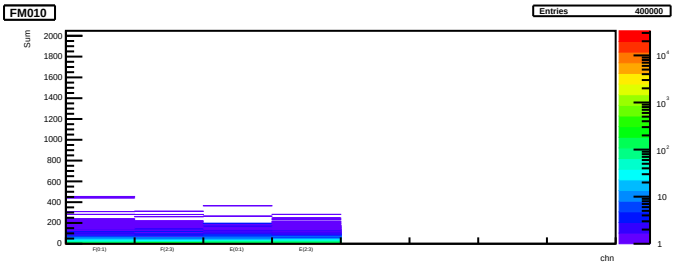

FM005
Entries 400000
Sum
chn

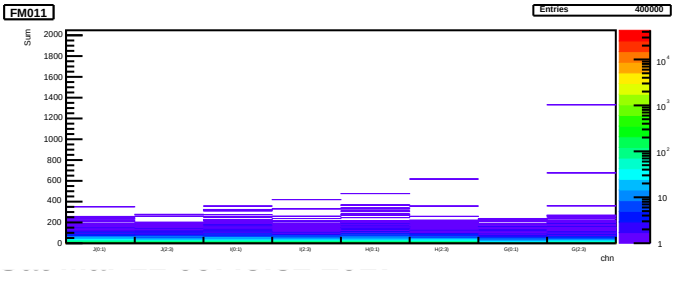

FM006
Entries 400000
Sum
chn

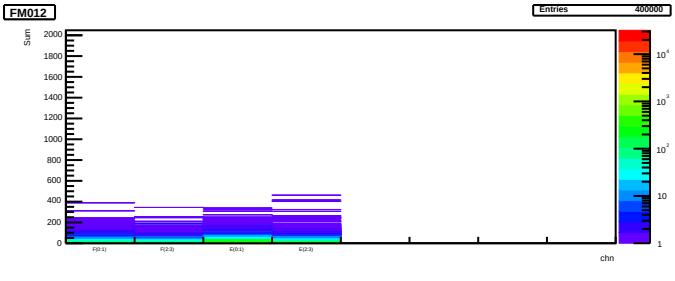

FM007
Entries 400000
Sum
chn

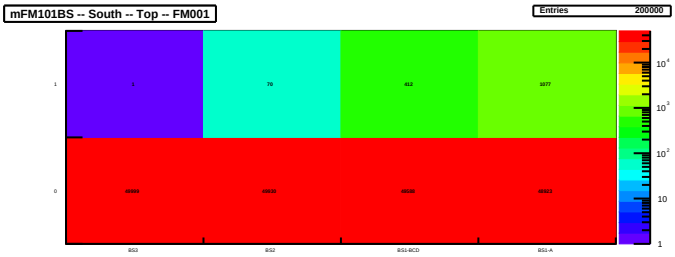

FM008
Entries 400000
Sum
chn

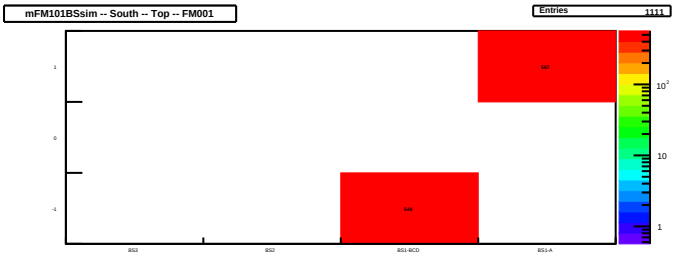

FM009
Entries 400000
Sum
chn

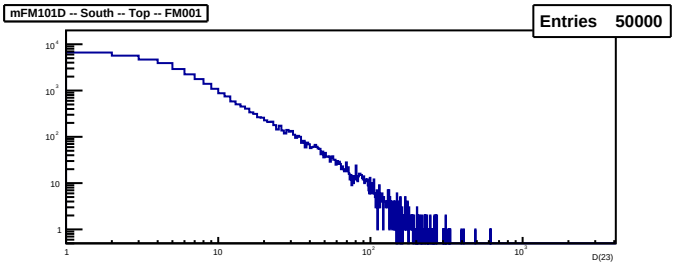

FM010
Entries 400000
Sum
chn

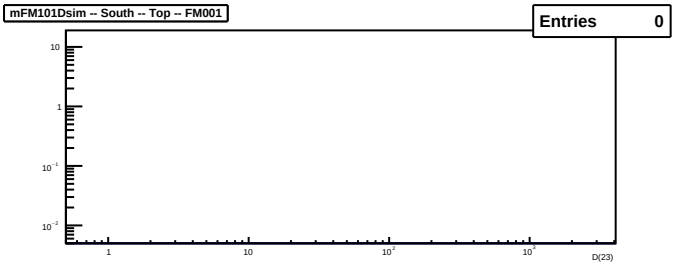

FM011
Entries 400000
Sum
chn

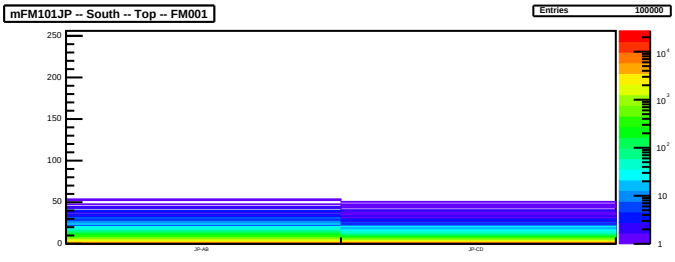

FM012
Entries 400000
Sum
chn


mFM101BS -- South -- Top -- FM001
Entries 200000
1
70
412
3077
49999
49930
49588
46923
BS3
BS2
BS1-BCD
BS1-A

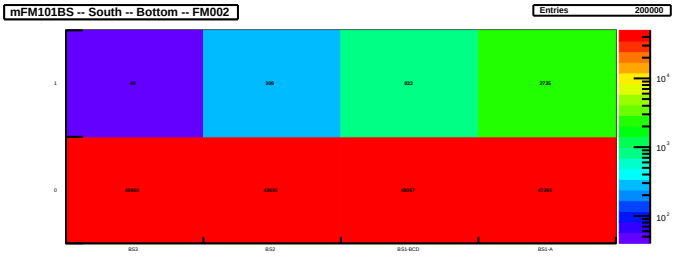

mFM101BSsim -- South -- Top -- FM001
Entries 1111
567
544
BS3
BS2
BS1-BCD
BS1-A

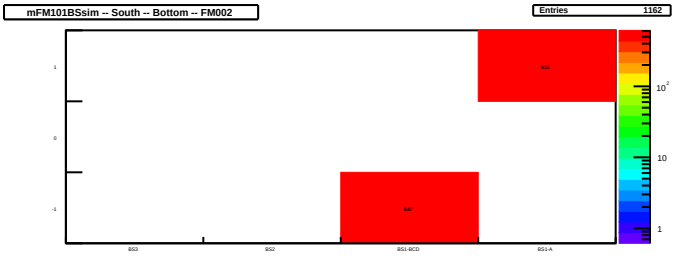

mFM101D -- South -- Top -- FM001
Entries 50000
D(23)

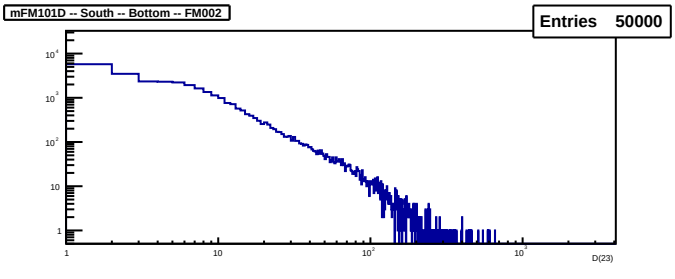

mFM101Dsim -- South -- Top -- FM001
Entries 0
D(23)

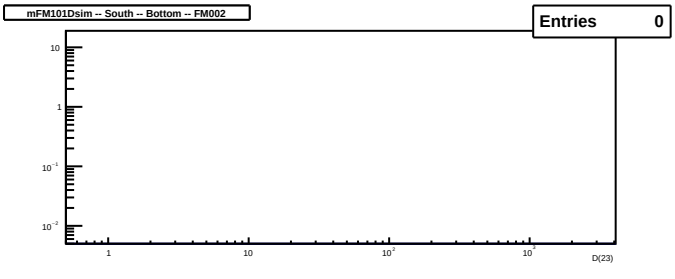

mFM101JP -- South -- Top -- FM001
Entries 100000
JP-AB
JP-CD

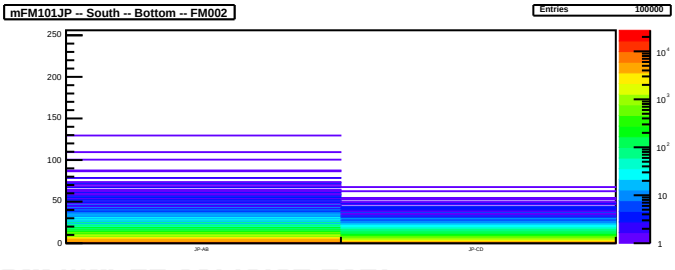
mFM101JPsim -- South -- Top -- FM001
Entries 0
JP-AB
JP-CD

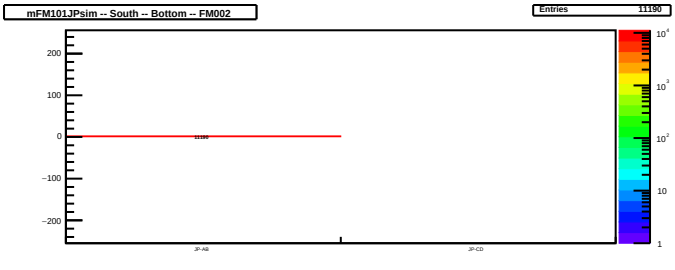

mFM101BS -- South -- Bottom -- FM002
Entries 200000
40
399
933
2736
49960
49601
49067
47265
BS3
BS2
BS1-BCD
BS1-A

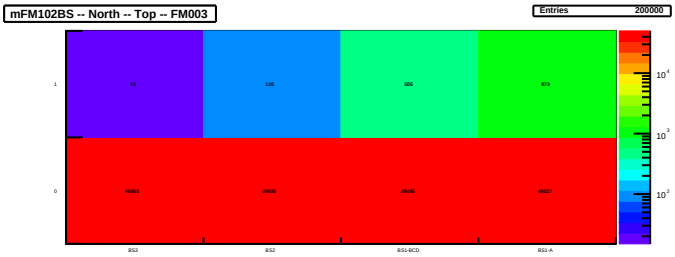

mFM101BSsim -- South -- Bottom -- FM002
Entries 1162
615
547
BS3
BS2
BS1-BCD
BS1-A

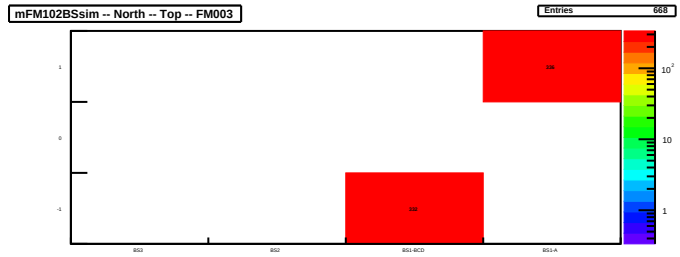

mFM101D -- South -- Bottom -- FM002
Entries 50000
D(23)

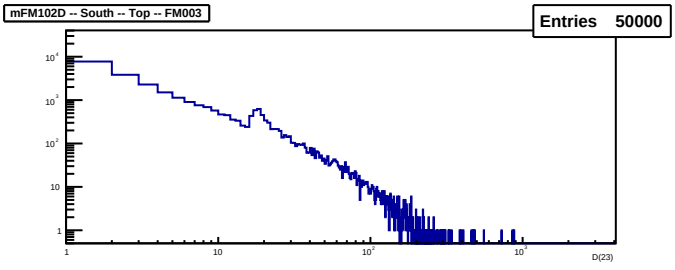

mFM101Dsim -- South -- Bottom -- FM002
Entries 0
D(23)

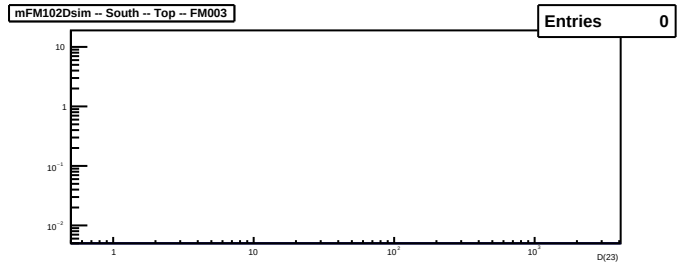

mFM101JP -- South -- Bottom -- FM002
Entries 100000
JP-AB
JP-CD

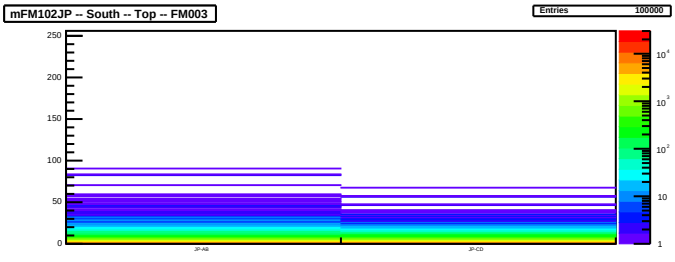

mFM101JPsim -- South -- Bottom -- FM002
Entries 11190
11190
JP-AB
JP-CD

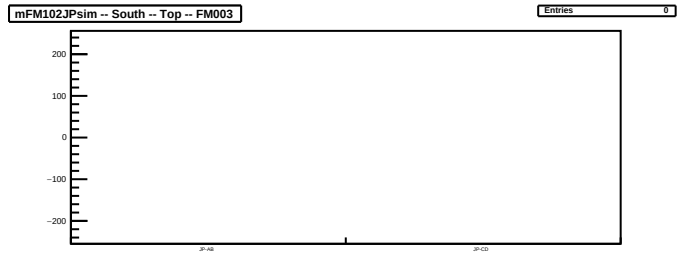

mFM102BS -- North -- Top -- FM003
Entries 200000

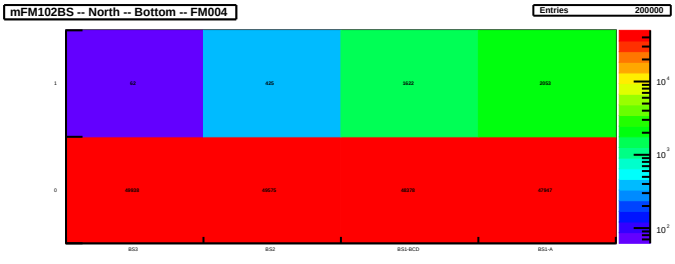

mFM102BSsim -- North -- Top -- FM003
Entries 668

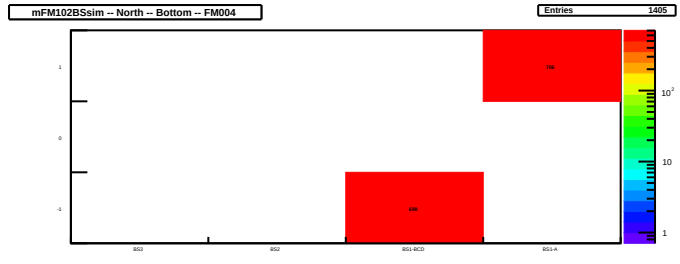

mFM102D -- South -- Top -- FM003
Entries 50000
D(23)

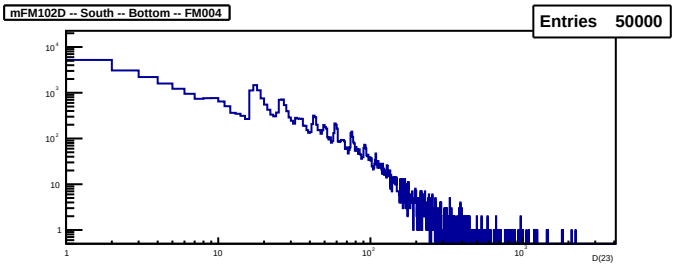

mFM102Dsim -- South -- Top -- FM003
Entries 0
D(23)

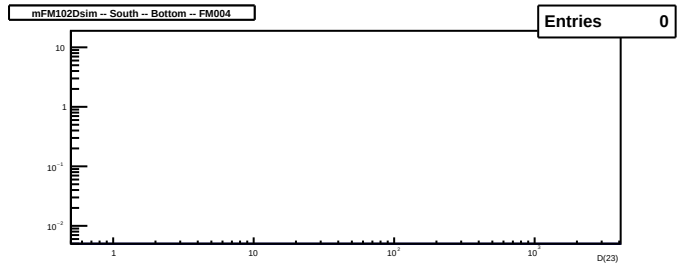

mFM102JP -- South -- Top -- FM003
Entries 100000

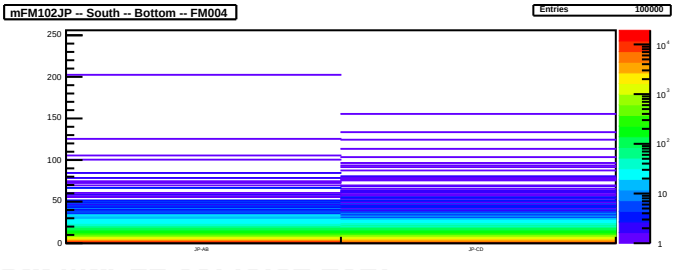

mFM102JPsim -- South -- Top -- FM003
Entries 0

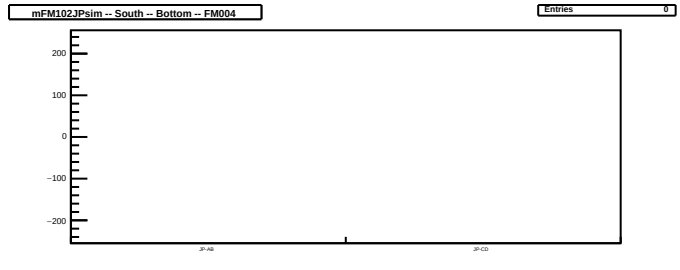

mFM102BS -- North -- Bottom -- FM004
Entries 200000

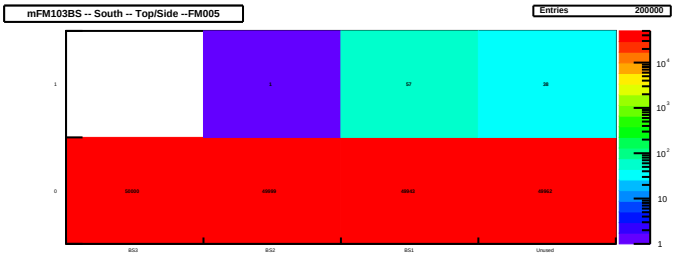

mFM102BSsim -- North -- Bottom -- FM004
Entries 1405

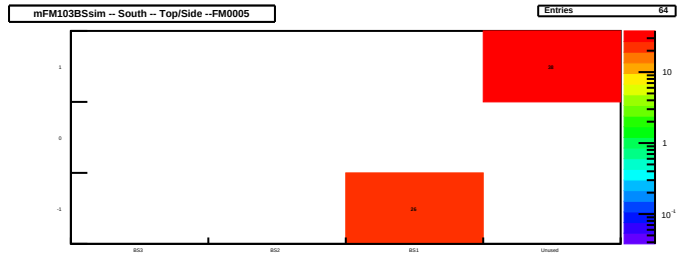

mFM102D -- South -- Bottom -- FM004
Entries 50000
D(23)

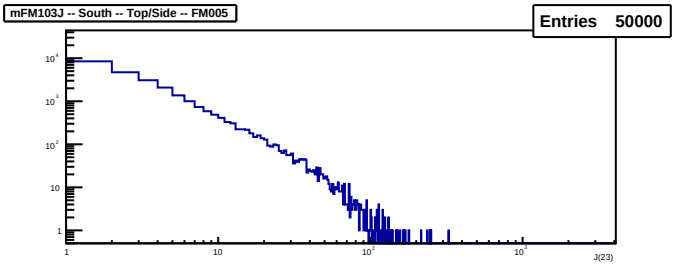

mFM102Dsim -- South -- Bottom -- FM004
Entries 0
D(23)

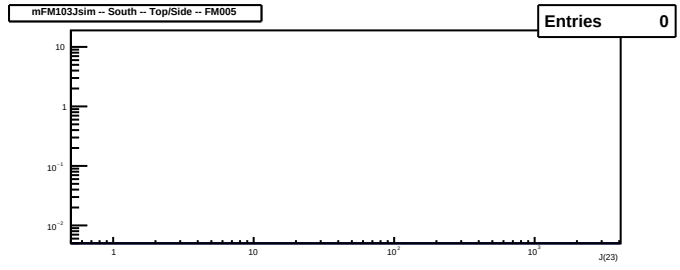

mFM102JP -- South -- Bottom -- FM004
Entries 100000

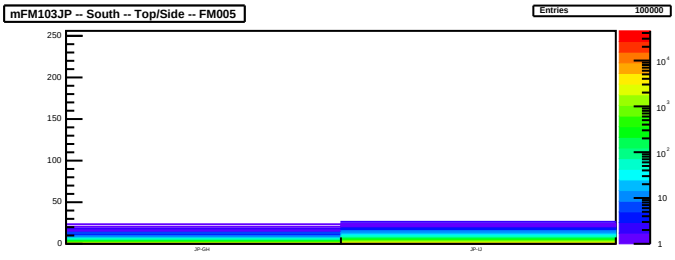

mFM102JPsim -- South -- Bottom -- FM004
Entries 0


mFM103BS -- South -- Top/Side --FM005
Entries 200000
BS3
BS2
BS1
Unused

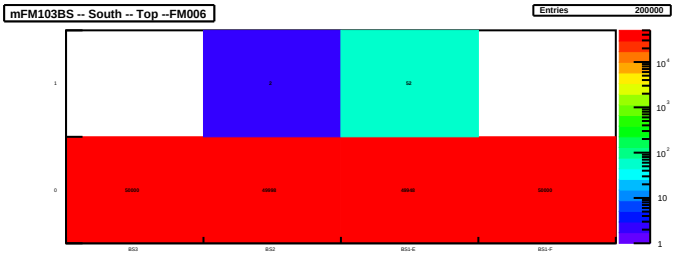

mFM103BSsim -- South -- Top/Side --FM0005
Entries 64
BS3
BS2
BS1
Unused

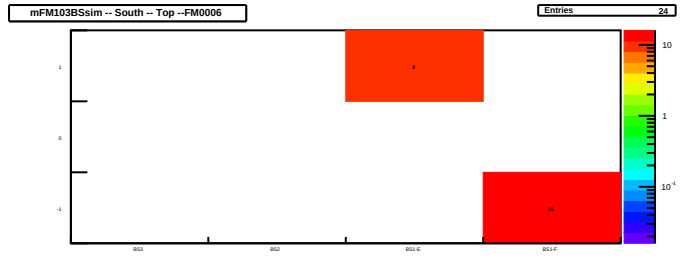

mFM103J -- South -- Top/Side -- FM005
Entries 50000
J(23)

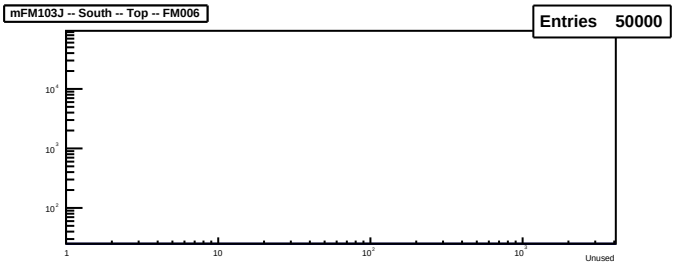

mFM103Jsim -- South -- Top/Side -- FM005
Entries 0
J(23)

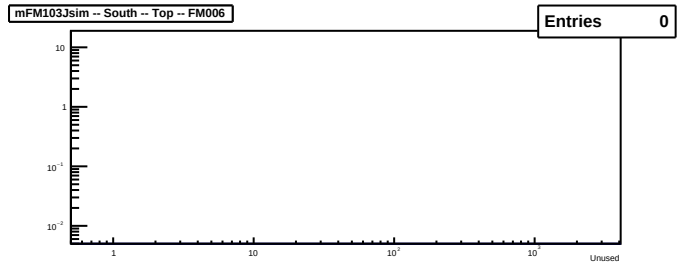

mFM103JP -- South -- Top/Side -- FM005
Entries 100000
JP-GH
JP-IJ

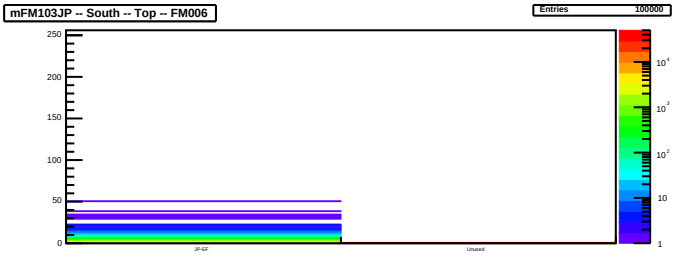
mFM103JPsim -- South -- Top/Side -- FM005
Entries 0
JP-GH
JP-IJ

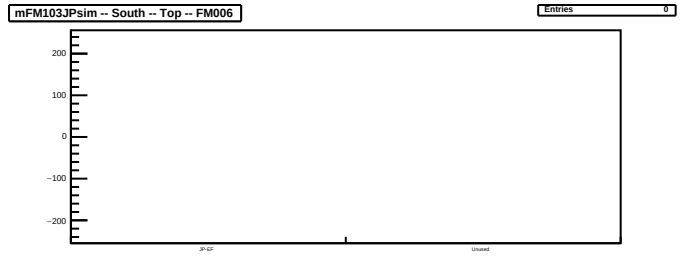

mFM103BS -- South -- Top --FM006
Entries 200000
BS3
BS2
BS1-E
BS1-F

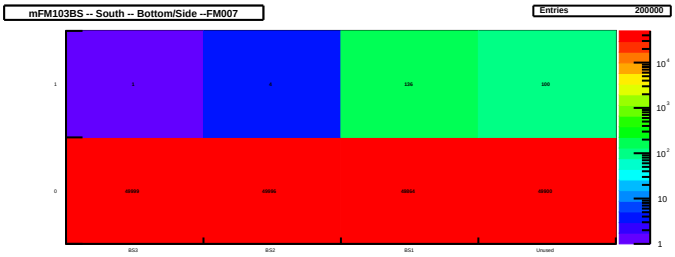

mFM103BSsim -- South -- Top --FM0006
Entries 24
BS3
BS2
BS1-E
BS1-F

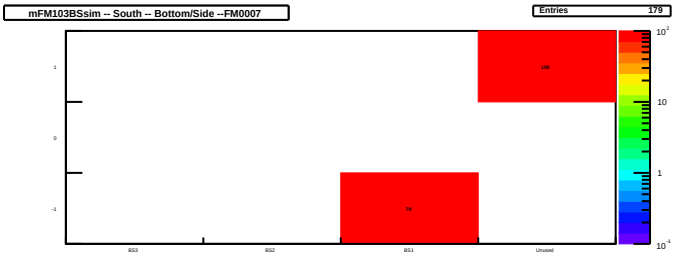

mFM103J -- South -- Top -- FM006
Entries 50000
Unused

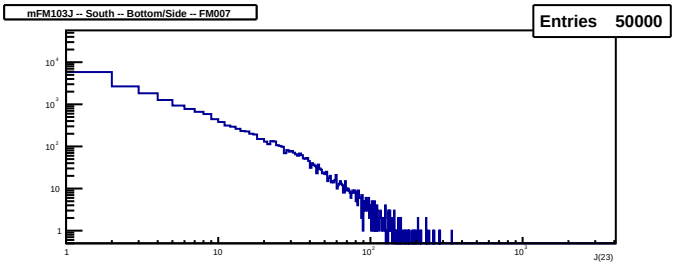

mFM103Jsim -- South -- Top -- FM006
Entries 0
Unused

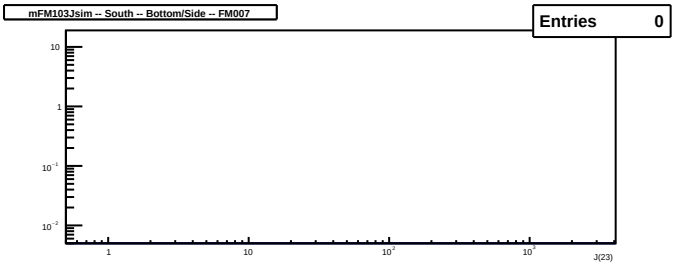

mFM103JP -- South -- Top -- FM006
Entries 100000
JP-EF
Unused

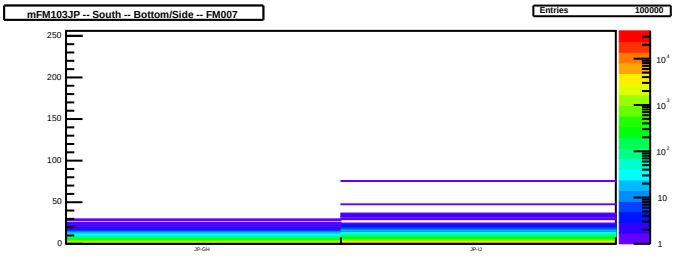

mFM103JPsim -- South -- Top -- FM006
Entries 0
JP-EF
Unused

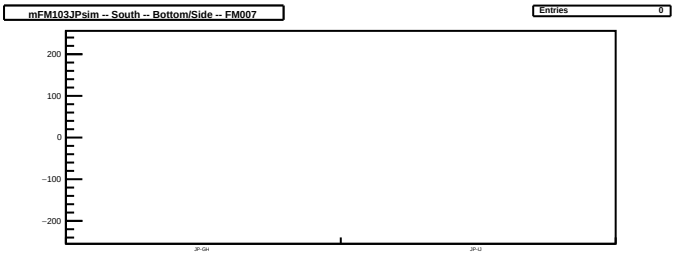

mFM103BS -- South -- Bottom/Side --FM007
Entries 200000

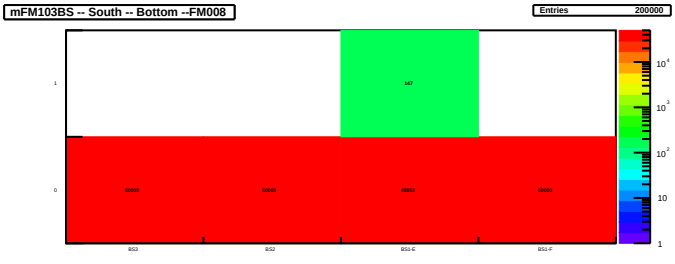

mFM103BSsim -- South -- Bottom/Side --FM0007
Entries 179

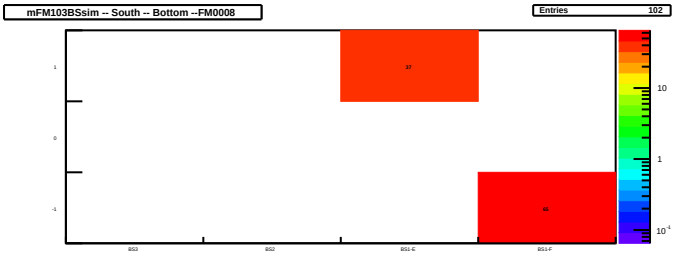

mFM103J -- South -- Bottom/Side -- FM007
Entries 50000
J(23)

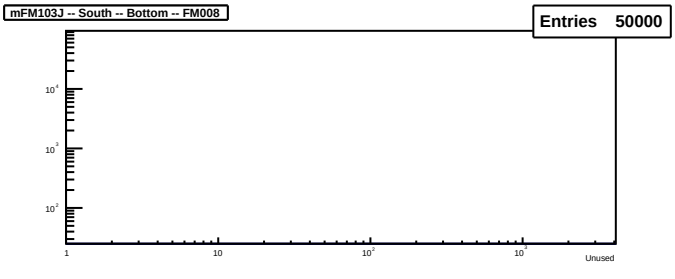

mFM103Jsim -- South -- Bottom/Side -- FM007
Entries 0
J(23)

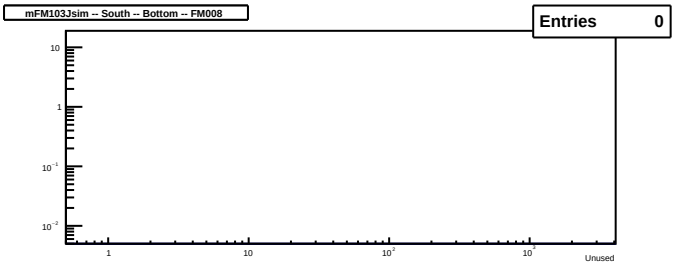

mFM103JP -- South -- Bottom/Side -- FM007
Entries 100000

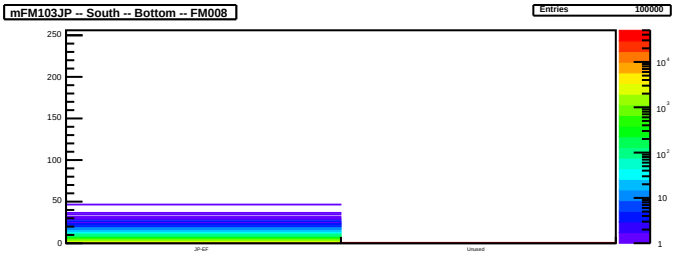

mFM103JPsim -- South -- Bottom/Side -- FM007
Entries 0

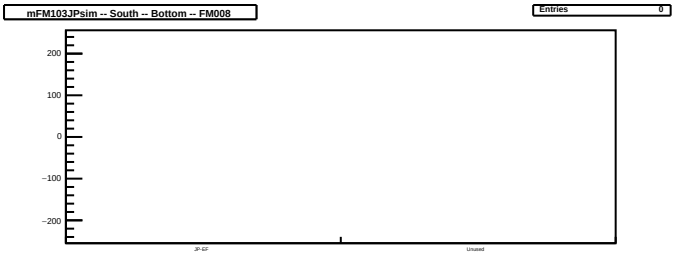

mFM103BS -- South -- Bottom --FM008
Entries 200000

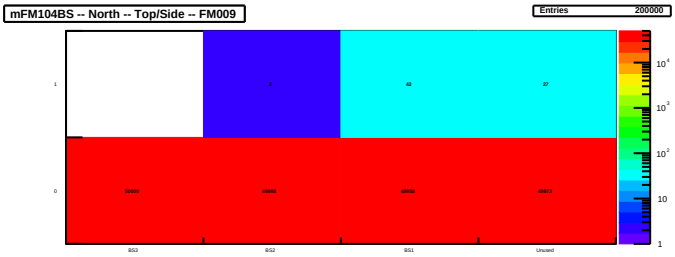

mFM103BSsim -- South -- Bottom --FM0008
Entries 102

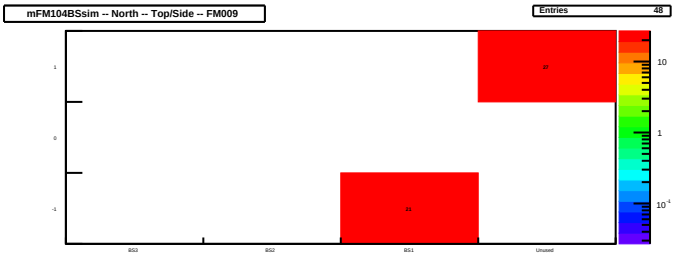

mFM103J -- South -- Bottom -- FM008
Entries 50000
Unused

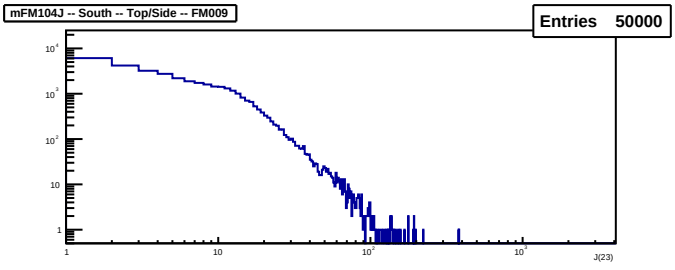

mFM103Jsim -- South -- Bottom -- FM008
Entries 0
Unused

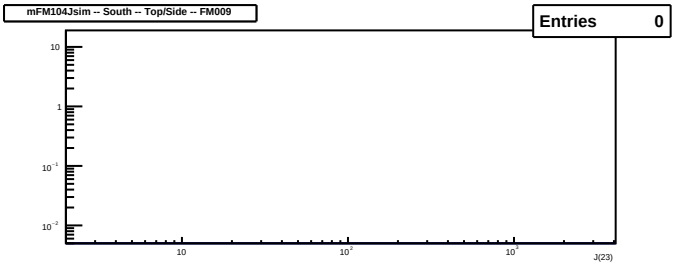

mFM103JP -- South -- Bottom -- FM008
Entries 100000

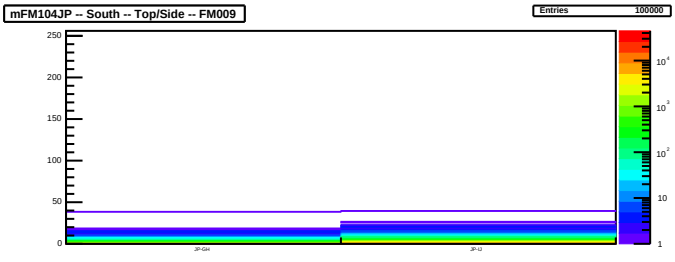

mFM103JPsim -- South -- Bottom -- FM008
Entries 0

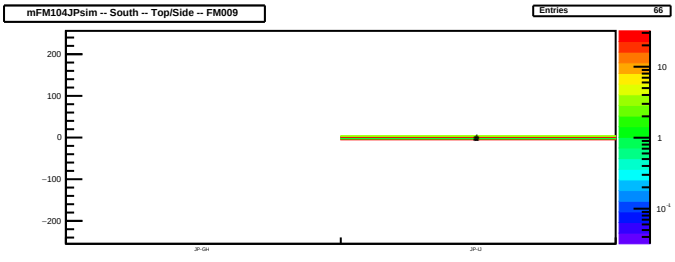

mFM104BS -- North -- Top/Side -- FM009
Entries 200000

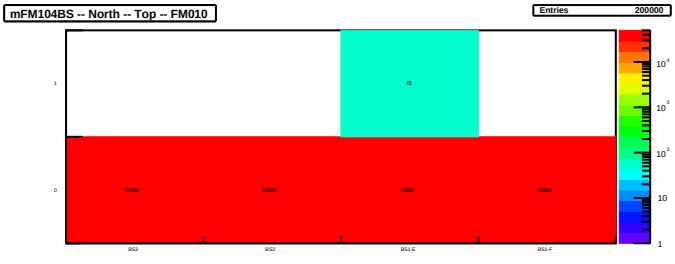

mFM104BSsim -- North -- Top/Side -- FM009
Entries 48

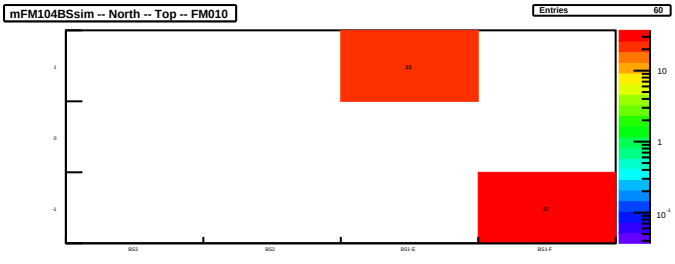

mFM104J -- South -- Top/Side -- FM009
Entries 50000
J(23)

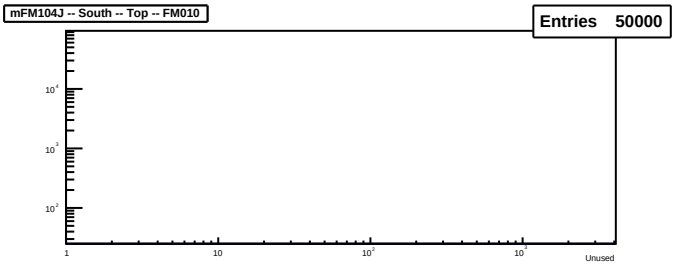

mFM104Jsim -- South -- Top/Side -- FM009
Entries 0
J(23)

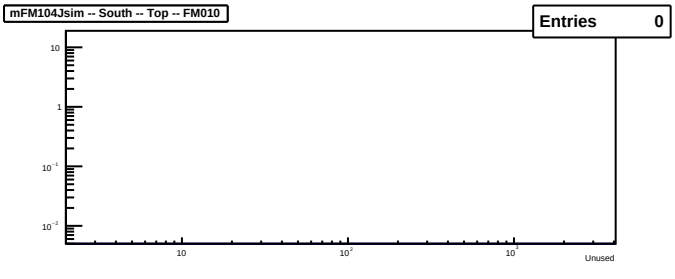

mFM104JP -- South -- Top/Side -- FM009
Entries 100000

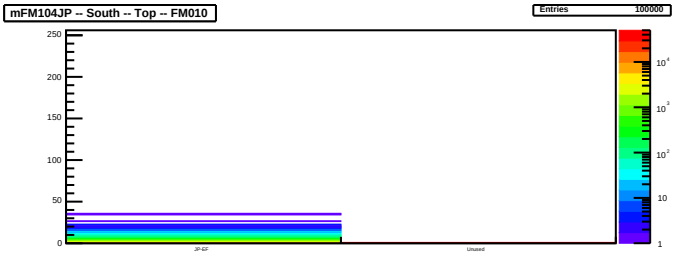

mFM104JPsim -- South -- Top/Side -- FM009
Entries 66

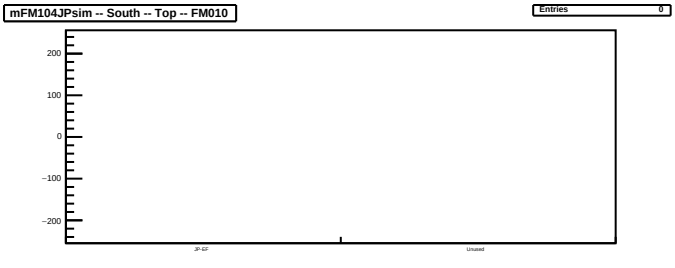

mFM104BS -- North -- Top -- FM010
Entries 200000

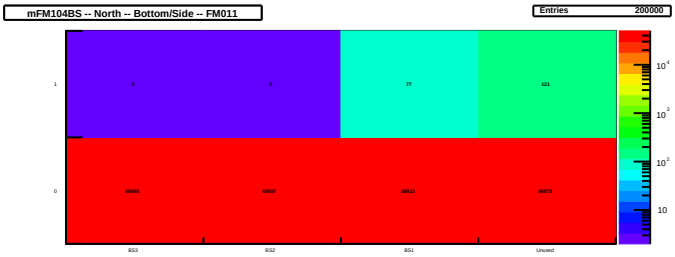

mFM104BSsim -- North -- Top -- FM010
Entries 60

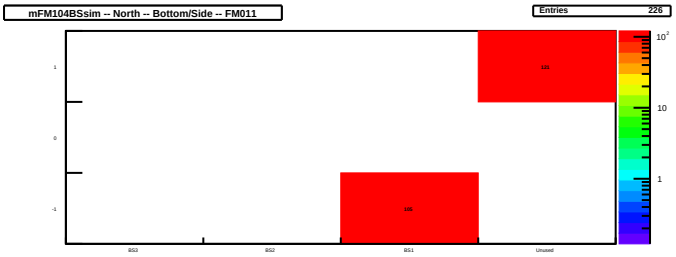

mFM104J -- South -- Top -- FM010
Entries 50000
Unused

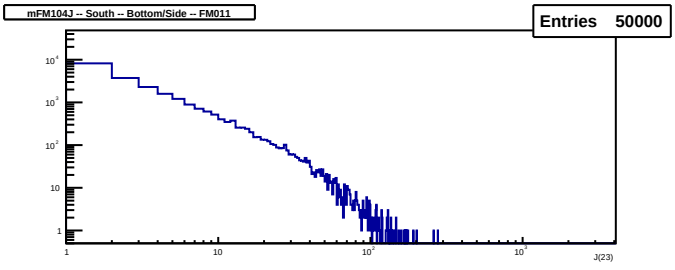

mFM104Jsim -- South -- Top -- FM010
Entries 0
Unused

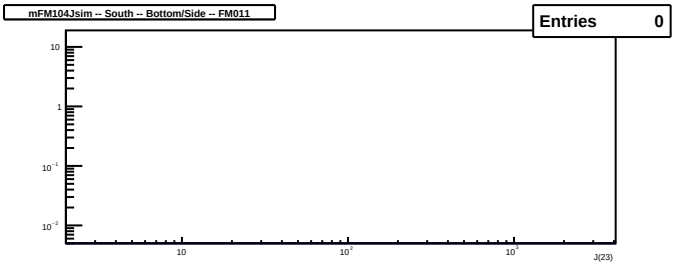

mFM104JP -- South -- Top -- FM010
Entries 100000

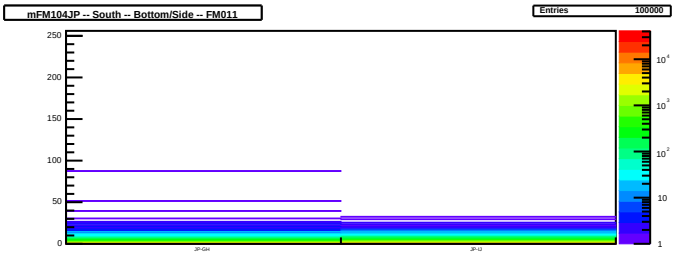

mFM104JPsim -- South -- Top -- FM010
Entries 0

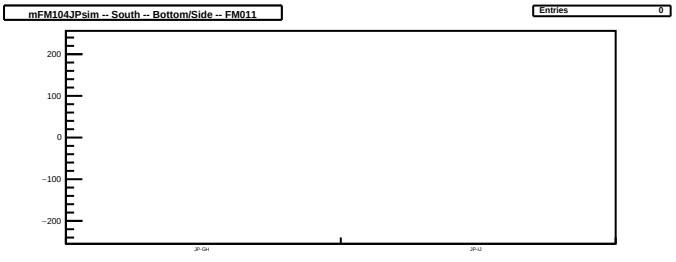

mFM104BS -- North -- Bottom/Side -- FM011
Entries 200000
BS3
BS2
BS1
Unused

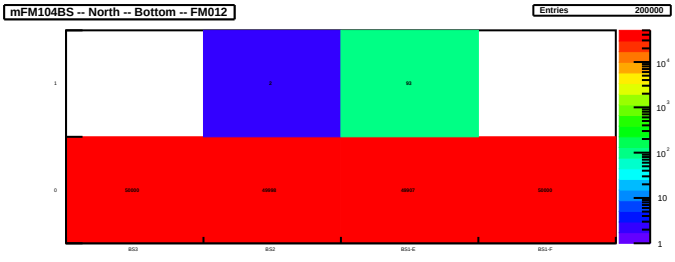

mFM104BSsim -- North -- Bottom/Side -- FM011
Entries 226
BS3
BS2
BS1
Unused

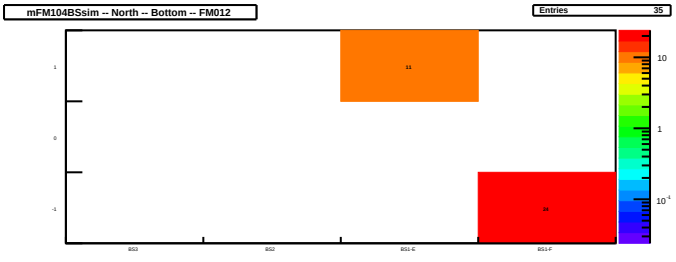

mFM104J -- South -- Bottom/Side -- FM011
Entries 50000
J(23)

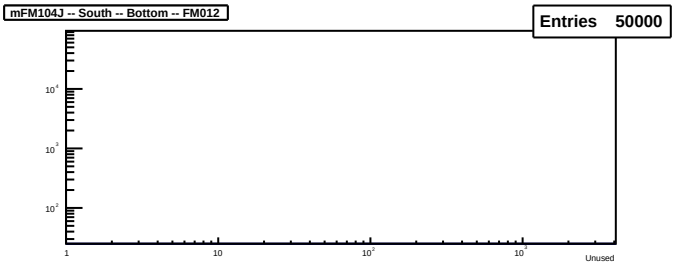

mFM104Jsim -- South -- Bottom/Side -- FM011
Entries 0
J(23)

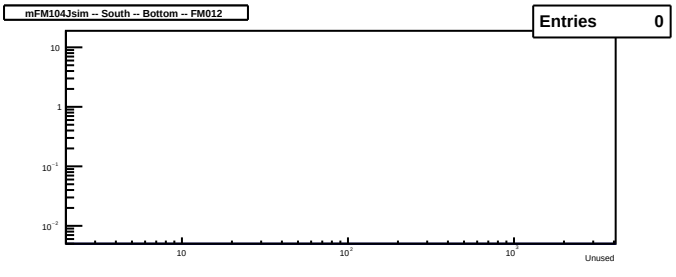

mFM104JP -- South -- Bottom/Side -- FM011
Entries 100000
JP-GH
JP-IJ

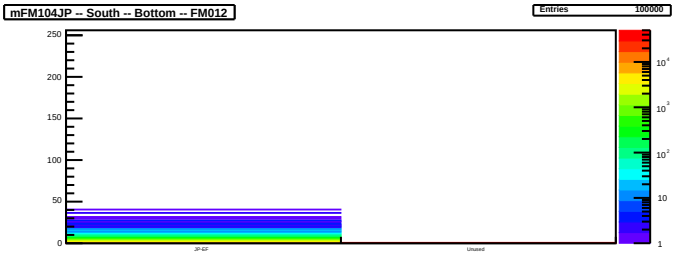

mFM104JPsim -- South -- Bottom/Side -- FM011
Entries 0
JP-GH
JP-IJ


mFM104BS -- North -- Bottom -- FM012
Entries 200000
BS3
BS2
BS1-E
BS1-F

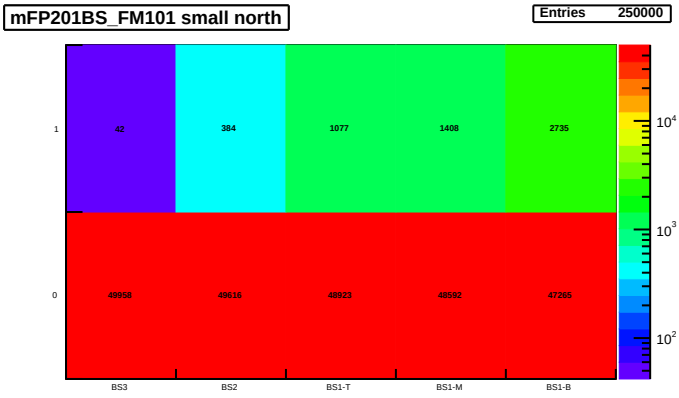

mFM104BSsim -- North -- Bottom -- FM012
Entries 35
BS3
BS2
BS1-E
BS1-F

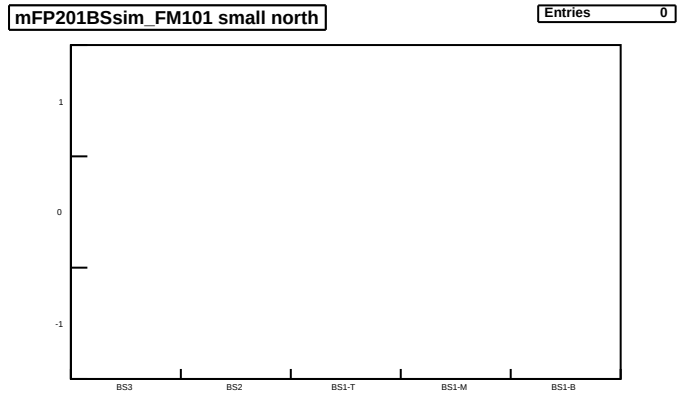

mFM104J -- South -- Bottom -- FM012
Entries 50000
Unused

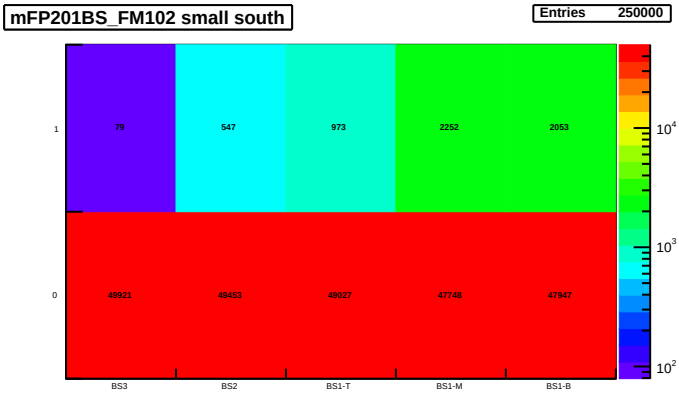

mFM104Jsim -- South -- Bottom -- FM012
Entries 0
Unused

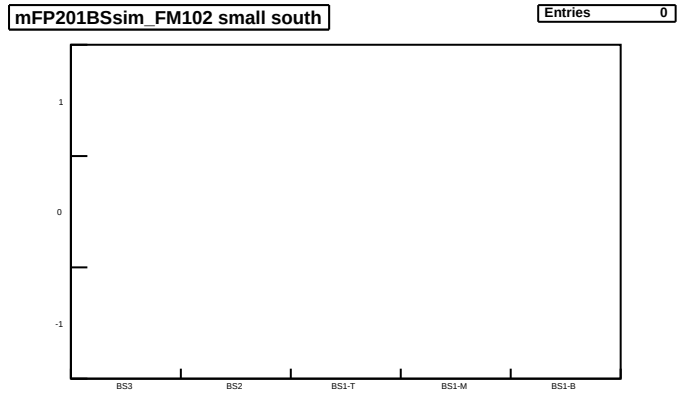

mFM104JP -- South -- Bottom -- FM012
Entries 100000
JP-EF
Unused

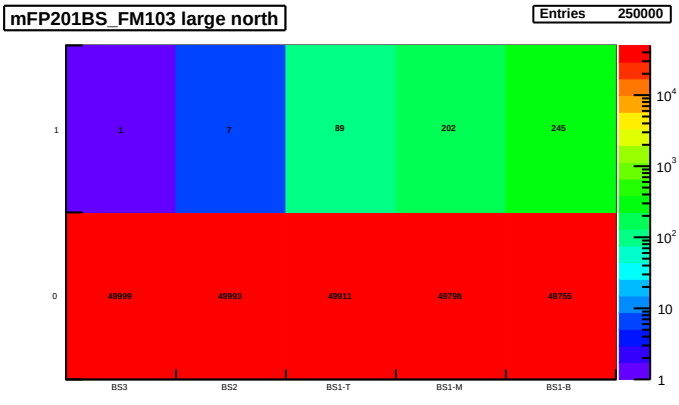
mFM104JPsim -- South -- Bottom -- FM012
Entries 0
JP-EF
Unused

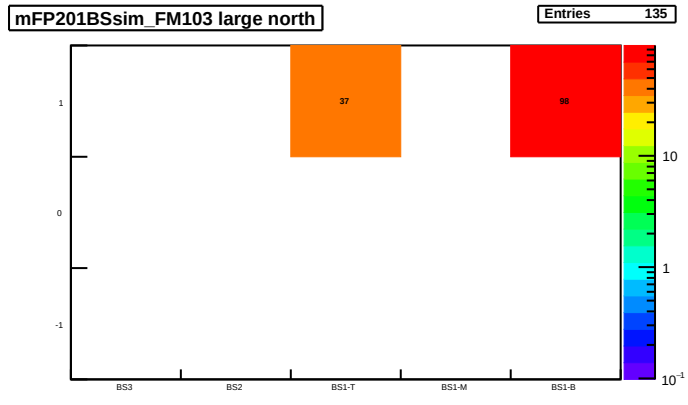

mFP201BS_FM101 small north
Entries 250000
42
384
1077
1408
2735
49958
49616
48923
48592
47265
BS3
BS2
BS1-T
BS1-M
BS1-B

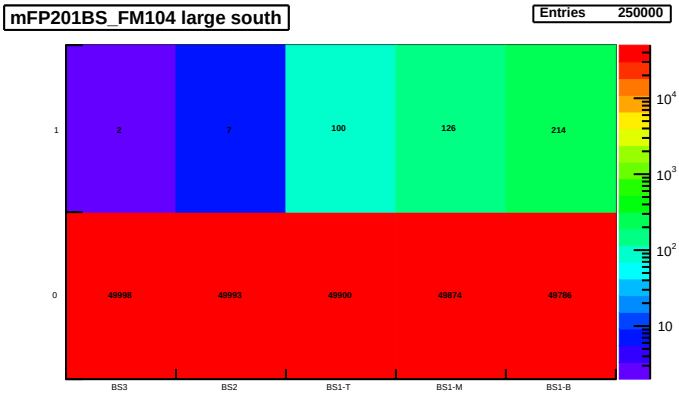

mFP201BSsim_FM101 small north
Entries 0
BS3
BS2
BS1-T
BS1-M
BS1-B

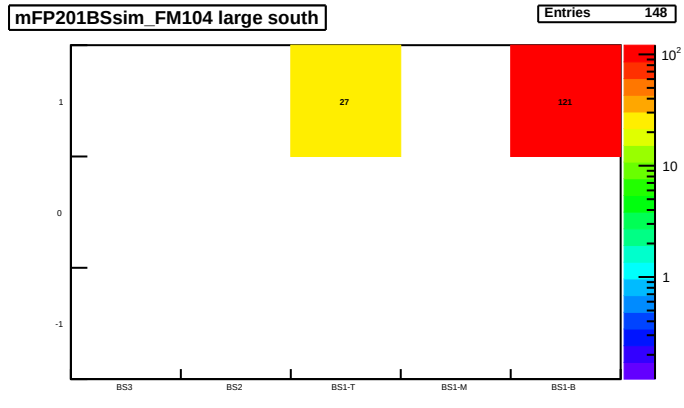

mFP201BS_FM102 small south
Entries 250000
79
547
973
2252
2053
49921
49453
49027
47748
47947
BS3
BS2
BS1-T
BS1-M
BS1-B

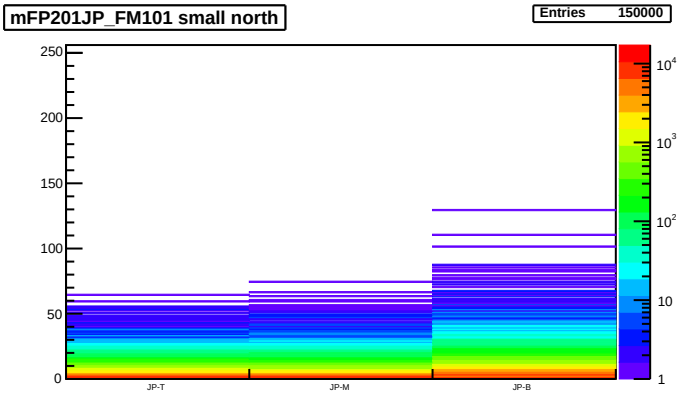

mFP201BSsim_FM102 small south
Entries 0
BS3
BS2
BS1-T
BS1-M
BS1-B

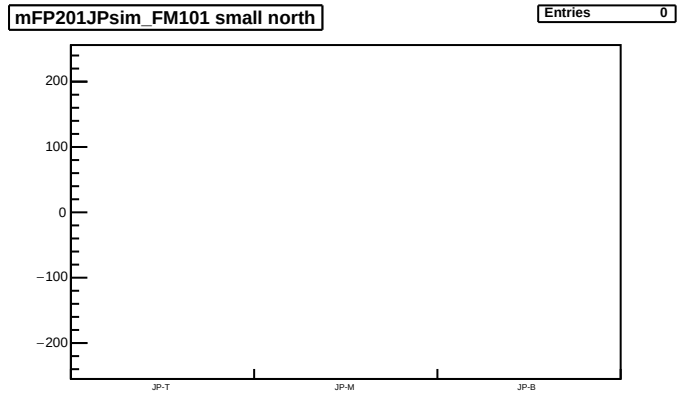

mFP201BS_FM103 large north
Entries 250000
1
7
89
202
245
49999
49993
49911
49798
49755
BS3
BS2
BS1-T
BS1-M
BS1-B

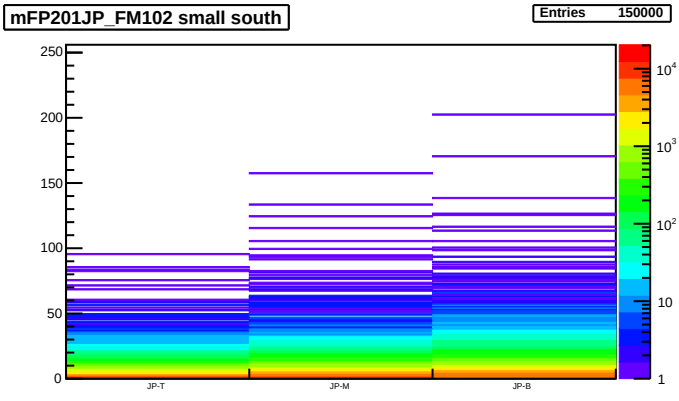

mFP201BSsim_FM103 large north
Entries 135
37
98
BS3
BS2
BS1-T
BS1-M
BS1-B

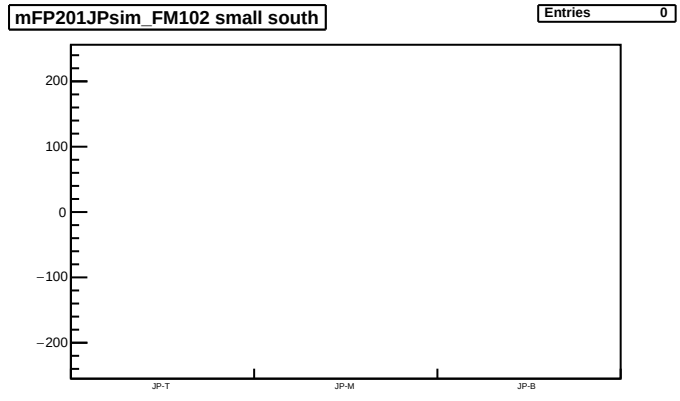

mFP201BS_FM104 large south
Entries 250000
2
7
100
126
214
49998
49993
49900
49874
49786
BS3
BS2
BS1-T
BS1-M
BS1-B

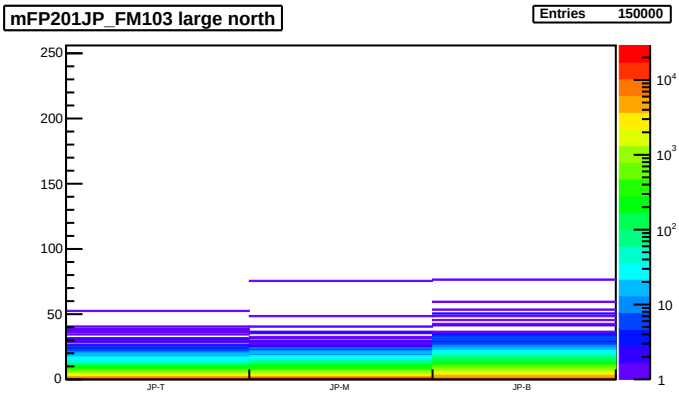

mFP201BSsim_FM104 large south
Entries 148
27
121
BS3
BS2
BS1-T
BS1-M
BS1-B

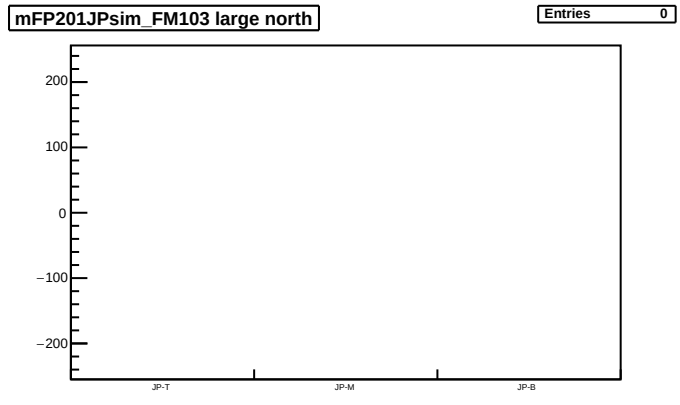

mFP201JP_FM101 small north
Entries 150000
250
200
150
100
50
0
JP-T
JP-M
JP-B
$10^4$
$10^3$
$10^2$
10
1

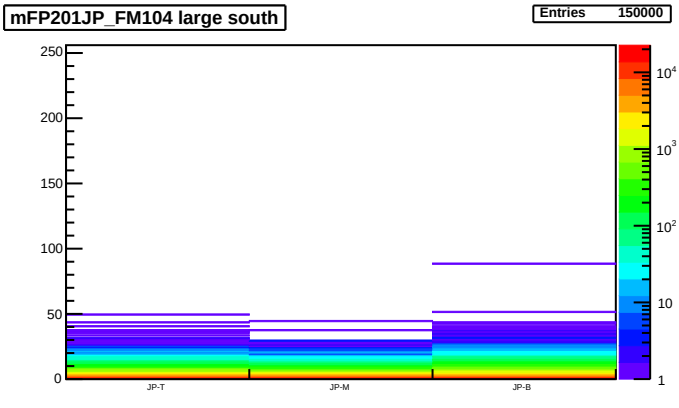

mFP201JPsim_FM101 small north
Entries 0
200
100
0
−100
−200
JP-T
JP-M
JP-B

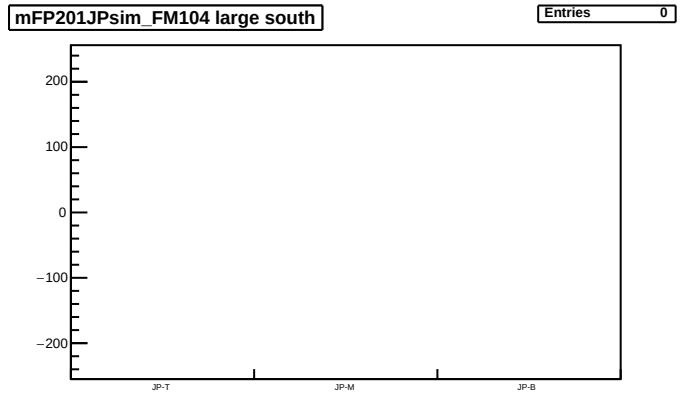

mFP201JP_FM102 small south
Entries 150000
250
200
150
100
50
0
JP-T
JP-M
JP-B
$10^4$
$10^3$
$10^2$
10
1

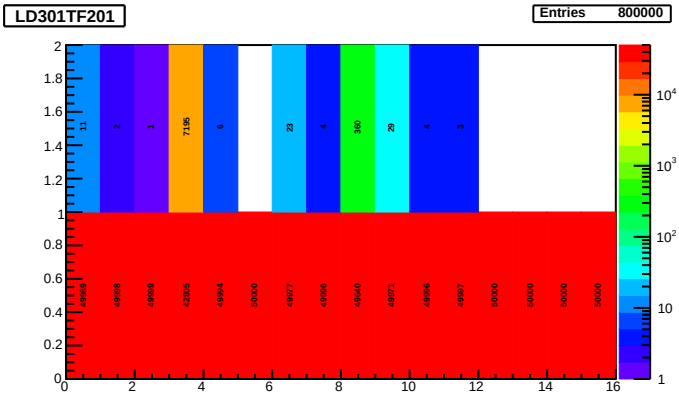

mFP201JPsim_FM102 small south
Entries 0
200
100
0
−100
−200
JP-T
JP-M
JP-B

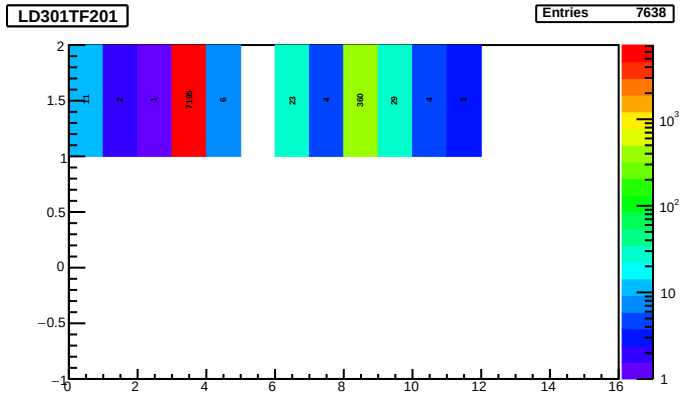

mFP201JP_FM103 large north
Entries 150000
250
200
150
100
50
0
JP-T
JP-M
JP-B
$10^4$
$10^3$
$10^2$
10
1

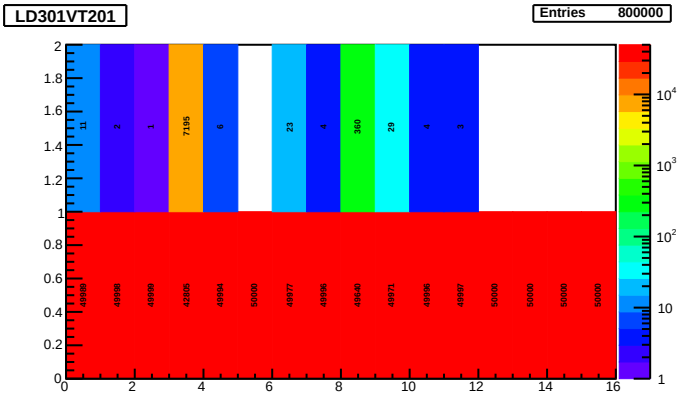

mFP201JPsim_FM103 large north
Entries 0
200
100
0
−100
−200
JP-T
JP-M
JP-B

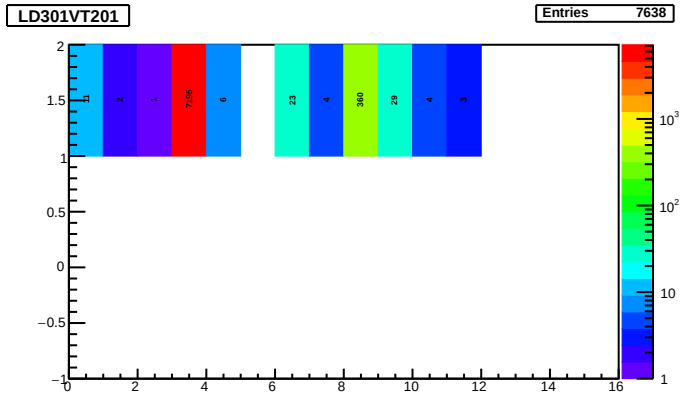

mFP201JP_FM104 large south
Entries 150000
250
200
150
100
50
0
JP-T
JP-M
JP-B
$10^4$
$10^3$
$10^2$
10
1

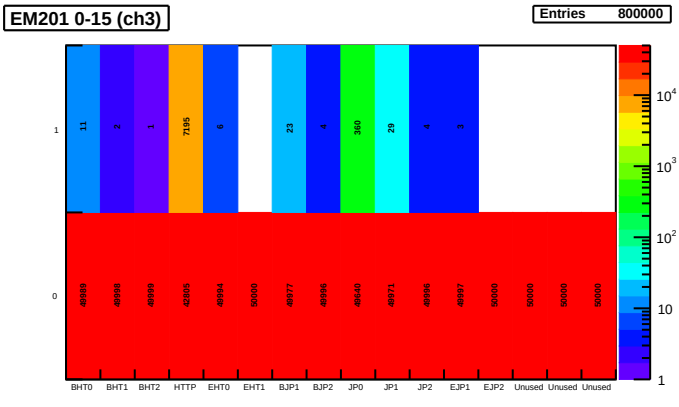

mFP201JPsim_FM104 large south
Entries 0
200
100
0
−100
−200
JP-T
JP-M
JP-B

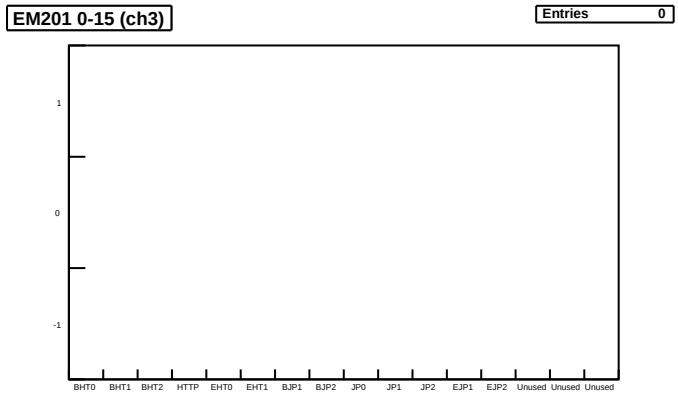

LD301TF201
Entries 800000

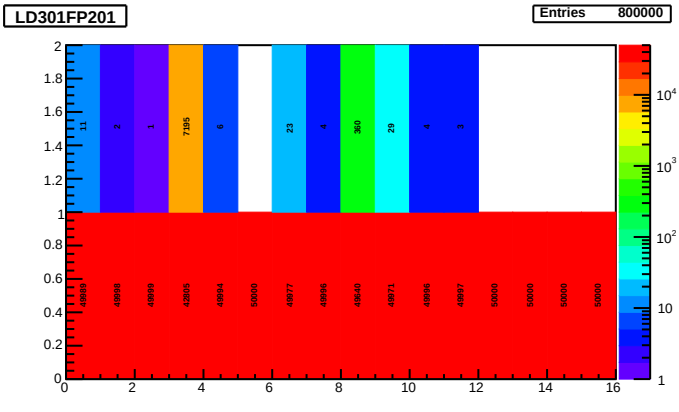

LD301TF201
Entries 7638

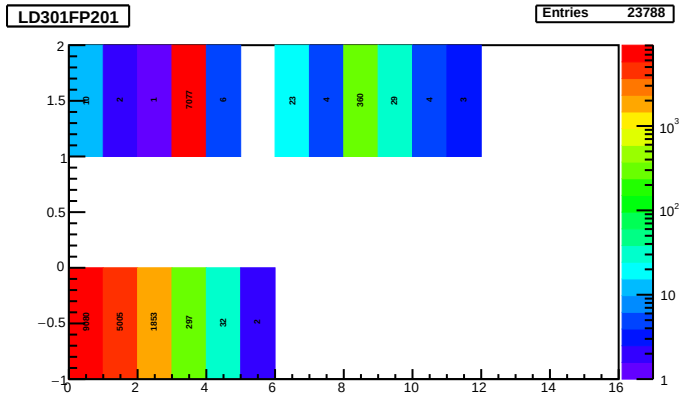

LD301VT201
Entries 800000


LD301VT201
Entries 7638


EM201 0-15 (ch3)
Entries 800000
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


LD301FP201
Entries 800000


LD301FP201
Entries 23788