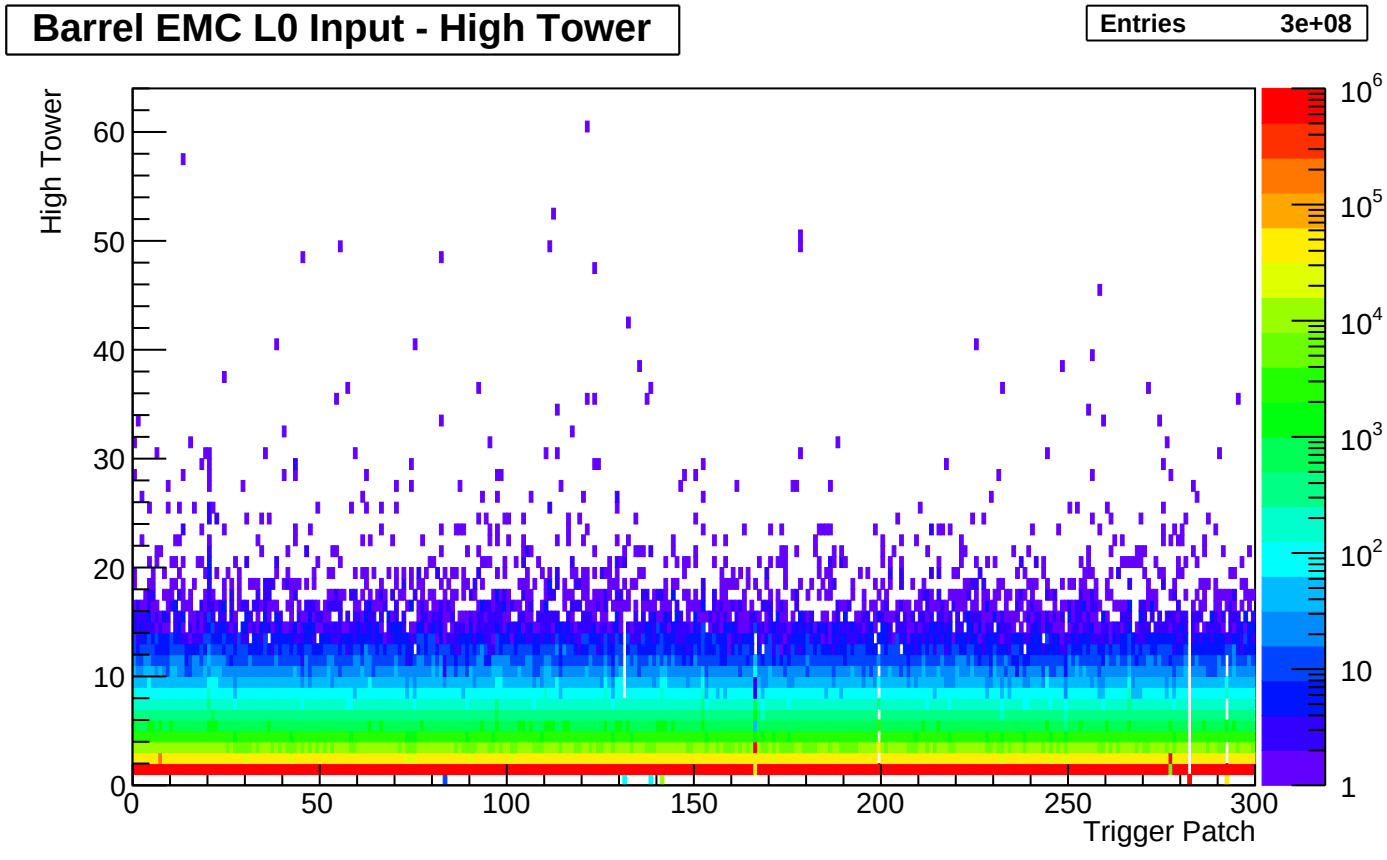
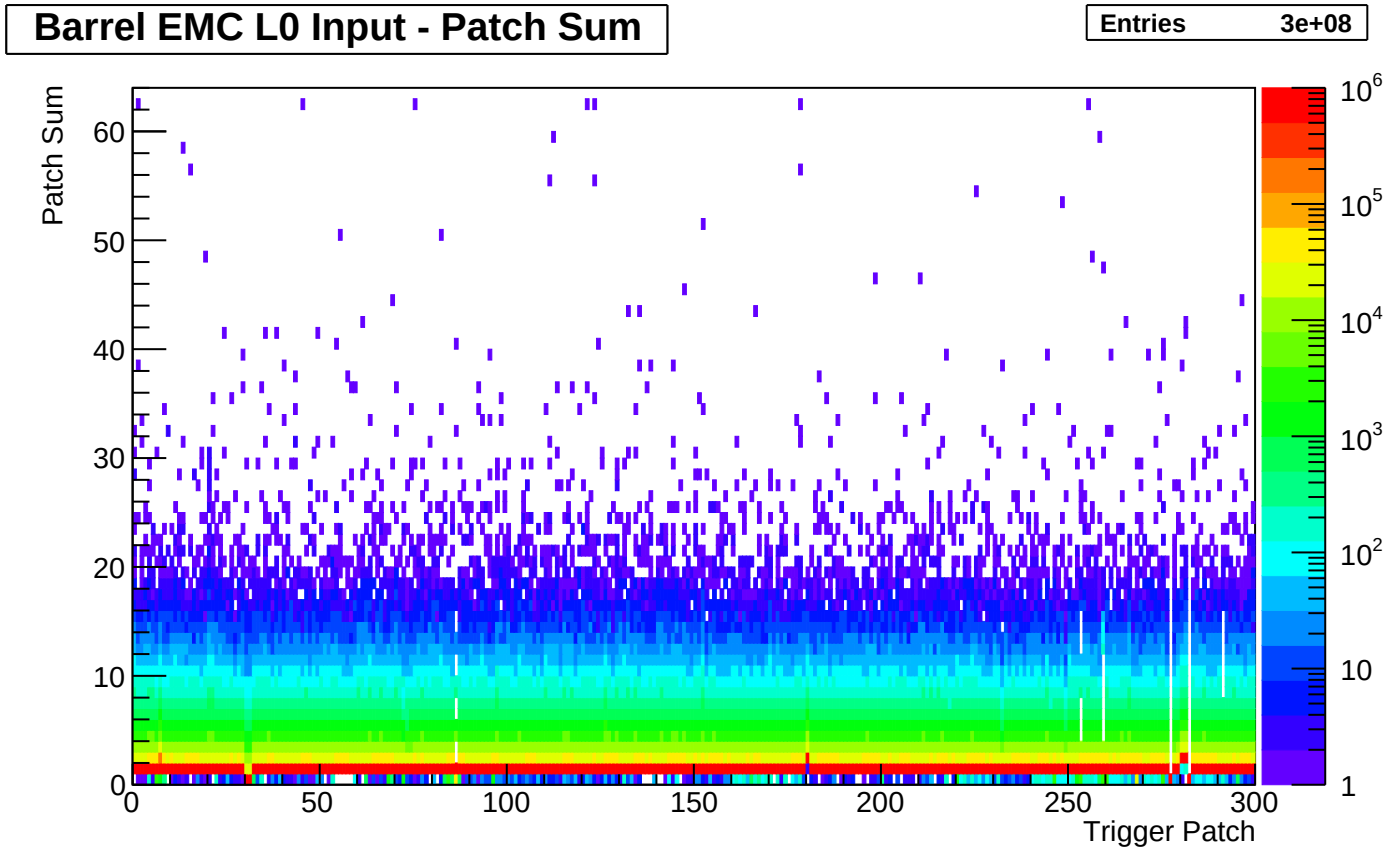
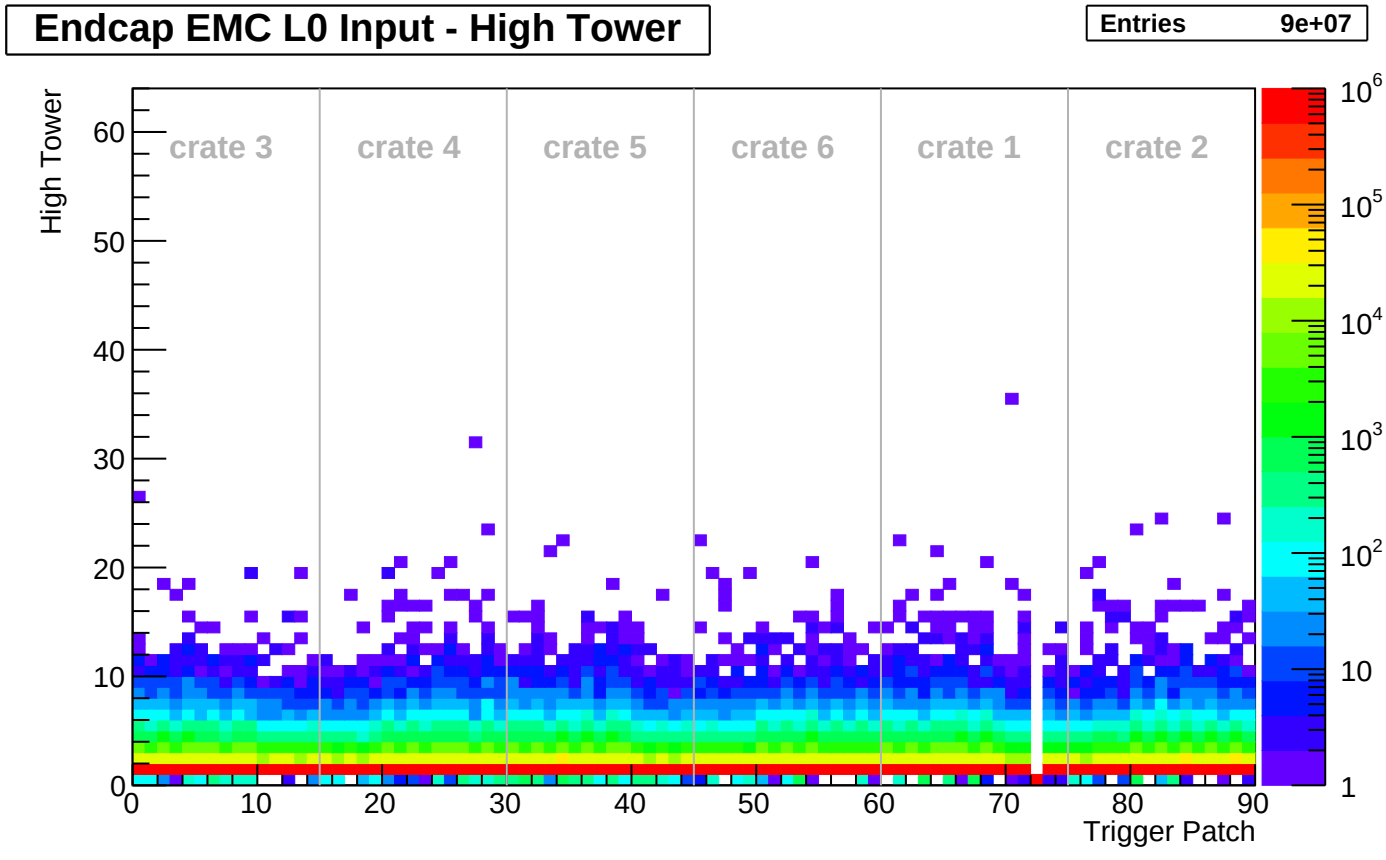
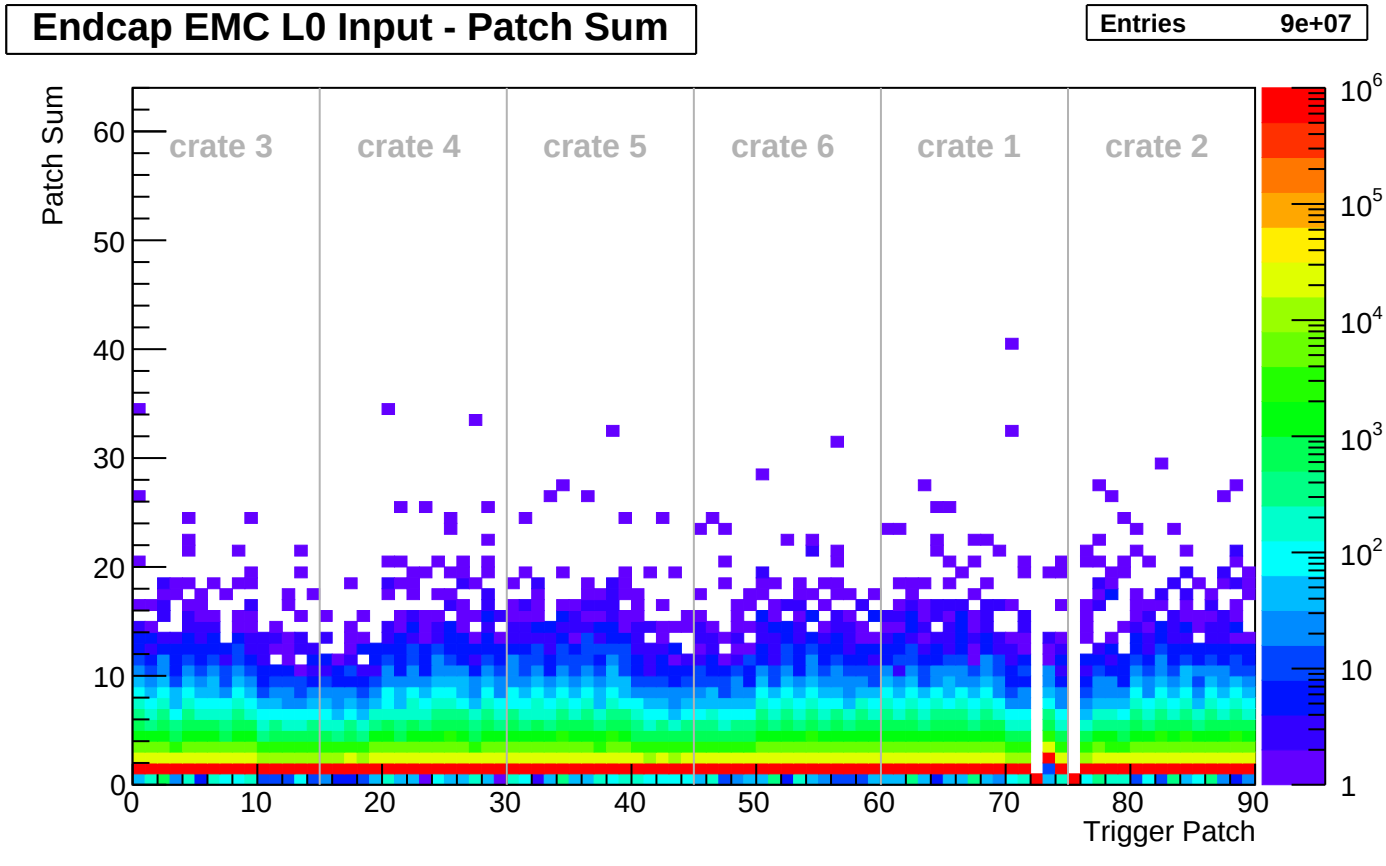
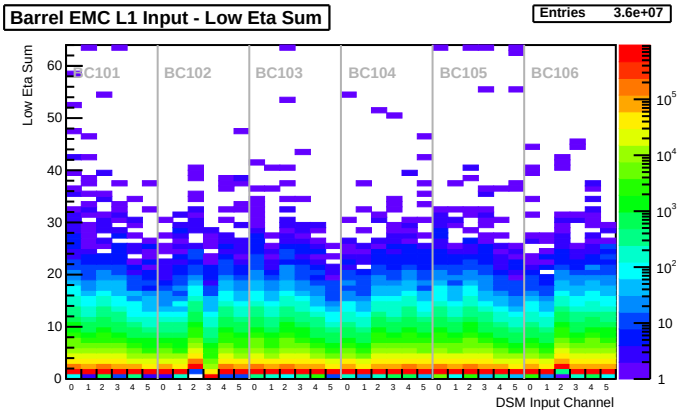
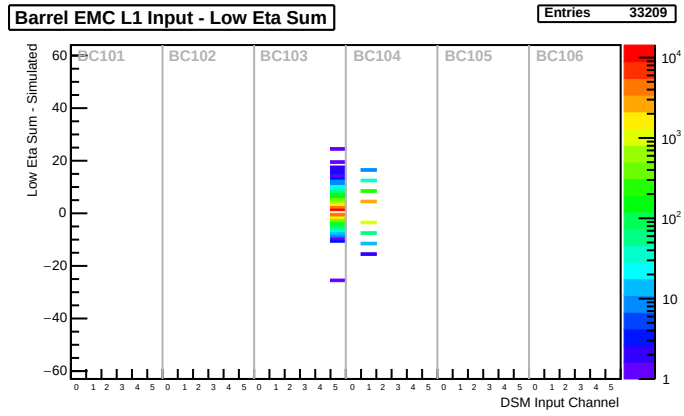

Barrel EMC L0 Input - High Tower
Entries 3e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

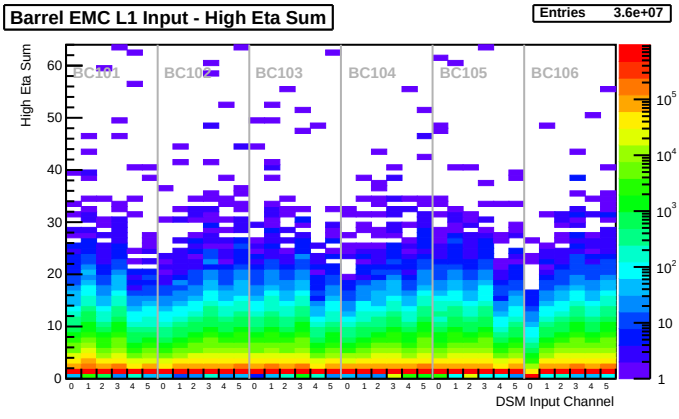

Barrel EMC L0 Input - Patch Sum
Entries 3e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5
10^6

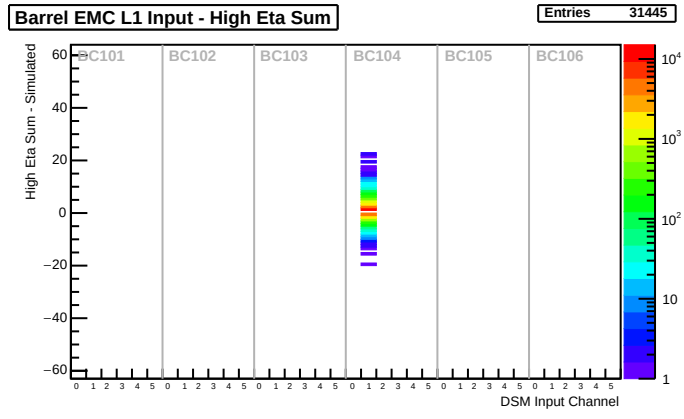

Endcap EMC L0 Input - High Tower
Entries 9e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

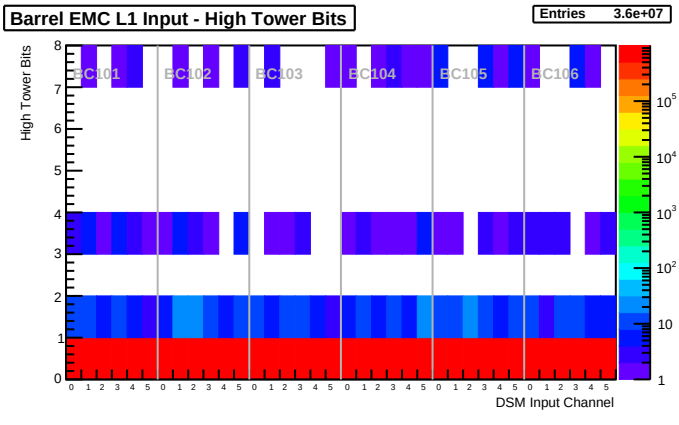

Endcap EMC L0 Input - Patch Sum
Entries 9e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

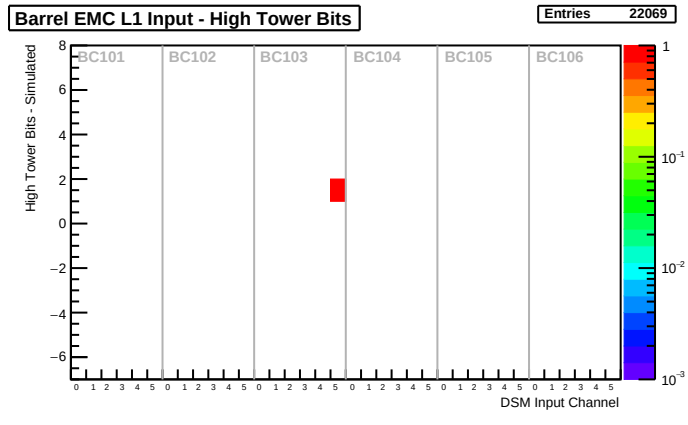

Barrel EMC L1 Input - Low Eta Sum
Entries 3.6e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

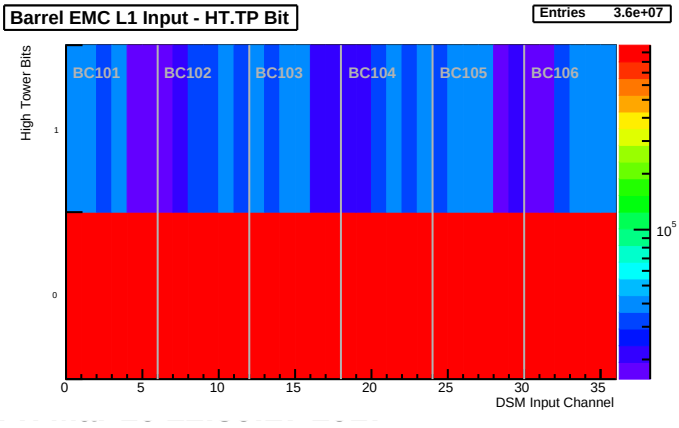

Barrel EMC L1 Input - Low Eta Sum
Entries 33209
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

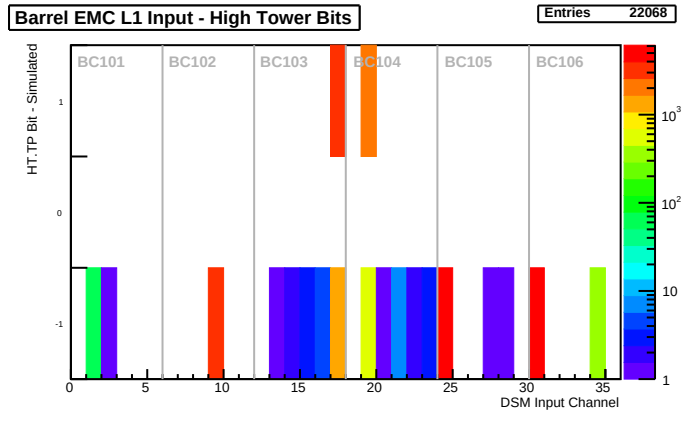

Barrel EMC L1 Input - High Eta Sum
Entries 3.6e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

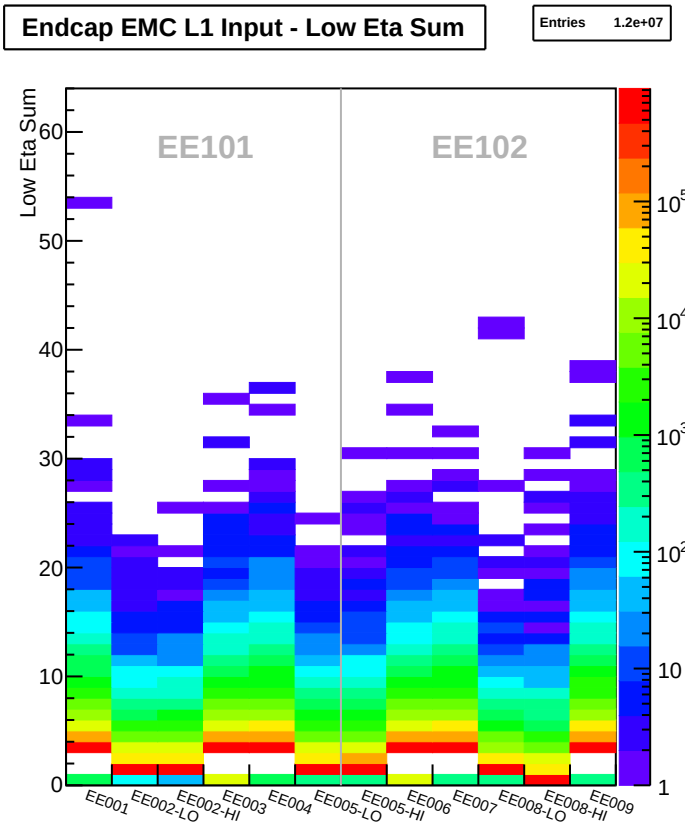

Barrel EMC L1 Input - High Eta Sum
Entries 31445
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

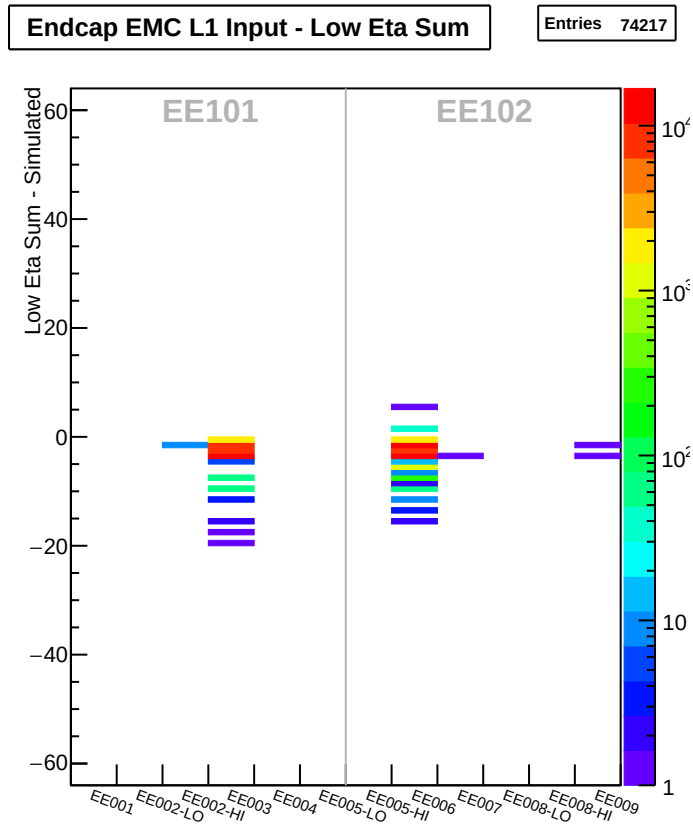

Barrel EMC L1 Input - High Tower Bits
Entries 3.6e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

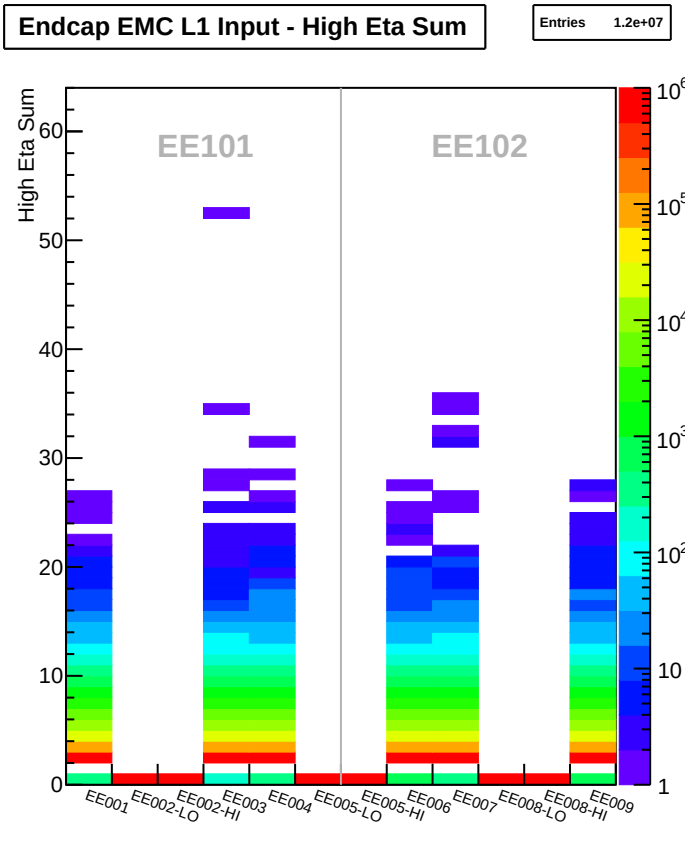

Barrel EMC L1 Input - High Tower Bits
Entries 22069
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

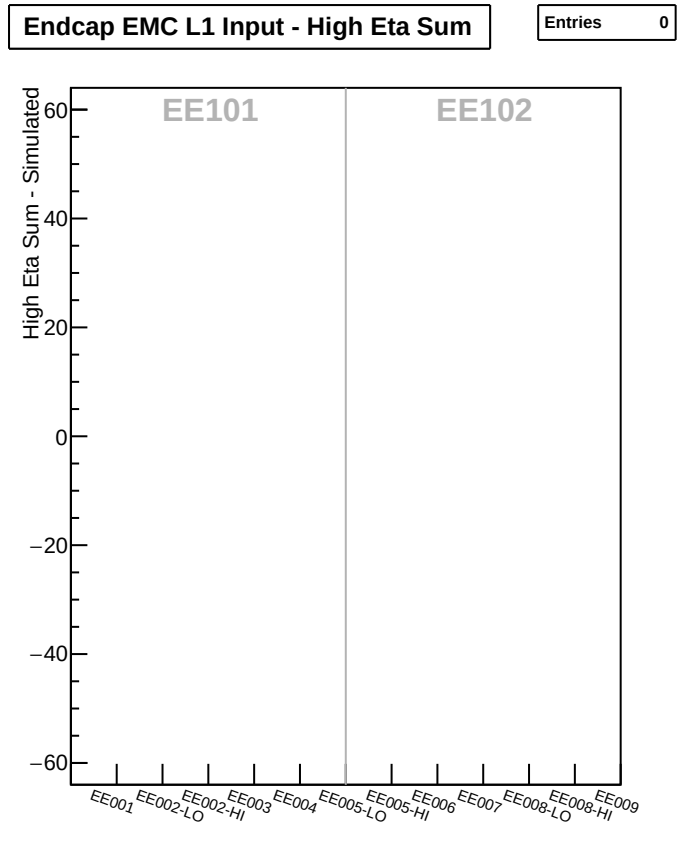

Barrel EMC L1 Input - HT.TP Bit
Entries 3.6e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

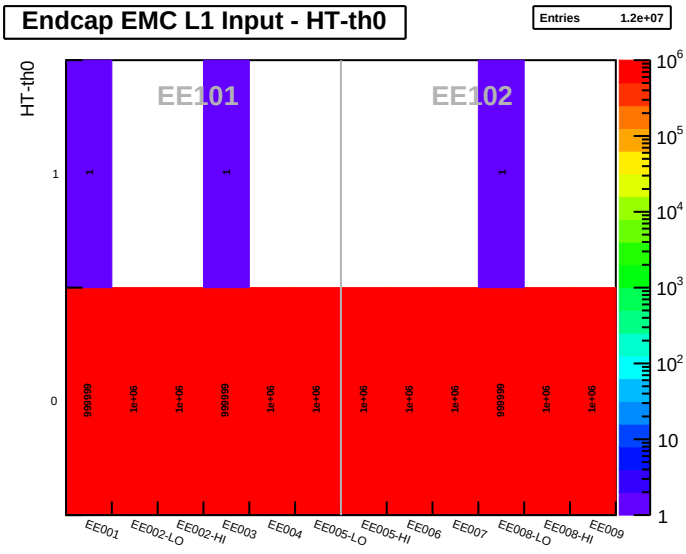

Barrel EMC L1 Input - High Tower Bits
Entries 22068
HT.TP Bit - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

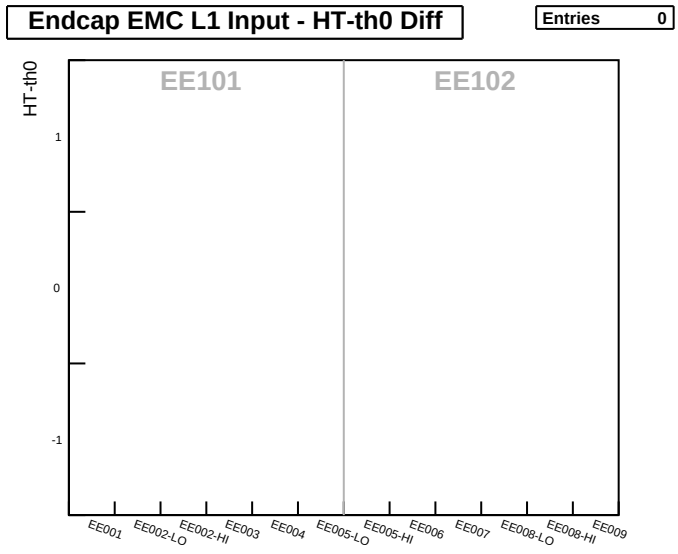

Endcap EMC L1 Input - Low Eta Sum
Entries 1.2e+07
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

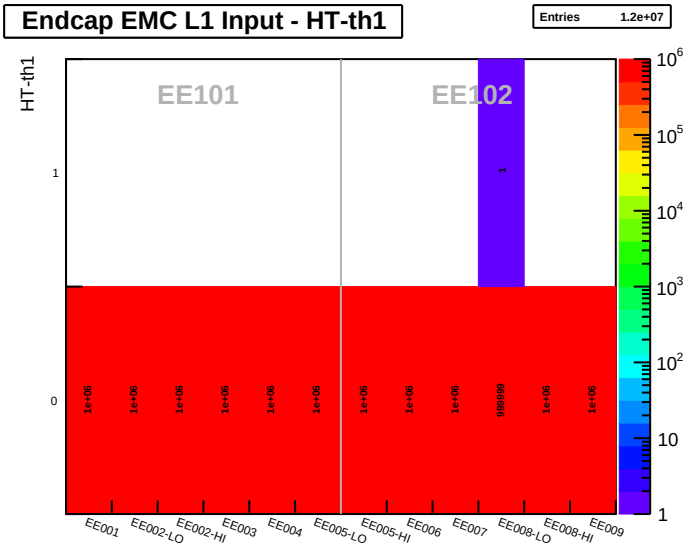

Endcap EMC L1 Input - Low Eta Sum
Entries 74217
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

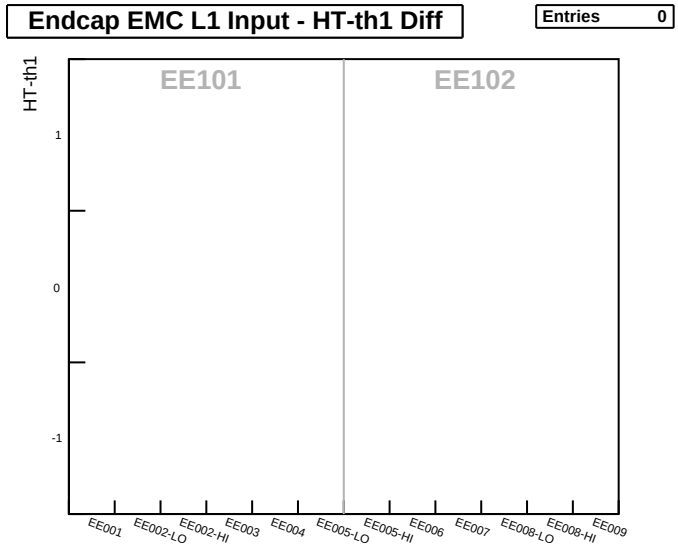

Endcap EMC L1 Input - High Eta Sum
Entries 1.2e+07
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

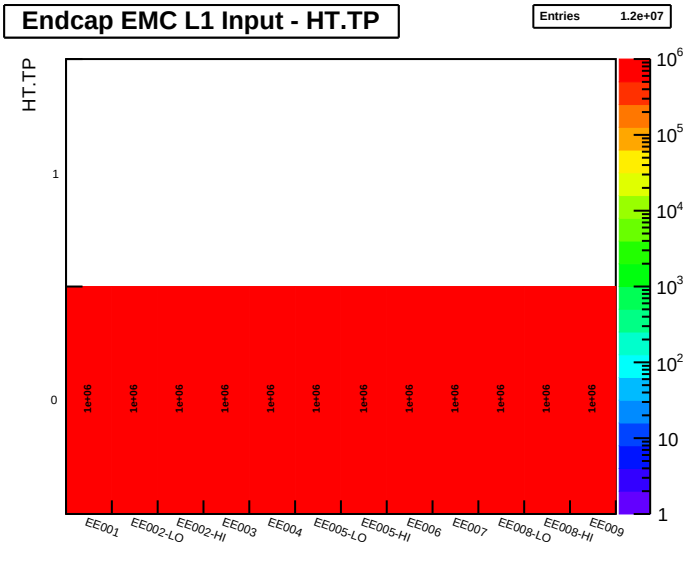

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

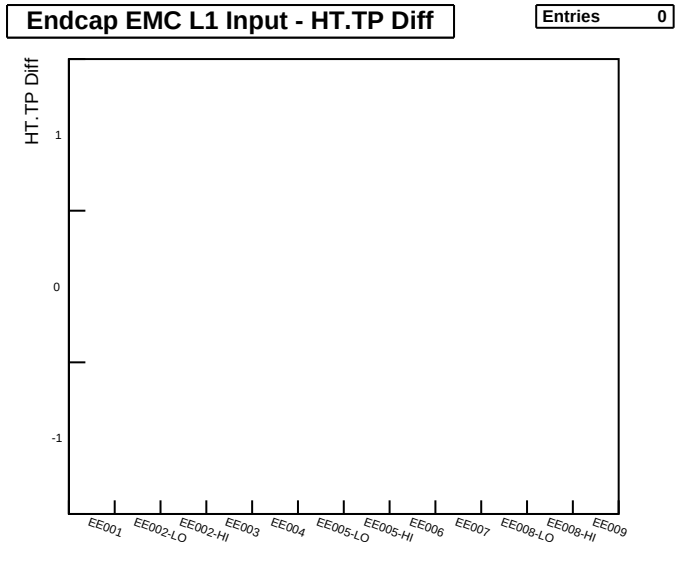

Endcap EMC L1 Input - HT-th0
Entries 1.2e+07
HT-th0
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

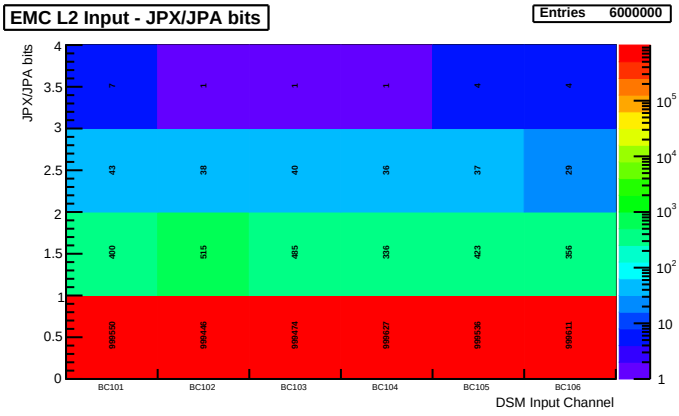

Endcap EMC L1 Input - HT-th0 Diff
Entries 0
HT-th0
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

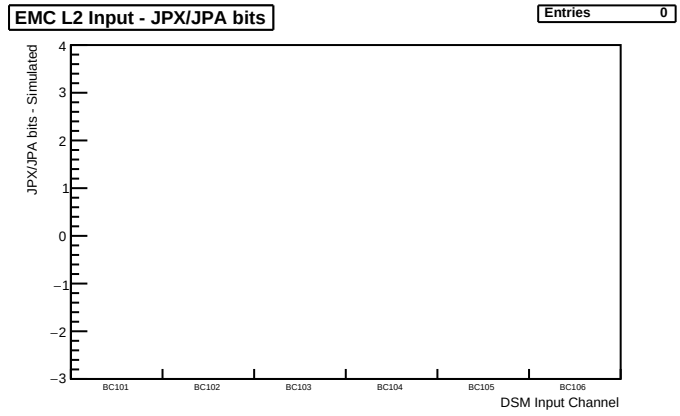

Endcap EMC L1 Input - HT-th1
Entries 1.2e+07
HT-th1
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

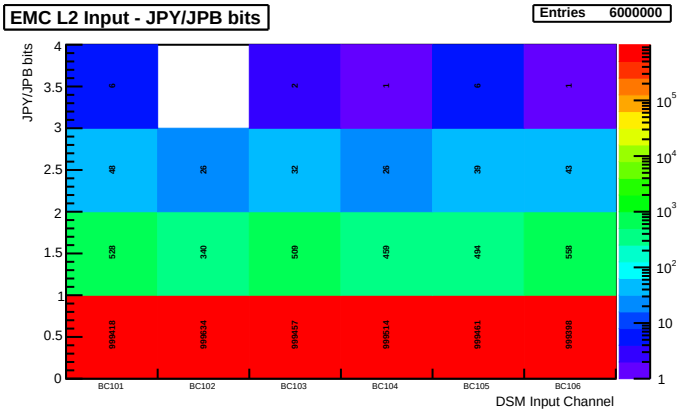

Endcap EMC L1 Input - HT-th1 Diff
Entries 0
HT-th1
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

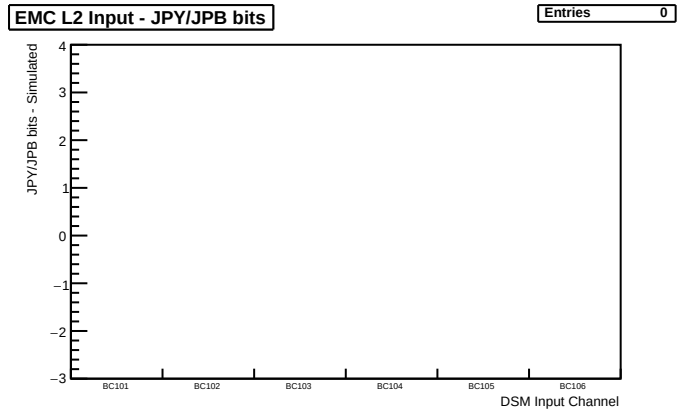

Endcap EMC L1 Input - HT.TP
Entries 1.2e+07
HT.TP
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

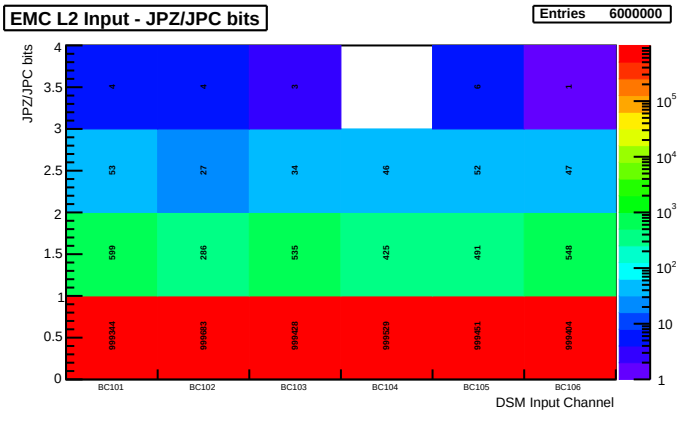

Endcap EMC L1 Input - HT.TP Diff
Entries 0
HT.TP Diff
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

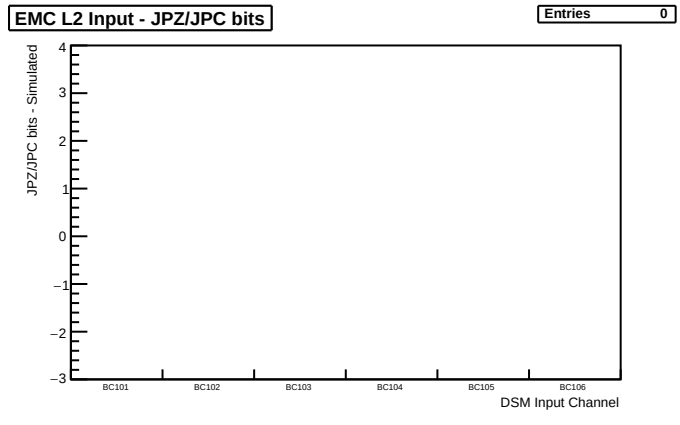

EMC L2 Input - JPX/JPA bits
Entries 6000000
JPX/JPA bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

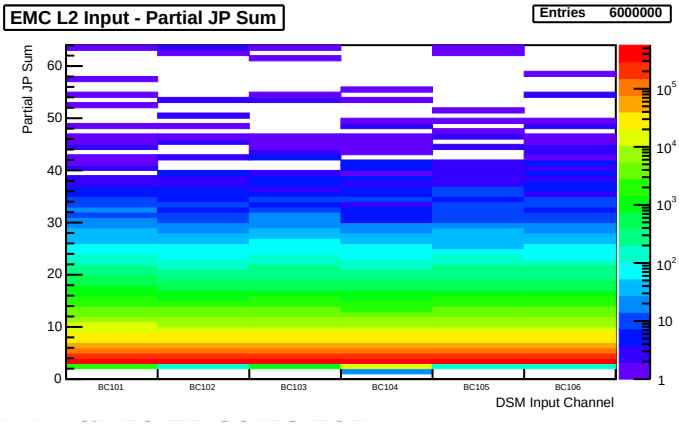

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

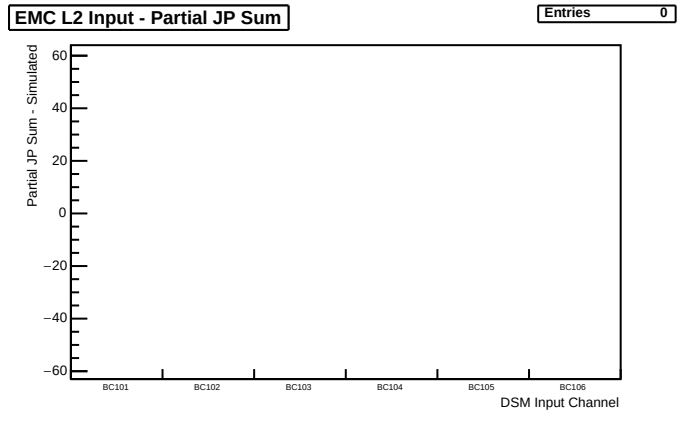

EMC L2 Input - JPY/JPB bits
Entries 6000000
JPY/JPB bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

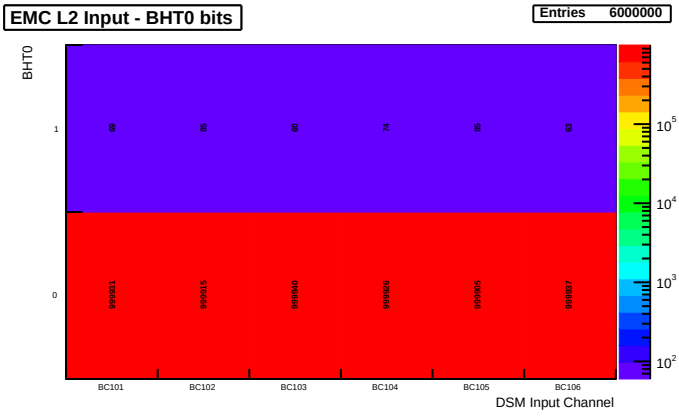

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

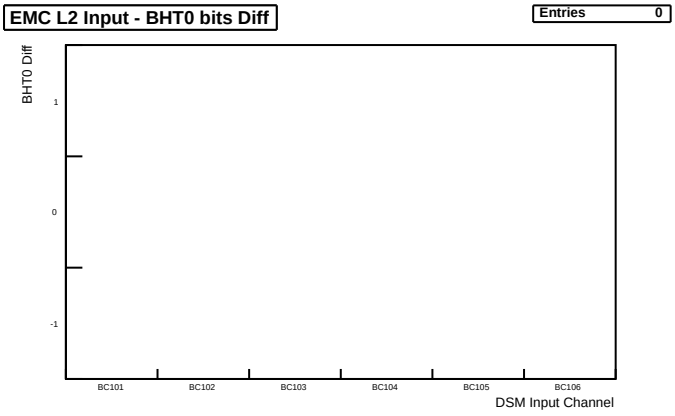

EMC L2 Input - JPZ/JPC bits
Entries 6000000
JPZ/JPC bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

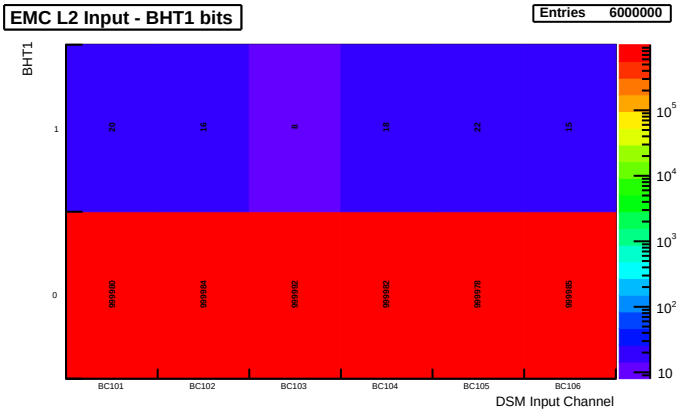

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

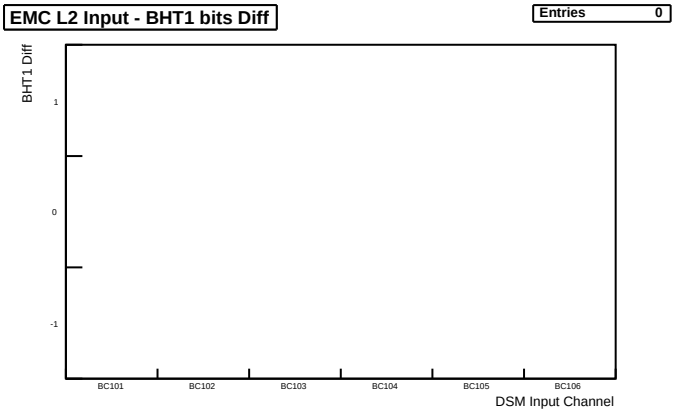

EMC L2 Input - Partial JP Sum
Entries 6000000
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

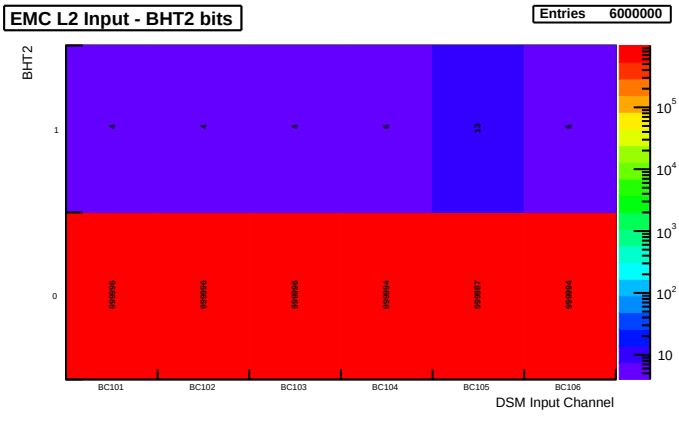

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

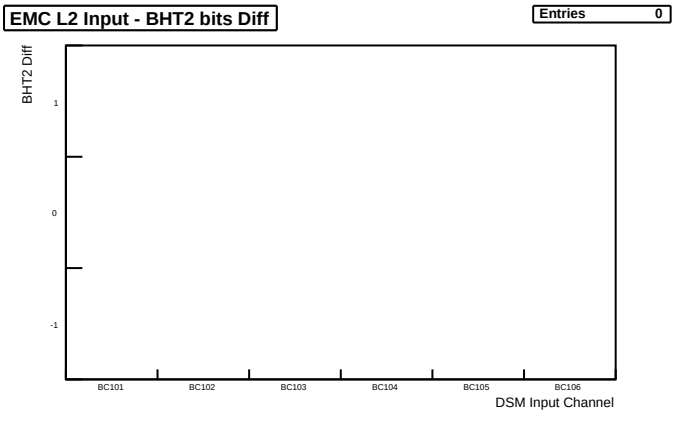

EMC L2 Input - BHT0 bits
Entries 6000000
BHT0
1
0
89
85
60
74
95
63
999931
999915
999940
999926
999905
999937
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel
10^5
10^4
10^3
10^2

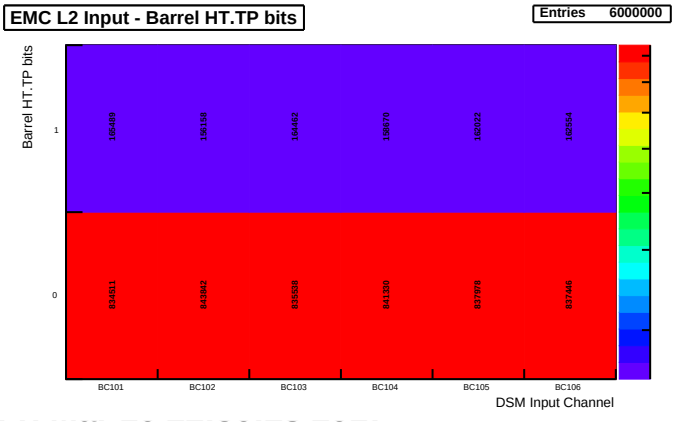

EMC L2 Input - BHT0 bits Diff
Entries 0
BHT0 Diff
1
0
-1
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

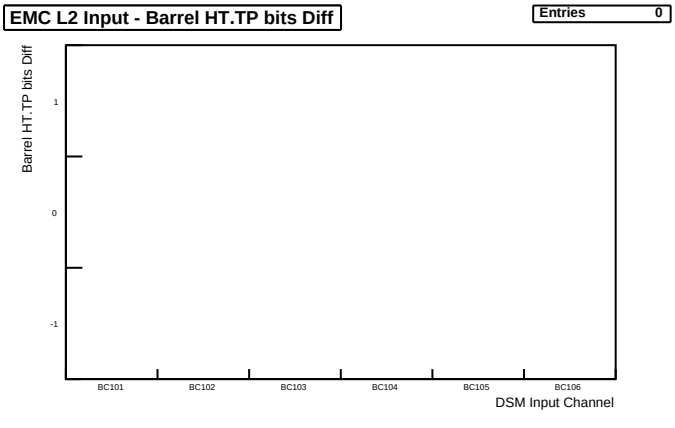

EMC L2 Input - BHT1 bits
Entries 6000000
BHT1
1
0
20
16
8
18
22
15
999930
999934
999932
999932
999978
999935
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel
10^5
10^4
10^3
10^2
10

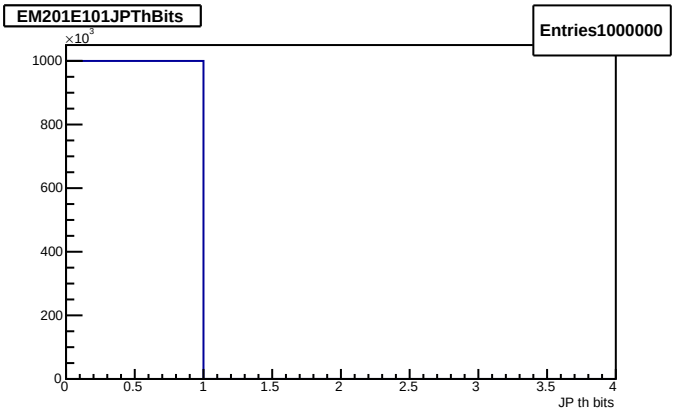

EMC L2 Input - BHT1 bits Diff
Entries 0
BHT1 Diff
1
0
-1
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

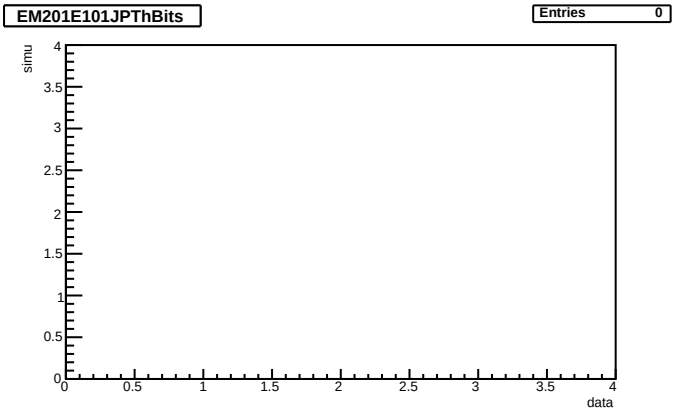

EMC L2 Input - BHT2 bits
Entries 6000000
BHT2
1
0
4
4
4
6
13
6
999996
999996
999996
999994
999987
999994
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel
10^5
10^4
10^3
10^2
10

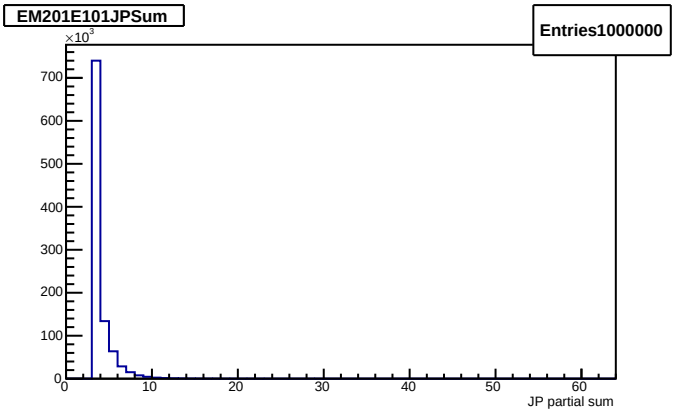

EMC L2 Input - BHT2 bits Diff
Entries 0
BHT2 Diff
1
0
-1
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

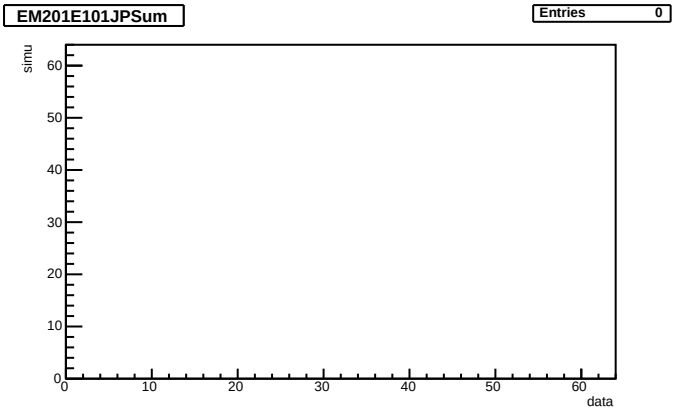

EMC L2 Input - Barrel HT.TP bits
Entries 6000000
Barrel HT.TP bits
1
0
165489
159159
164462
159670
162622
162554
834911
843942
835538
841320
837978
837446
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

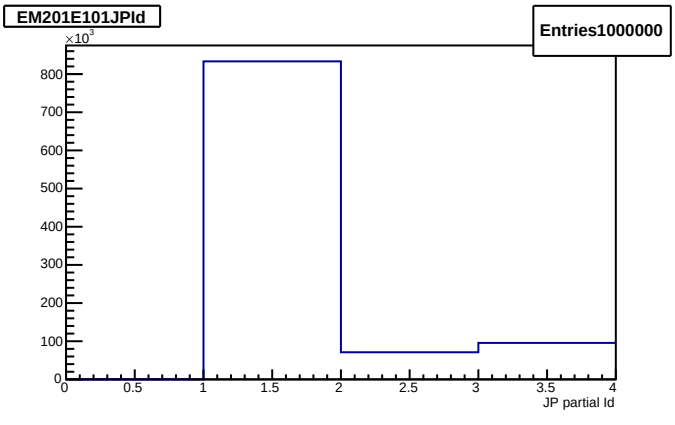

EMC L2 Input - Barrel HT.TP bits Diff
Entries 0
Barrel HT.TP bits Diff
1
0
-1
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

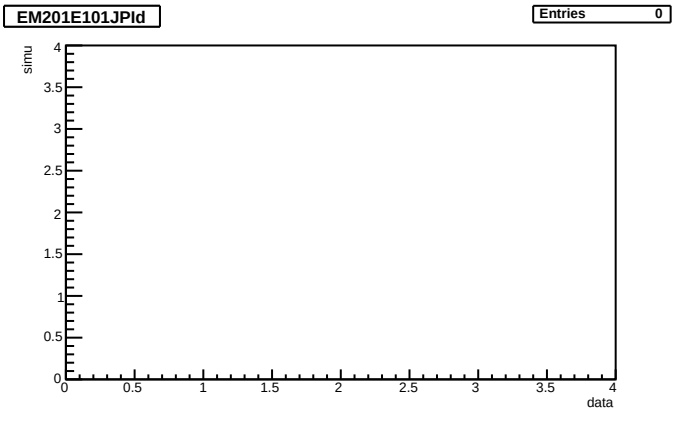

EM201E101JPThBits
Entries1000000
×10^3
JP th bits

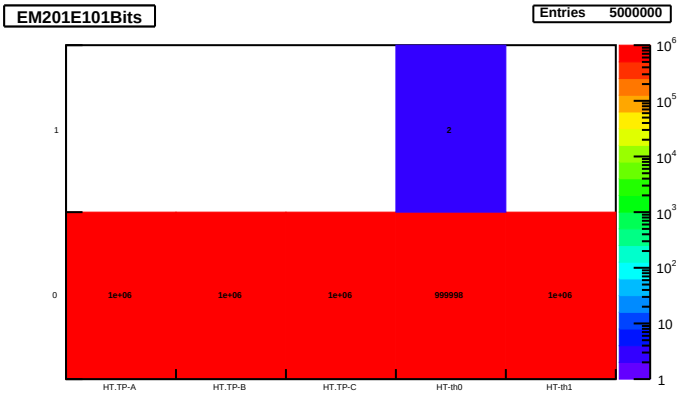

EM201E101JPThBits
Entries 0
simu
data

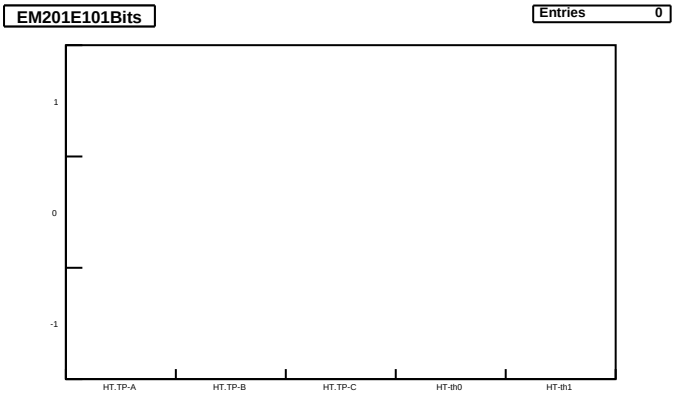

EM201E101JPSum
Entries1000000
×10^3
JP partial sum

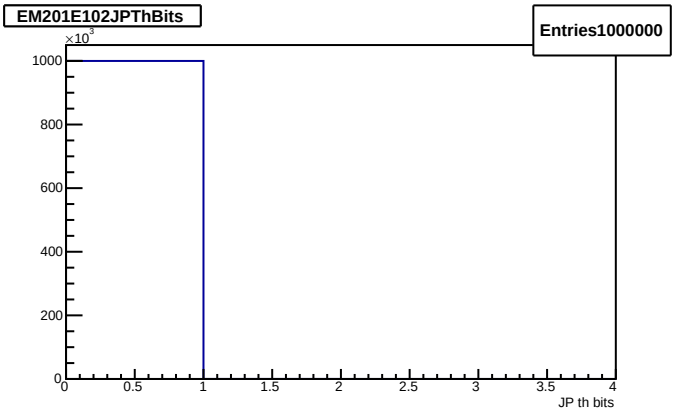

EM201E101JPSum
Entries 0
simu
data

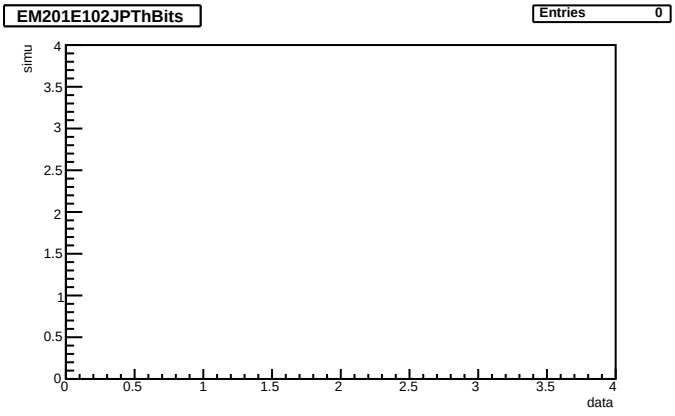

EM201E101JPId
Entries1000000
×10^3
JP partial Id

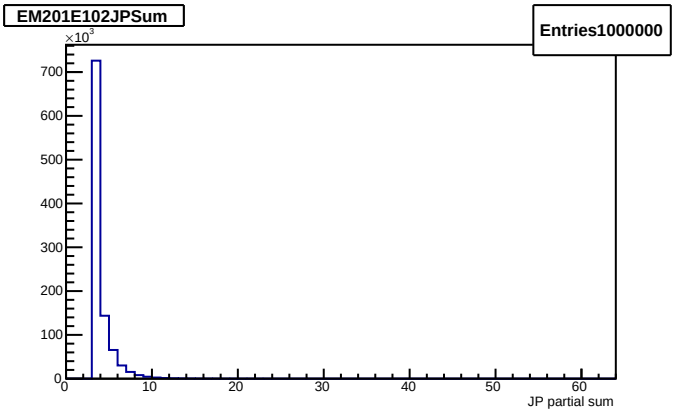

EM201E101JPId
Entries 0
simu
data

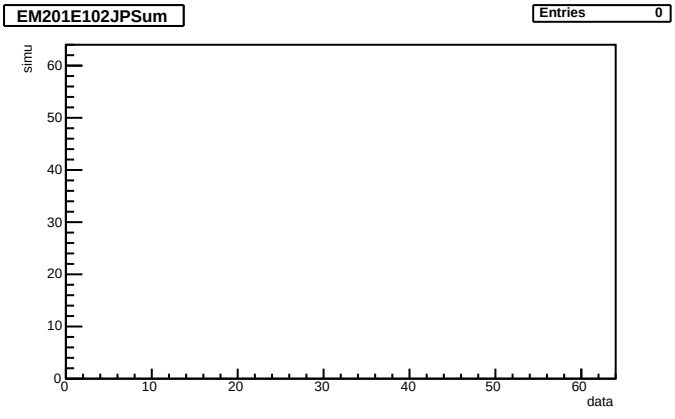

EM201E101Bits
Entries 5000000
1e+06
1e+06
1e+06
999998
1e+06
2
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

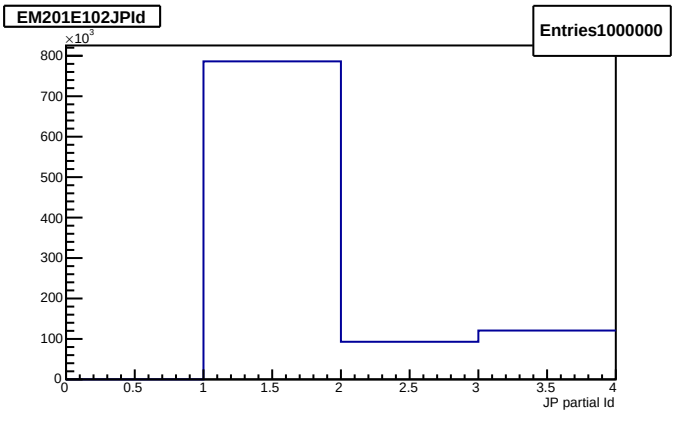

EM201E101Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

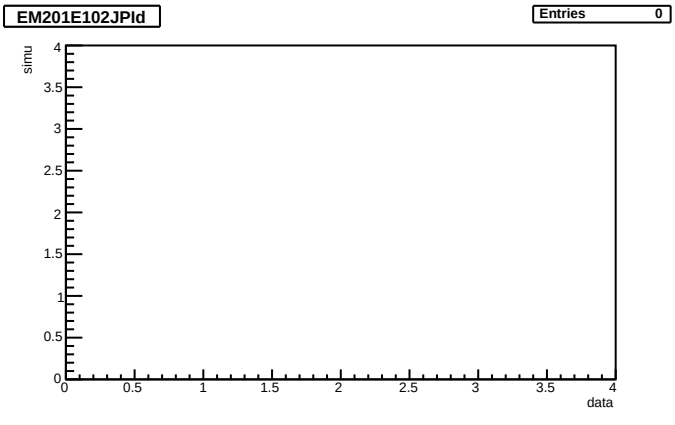

EM201E102JPThBits
Entries1000000
×10^3
JP th bits

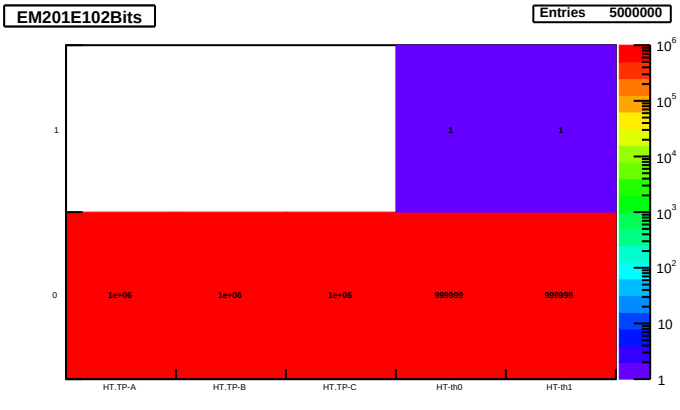

EM201E102JPThBits
Entries 0
simu
data

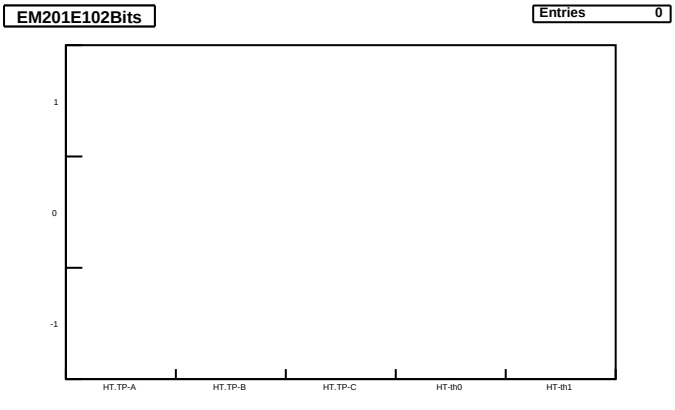

EM201E102JPSum
Entries1000000
×10^3
JP partial sum

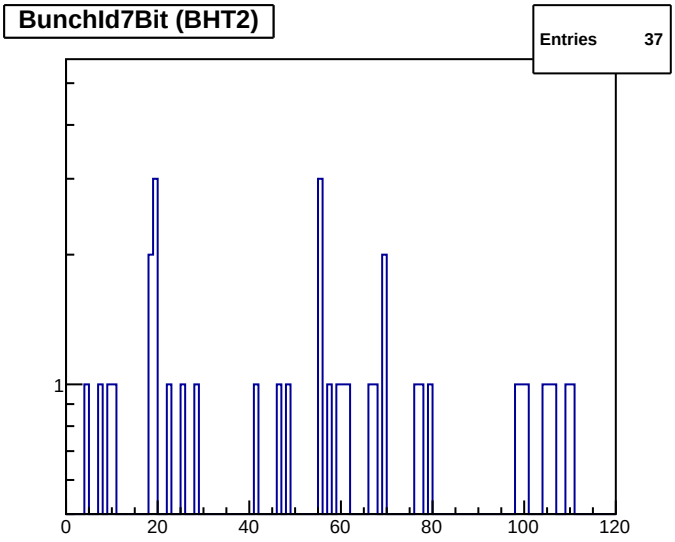

EM201E102JPSum
Entries 0
simu
data

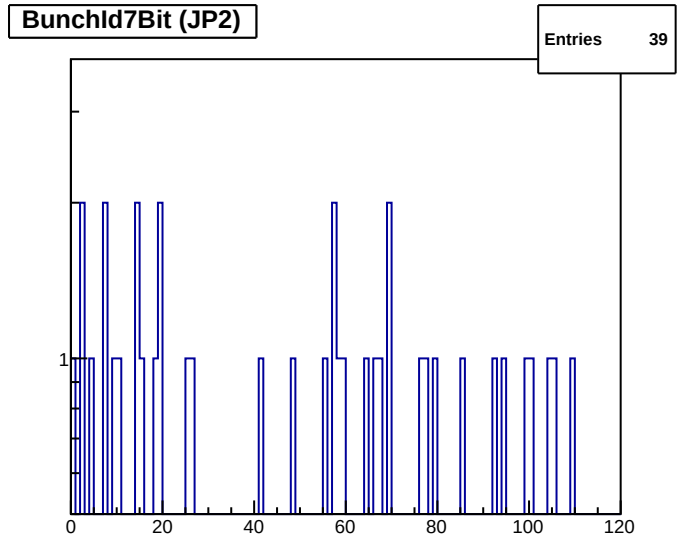

EM201E102JPId
Entries1000000
×10^3
JP partial Id

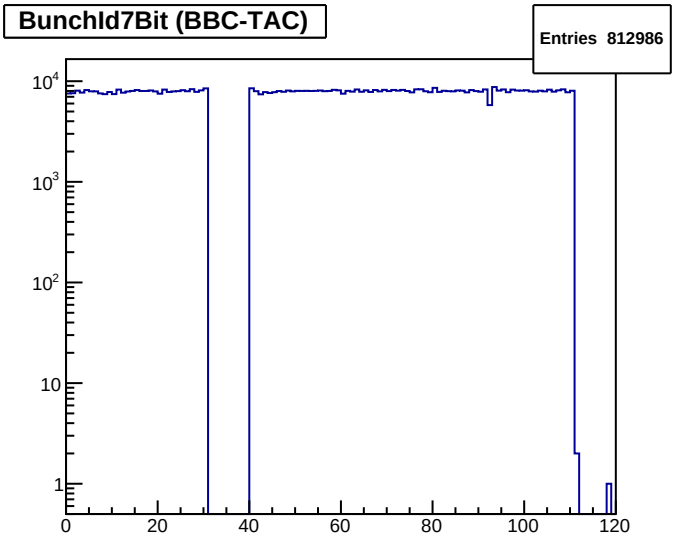

EM201E102JPId
Entries 0
simu
data

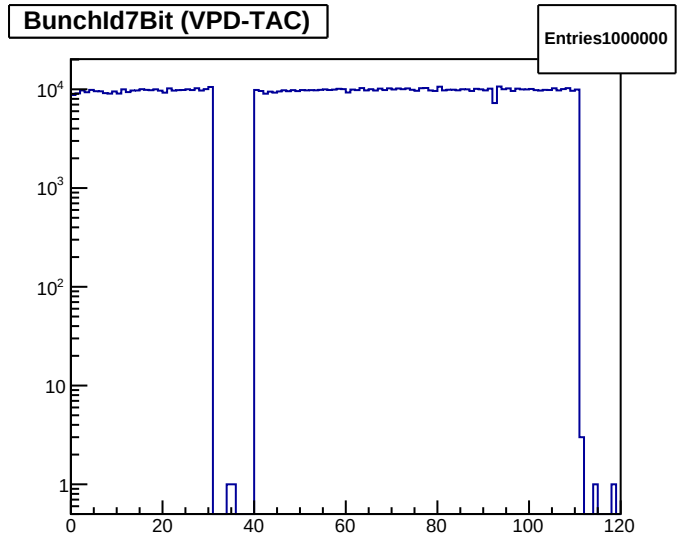

EM201E102Bits
Entries 5000000
1
1
1
1e+06
1e+06
1e+06
999999
999999
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

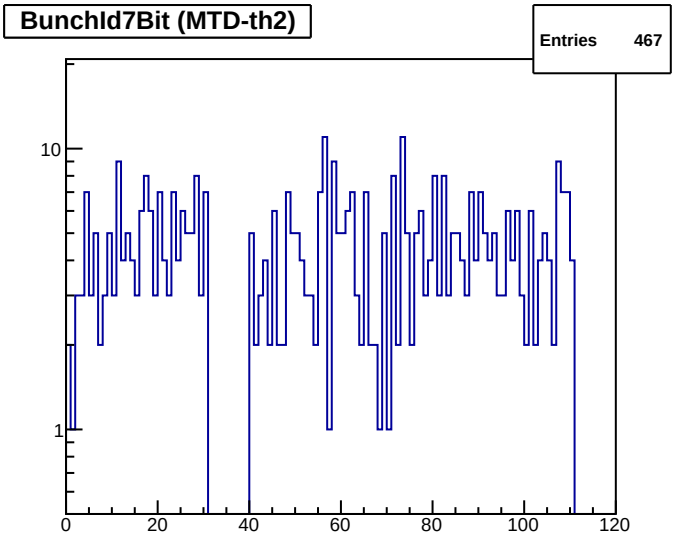

EM201E102Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

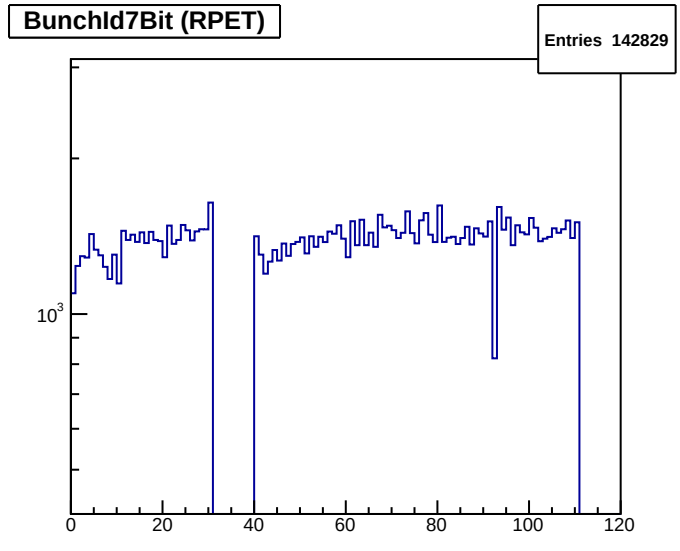

BunchId7Bit (BHT2)
Entries 37
1
0
20
40
60
80
100
120

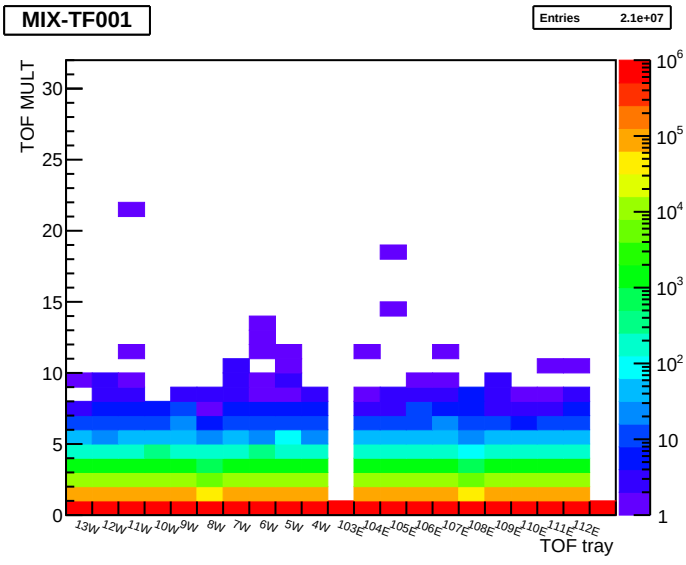

BunchId7Bit (JP2)
Entries 39
1
0
20
40
60
80
100
120

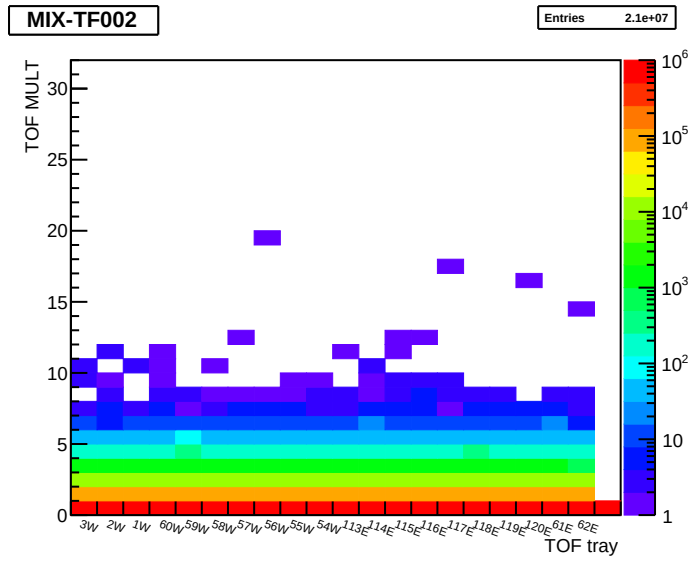

BunchId7Bit (BBC-TAC)
Entries 812986
10^4
10^3
10^2
10
1
0
20
40
60
80
100
120

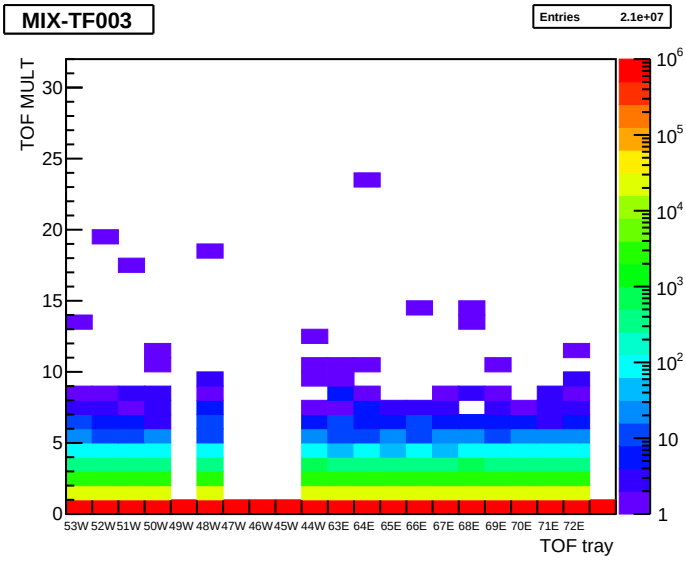

BunchId7Bit (VPD-TAC)
Entries1000000
10^4
10^3
10^2
10
1
0
20
40
60
80
100
120

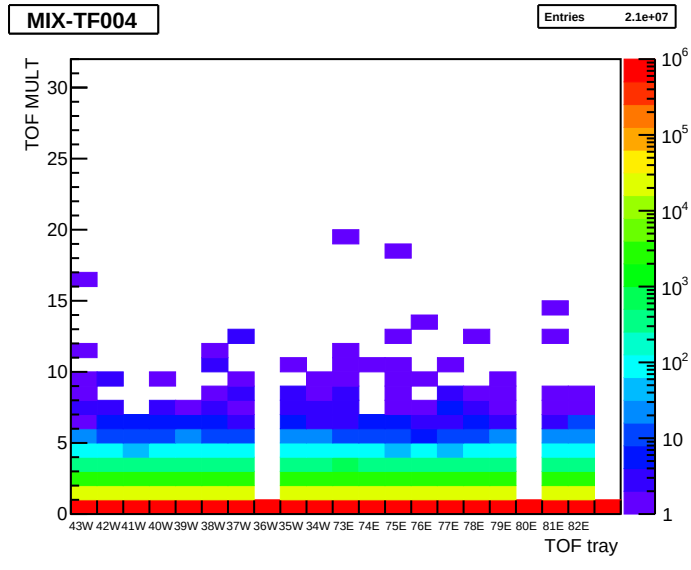

BunchId7Bit (MTD-th2)
Entries 467
10
1
0
20
40
60
80
100
120

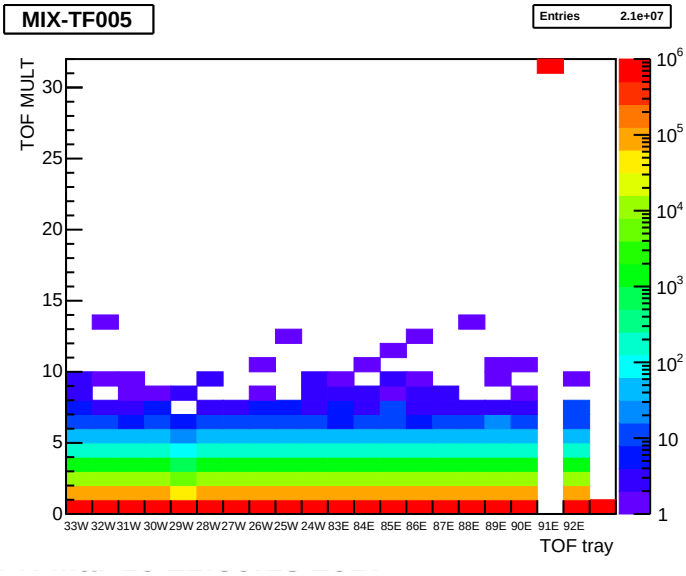

BunchId7Bit (RPET)
Entries 142829
10^3
0
20
40
60
80
100
120

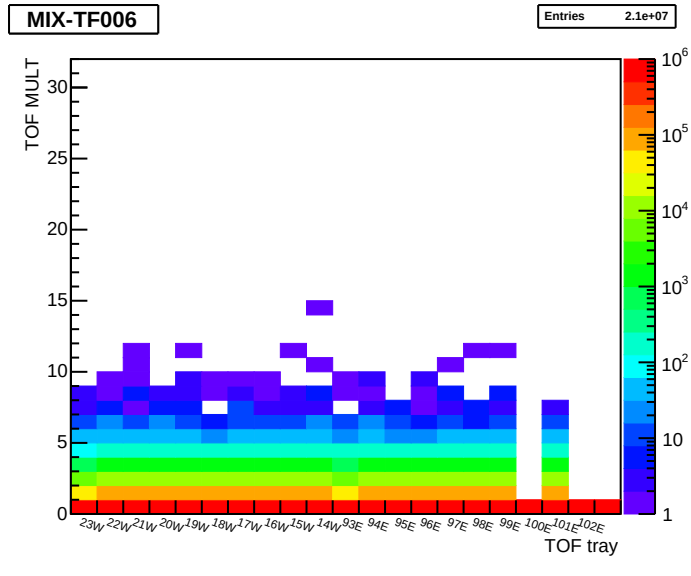

MIX-TF001
Entries 2.1e+07
TOF MULT
TOF tray

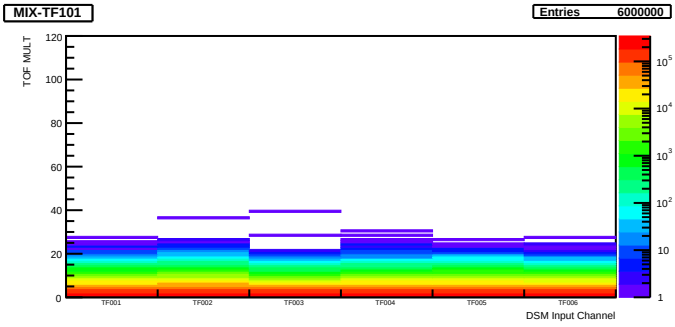

MIX-TF002
Entries 2.1e+07
TOF MULT
TOF tray

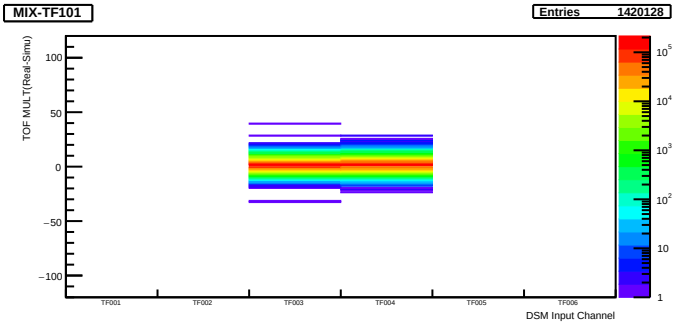

MIX-TF003
Entries 2.1e+07
TOF MULT
TOF tray

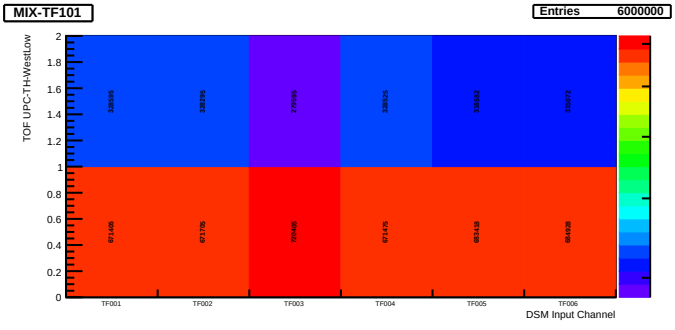

MIX-TF004
Entries 2.1e+07
TOF MULT
TOF tray

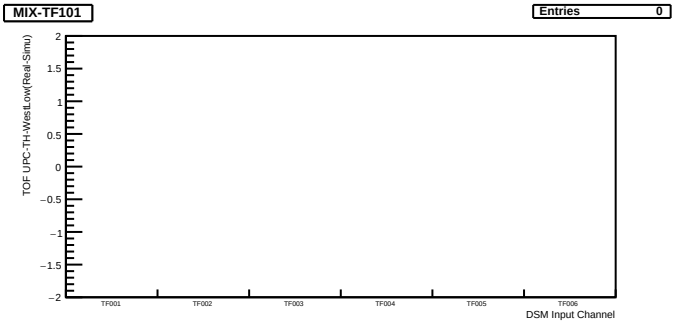

MIX-TF005
Entries 2.1e+07
TOF MULT
TOF tray

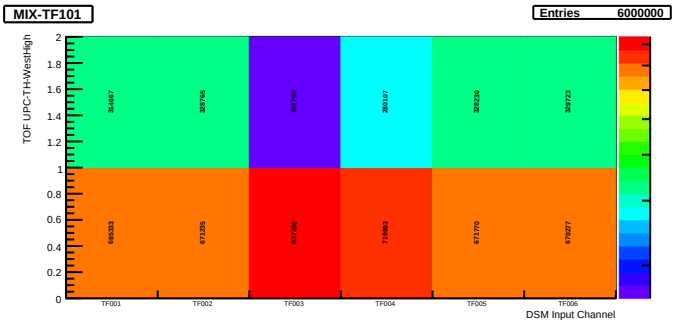

MIX-TF006
Entries 2.1e+07
TOF MULT
TOF tray

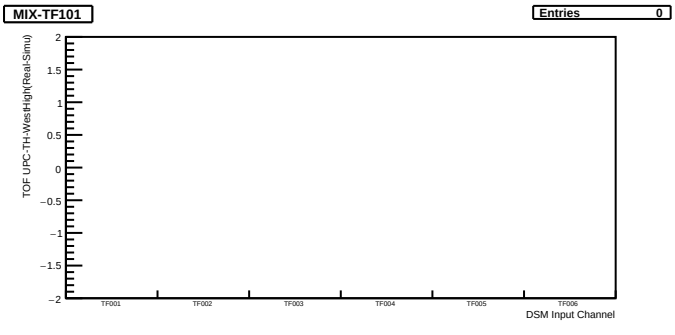

MIX-TF101
Entries 6000000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

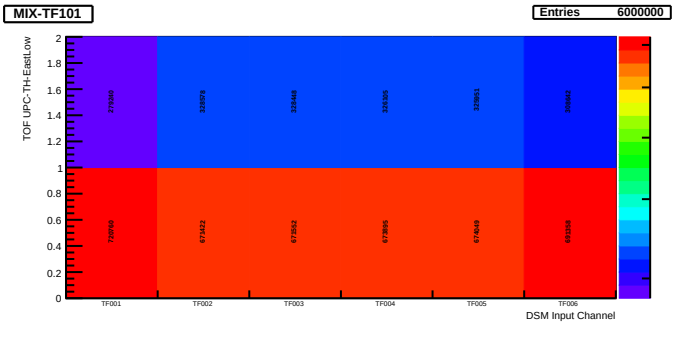

MIX-TF101
Entries 1420128
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

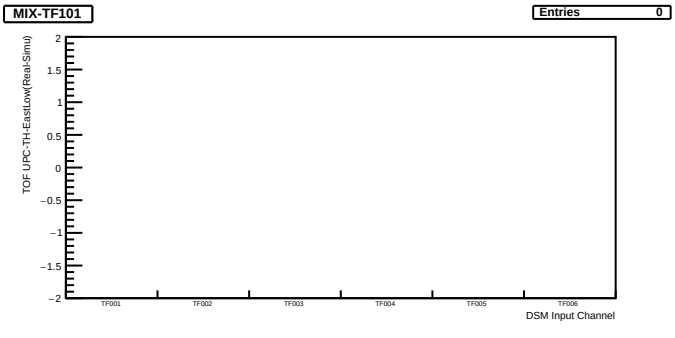

MIX-TF101
Entries 6000000
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

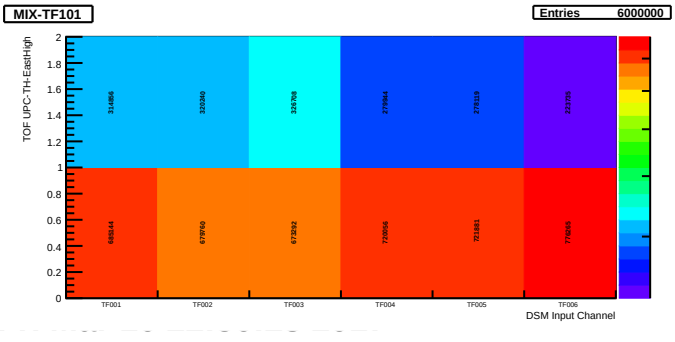

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

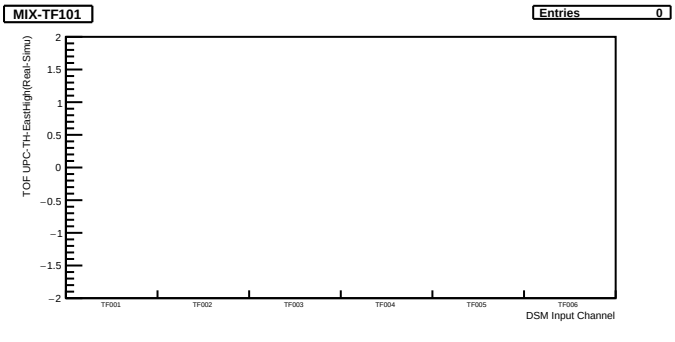

MIX-TF101
Entries 6000000
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

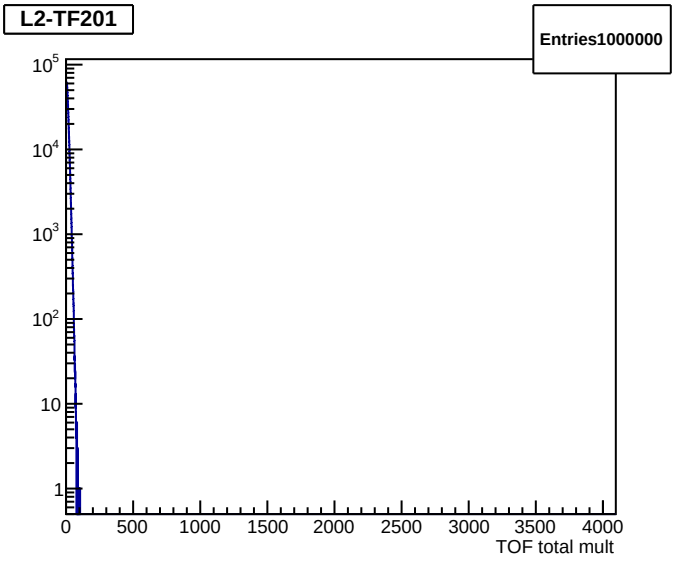

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

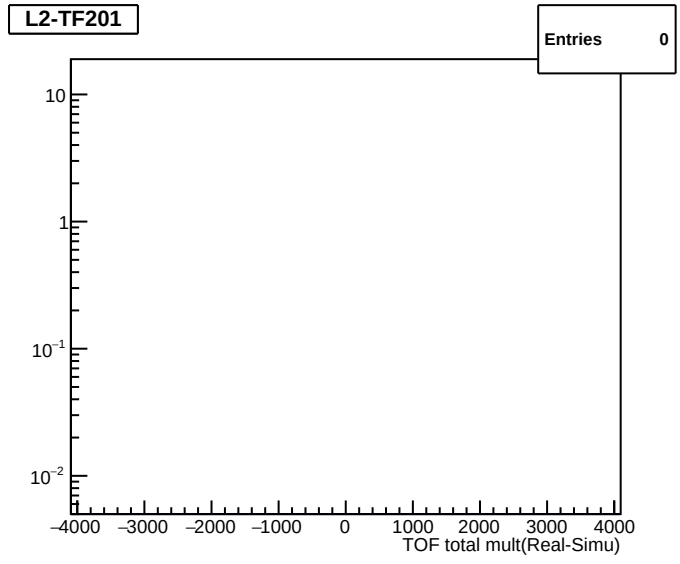

MIX-TF101
Entries 6000000
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

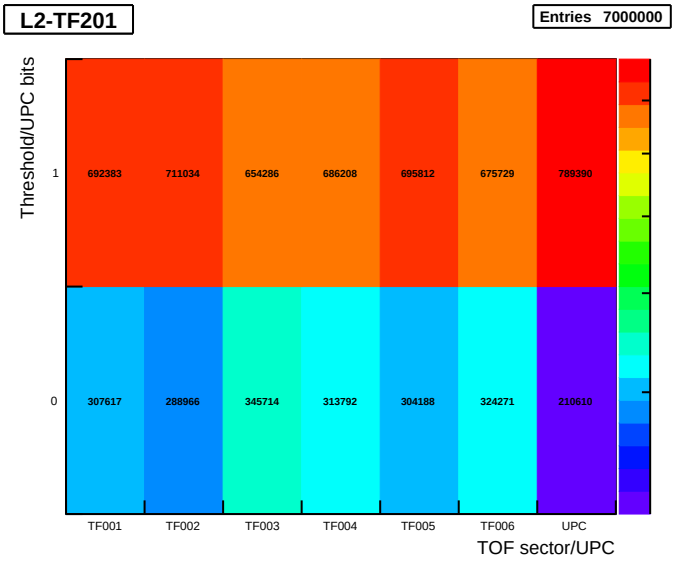

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

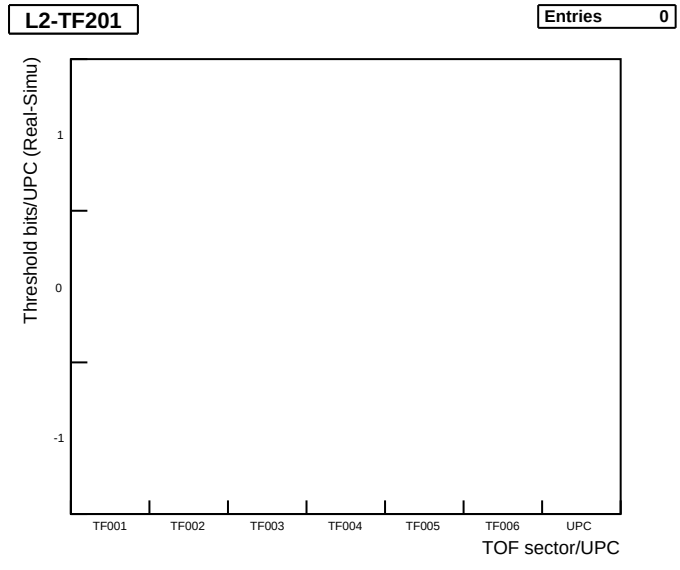

MIX-TF101
Entries 6000000
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

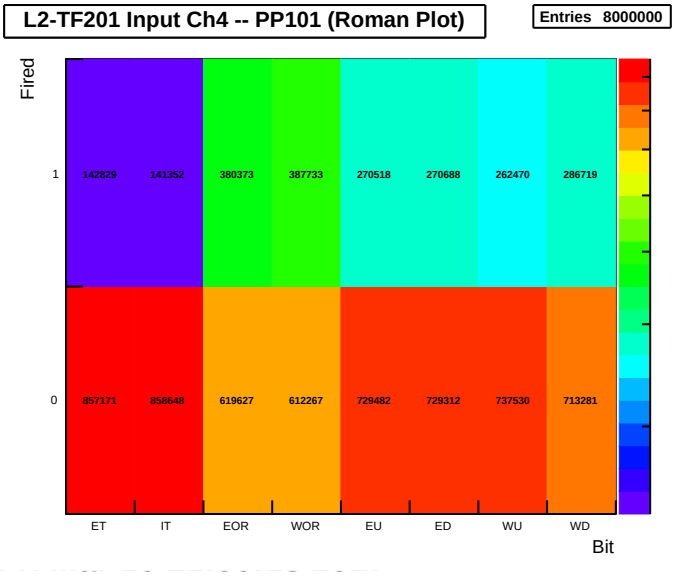

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

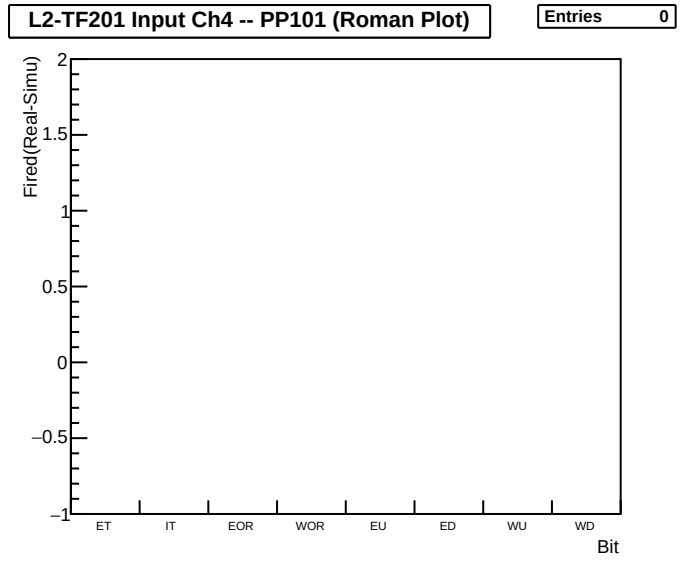

L2-TF201
Entries1000000
TOF total mult

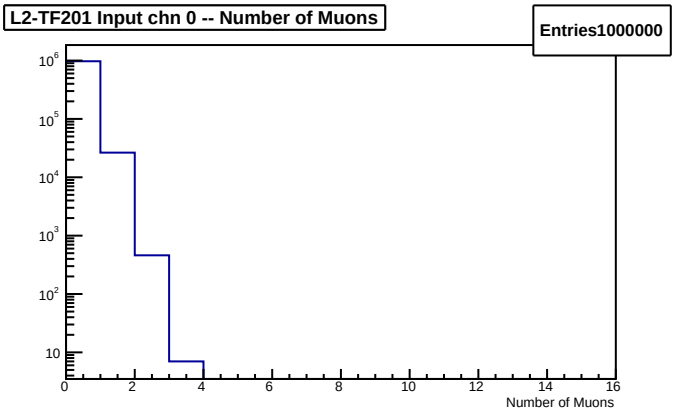

L2-TF201
Entries 0
TOF total mult(Real-Simu)

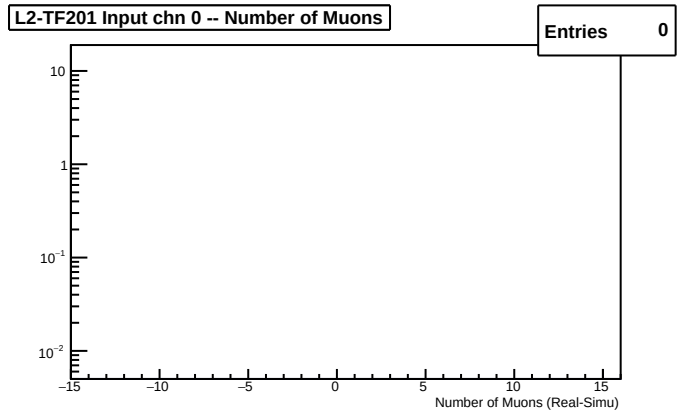

L2-TF201
Entries 7000000
Threshold/UPC bits
692383
711034
654286
686208
695812
675729
789390
307617
288966
345714
313792
304188
324271
210610
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

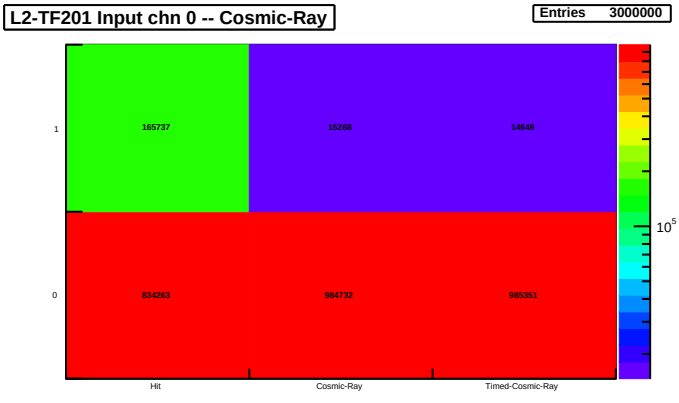

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

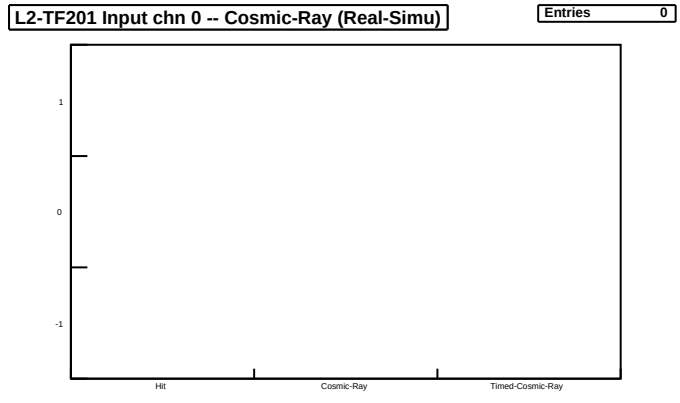

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 8000000
Fired
142829
141352
380373
387733
270518
270688
262470
286719
857171
858648
619627
612267
729482
729312
737530
713281
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

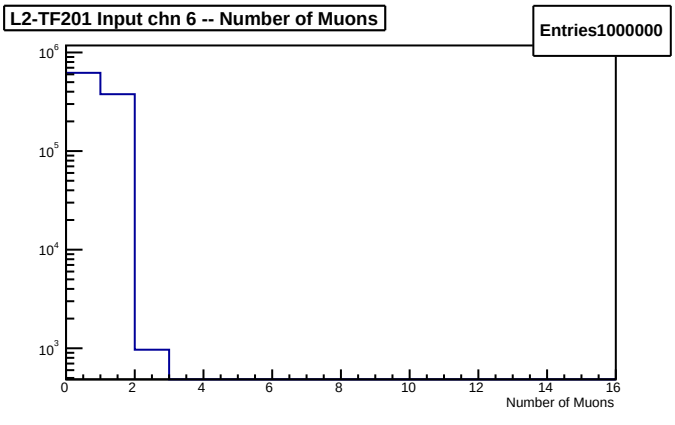

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 0
Fired(Real-Simu)
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

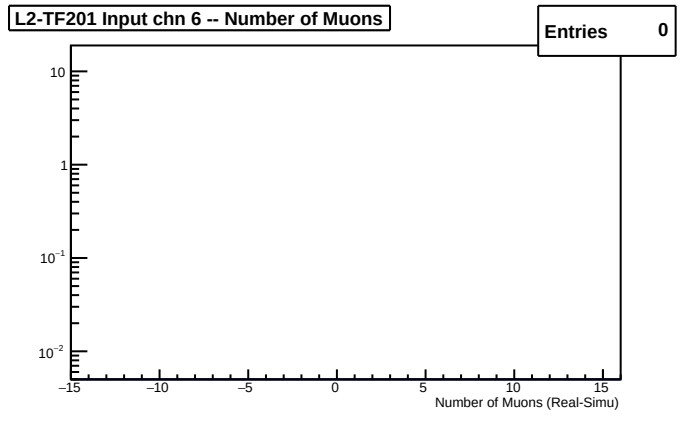

L2-TF201 Input chn 0 -- Number of Muons
Entries1000000
Number of Muons

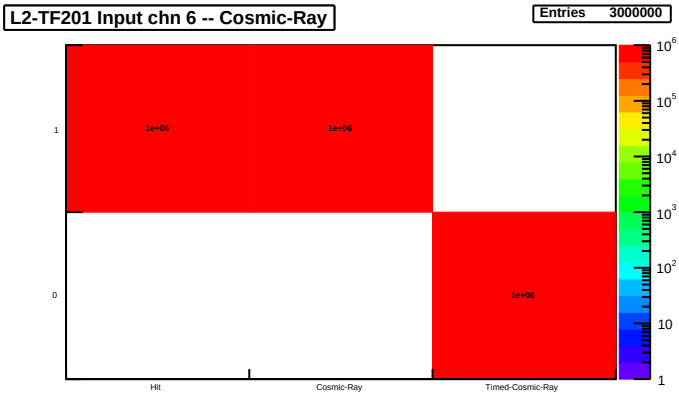

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

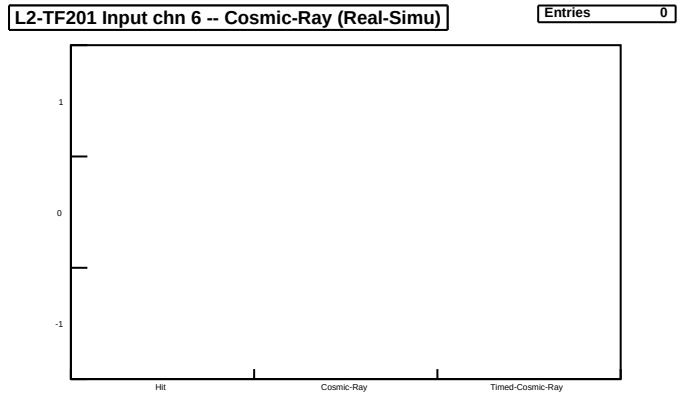

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 3000000
165737
15268
14649
834263
984732
985351
Hit
Cosmic-Ray
Timed-Cosmic-Ray

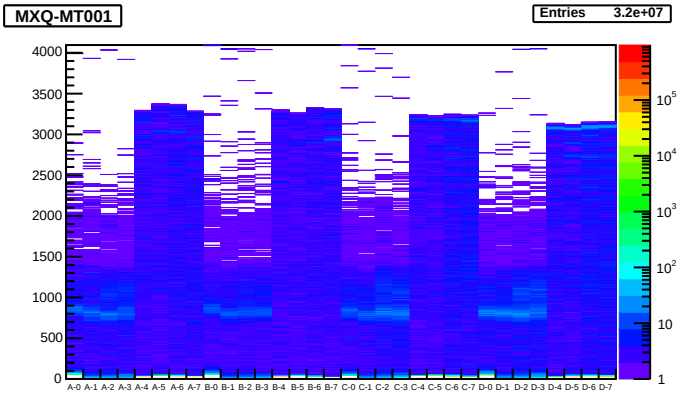

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

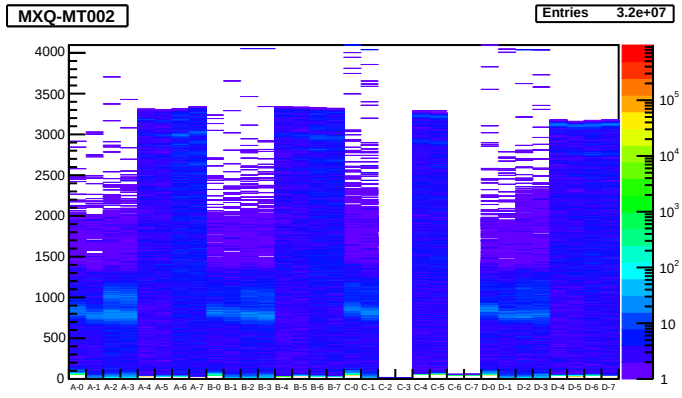

L2-TF201 Input chn 6 -- Number of Muons
Entries1000000
Number of Muons

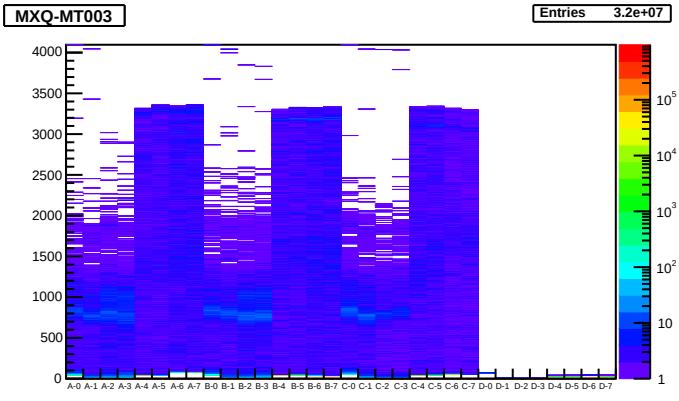

L2-TF201 Input chn 6 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

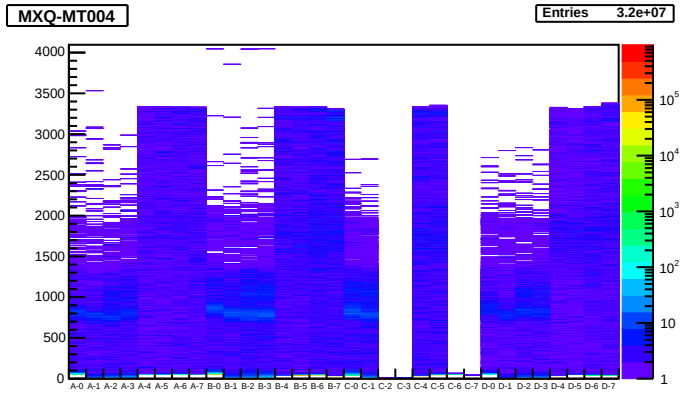

L2-TF201 Input chn 6 -- Cosmic-Ray
Entries 3000000
1e+06
1e+06
1e+06
Hit
Cosmic-Ray
Timed-Cosmic-Ray

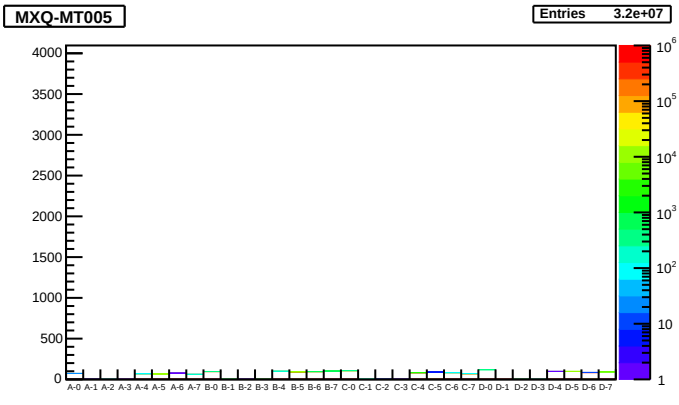

L2-TF201 Input chn 6 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

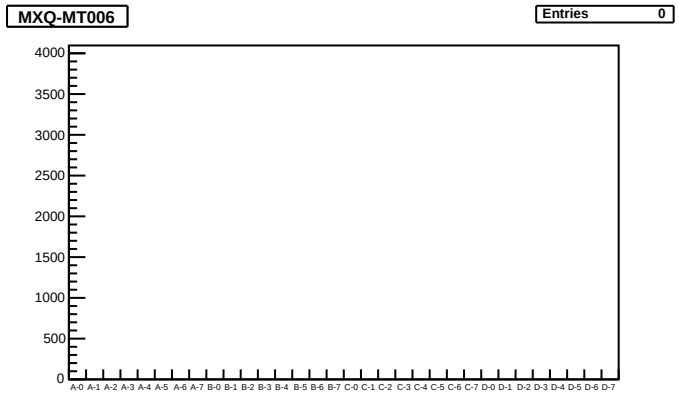

MXQ-MT001
Entries 3.2e+07

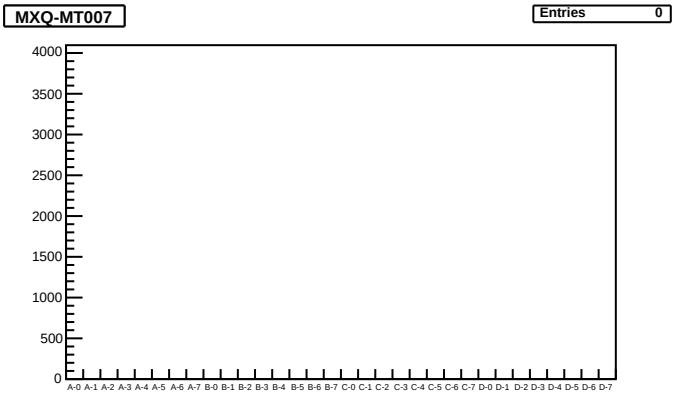

MXQ-MT002
Entries 3.2e+07

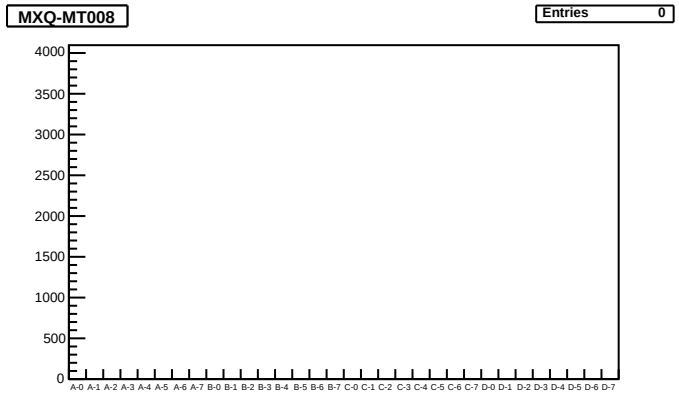

MXQ-MT003
Entries 3.2e+07

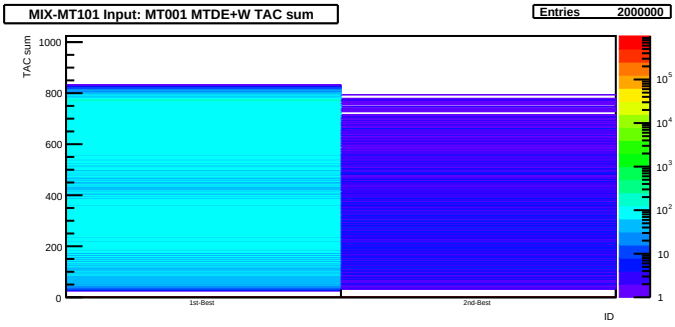

MXQ-MT004
Entries 3.2e+07

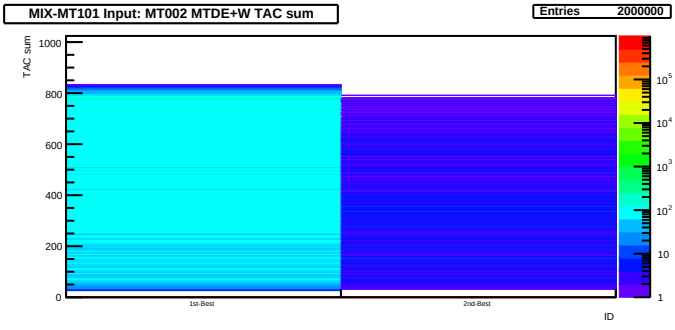

MXQ-MT005
Entries 3.2e+07

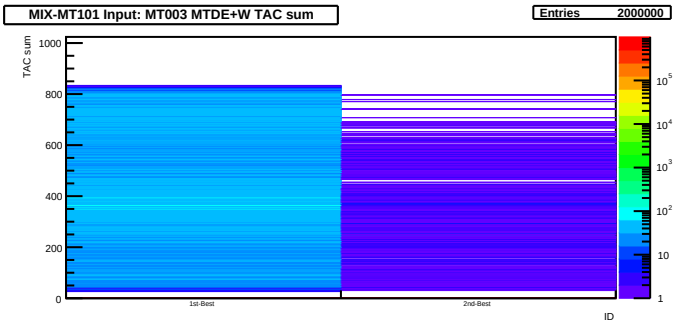

MXQ-MT006
Entries 0

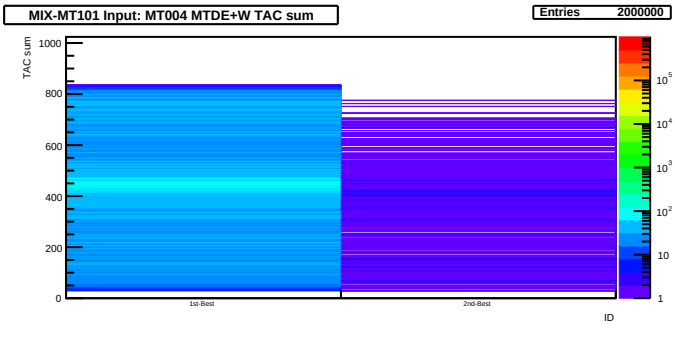

MXQ-MT007
Entries 0

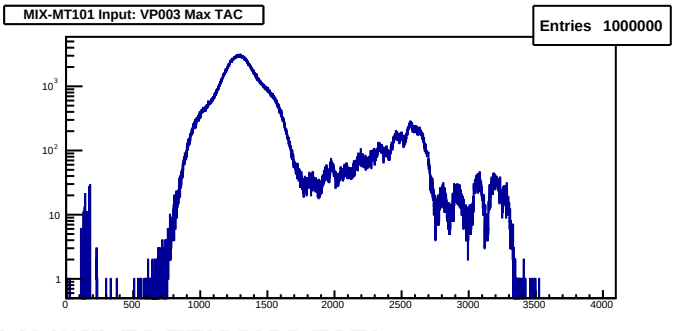

MXQ-MT008
Entries 0

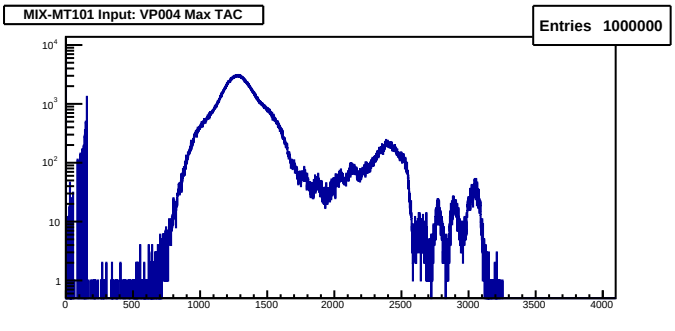

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 2000000
TAC sum
1st-Best
2nd-Best
ID

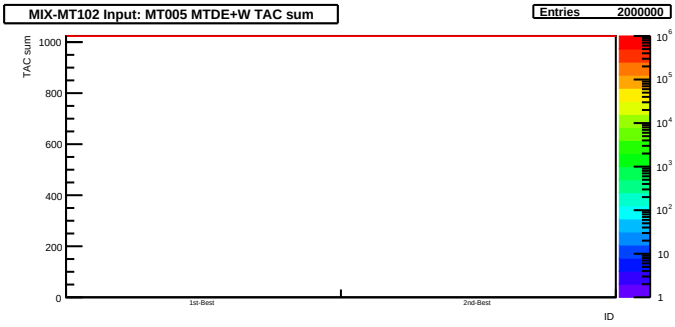

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 2000000
TAC sum
1st-Best
2nd-Best
ID

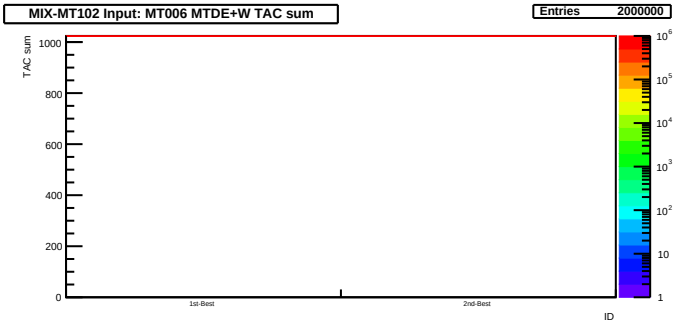

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 2000000
TAC sum
1st-Best
2nd-Best
ID

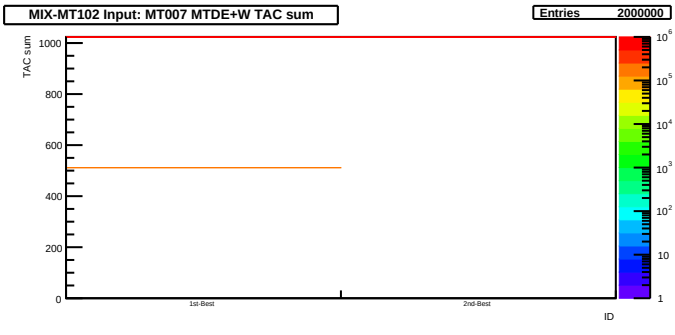

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 2000000
TAC sum
1st-Best
2nd-Best
ID

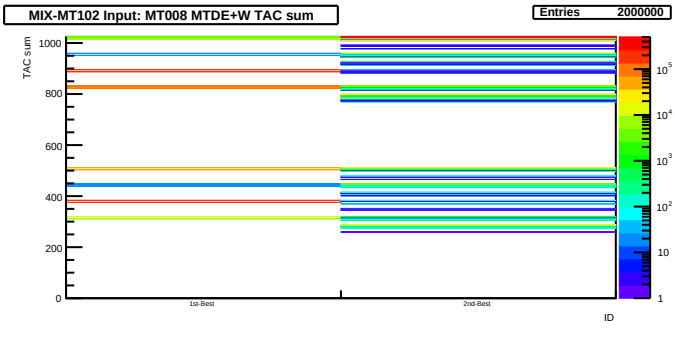

MIX-MT101 Input: VP003 Max TAC
Entries 1000000

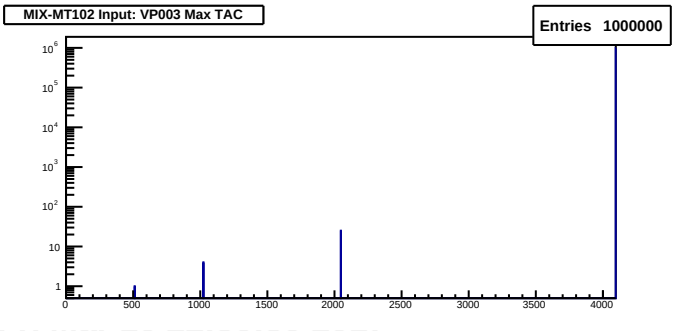

MIX-MT101 Input: VP004 Max TAC
Entries 1000000

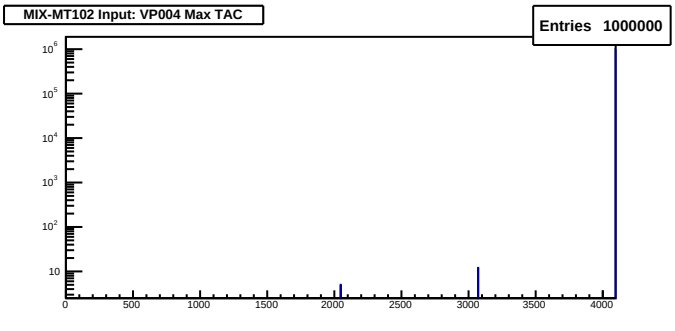

MIX-MT102 Input: MT005 MTDE+W TAC sum
Entries 2000000
TAC sum
1st-Best
2nd-Best
ID

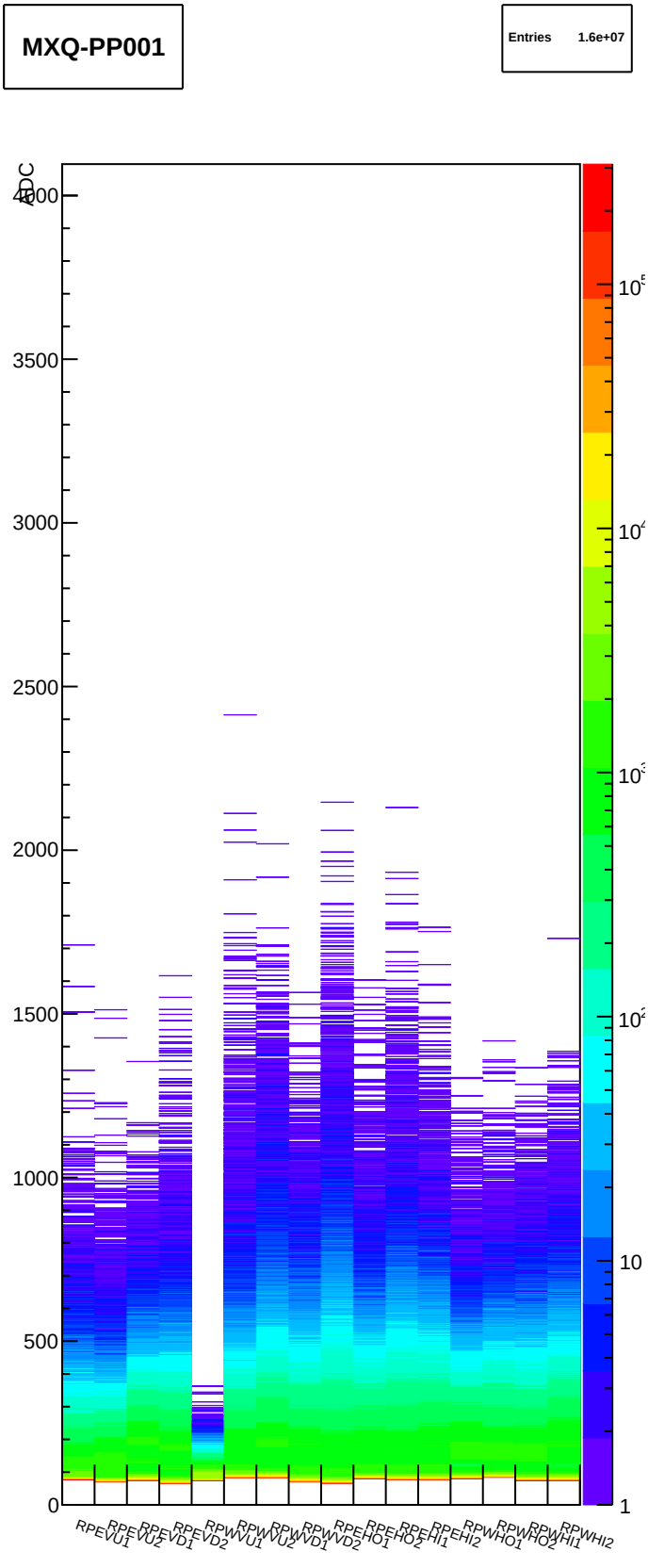

MIX-MT102 Input: MT006 MTDE+W TAC sum
Entries 2000000
TAC sum
1st-Best
2nd-Best
ID

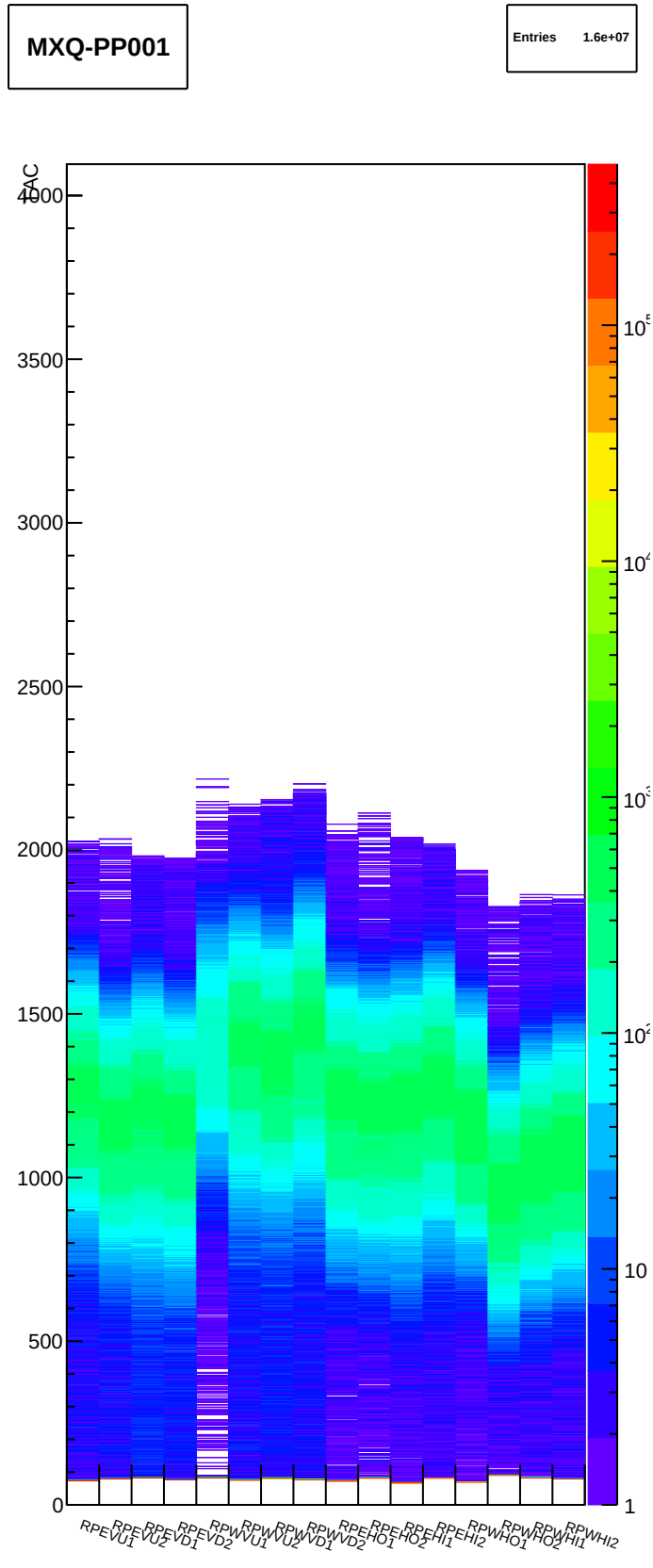

MIX-MT102 Input: MT007 MTDE+W TAC sum
Entries 2000000
TAC sum
1st-Best
2nd-Best
ID

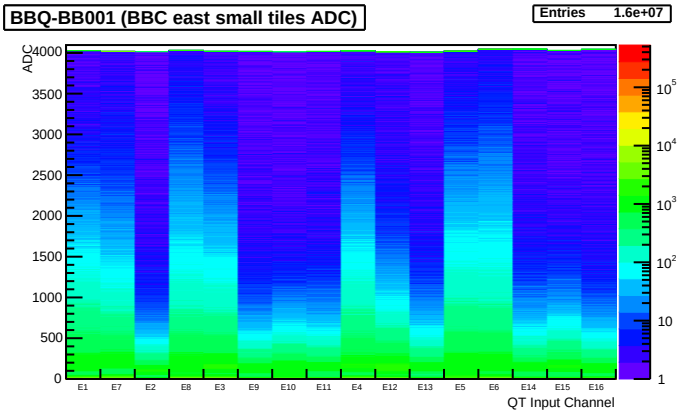

MIX-MT102 Input: MT008 MTDE+W TAC sum
Entries 2000000
TAC sum
1st-Best
2nd-Best
ID

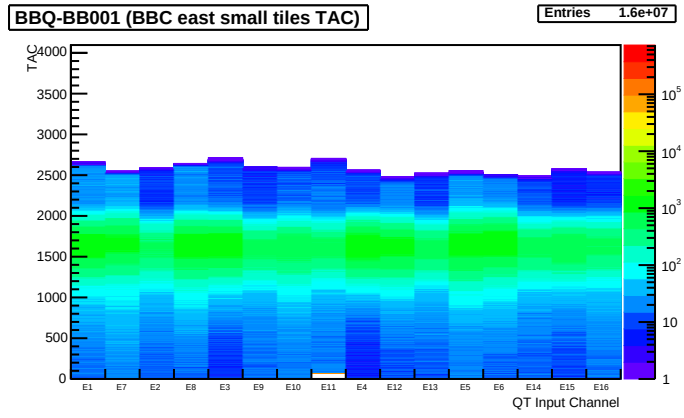

MIX-MT102 Input: VP003 Max TAC
Entries 1000000

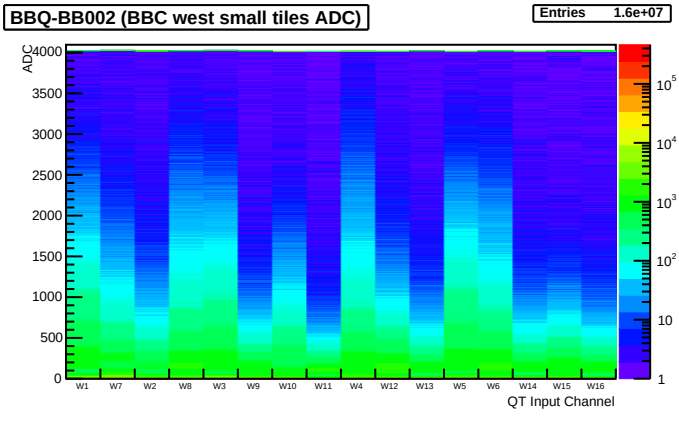

MIX-MT102 Input: VP004 Max TAC
Entries 1000000

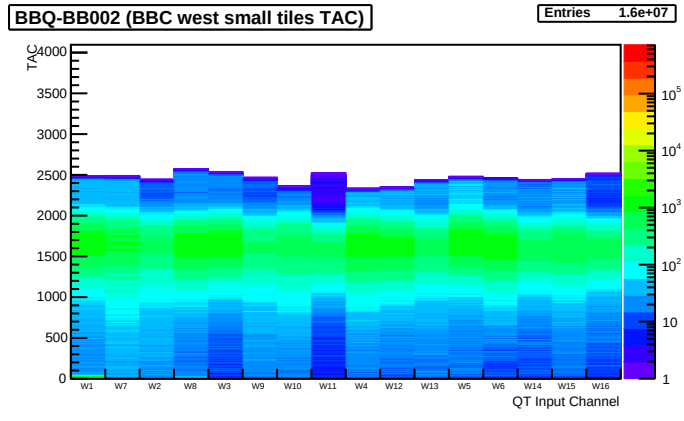

MXQ-PP001
Entries 1.6e+07
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10^5
10^4
10^3
10^2
10
1

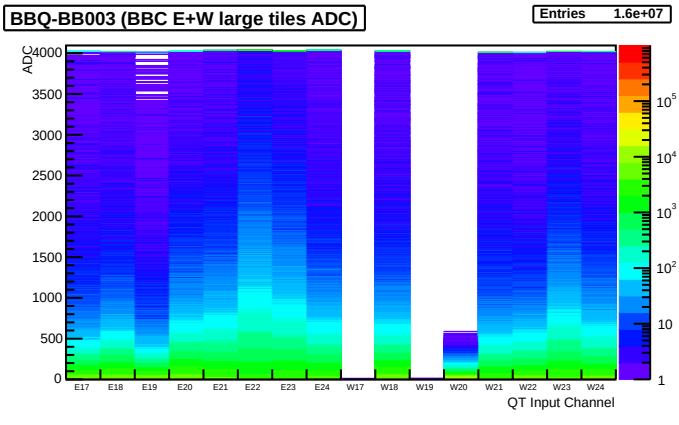

MXQ-PP001
Entries 1.6e+07
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10^5
10^4
10^3
10^2
10
1

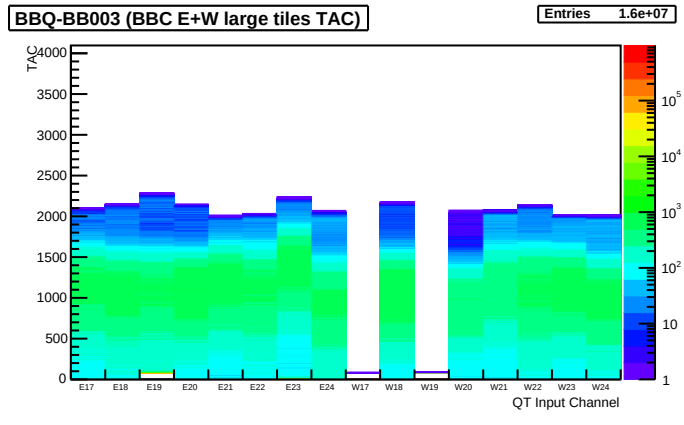

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.6e+07
ADC
QT Input Channel

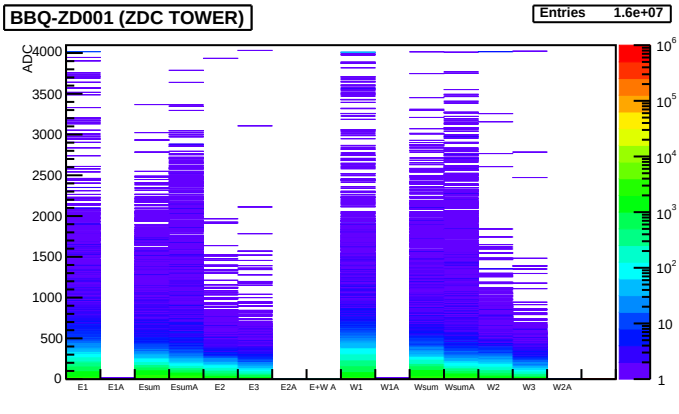

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.6e+07
TAC
QT Input Channel

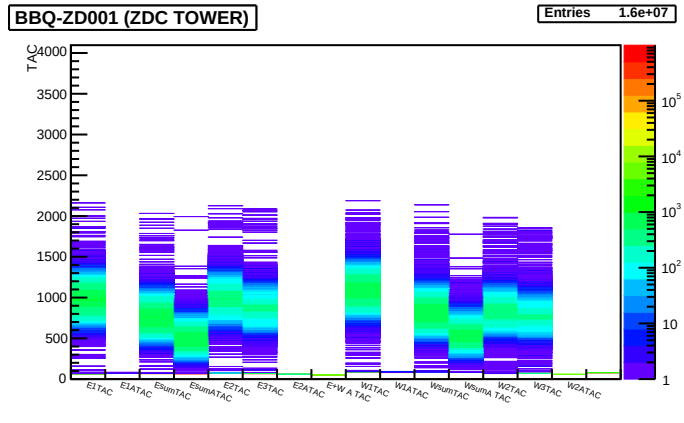

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.6e+07
ADC
QT Input Channel

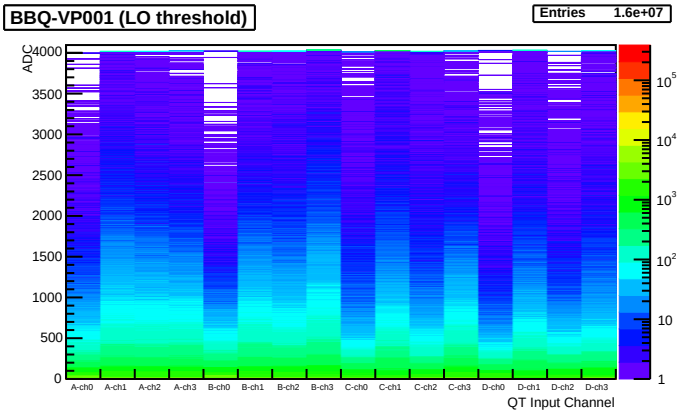

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.6e+07
TAC
QT Input Channel

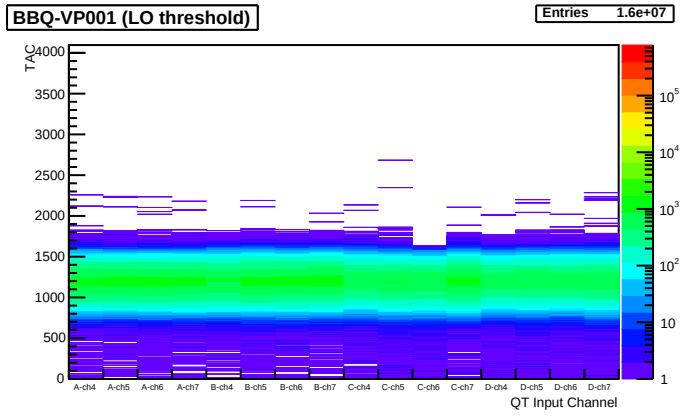

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.6e+07
ADC
QT Input Channel

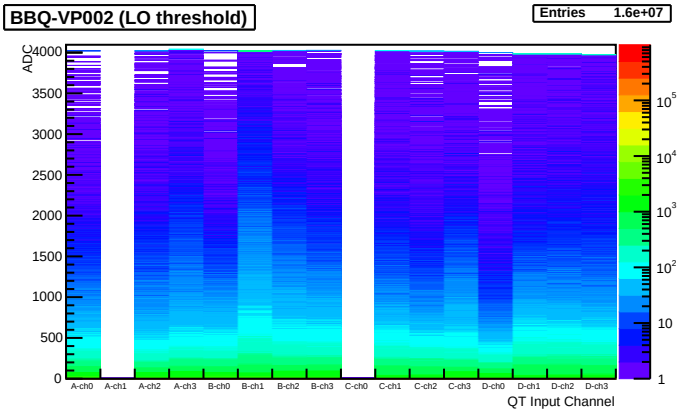

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.6e+07
TAC
QT Input Channel

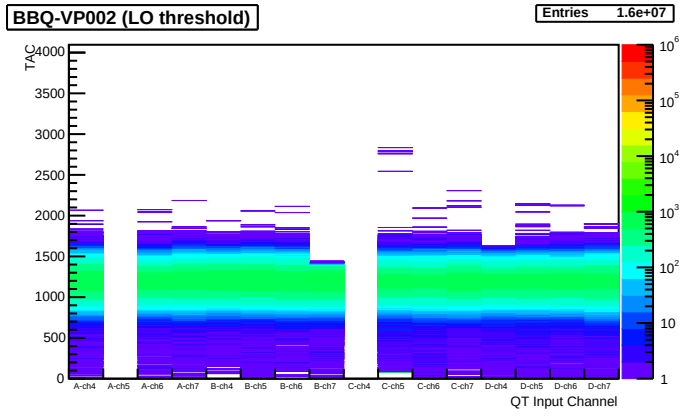

BBQ-ZD001 (ZDC TOWER)
Entries 1.6e+07
ADC

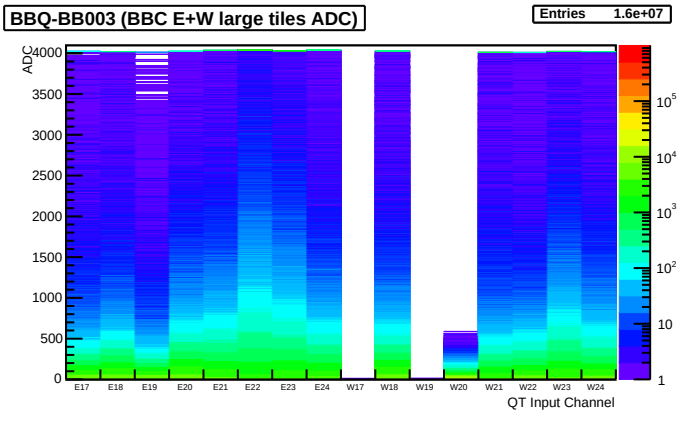

BBQ-ZD001 (ZDC TOWER)
Entries 1.6e+07
TAC

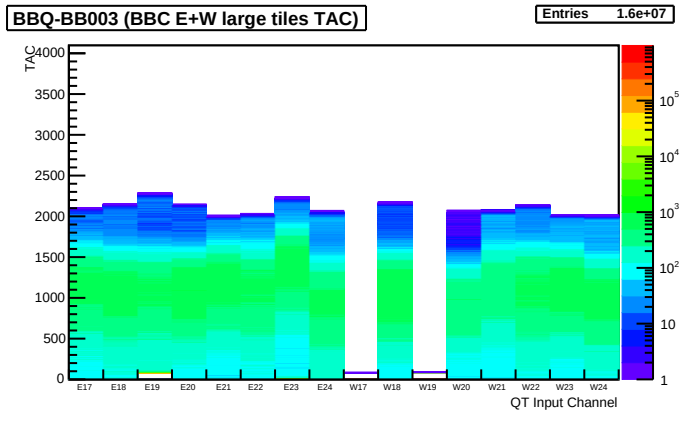

BBQ-VP001 (LO threshold)
Entries 1.6e+07
ADC
QT Input Channel

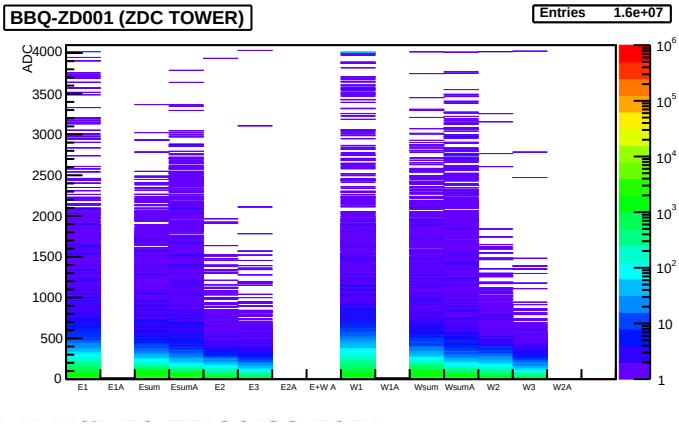

BBQ-VP001 (LO threshold)
Entries 1.6e+07
TAC
QT Input Channel

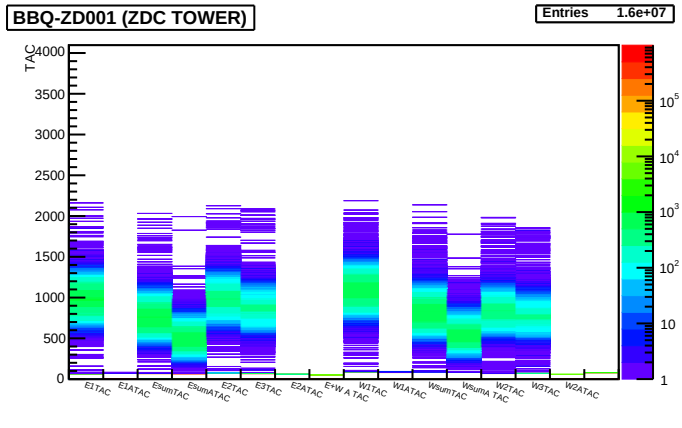

BBQ-VP002 (LO threshold)
Entries 1.6e+07
ADC
QT Input Channel

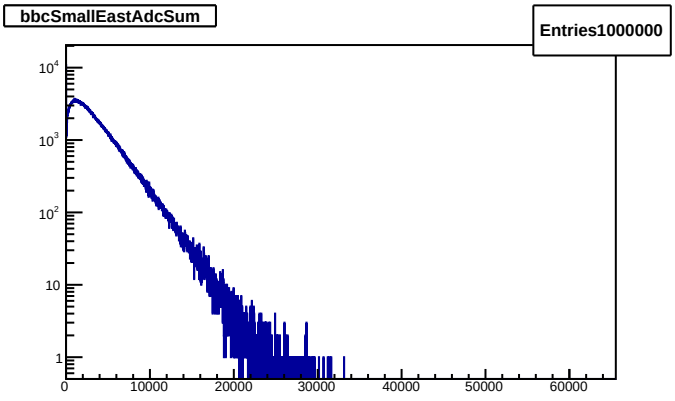

BBQ-VP002 (LO threshold)
Entries 1.6e+07
TAC
QT Input Channel

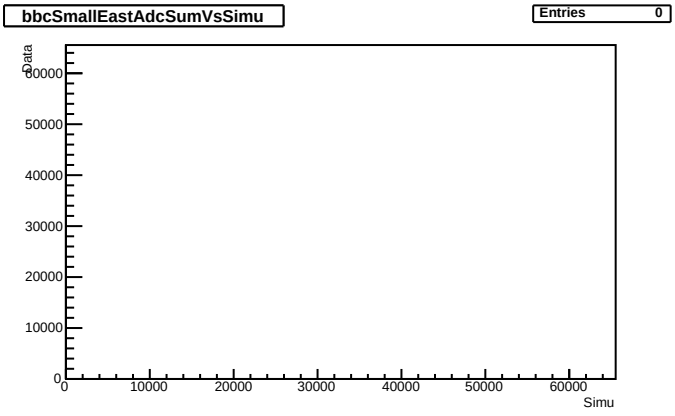

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.6e+07
ADC
QT Input Channel

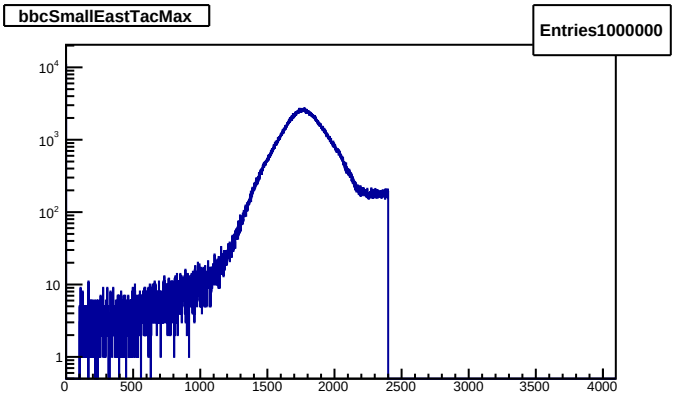

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.6e+07
TAC
QT Input Channel

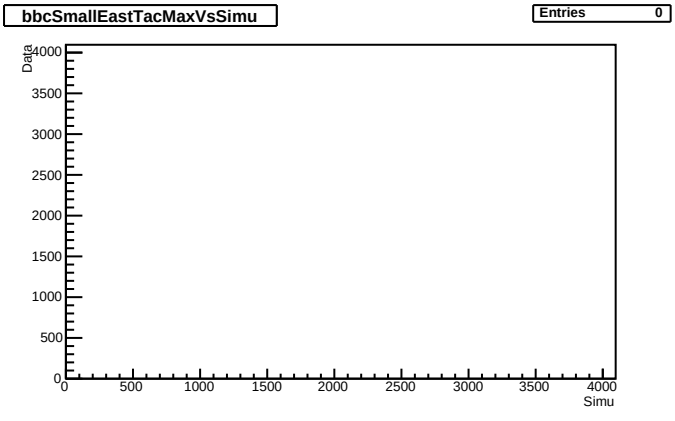

BBQ-ZD001 (ZDC TOWER)
Entries 1.6e+07
ADC

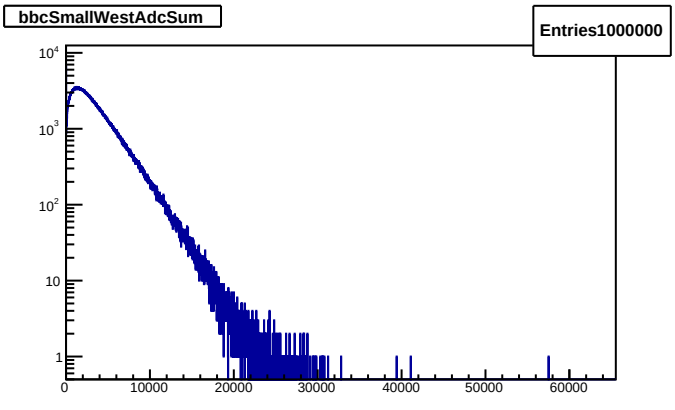

BBQ-ZD001 (ZDC TOWER)
Entries 1.6e+07
TAC

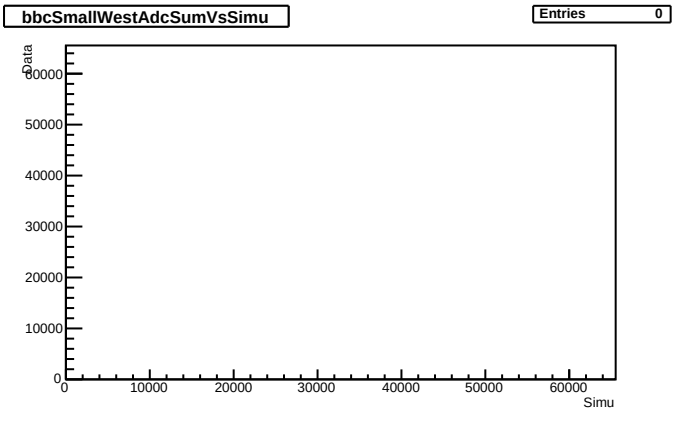

bbcSmallEastAdcSum
Entries1000000

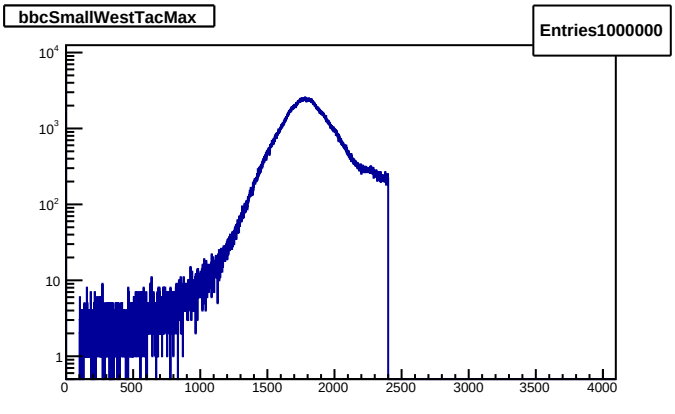

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

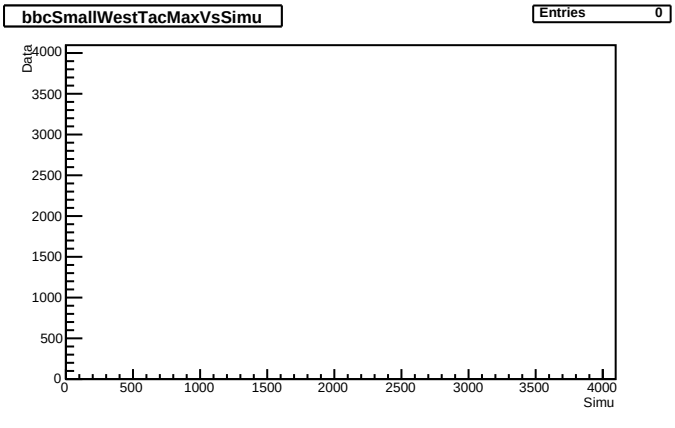

bbcSmallEastTacMax
Entries1000000

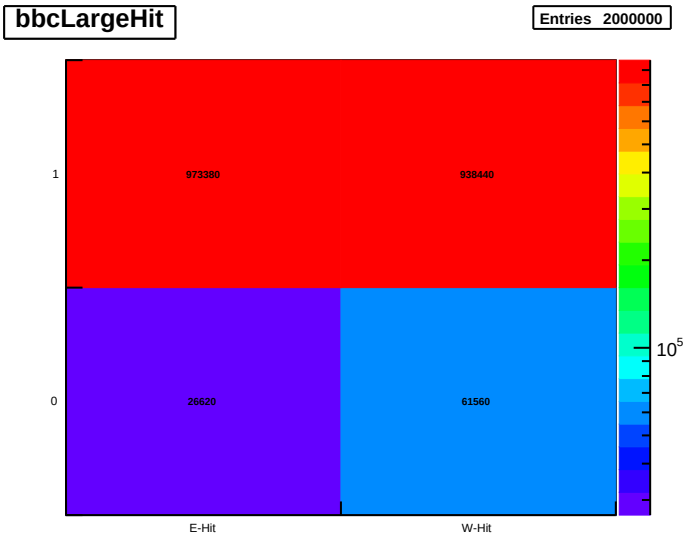

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

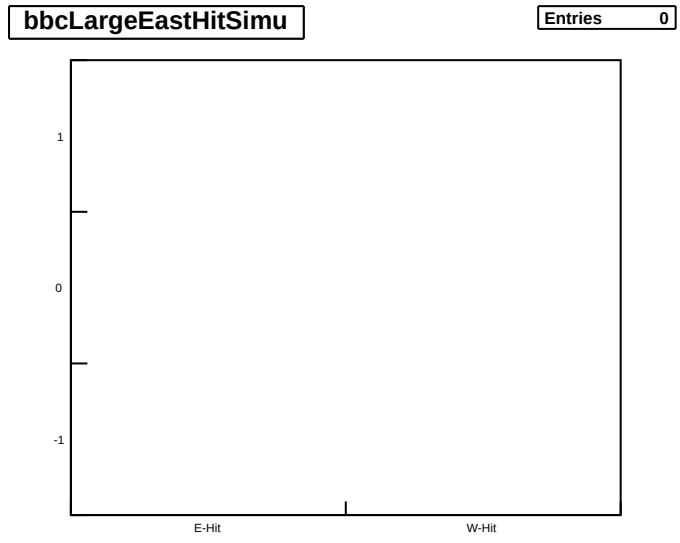

bbcSmallWestAdcSum
Entries1000000

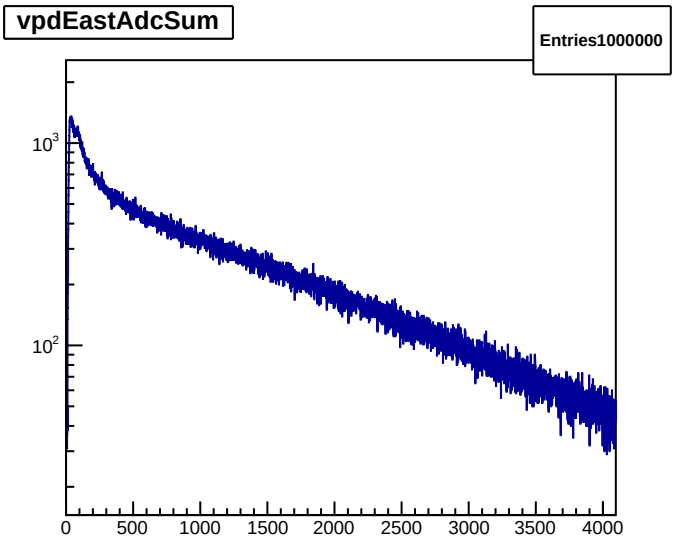

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

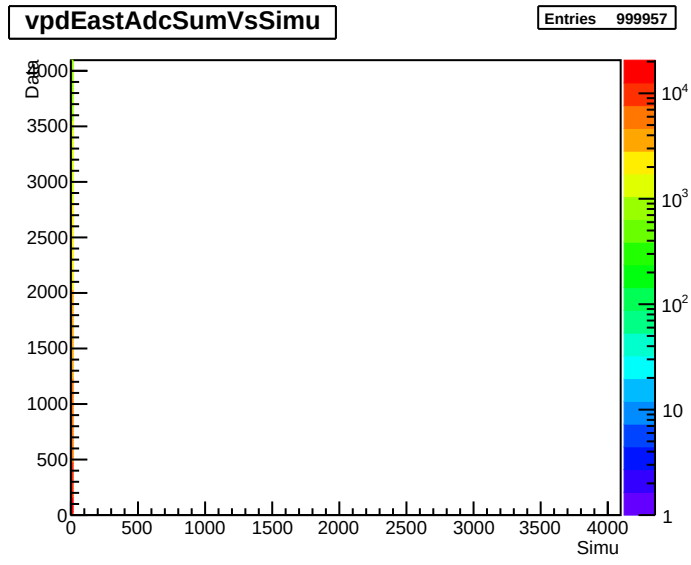

bbcSmallWestTacMax
Entries1000000

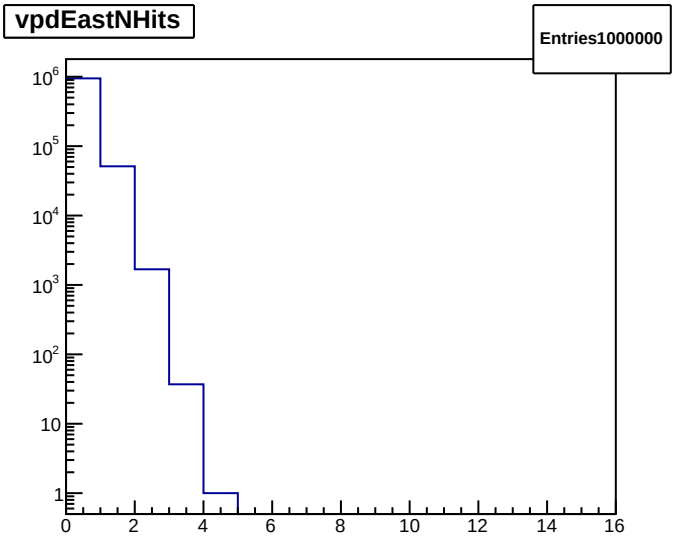

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

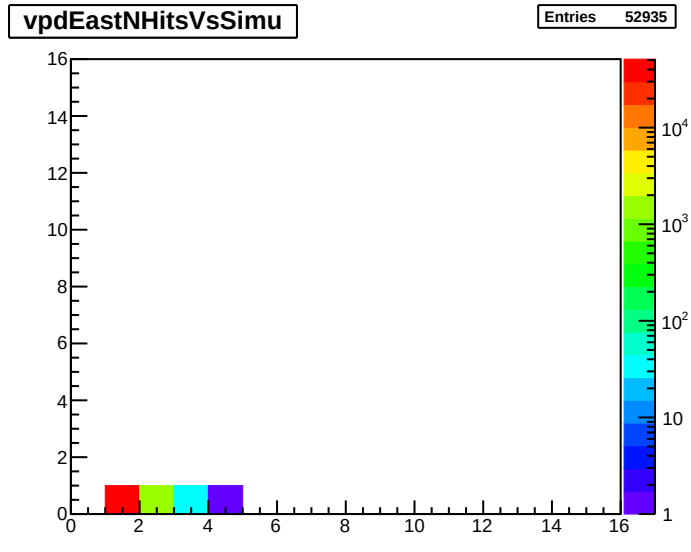

bbcLargeHit
Entries 2000000
973380
938440
26620
61560
1
0
E-Hit
W-Hit
10^5

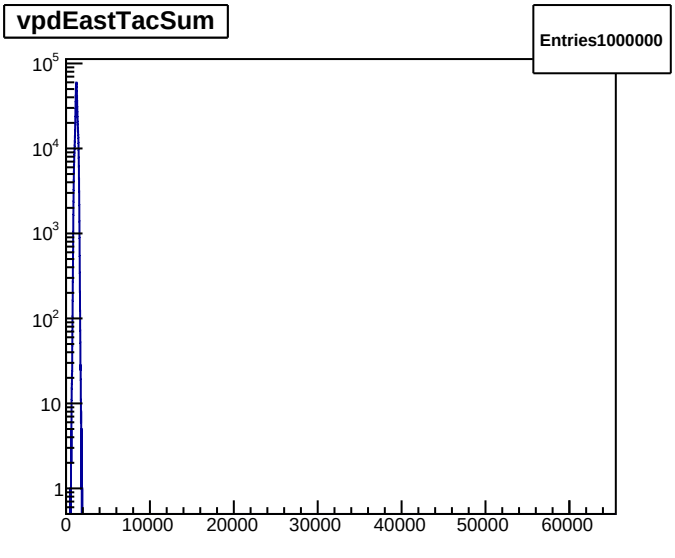

bbcLargeEastHitSimu
Entries 0
1
0
-1
E-Hit
W-Hit

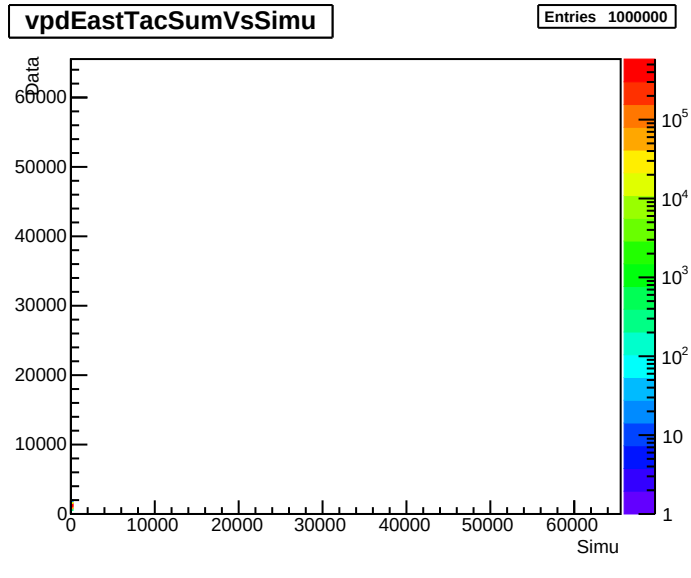

vpdEastAdcSum
Entries1000000

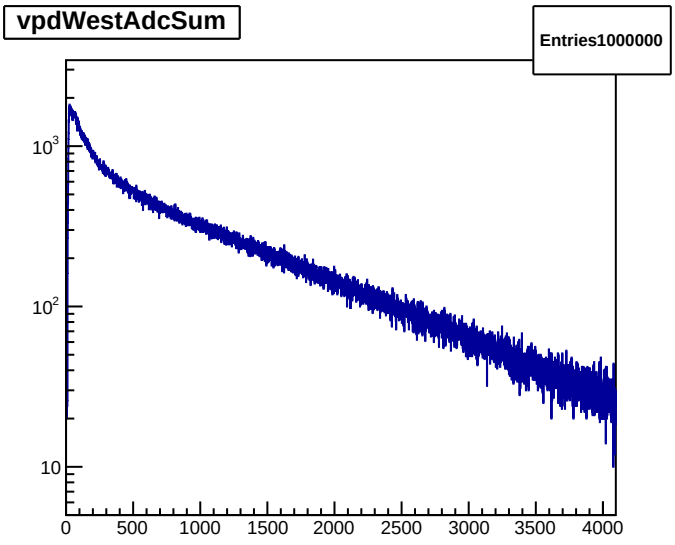

vpdEastAdcSumVsSimu
Entries 999957
Data
Simu

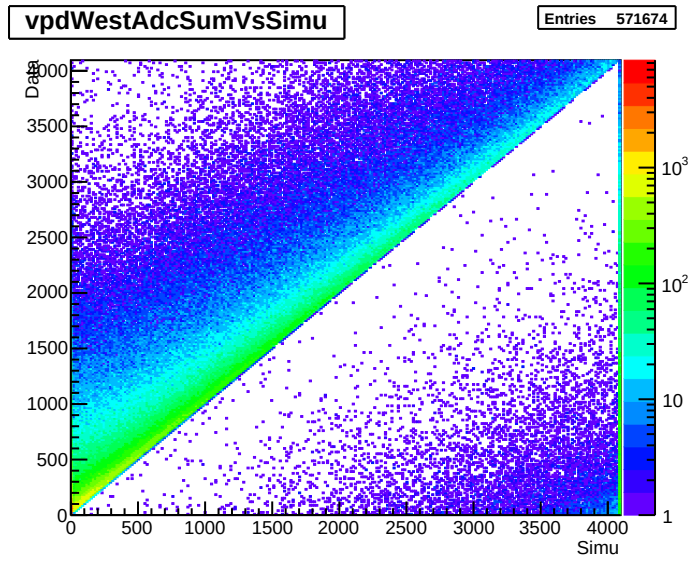

vpdEastNHits
Entries1000000

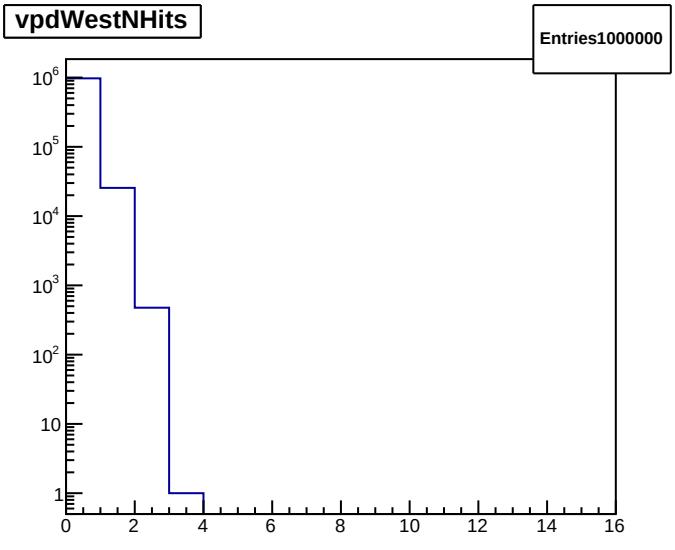

vpdEastNHitsVsSimu
Entries 52935

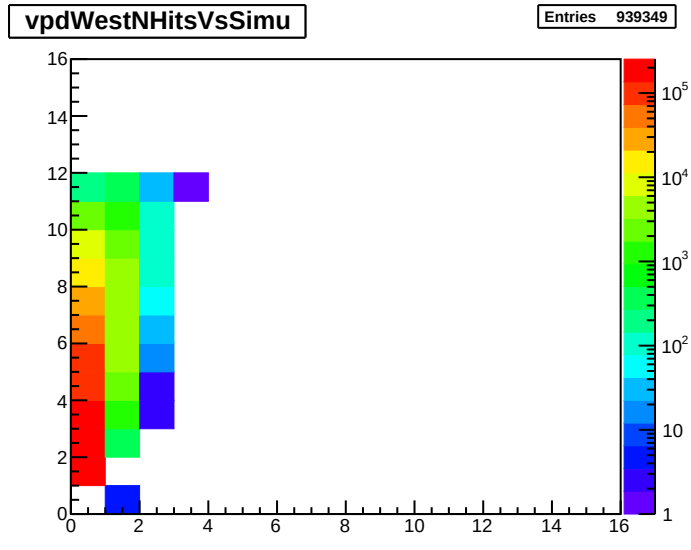

vpdEastTacSum
Entries1000000

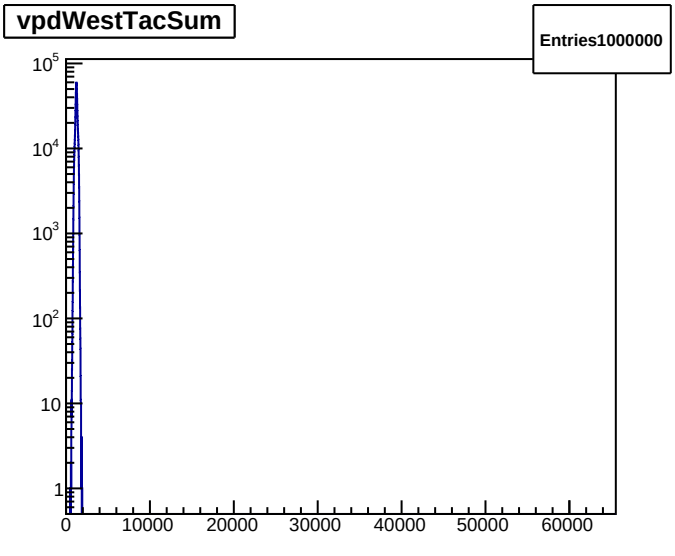

vpdEastTacSumVsSimu
Entries 1000000
Data
Simu

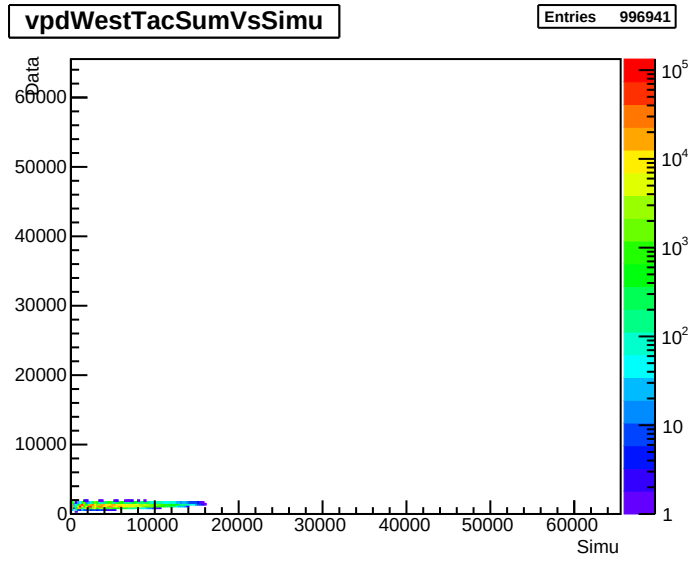

vpdWestAdcSum
Entries1000000

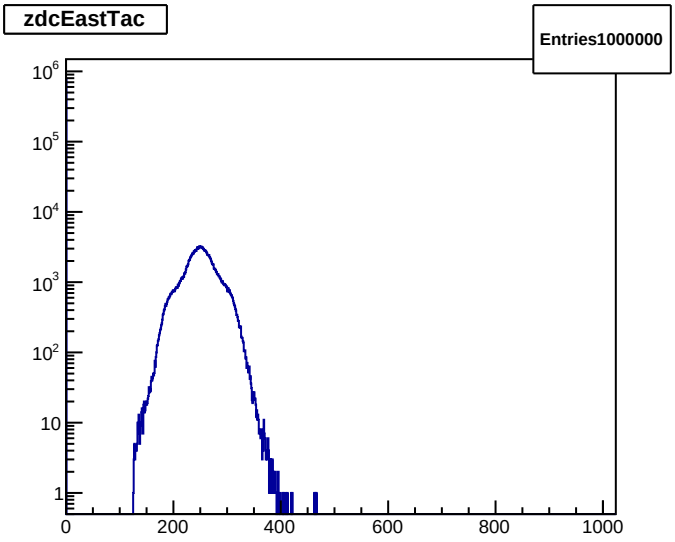

vpdWestAdcSumVsSimu
Entries 571674
Data
Simu

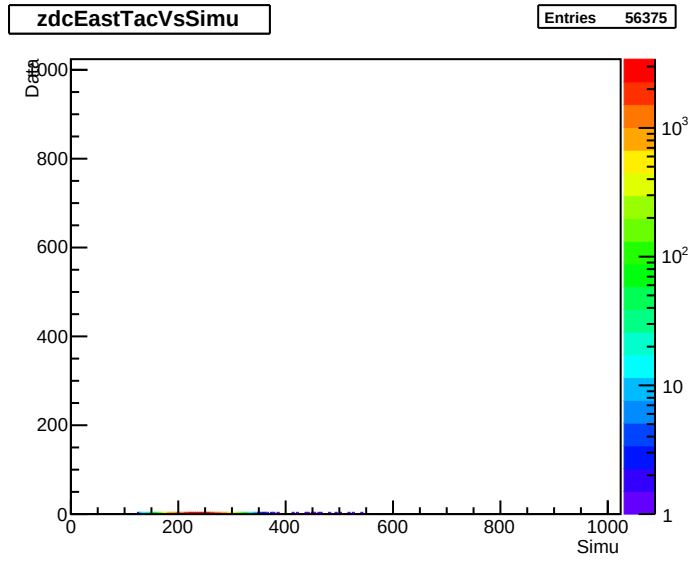

vpdWestNHits
Entries1000000

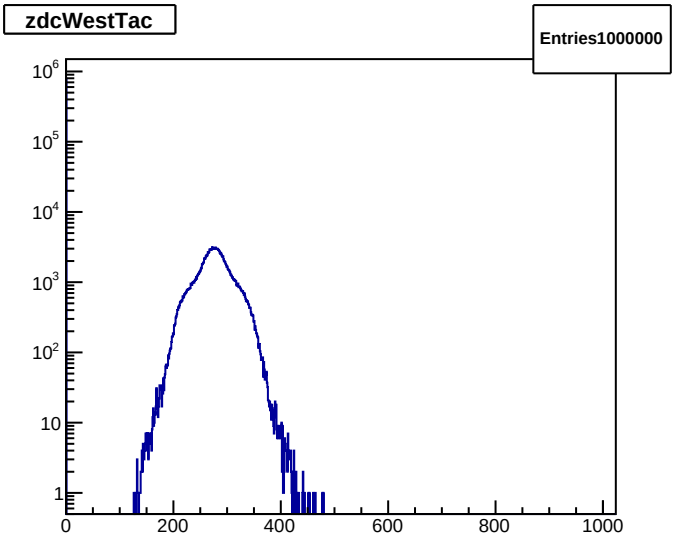

vpdWestNHitsVsSimu
Entries 939349

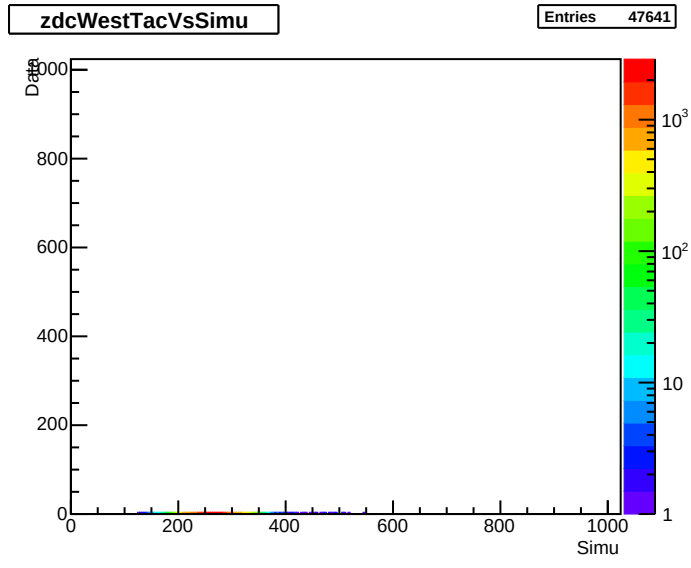

vpdWestTacSum
Entries1000000

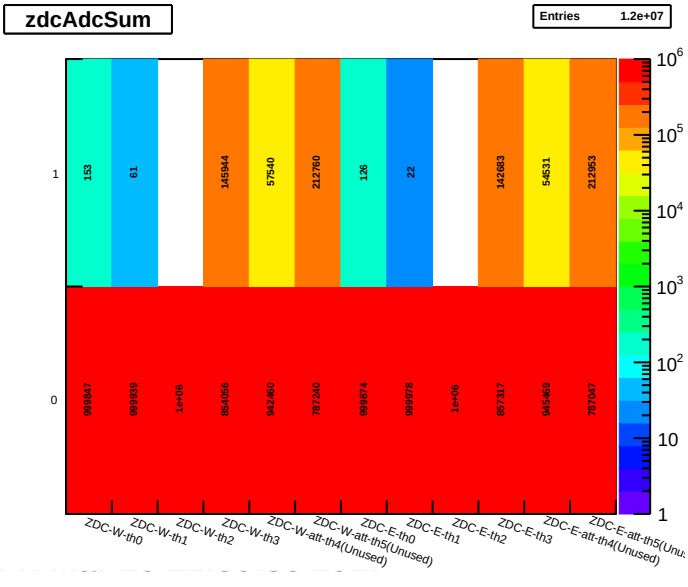

vpdWestTacSumVsSimu
Entries 996941
Data
Simu

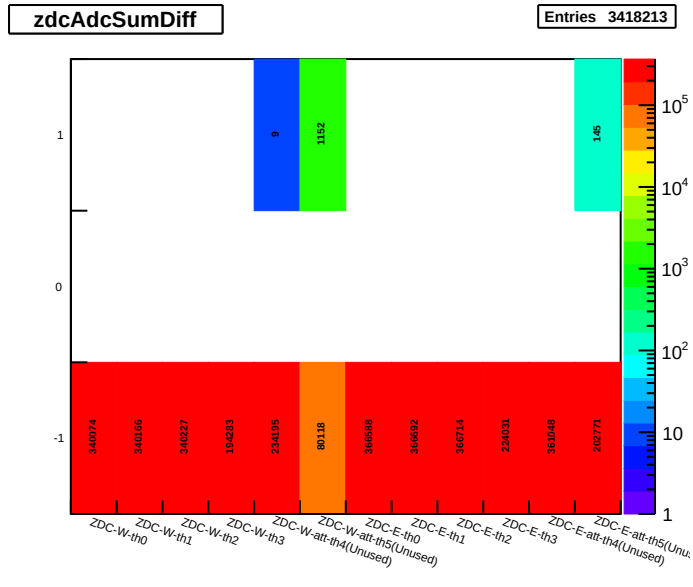

zdcEastTac
Entries1000000

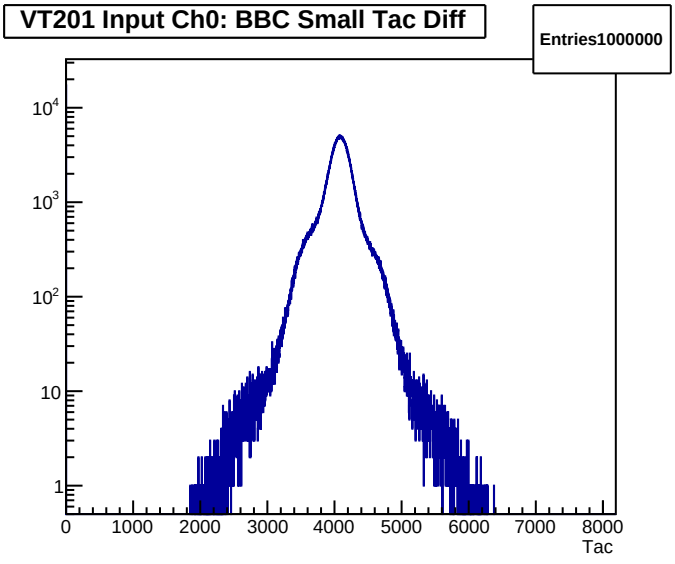

zdcEastTacVsSimu
Entries 56375
Data
Simu

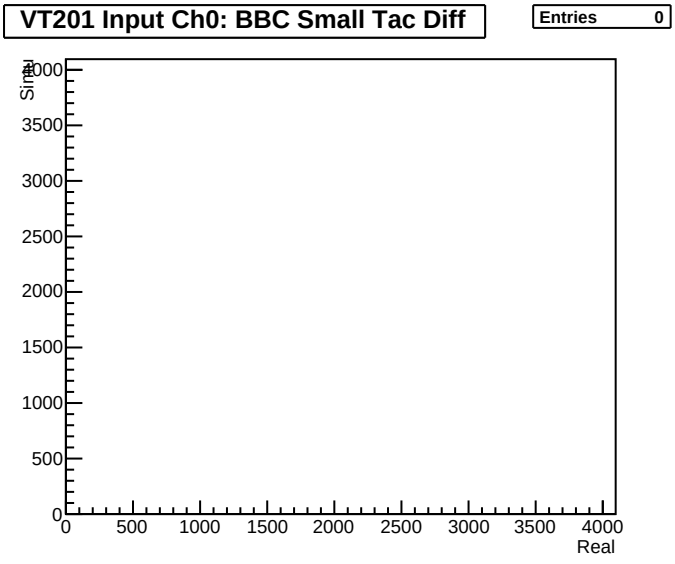

zdcWestTac
Entries1000000

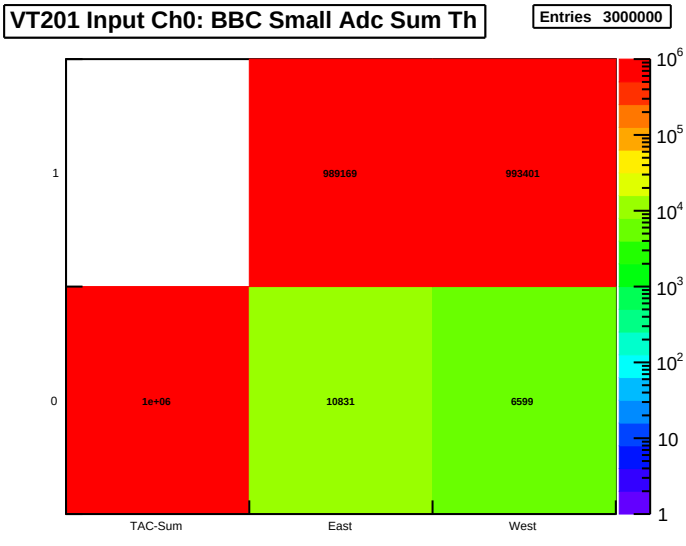

zdcWestTacVsSimu
Entries 47641
Data
Simu

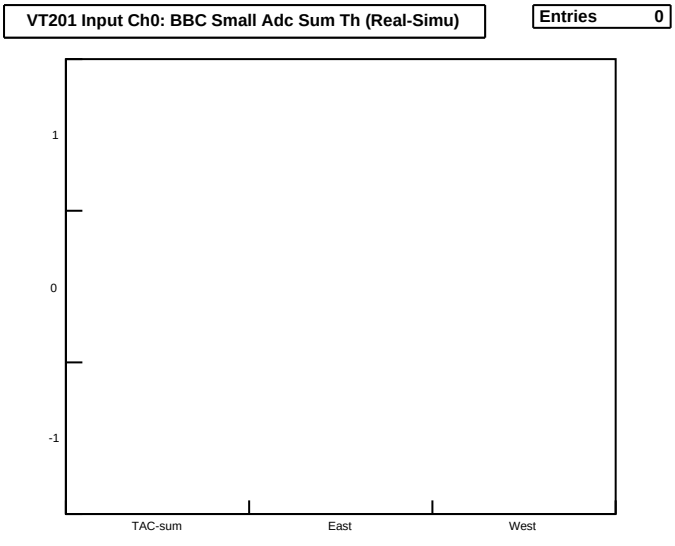

zdcAdcSum
Entries 1.2e+07
153
61
145944
57540
212760
126
22
142683
54531
212953
999847
999939
1e+06
854056
942460
787240
999874
999978
1e+06
857317
945469
787047
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

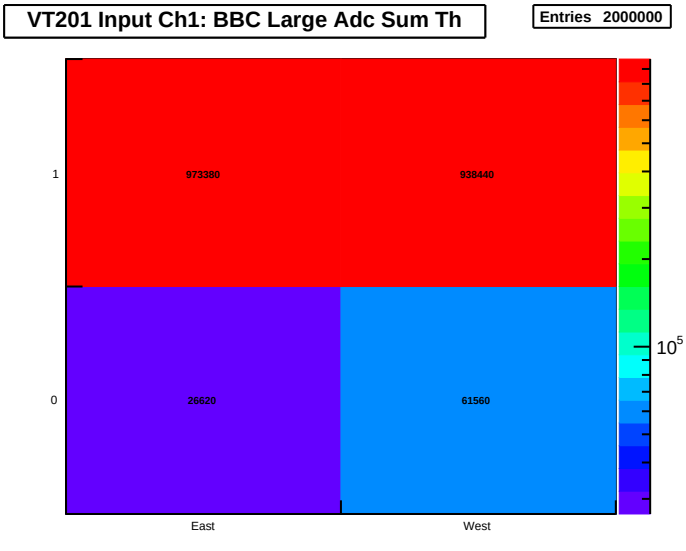

zdcAdcSumDiff
Entries 3418213
9
1152
145
340074
340166
340227
194283
234195
80118
366598
366692
366714
224031
361048
202771
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

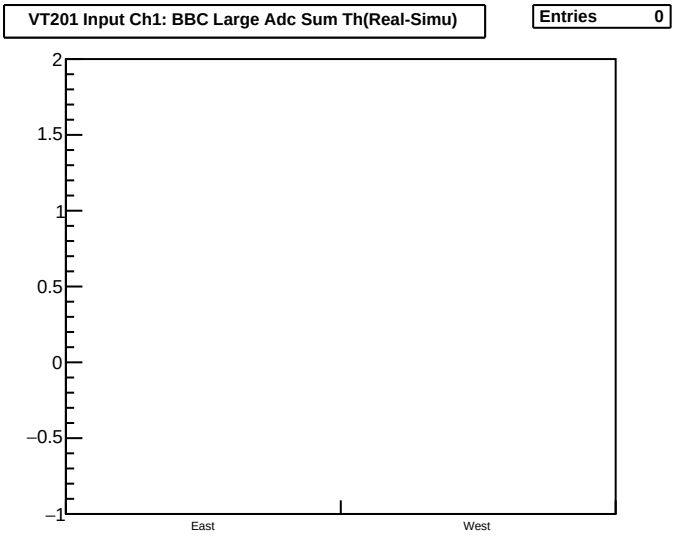

VT201 Input Ch0: BBC Small Tac Diff
Entries1000000
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

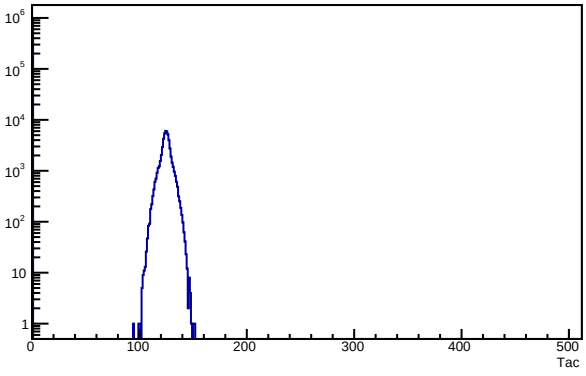

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 3000000
989169
993401
1e+06
10831
6599
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

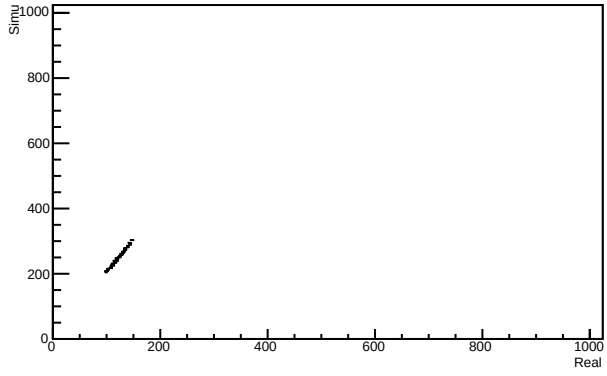

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 2000000
973380
938440
26620
61560
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

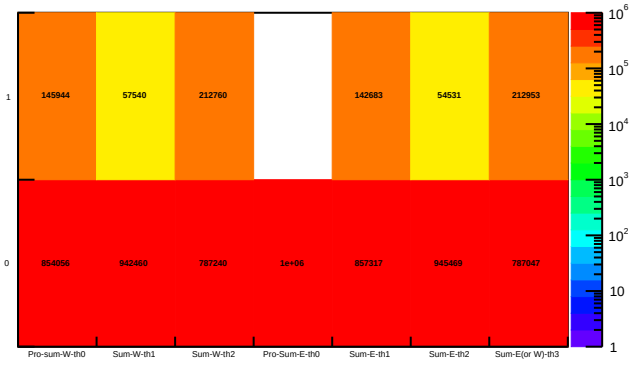
VT201 Input Ch2: ZDC Tac Diff



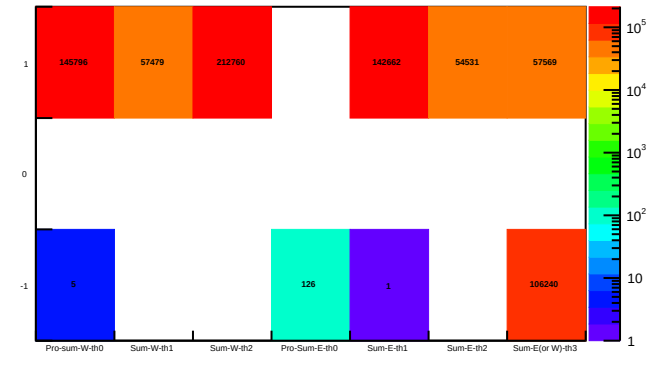
VT201 Input Ch2: ZDC Tac Diff



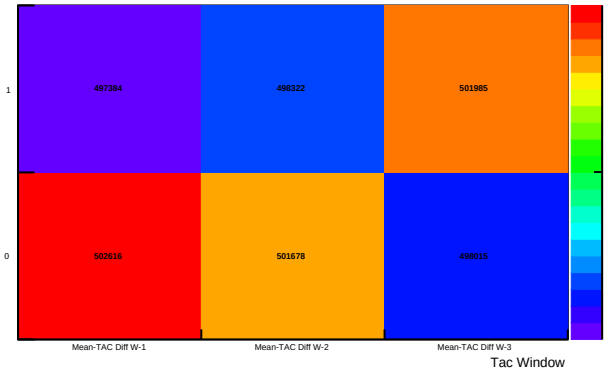
VT201 Input Ch2: ZDC Adc Sum Th



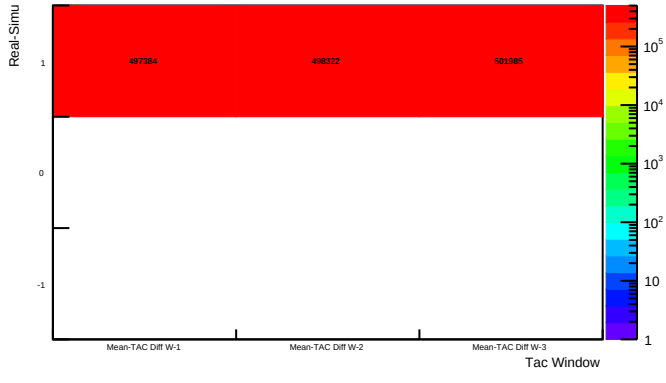
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



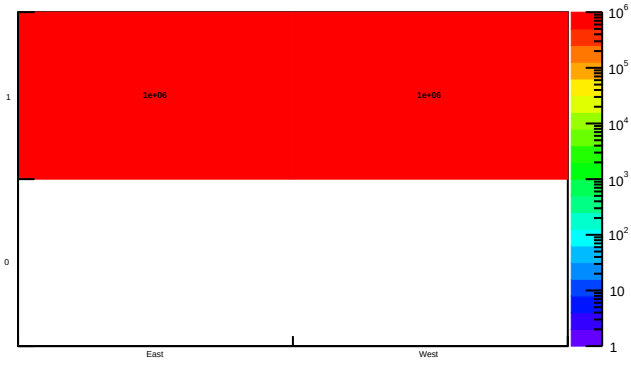
VT201 Input Ch4: VPD Tac Diff



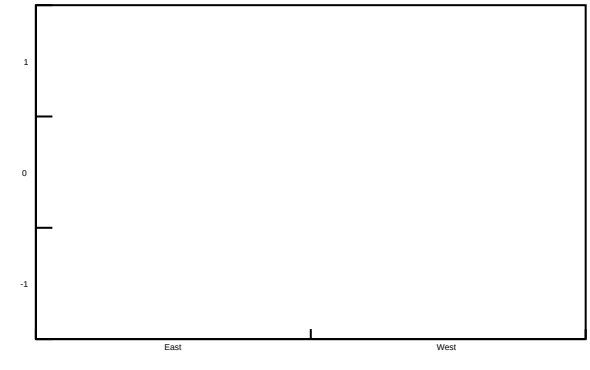
VT201 Input Ch4: VPD Tac Diff

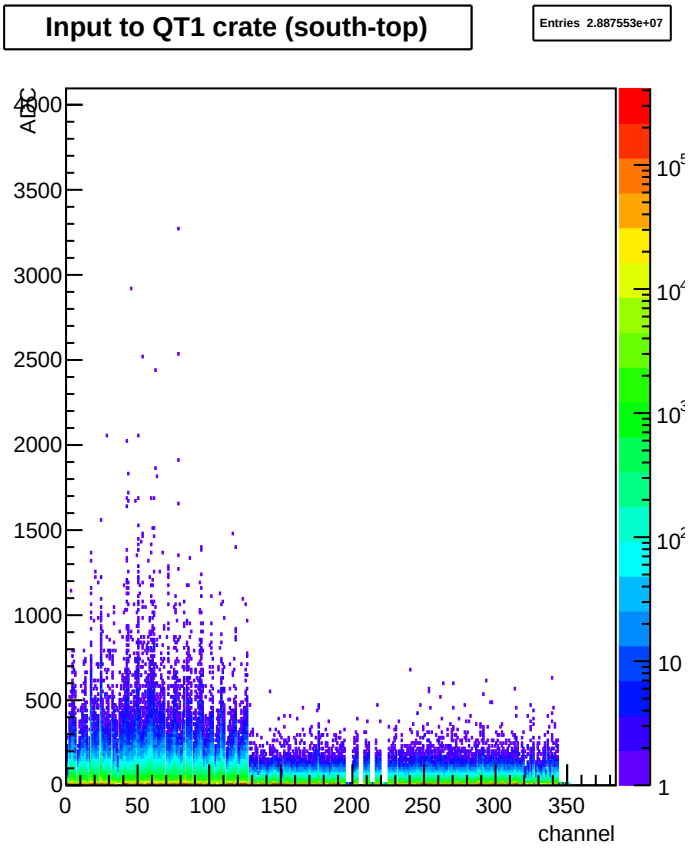


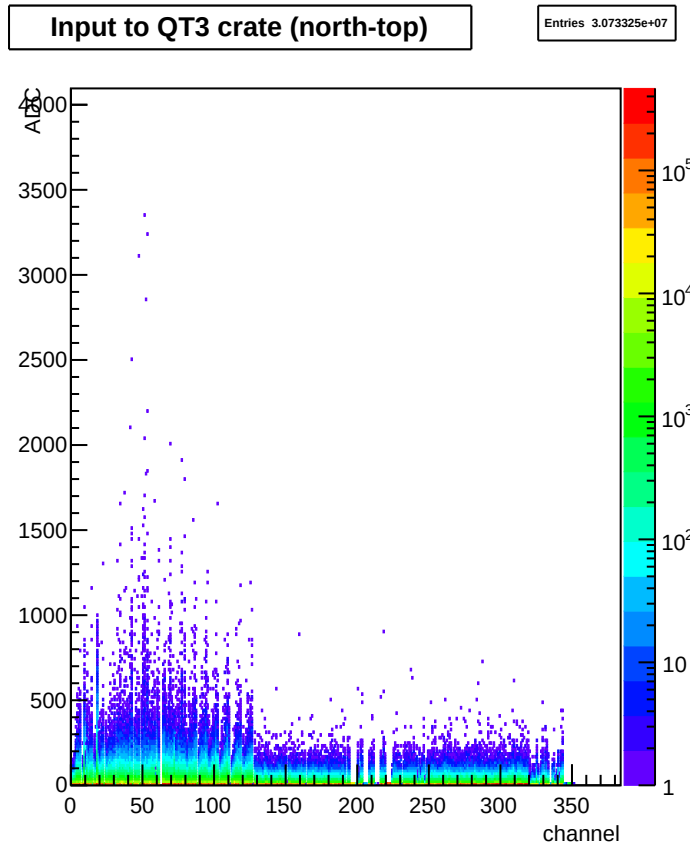
VT201 Input Ch4: VPD Adc Sum Th

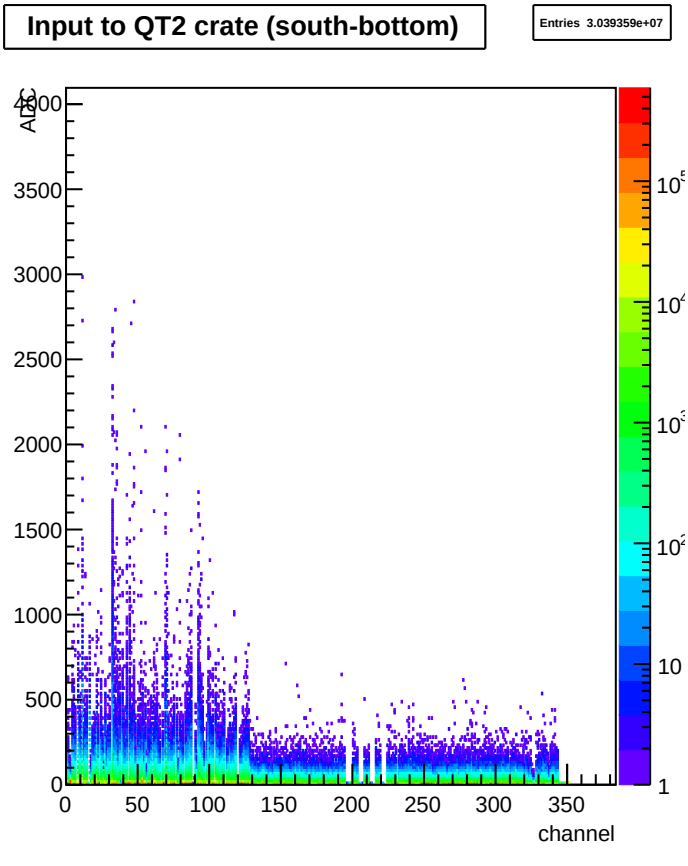


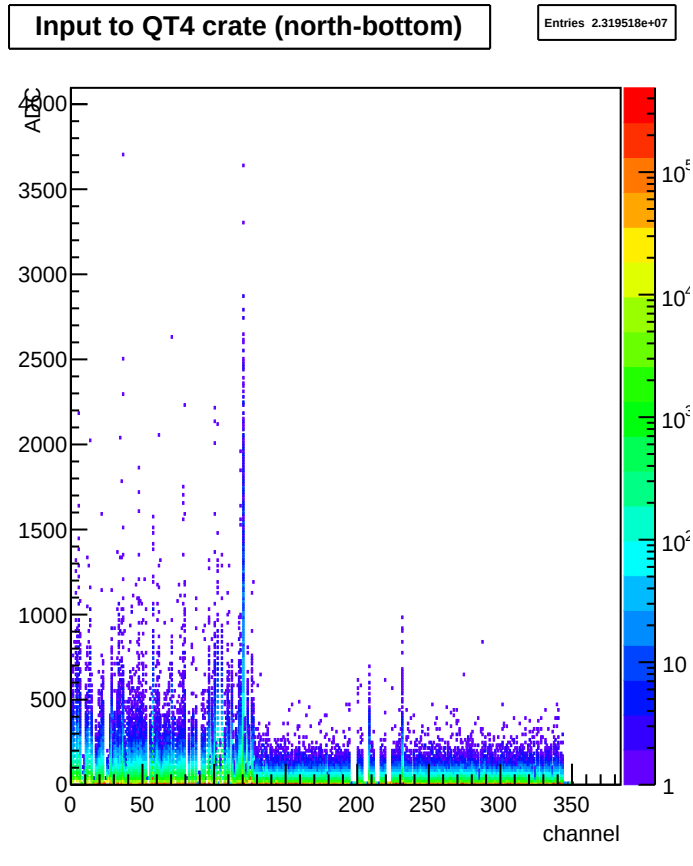
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

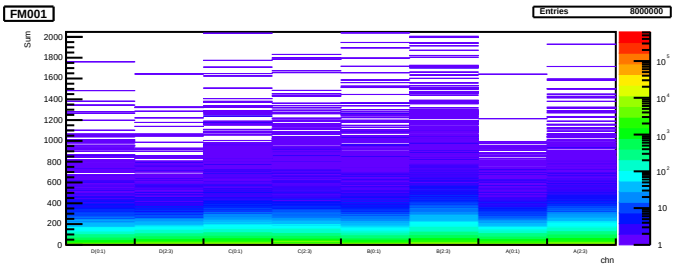


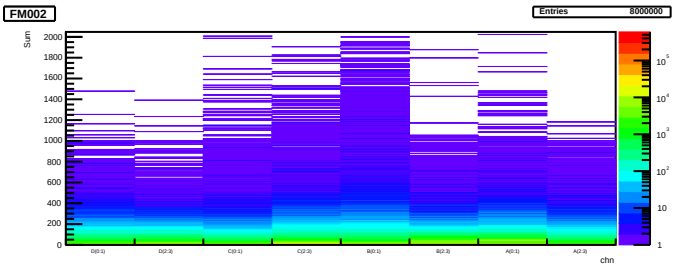

Input to QT1 crate (south-top)
Entries 2.887553e+07
ADC
channel

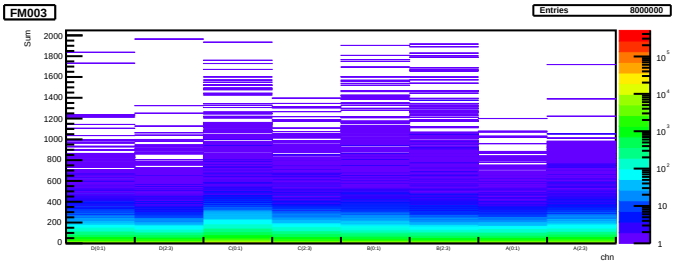

Input to QT3 crate (north-top)
Entries 3.073325e+07
ADC
channel

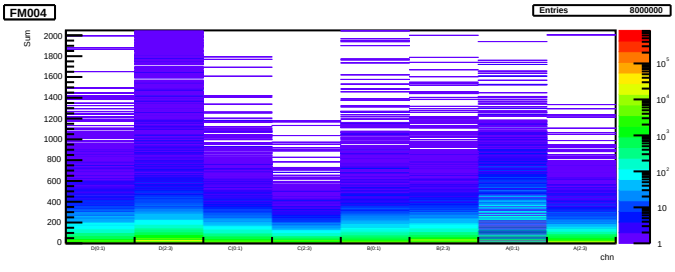

Input to QT2 crate (south-bottom)
Entries 3.039359e+07
ADC
channel

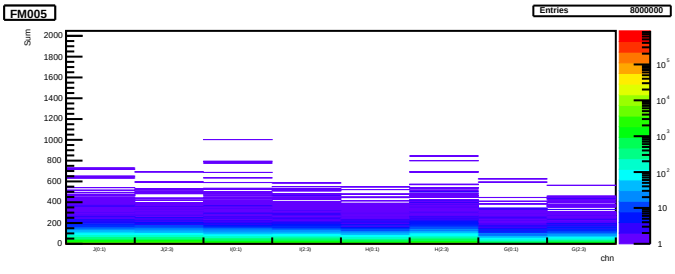

Input to QT4 crate (north-bottom)
Entries 2.319518e+07
ADC
channel

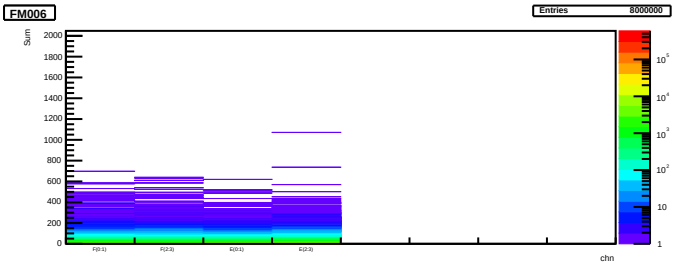

FM001
Entries 8000000
Sum
chn

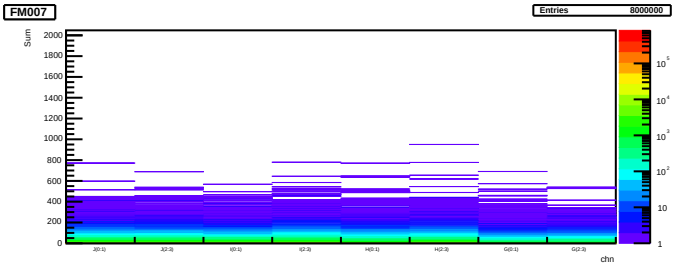

FM002
Entries 8000000
Sum
chn

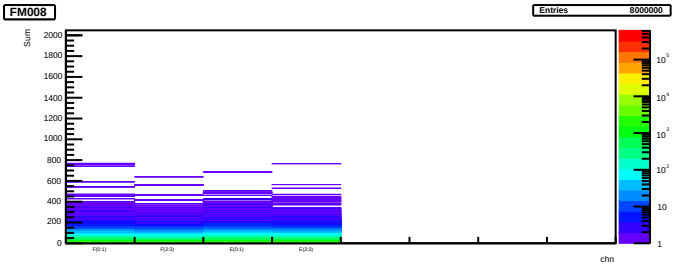

FM003
Entries 8000000
Sum
chn

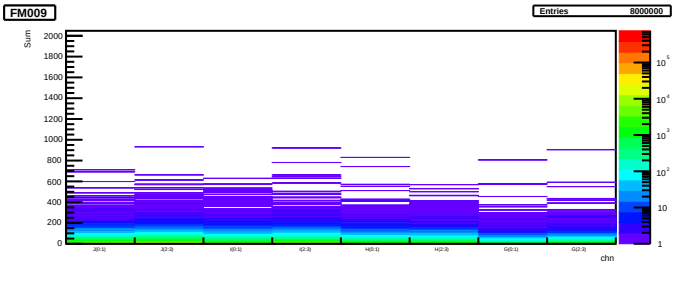

FM004
Entries 8000000
Sum
chn

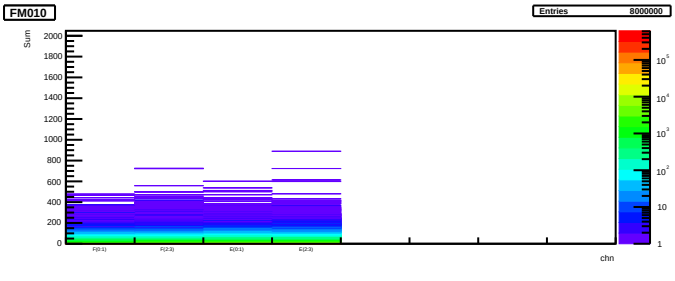

FM005
Entries 8000000
Sum
chn

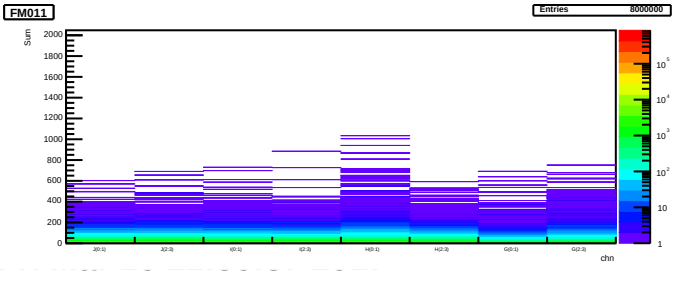

FM006
Entries 8000000
Sum
chn

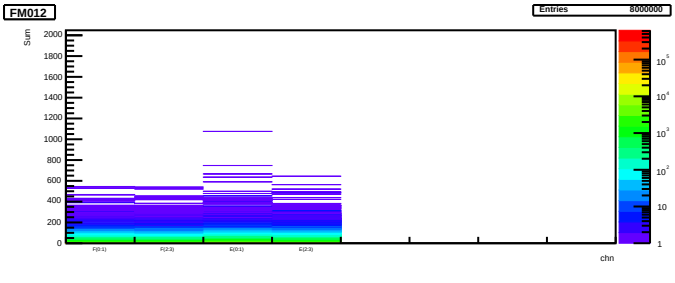

FM007
Entries 8000000
Sum
chn

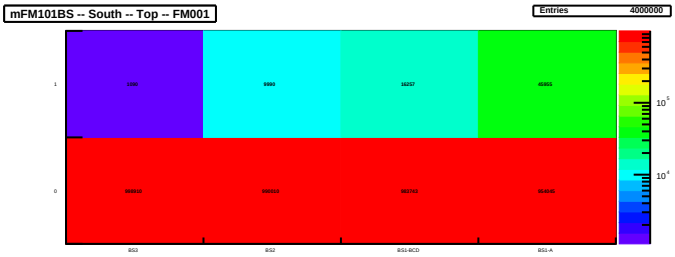

FM008
Entries 8000000
Sum
chn

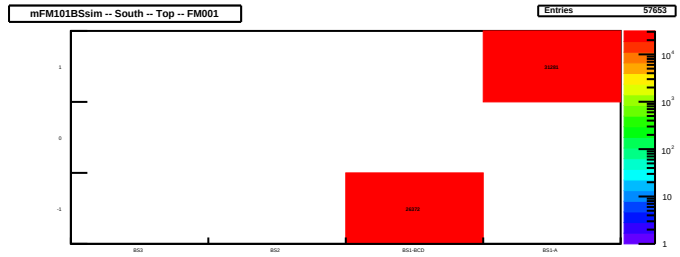

FM009
Entries 8000000
Sum
chn

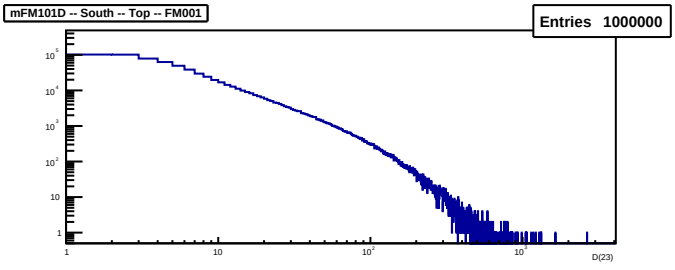

FM010
Entries 8000000
Sum
chn

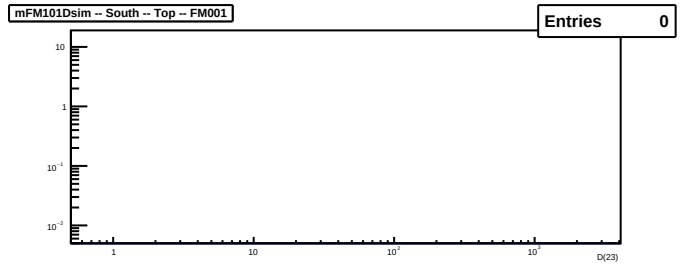

FM011
Entries 8000000
Sum
chn

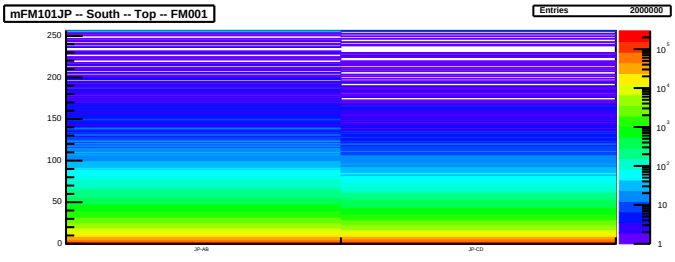

FM012
Entries 8000000
Sum
chn

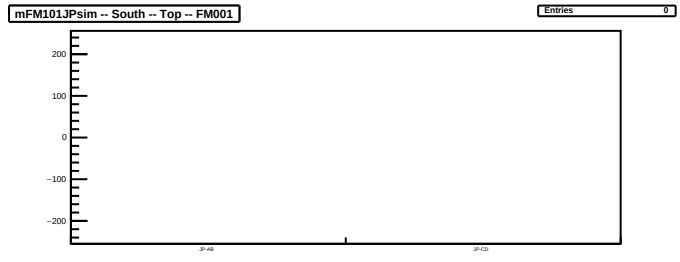

mFM101BS -- South -- Top -- FM001
Entries 4000000

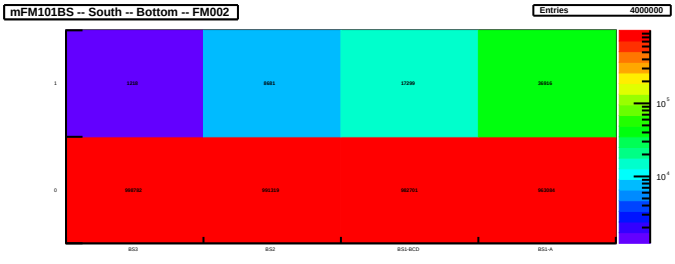

mFM101BSsim -- South -- Top -- FM001
Entries 57653

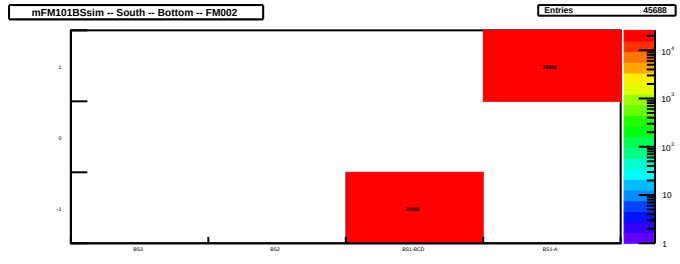

mFM101D -- South -- Top -- FM001
Entries 1000000
D(23)

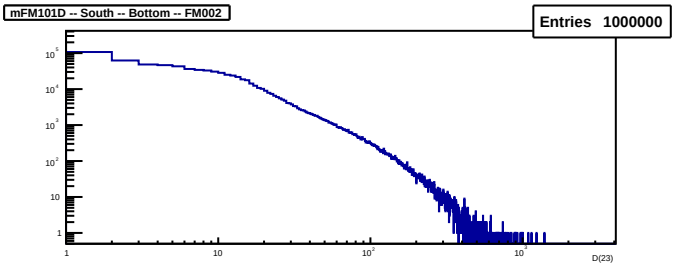

mFM101Dsim -- South -- Top -- FM001
Entries 0
D(23)

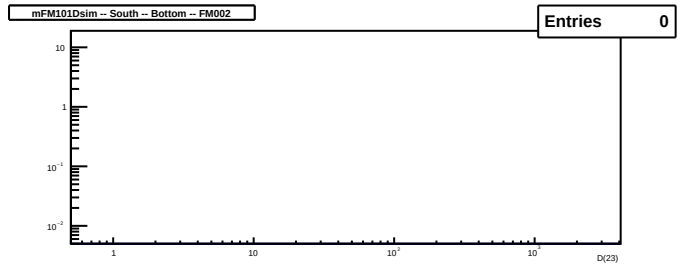

mFM101JP -- South -- Top -- FM001
Entries 2000000

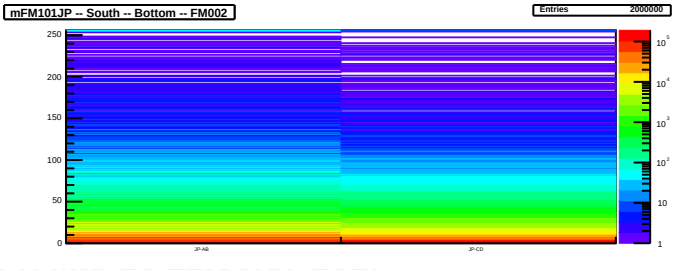

mFM101JPsim -- South -- Top -- FM001
Entries 0

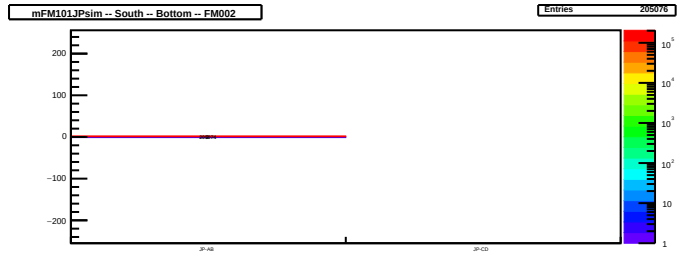

mFM101BS -- South -- Bottom -- FM002
Entries 4000000

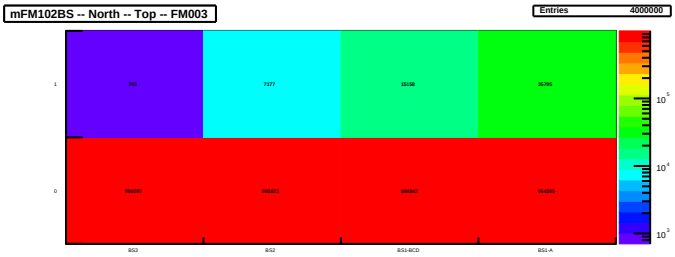

mFM101BSsim -- South -- Bottom -- FM002
Entries 45688

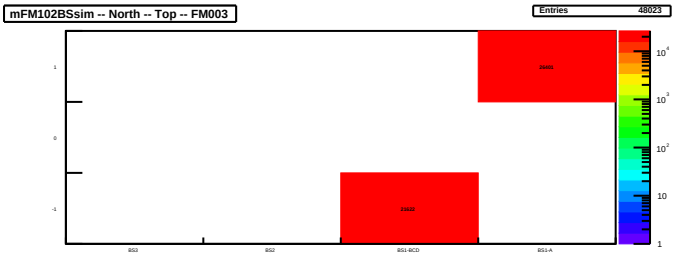

mFM101D -- South -- Bottom -- FM002
Entries 1000000
D(23)

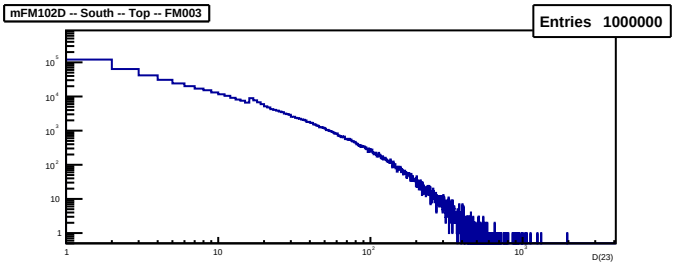

mFM101Dsim -- South -- Bottom -- FM002
Entries 0
D(23)

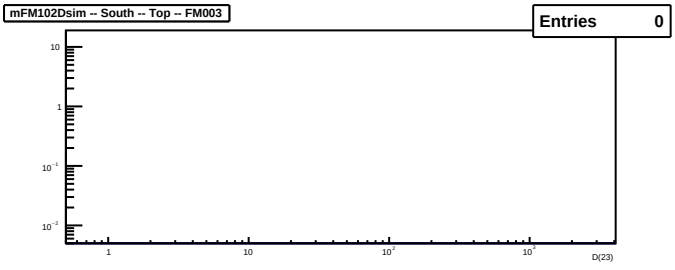

mFM101JP -- South -- Bottom -- FM002
Entries 2000000

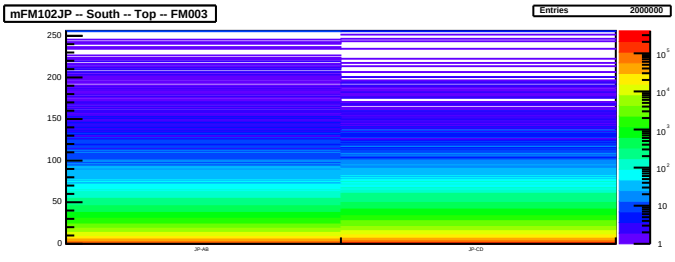

mFM101JPsim -- South -- Bottom -- FM002
Entries 205076


mFM102BS -- North -- Top -- FM003
Entries 4000000

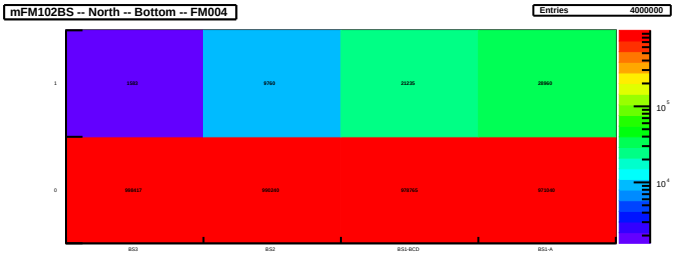

mFM102BSsim -- North -- Top -- FM003
Entries 48023

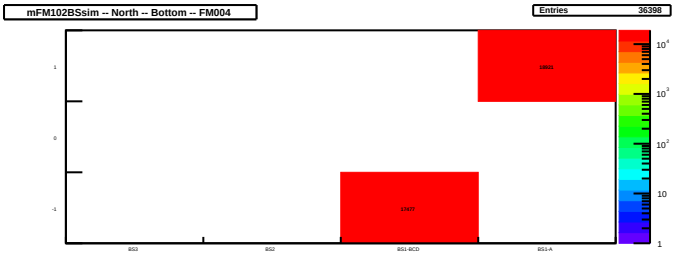

mFM102D -- South -- Top -- FM003
Entries 1000000
D(23)

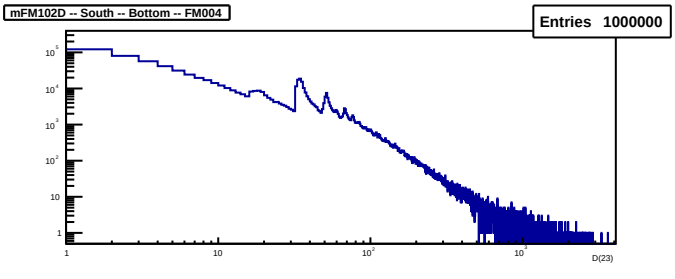

mFM102Dsim -- South -- Top -- FM003
Entries 0
D(23)

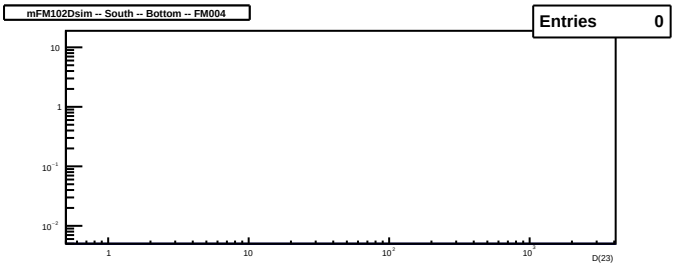

mFM102JP -- South -- Top -- FM003
Entries 2000000

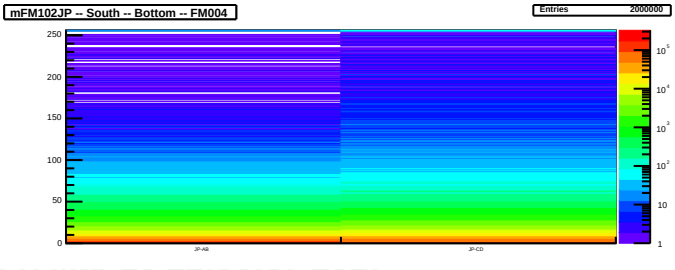
mFM102JPsim -- South -- Top -- FM003
Entries 0

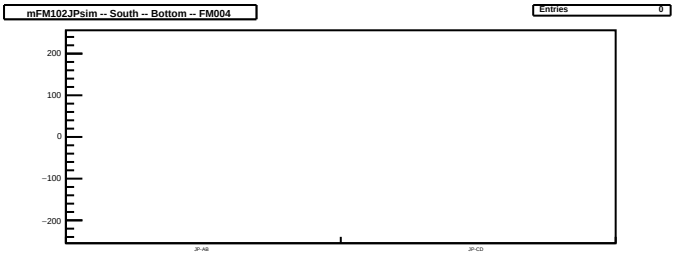

mFM102BS -- North -- Bottom -- FM004
Entries 4000000

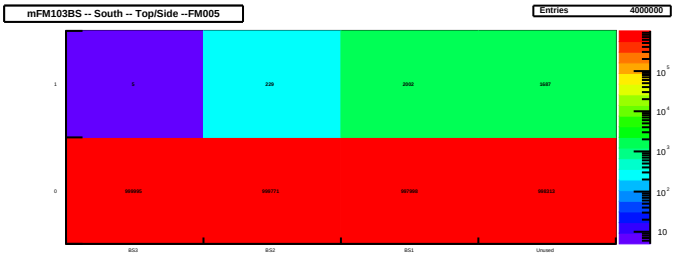

mFM102BSsim -- North -- Bottom -- FM004
Entries 36398

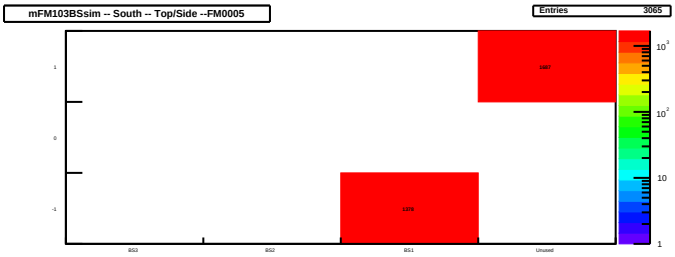

mFM102D -- South -- Bottom -- FM004
Entries 1000000
D(23)

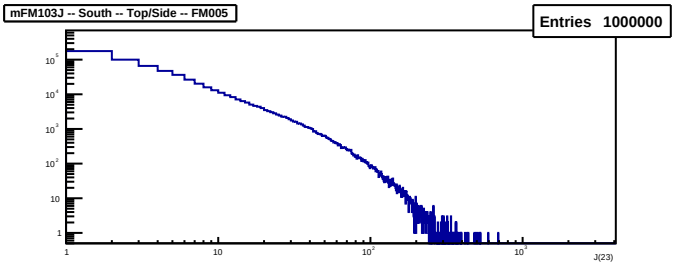

mFM102Dsim -- South -- Bottom -- FM004
Entries 0
D(23)

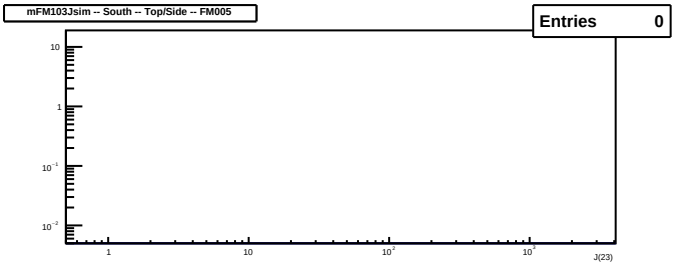

mFM102JP -- South -- Bottom -- FM004
Entries 2000000

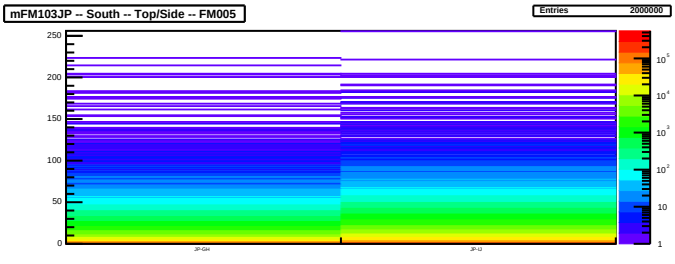

mFM102JPsim -- South -- Bottom -- FM004
Entries 0

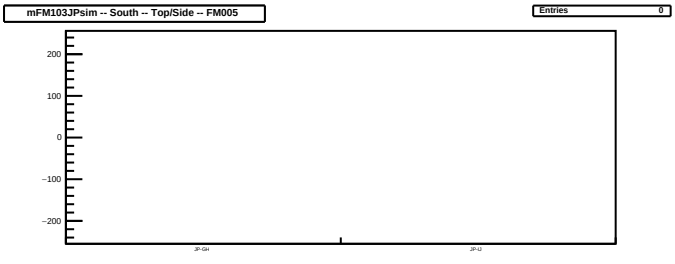

mFM103BS -- South -- Top/Side --FM005
Entries 4000000
BS3
BS2
BS1
Unused

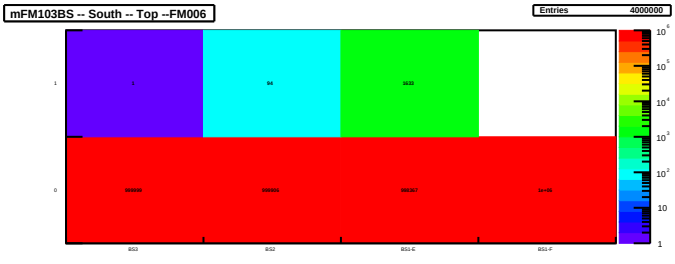

mFM103BSsim -- South -- Top/Side --FM0005
Entries 3065
BS3
BS2
BS1
Unused

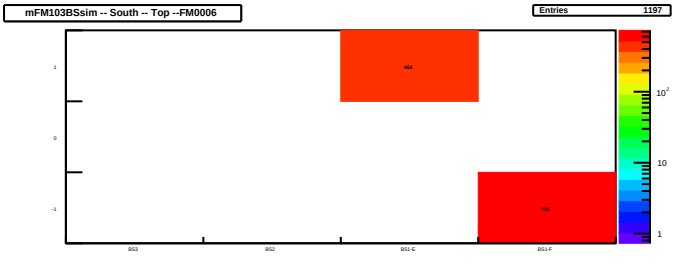

mFM103J -- South -- Top/Side -- FM005
Entries 1000000
J(23)

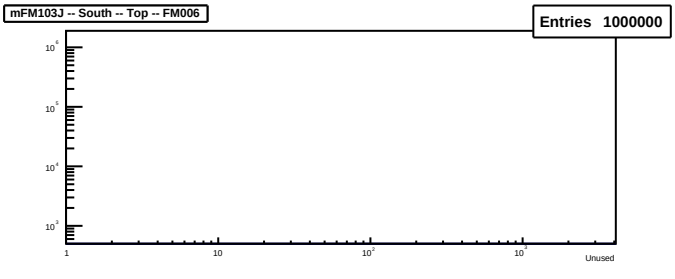

mFM103Jsim -- South -- Top/Side -- FM005
Entries 0
J(23)

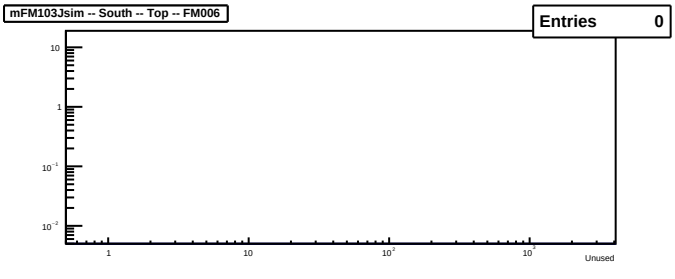

mFM103JP -- South -- Top/Side -- FM005
Entries 2000000
JP-G4
JP-U

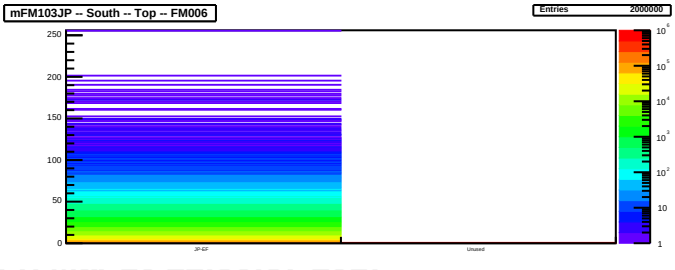

mFM103JPsim -- South -- Top/Side -- FM005
Entries 0
JP-G4
JP-U

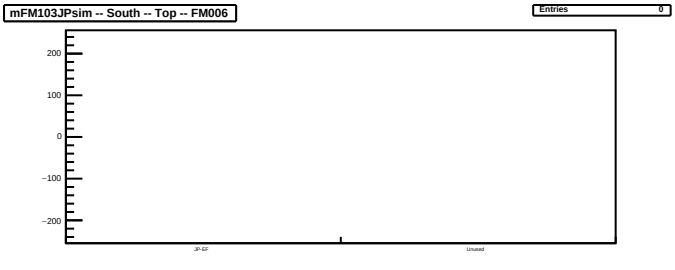

mFM103BS -- South -- Top --FM006
Entries 4000000
BS3
BS2
BS1-E
BS1-F

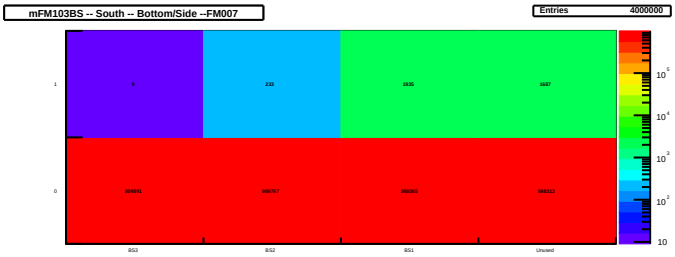

mFM103BSsim -- South -- Top --FM0006
Entries 1197
BS3
BS2
BS1-E
BS1-F

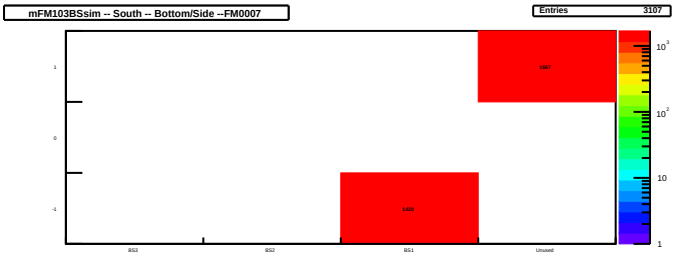

mFM103J -- South -- Top -- FM006
Entries 1000000
Unused

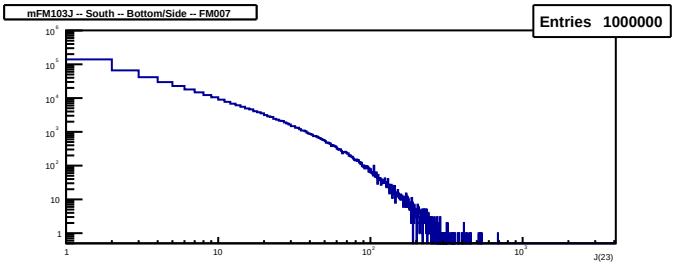

mFM103Jsim -- South -- Top -- FM006
Entries 0
Unused

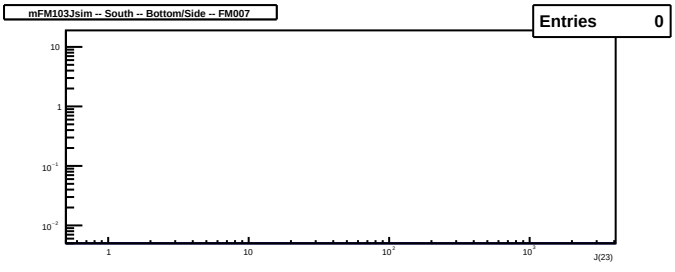

mFM103JP -- South -- Top -- FM006
Entries 2000000
JP-EF
Unused

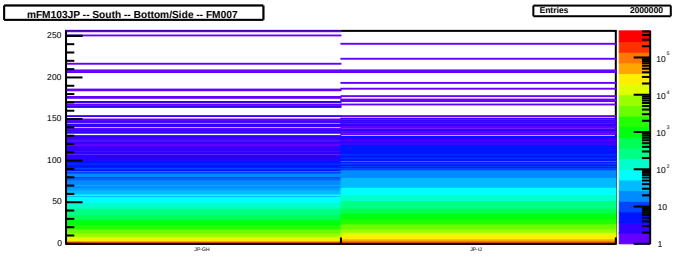

mFM103JPsim -- South -- Top -- FM006
Entries 0
JP-EF
Unused

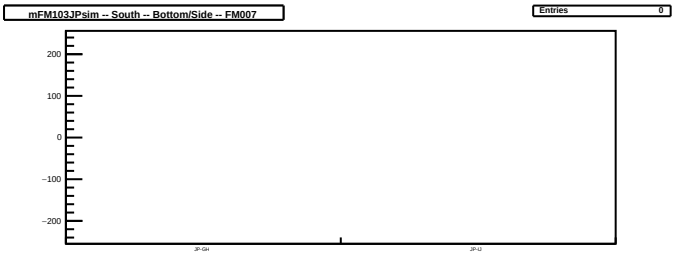

mFM103BS -- South -- Bottom/Side --FM007
Entries 4000000
BS3
BS2
BS1
Unused

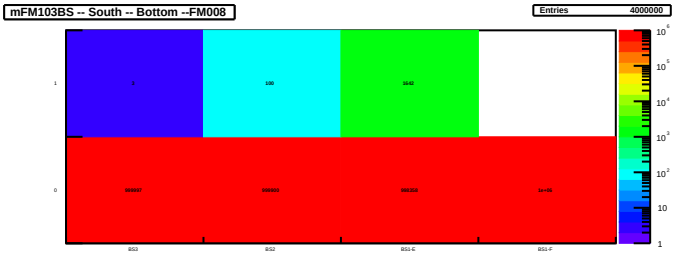

mFM103BSsim -- South -- Bottom/Side --FM0007
Entries 3107
BS3
BS2
BS1
Unused

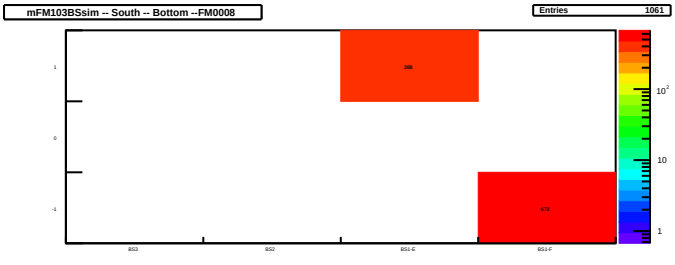

mFM103J -- South -- Bottom/Side -- FM007
Entries 1000000
J(23)

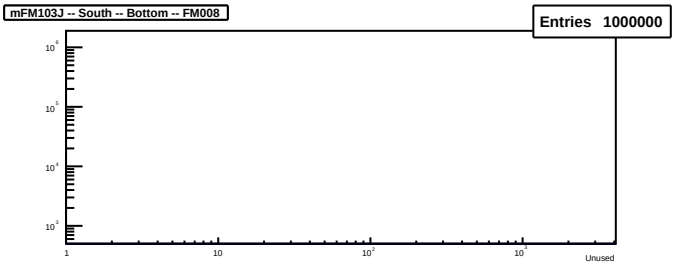

mFM103Jsim -- South -- Bottom/Side -- FM007
Entries 0
J(23)

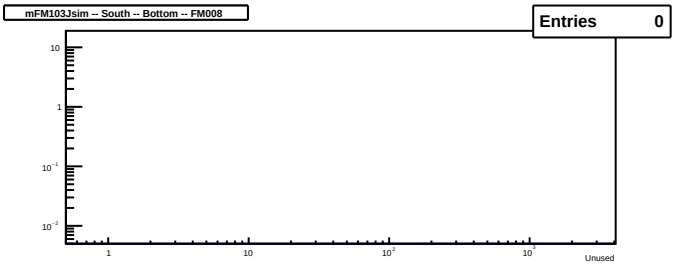

mFM103JP -- South -- Bottom/Side -- FM007
Entries 2000000
JP-GH
JP-IJ

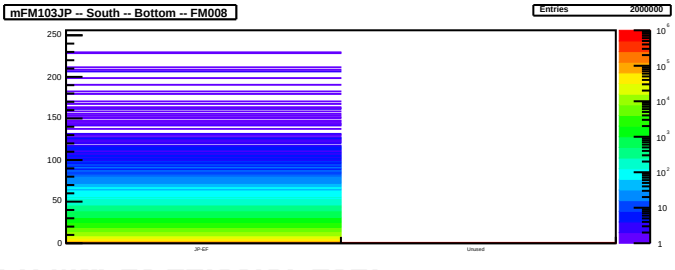

mFM103JPsim -- South -- Bottom/Side -- FM007
Entries 0
JP-GH
JP-IJ

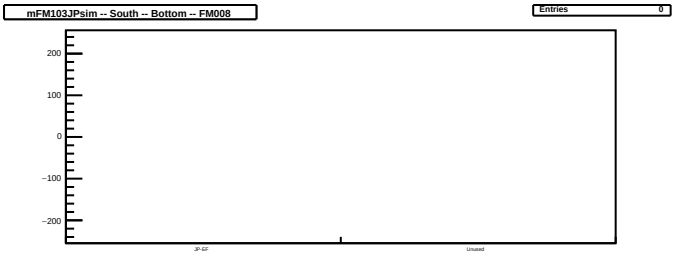

mFM103BS -- South -- Bottom --FM008
Entries 4000000
BS3
BS2
BS1-E
BS1-F

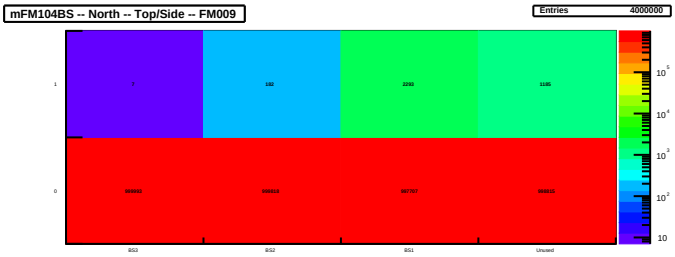

mFM103BSsim -- South -- Bottom --FM0008
Entries 1061
BS3
BS2
BS1-E
BS1-F

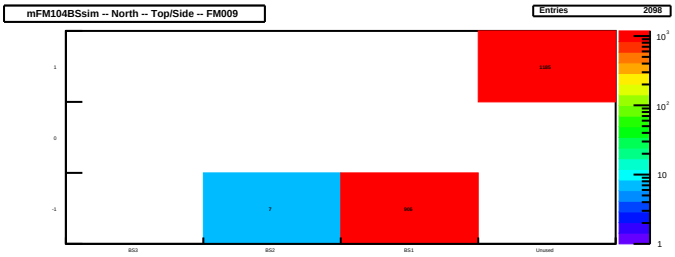

mFM103J -- South -- Bottom -- FM008
Entries 1000000
Unused

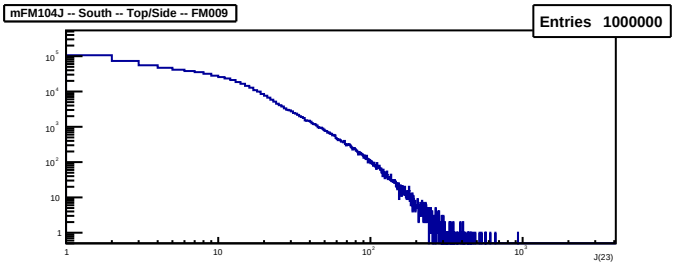

mFM103Jsim -- South -- Bottom -- FM008
Entries 0
Unused

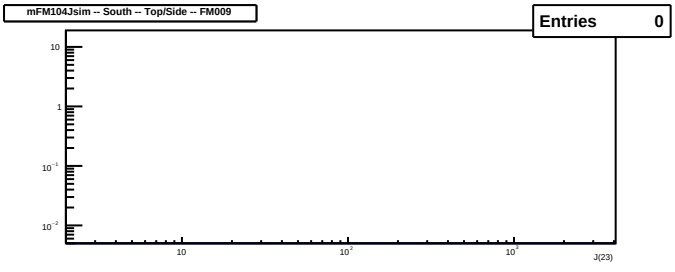

mFM103JP -- South -- Bottom -- FM008
Entries 2000000
JP-EF
Unused

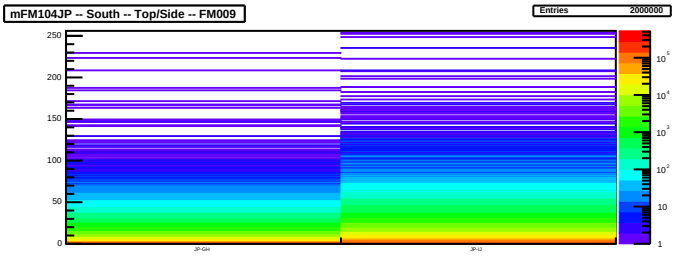

mFM103JPsim -- South -- Bottom -- FM008
Entries 0
JP-EF
Unused

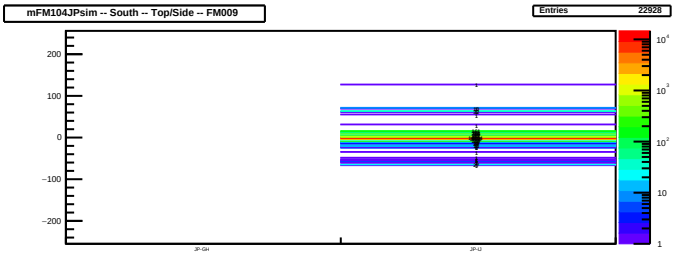

mFM104BS -- North -- Top/Side -- FM009
Entries 4000000
7
182
2293
1185
999993
999818
997707
998815
BS3
BS2
BS1
Unused

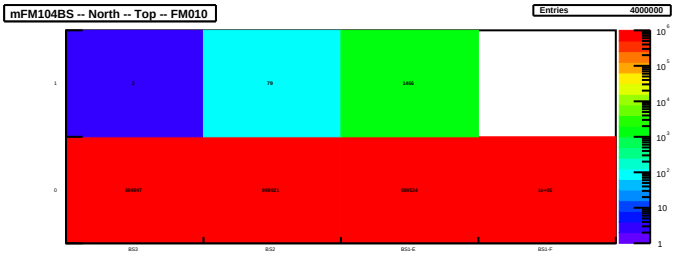

mFM104BSsim -- North -- Top/Side -- FM009
Entries 2098
1185
7
906
BS3
BS2
BS1
Unused

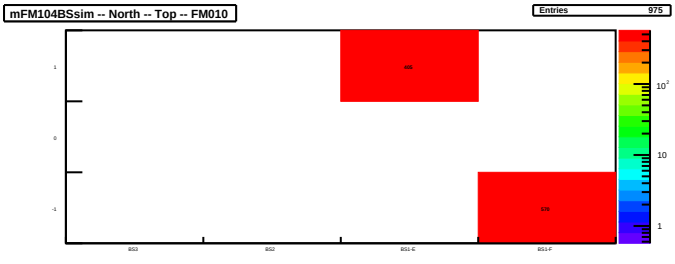

mFM104J -- South -- Top/Side -- FM009
Entries 1000000
J(23)

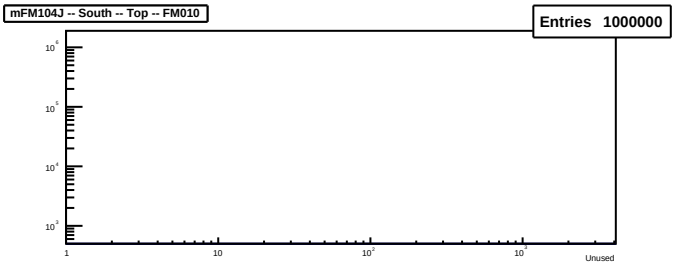

mFM104Jsim -- South -- Top/Side -- FM009
Entries 0
J(23)

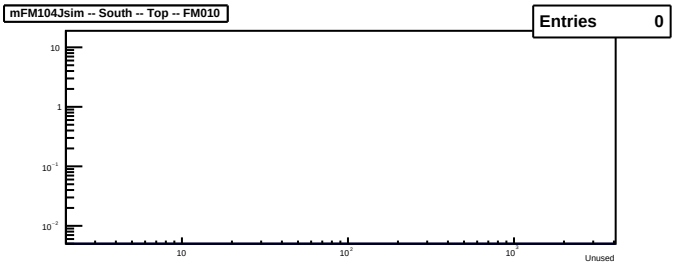

mFM104JP -- South -- Top/Side -- FM009
Entries 2000000
JP-GH
JP-IJ

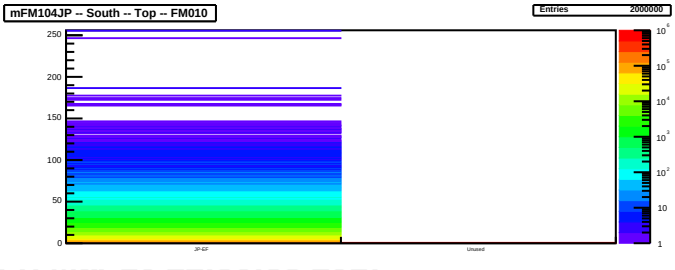

mFM104JPsim -- South -- Top/Side -- FM009
Entries 22928
JP-GH
JP-IJ

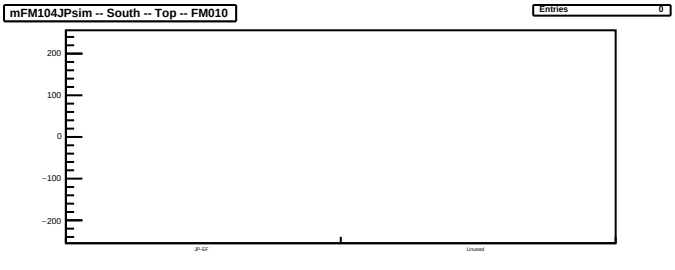

mFM104BS -- North -- Top -- FM010
Entries 4000000
1
79
1498
999997
999921
998524
1e+06
BS3
BS2
BS1-E
BS1-F

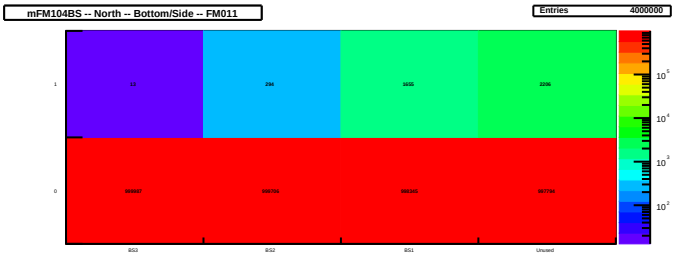

mFM104BSsim -- North -- Top -- FM010
Entries 975
405
570
BS3
BS2
BS1-E
BS1-F

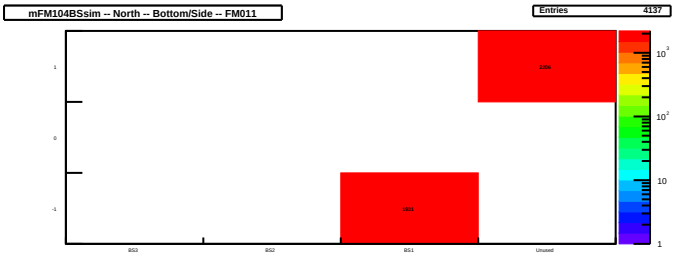

mFM104J -- South -- Top -- FM010
Entries 1000000
Unused

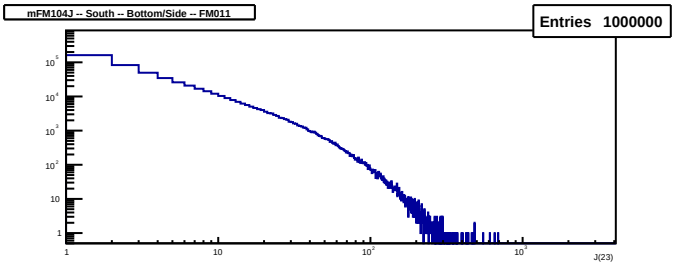

mFM104Jsim -- South -- Top -- FM010
Entries 0
Unused

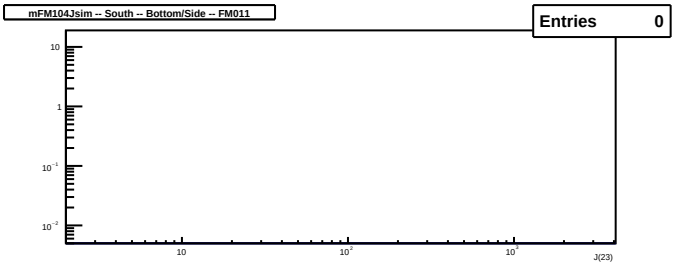

mFM104JP -- South -- Top -- FM010
Entries 2000000
JP-EF
Unused

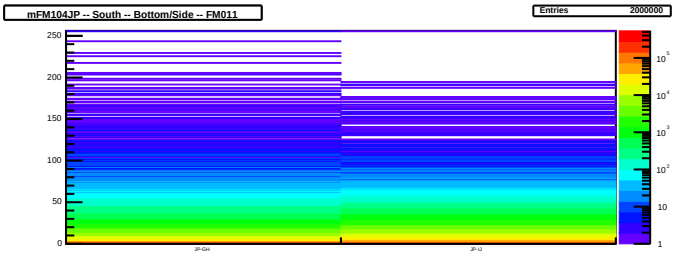

mFM104JPsim -- South -- Top -- FM010
Entries 0
JP-EF
Unused

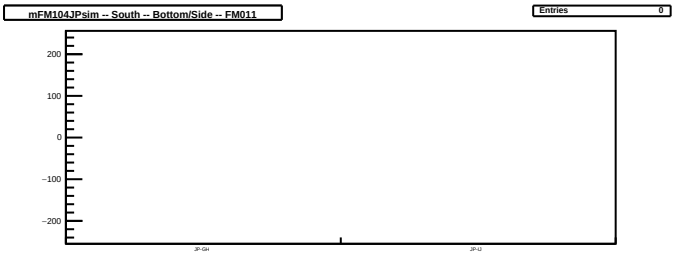

mFM104BS -- North -- Bottom/Side -- FM011
Entries 4000000

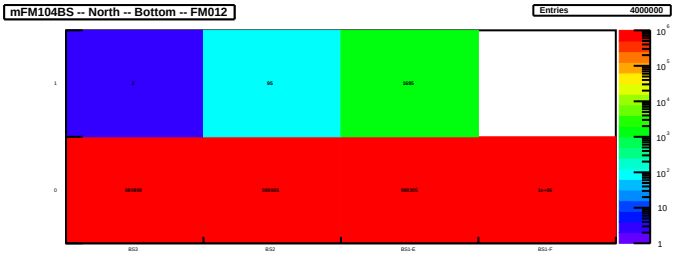

mFM104BSsim -- North -- Bottom/Side -- FM011
Entries 4137

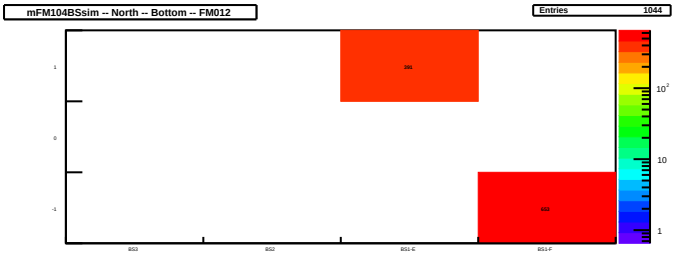

mFM104J -- South -- Bottom/Side -- FM011
Entries 1000000
J(23)

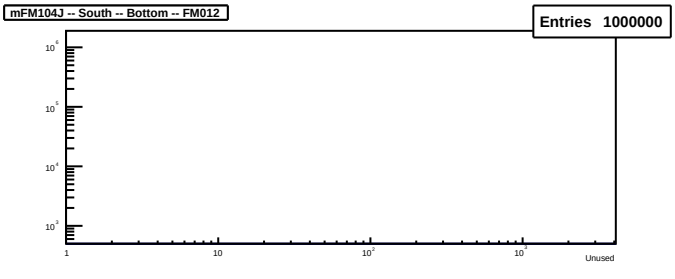

mFM104Jsim -- South -- Bottom/Side -- FM011
Entries 0
J(23)

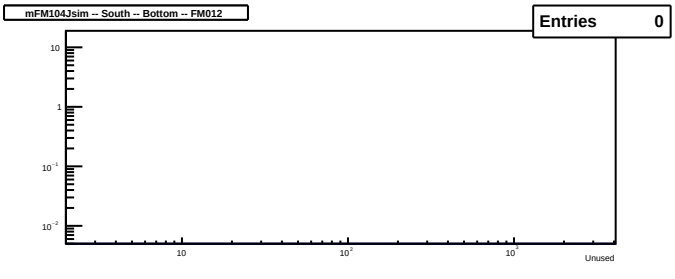

mFM104JP -- South -- Bottom/Side -- FM011
Entries 2000000

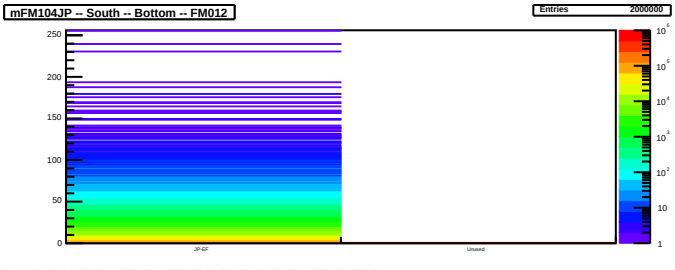

mFM104JPsim -- South -- Bottom/Side -- FM011
Entries 0

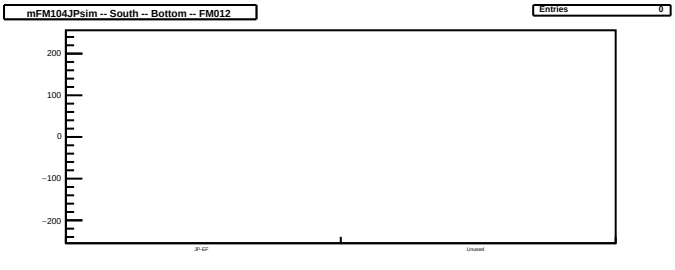

mFM104BS -- North -- Bottom -- FM012
Entries 4000000

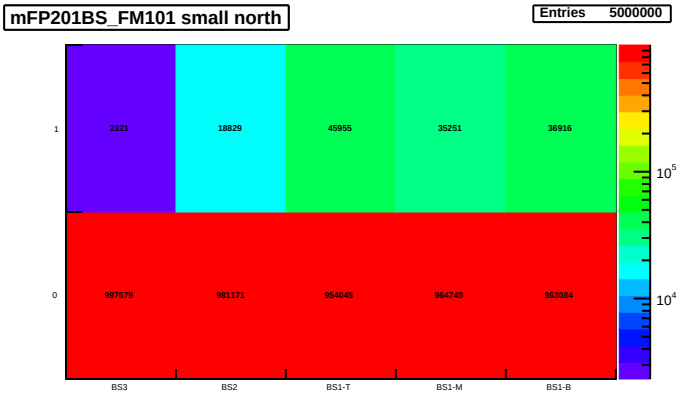

mFM104BSsim -- North -- Bottom -- FM012
Entries 1044

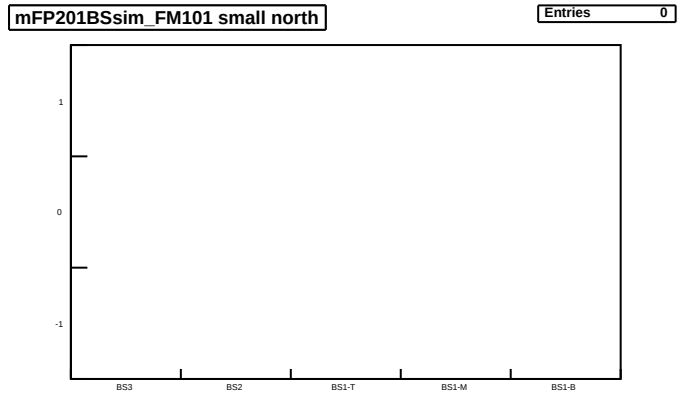

mFM104J -- South -- Bottom -- FM012
Entries 1000000
Unused

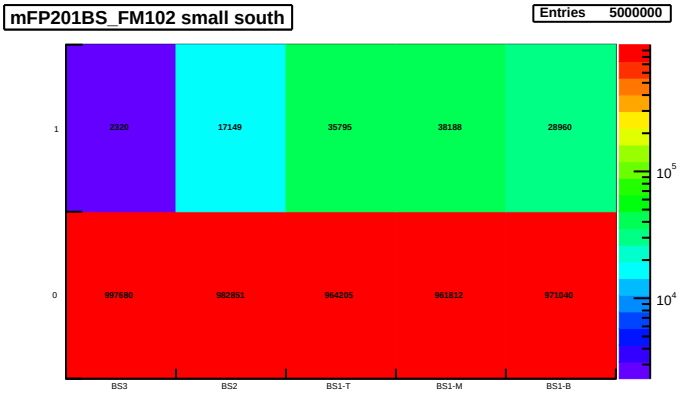

mFM104Jsim -- South -- Bottom -- FM012
Entries 0
Unused

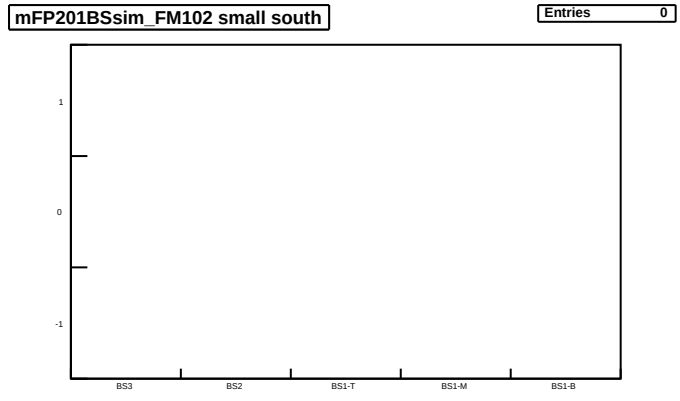

mFM104JP -- South -- Bottom -- FM012
Entries 2000000

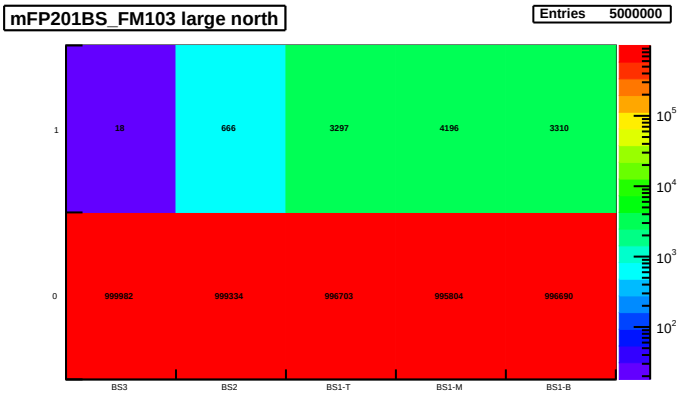

mFM104JPsim -- South -- Bottom -- FM012
Entries 0

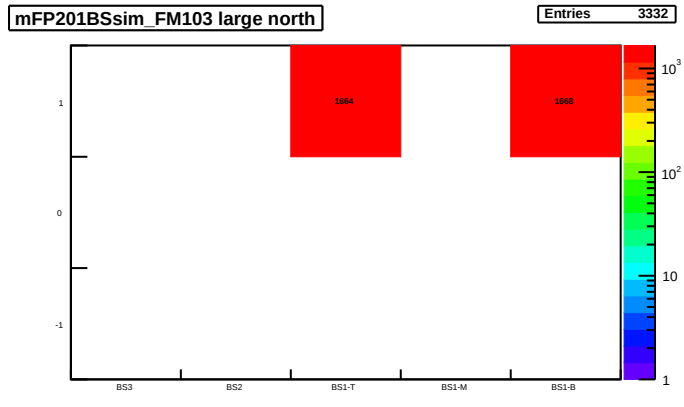

mFP201BS_FM101 small north
Entries 5000000
2321
18829
45955
35251
36916
997679
981171
954045
964749
963084
BS3
BS2
BS1-T
BS1-M
BS1-B

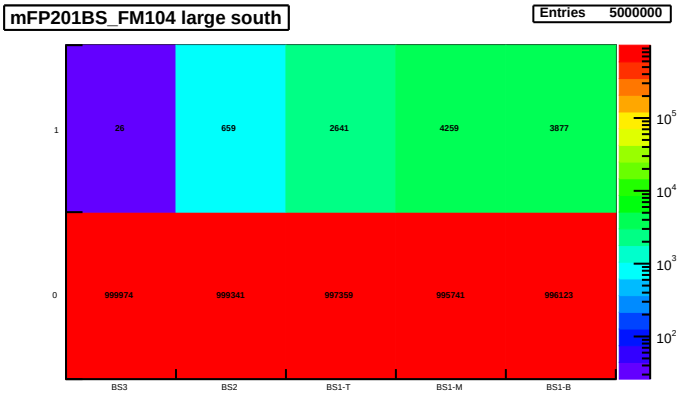

mFP201BSsim_FM101 small north
Entries 0
BS3
BS2
BS1-T
BS1-M
BS1-B

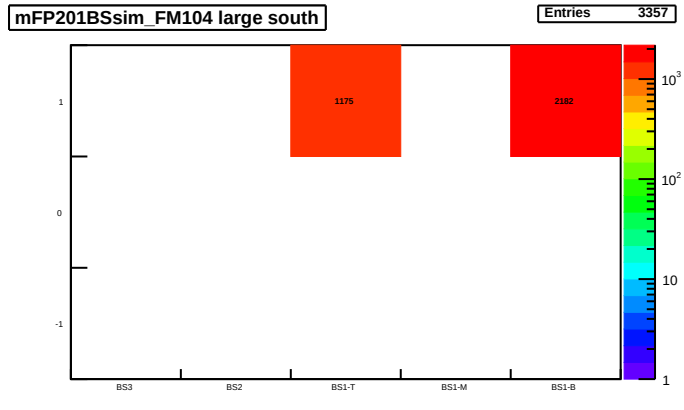

mFP201BS_FM102 small south
Entries 5000000
2320
17149
35795
38188
28960
997680
982851
964205
961812
971040
BS3
BS2
BS1-T
BS1-M
BS1-B

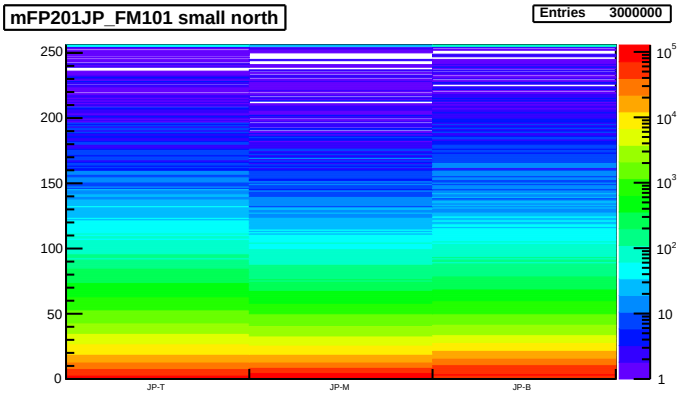

mFP201BSsim_FM102 small south
Entries 0
BS3
BS2
BS1-T
BS1-M
BS1-B

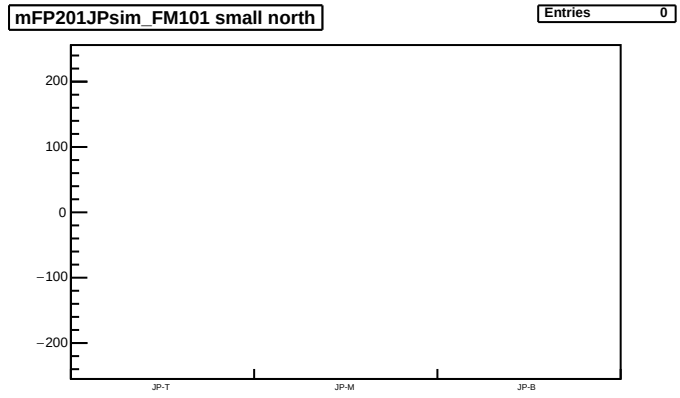

mFP201BS_FM103 large north
Entries 5000000
18
666
3297
4196
3310
999982
999334
996703
995804
996690
BS3
BS2
BS1-T
BS1-M
BS1-B

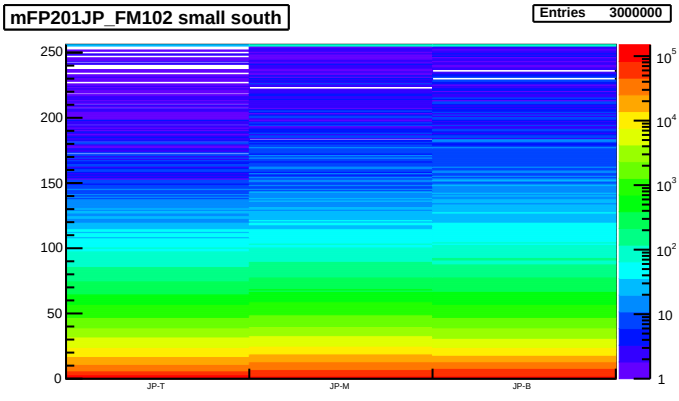

mFP201BSsim_FM103 large north
Entries 3332
1664
1668
BS3
BS2
BS1-T
BS1-M
BS1-B

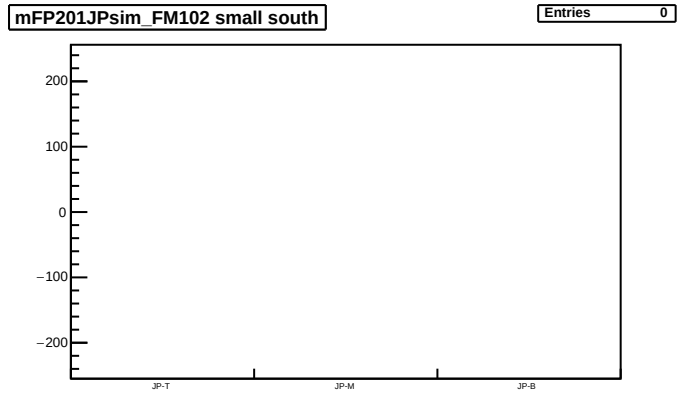

mFP201BS_FM104 large south
Entries 5000000
26
659
2641
4259
3877
999974
999341
997359
995741
996123
BS3
BS2
BS1-T
BS1-M
BS1-B

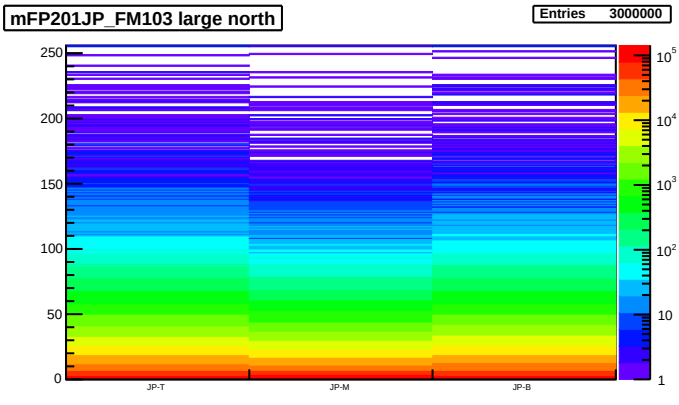

mFP201BSsim_FM104 large south
Entries 3357
1175
2182
BS3
BS2
BS1-T
BS1-M
BS1-B

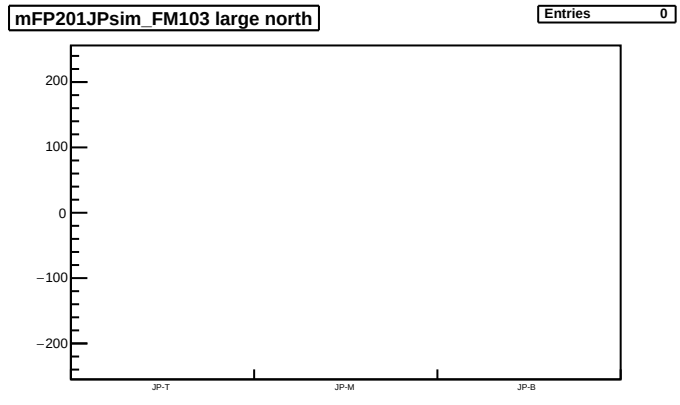

mFP201JP_FM101 small north
Entries 3000000
250
200
150
100
50
0
JP-T
JP-M
JP-B
10^5
10^4
10^3
10^2
10
1

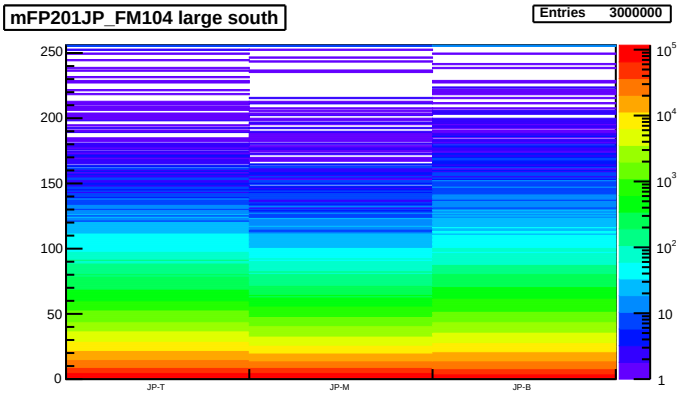

mFP201JPsim_FM101 small north
Entries 0
200
100
0
−100
−200
JP-T
JP-M
JP-B

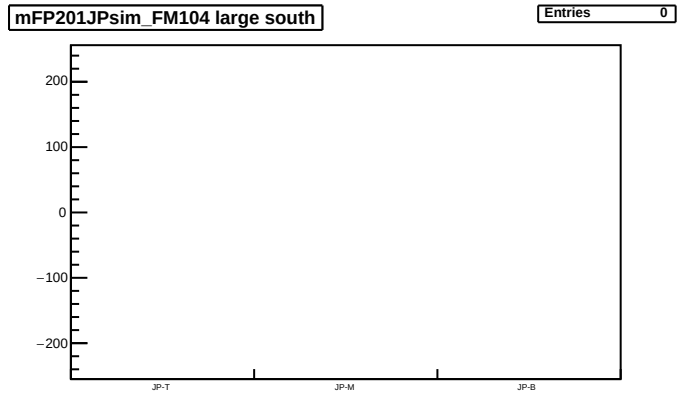

mFP201JP_FM102 small south
Entries 3000000
250
200
150
100
50
0
JP-T
JP-M
JP-B
10^5
10^4
10^3
10^2
10
1

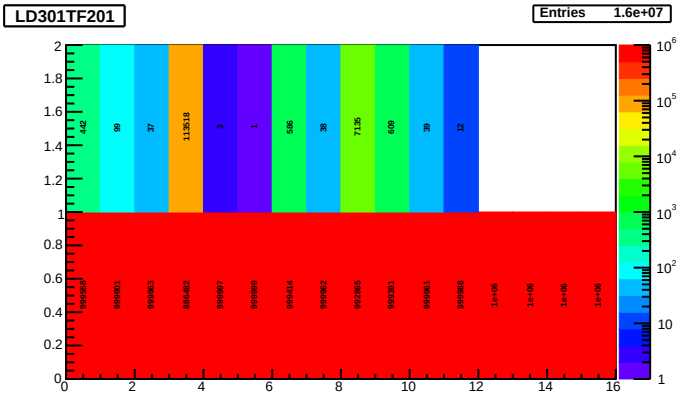

mFP201JPsim_FM102 small south
Entries 0
200
100
0
−100
−200
JP-T
JP-M
JP-B

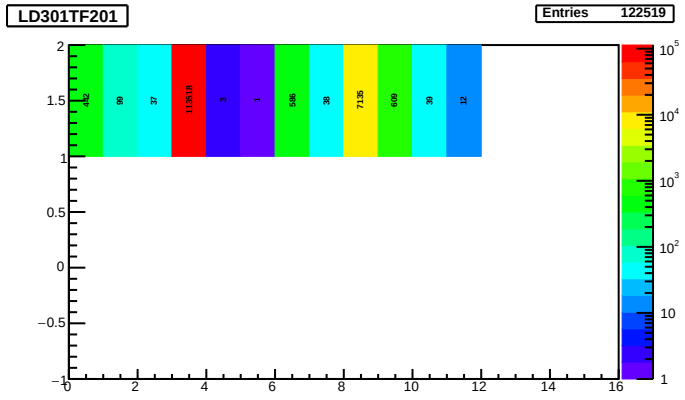

mFP201JP_FM103 large north
Entries 3000000
250
200
150
100
50
0
JP-T
JP-M
JP-B
10^5
10^4
10^3
10^2
10
1

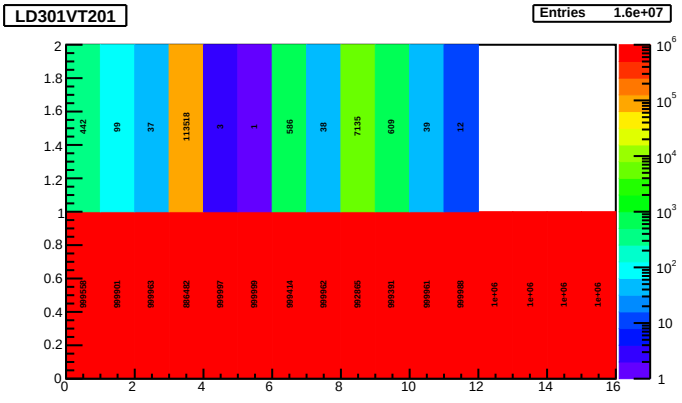

mFP201JPsim_FM103 large north
Entries 0
200
100
0
−100
−200
JP-T
JP-M
JP-B

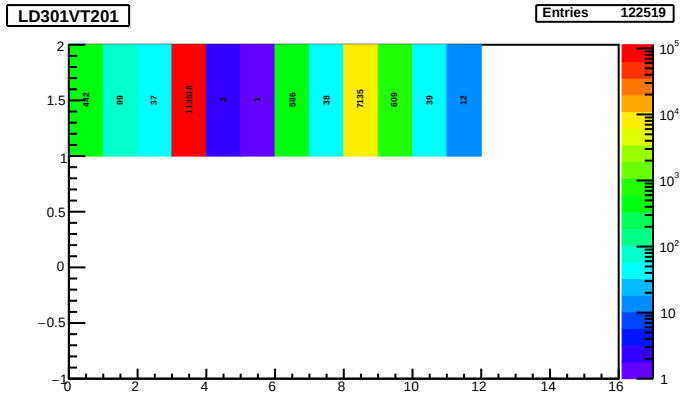

mFP201JP_FM104 large south
Entries 3000000
250
200
150
100
50
0
JP-T
JP-M
JP-B
10^5
10^4
10^3
10^2
10
1

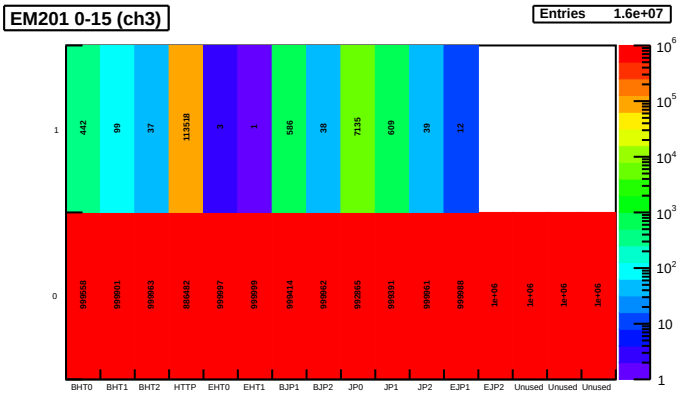

mFP201JPsim_FM104 large south
Entries 0
200
100
0
−100
−200
JP-T
JP-M
JP-B

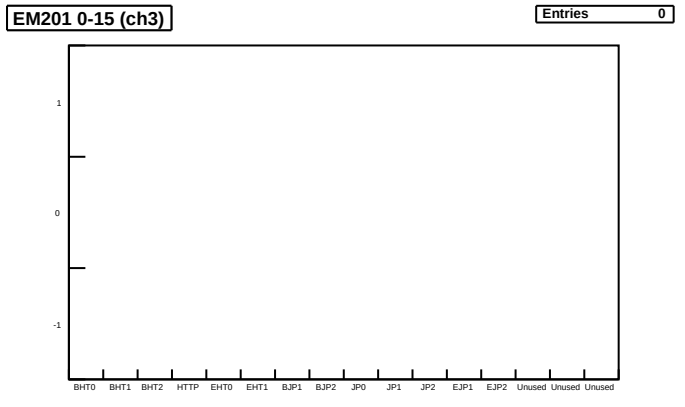

LD301TF201
Entries 1.6e+07

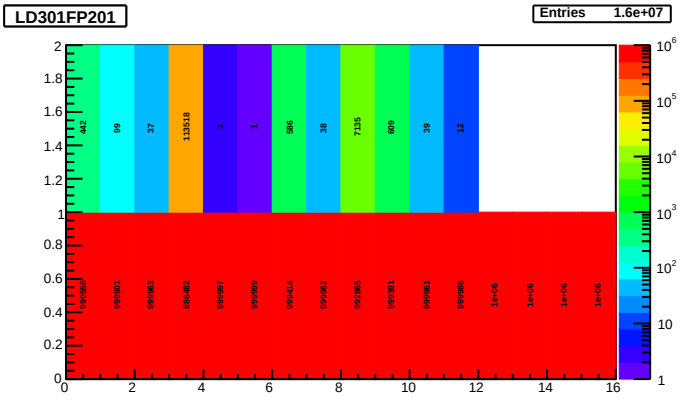

LD301TF201
Entries 122519

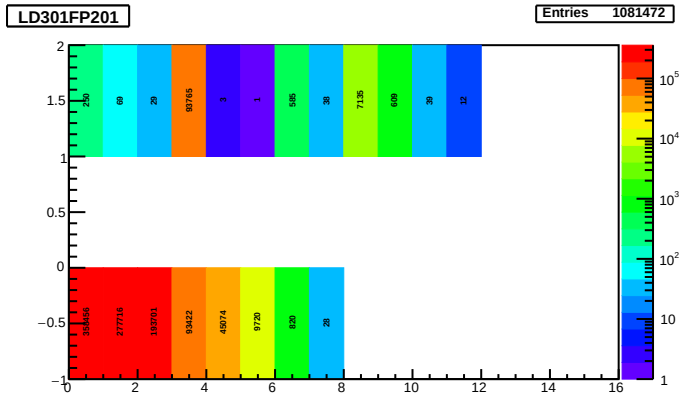

LD301VT201
Entries 1.6e+07


LD301VT201
Entries 122519


EM201 0-15 (ch3)
Entries 1.6e+07
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


LD301FP201
Entries 1.6e+07


LD301FP201
Entries 1081472