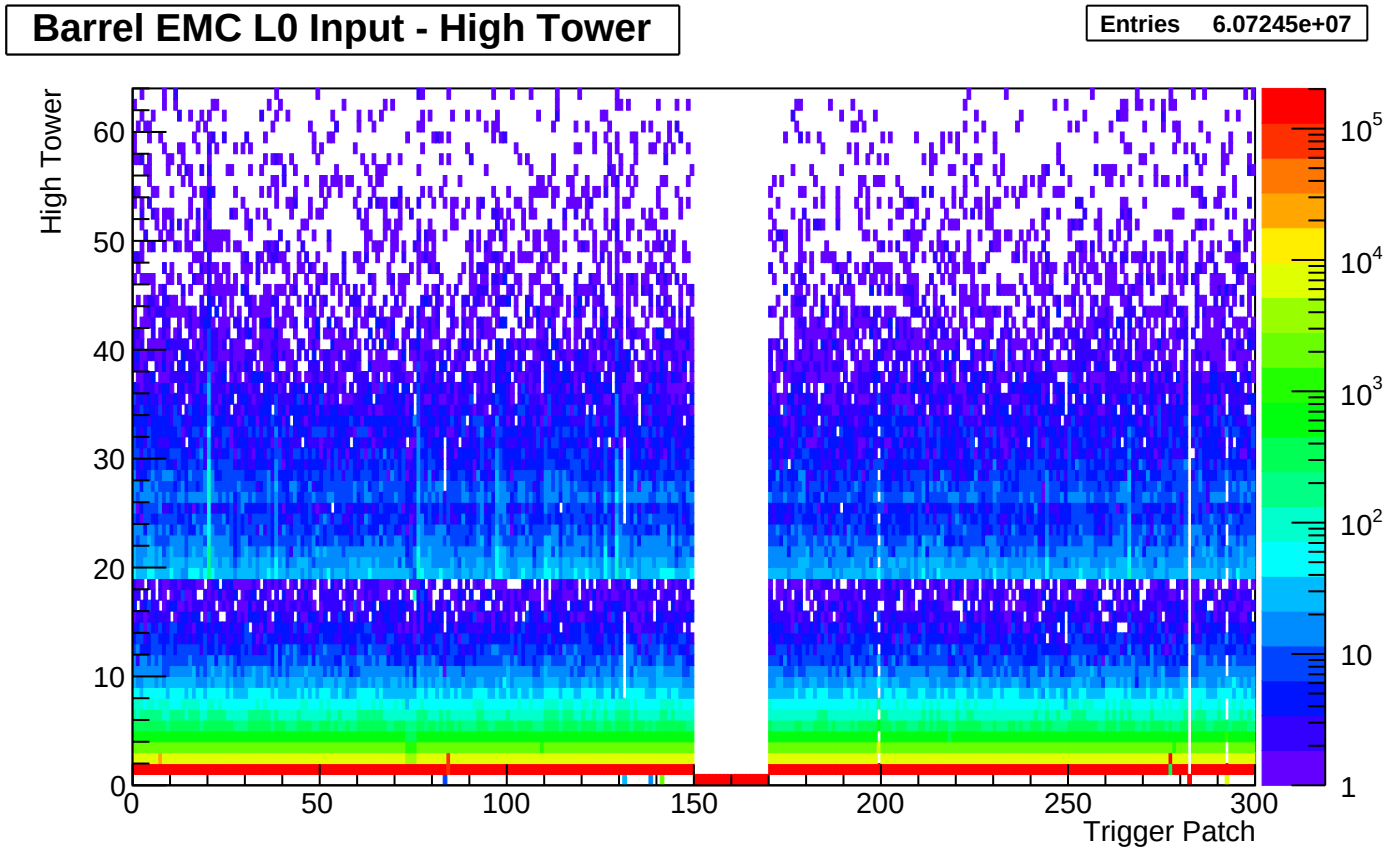
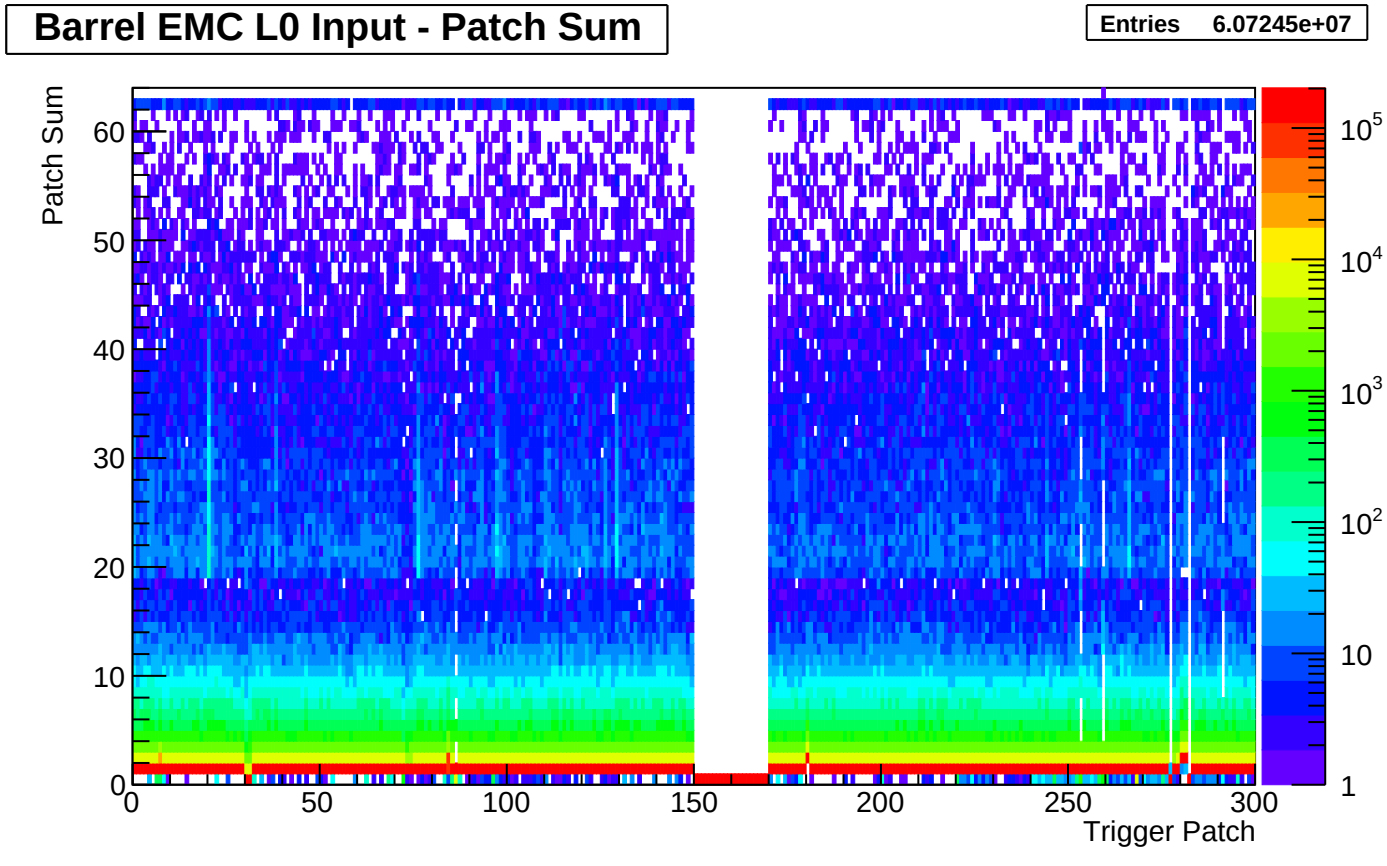
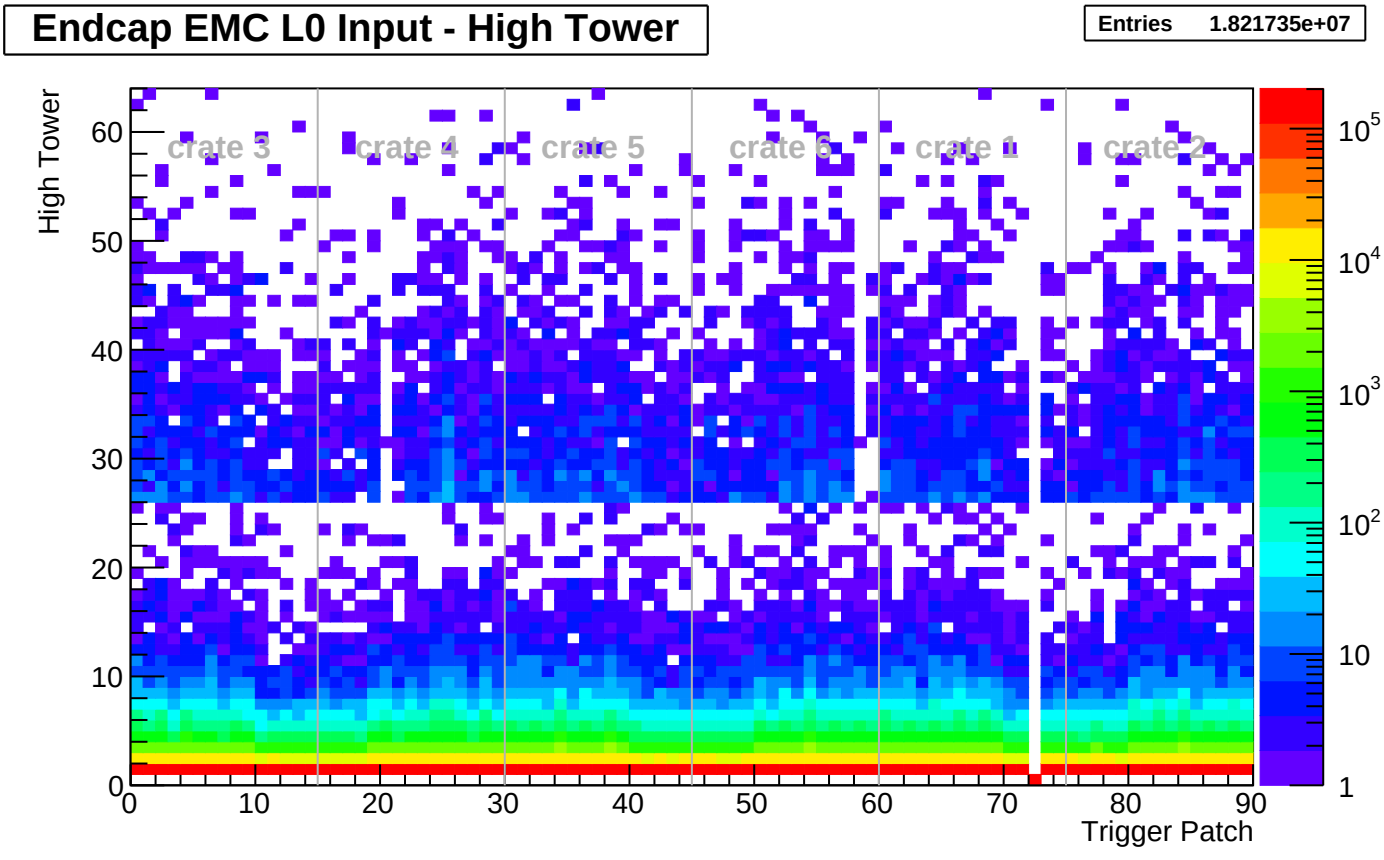
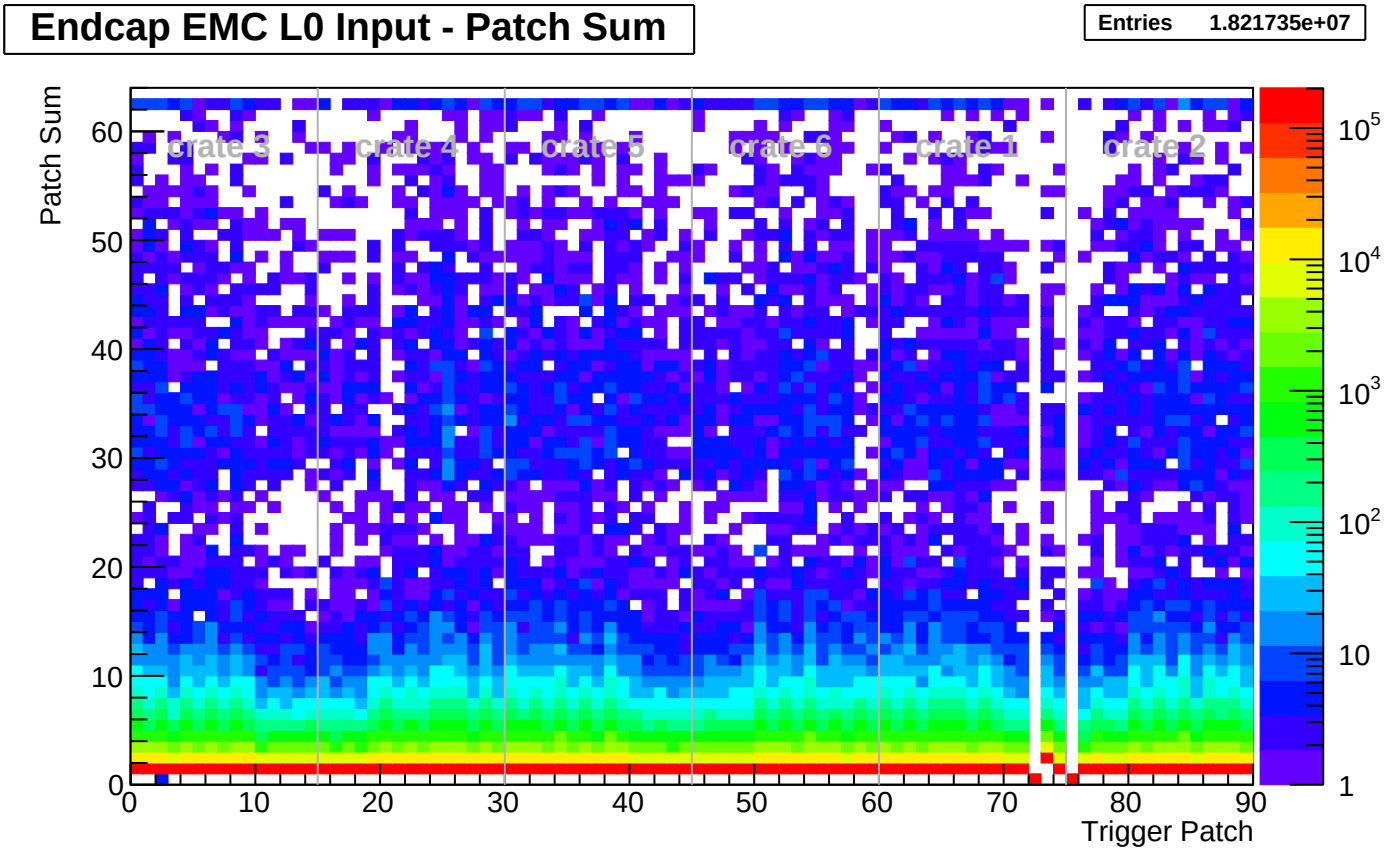
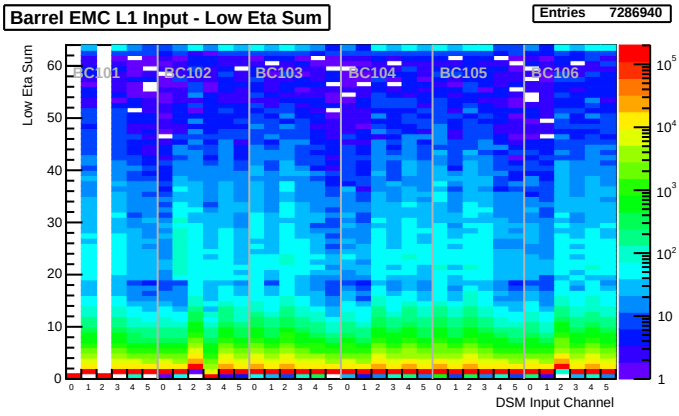
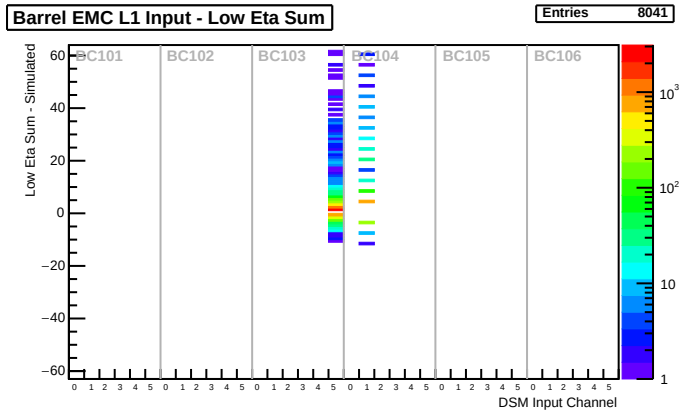

Barrel EMC L0 Input - High Tower
Entries 6.07245e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

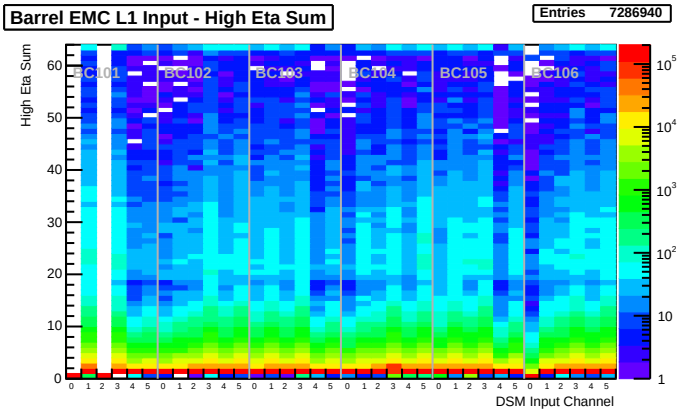

Barrel EMC L0 Input - Patch Sum
Entries 6.07245e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

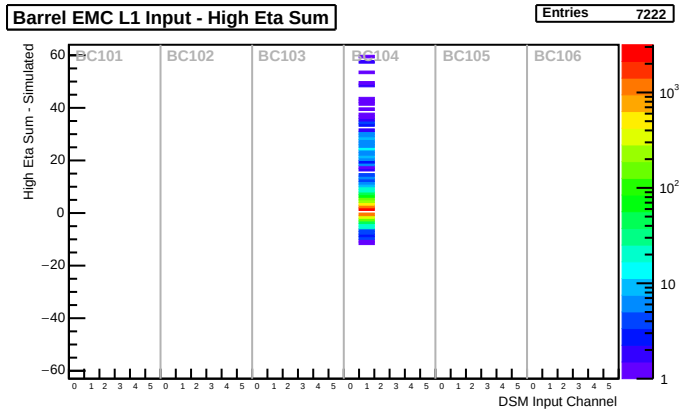

Endcap EMC L0 Input - High Tower
Entries 1.821735e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

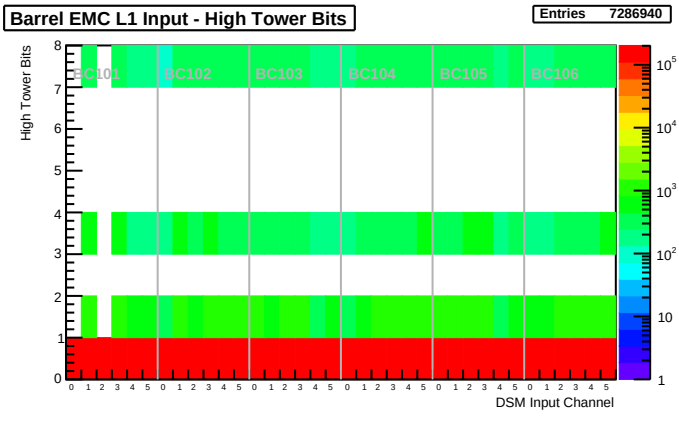

Endcap EMC L0 Input - Patch Sum
Entries 1.821735e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

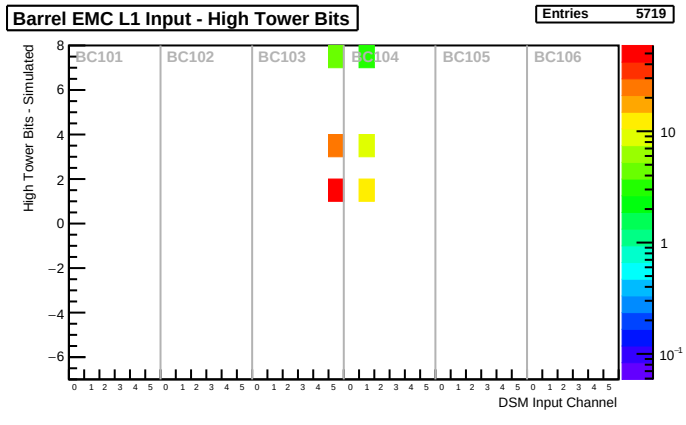

Barrel EMC L1 Input - Low Eta Sum
Entries 7286940
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

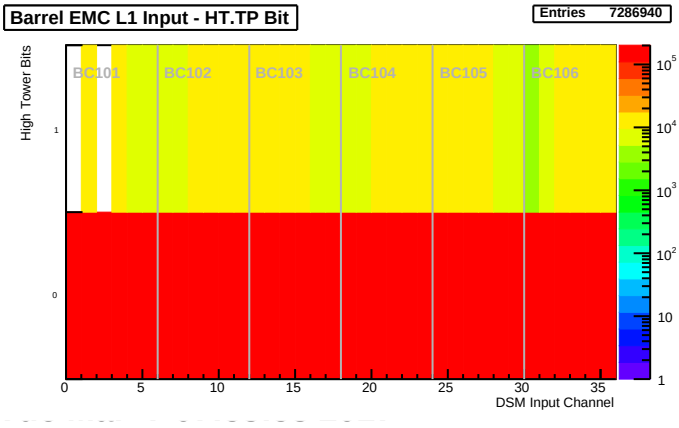

Barrel EMC L1 Input - Low Eta Sum
Entries 8041
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

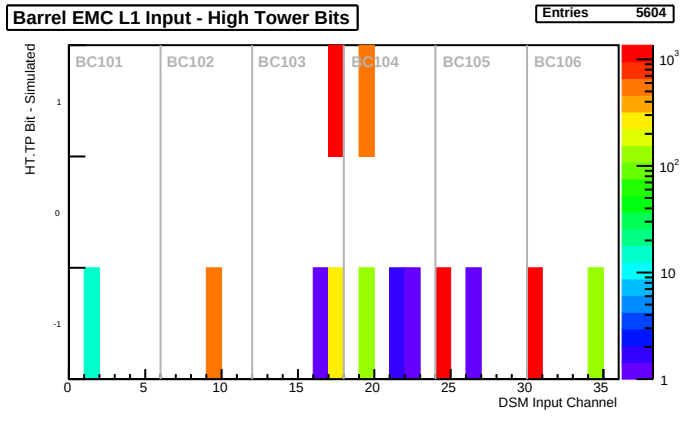

Barrel EMC L1 Input - High Eta Sum
Entries 7286940
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

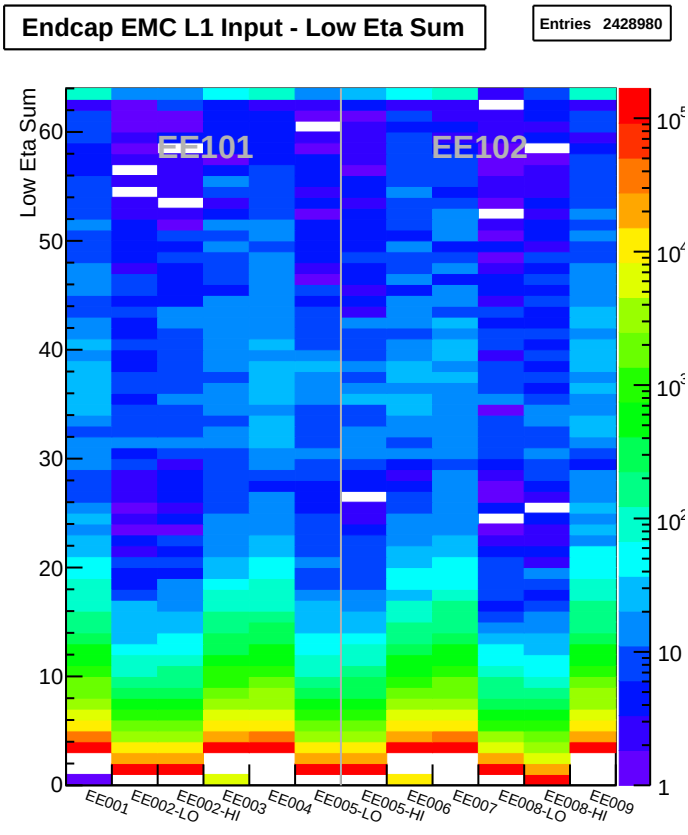

Barrel EMC L1 Input - High Eta Sum
Entries 7222
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

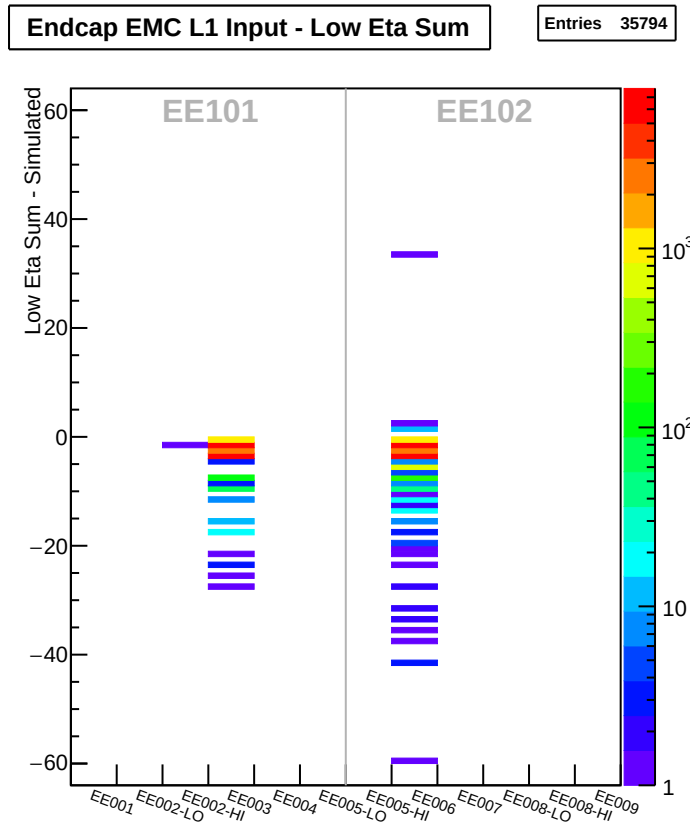

Barrel EMC L1 Input - High Tower Bits
Entries 7286940
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

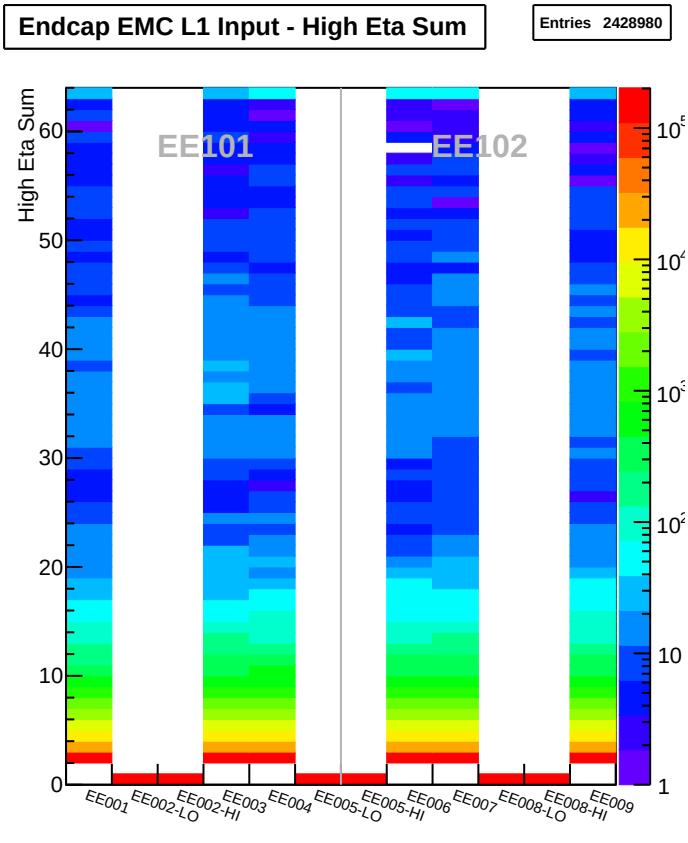

Barrel EMC L1 Input - High Tower Bits
Entries 5719
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

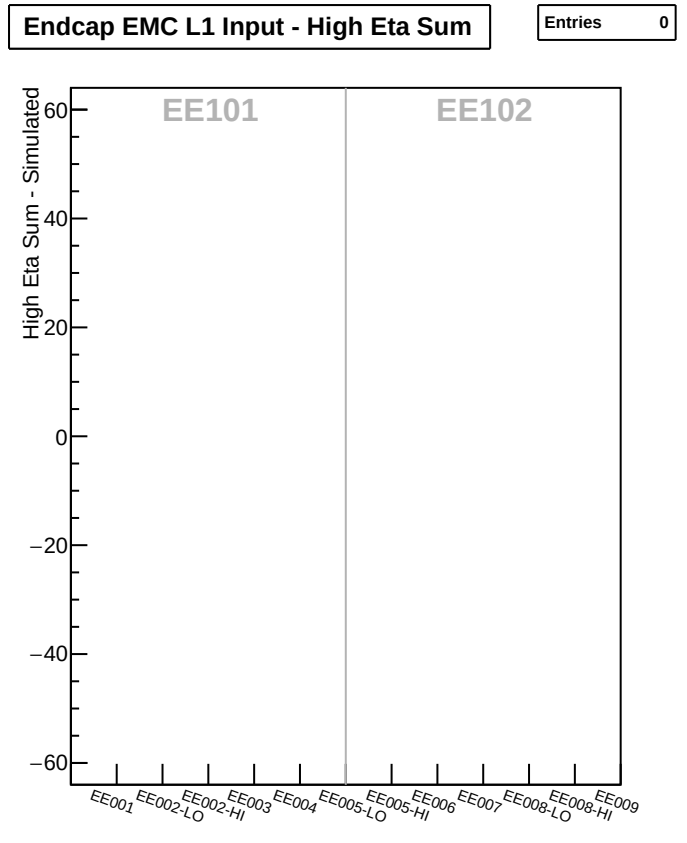

Barrel EMC L1 Input - HT.TP Bit
Entries 7286940
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

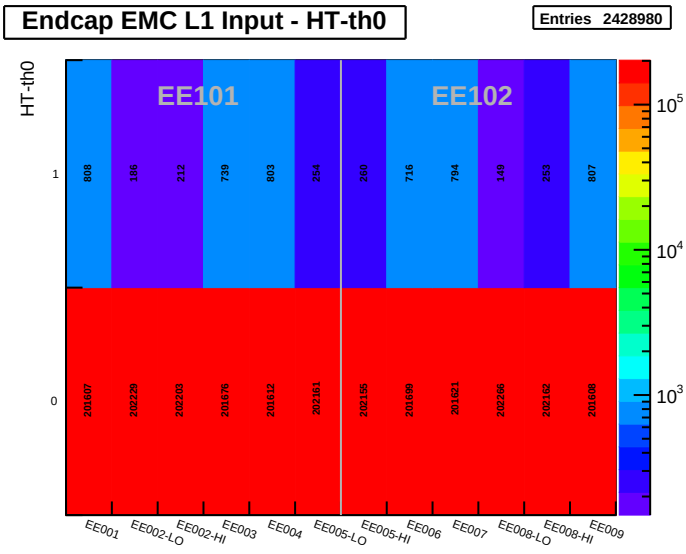

Barrel EMC L1 Input - High Tower Bits
Entries 5604
HT.TP Bit - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

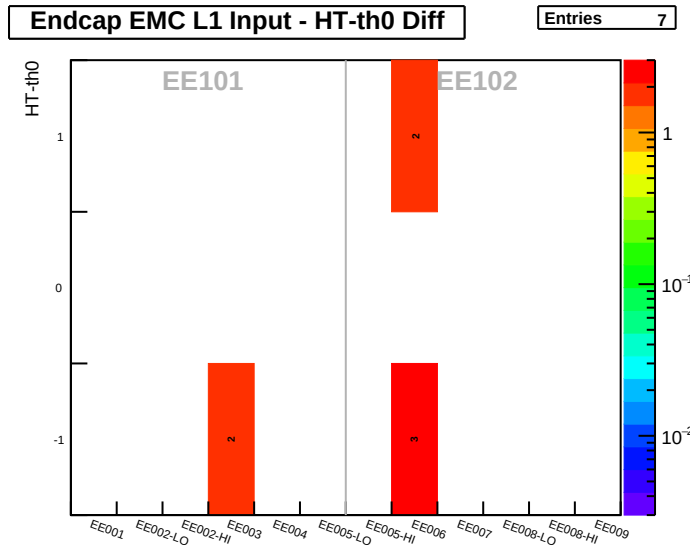

Endcap EMC L1 Input - Low Eta Sum
Entries 2428980
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

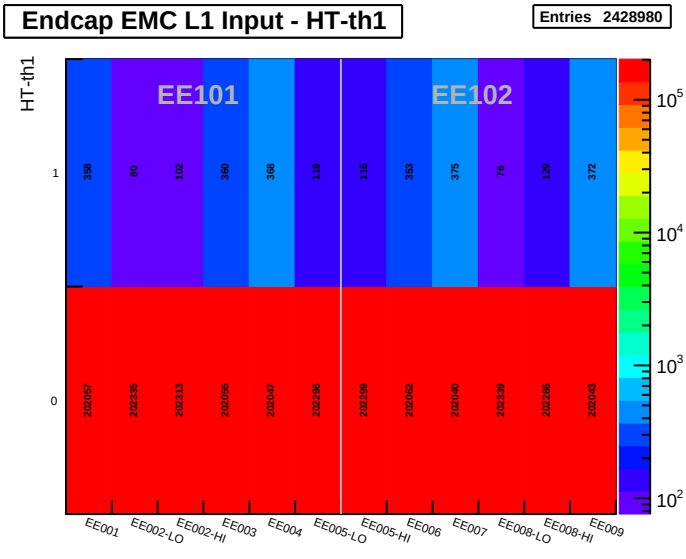

Endcap EMC L1 Input - Low Eta Sum
Entries 35794
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

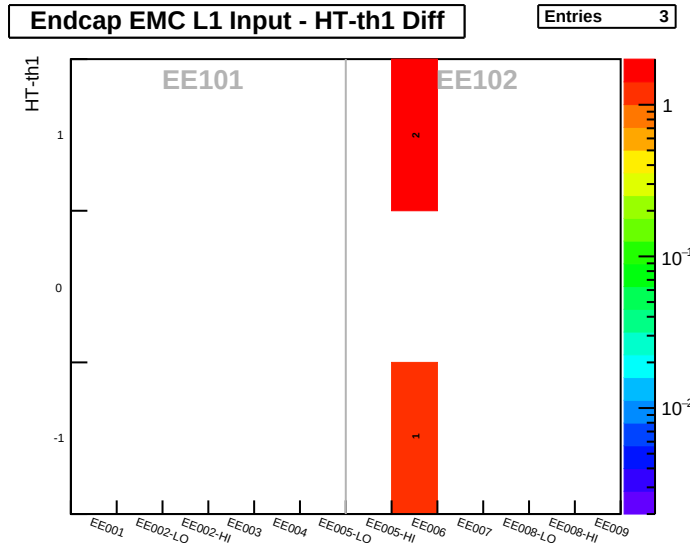

Endcap EMC L1 Input - High Eta Sum
Entries 2428980
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

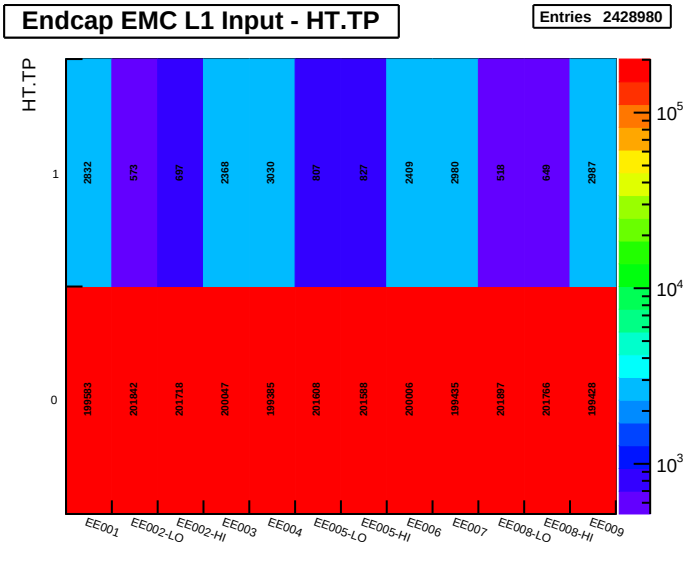

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

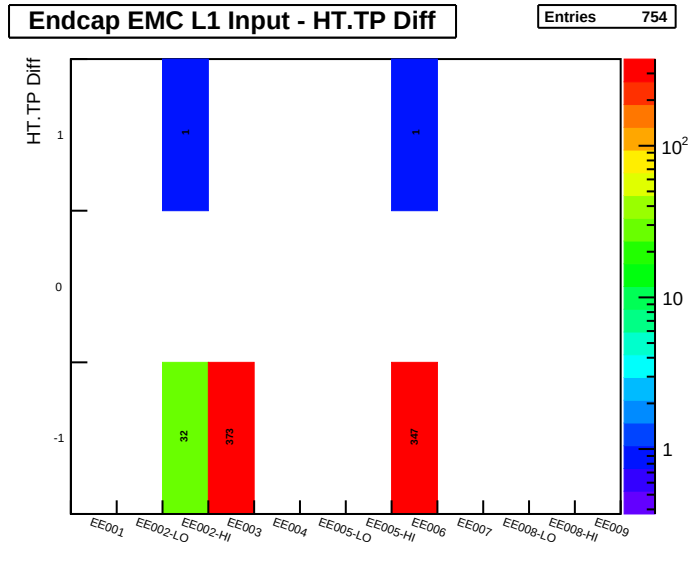

Endcap EMC L1 Input - HT-th0
Entries 2428980
HT-th0
EE101
EE102
808 186 212 739 803 254 260 716 794 149 253 807
201607 202229 202203 201676 201612 202161 202155 201699 201621 202266 202162 201608
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

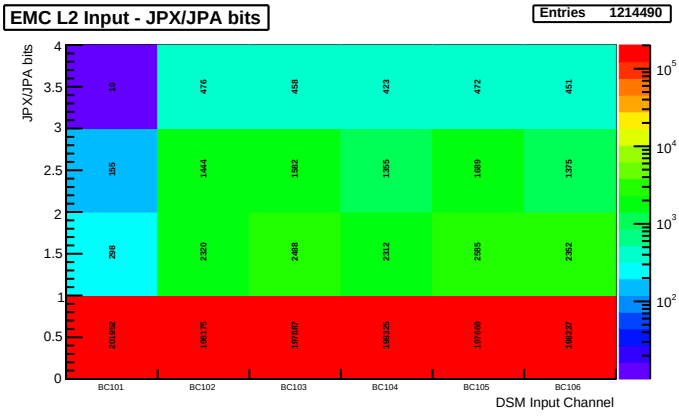

Endcap EMC L1 Input - HT-th0 Diff
Entries 7
HT-th0
EE101
EE102
2
2
3
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

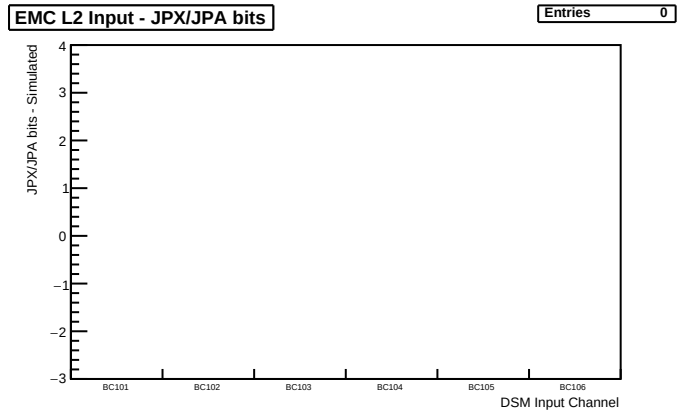

Endcap EMC L1 Input - HT-th1
Entries 2428980
HT-th1
EE101
EE102
358 80 102 360 368 119 116 333 375 76 126 372
202057 202335 202313 202055 202047 202296 202299 202062 202040 202339 202286 202043
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

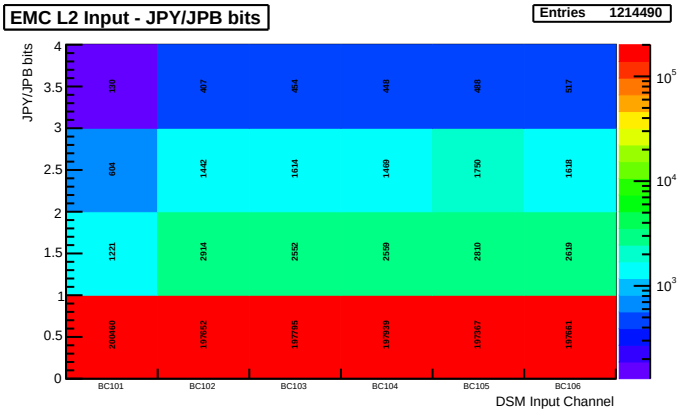

Endcap EMC L1 Input - HT-th1 Diff
Entries 3
HT-th1
EE101
EE102
2
1
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

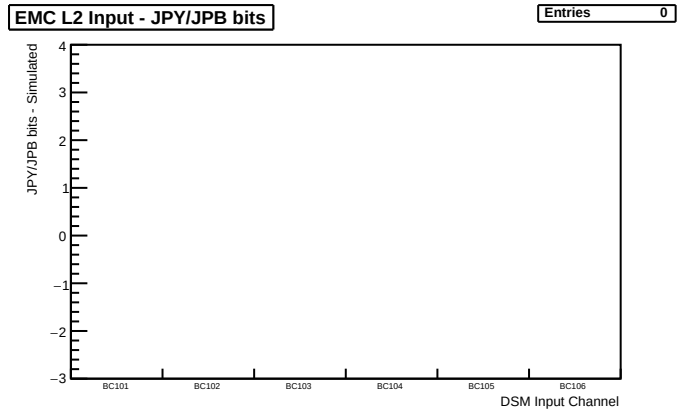

Endcap EMC L1 Input - HT.TP
Entries 2428980
HT.TP
2832 573 697 2368 3030 807 827 2409 2980 510 649 2987
199583 201842 201718 200047 199385 201608 201588 200006 199435 201897 201766 199426
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

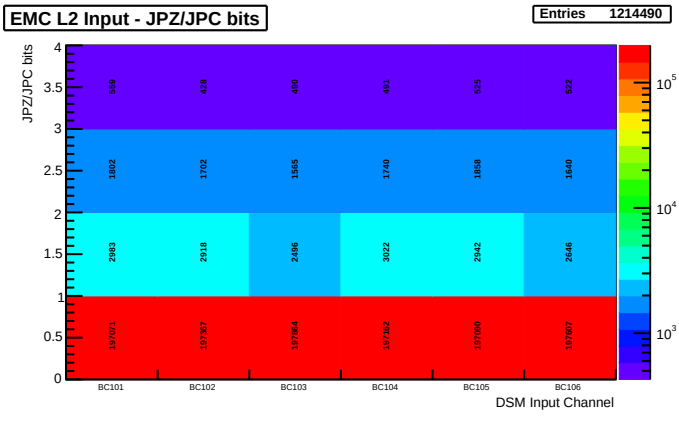

Endcap EMC L1 Input - HT.TP Diff
Entries 754
HT.TP Diff
1
1
32
373
347
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

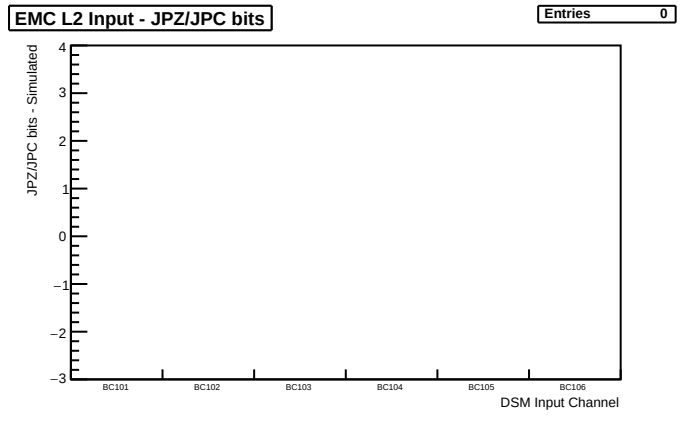

EMC L2 Input - JPX/JPA bits
Entries 1214490
JPX/JPA bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

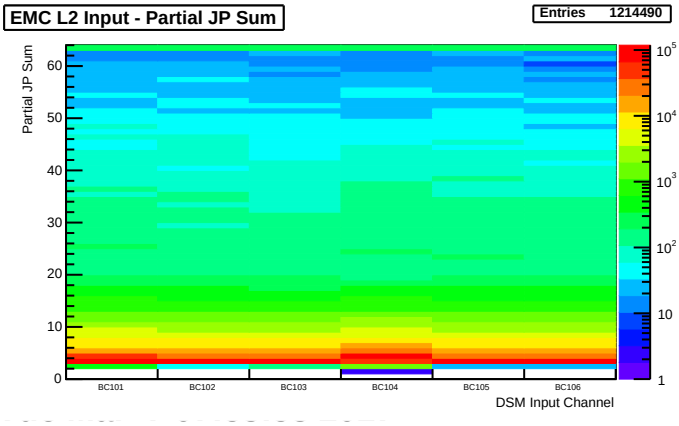

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

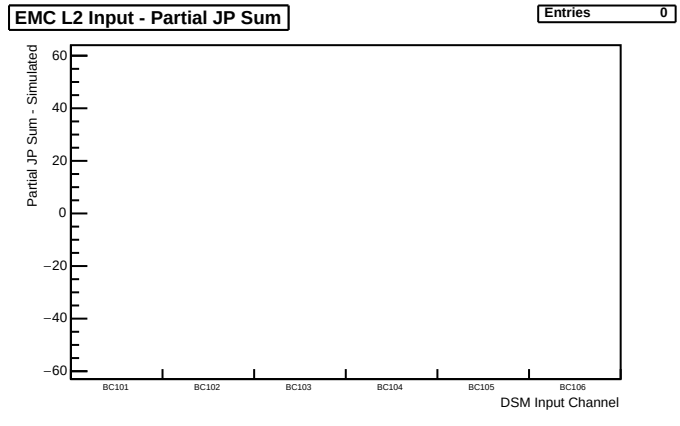

EMC L2 Input - JPY/JPB bits
Entries 1214490
JPY/JPB bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

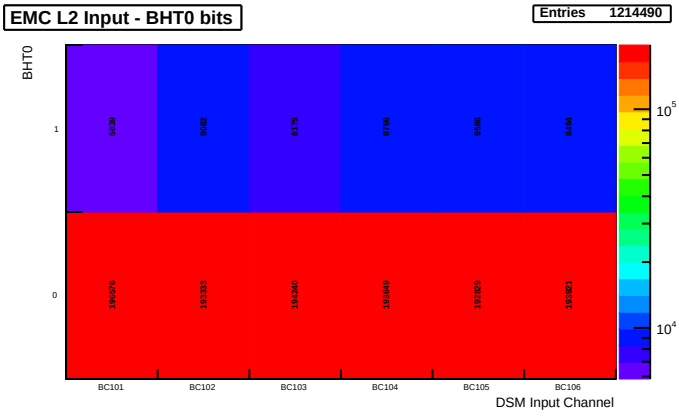

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

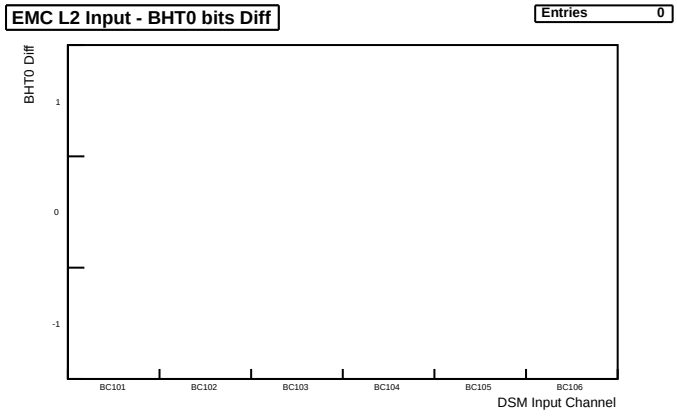

EMC L2 Input - JPZ/JPC bits
Entries 1214490
JPZ/JPC bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

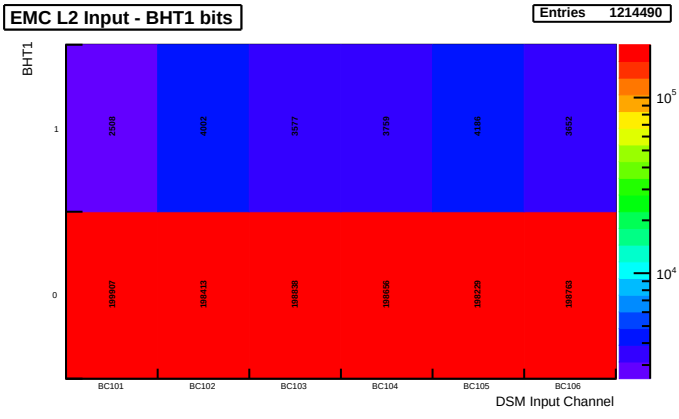

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

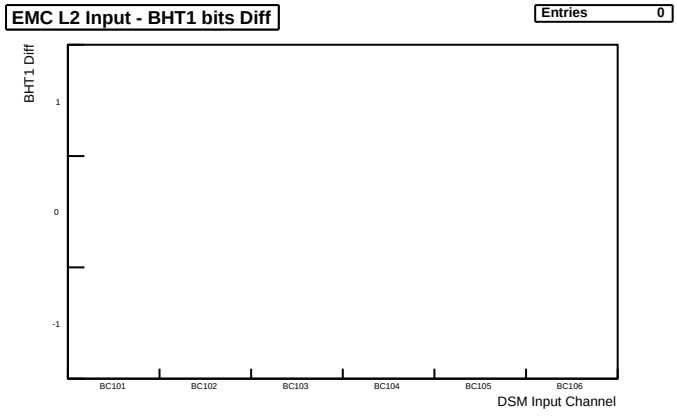

EMC L2 Input - Partial JP Sum
Entries 1214490
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

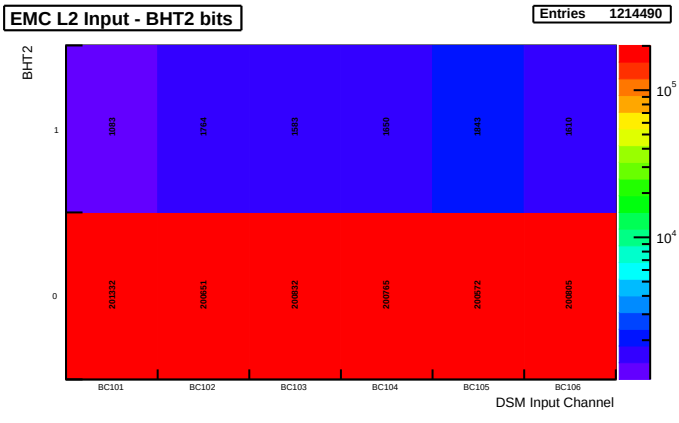

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

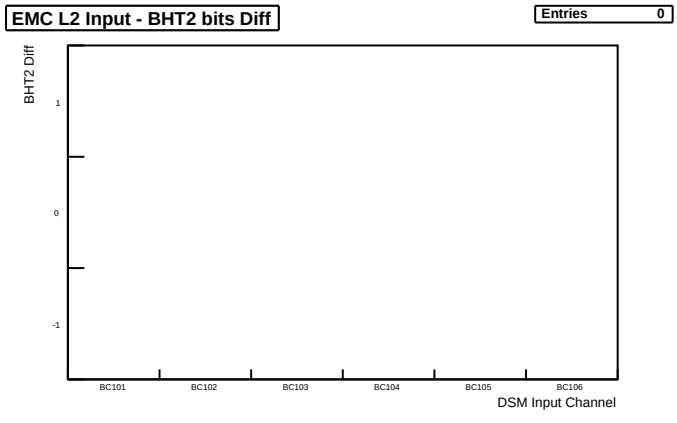

EMC L2 Input - BHT0 bits
Entries 1214490
BHT0
1
0
5939
9092
8175
9766
9598
8494
196576
193333
194340
193949
192829
193921
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel
10^5
10^4

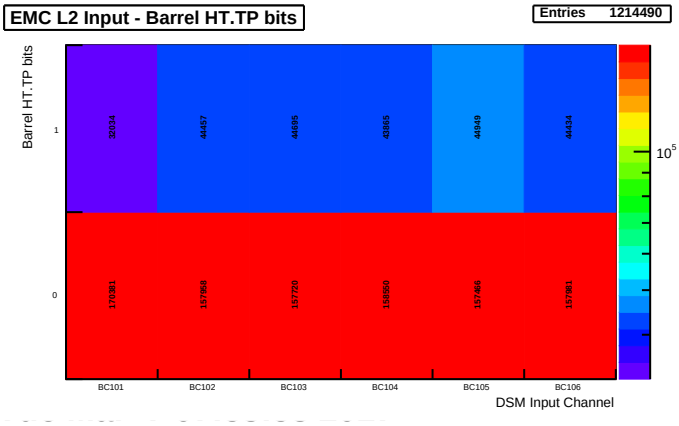

EMC L2 Input - BHT0 bits Diff
Entries 0
BHT0 Diff
1
0
-1
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

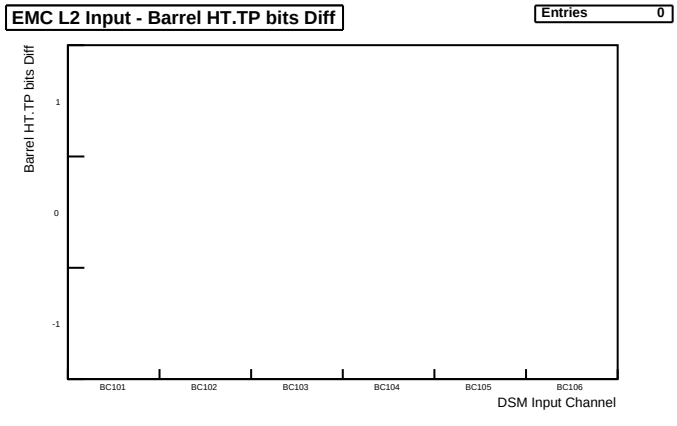

EMC L2 Input - BHT1 bits
Entries 1214490
BHT1
1
0
2508
4002
3577
3759
4136
3652
199907
198413
198838
198656
198229
198763
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel
10^5
10^4

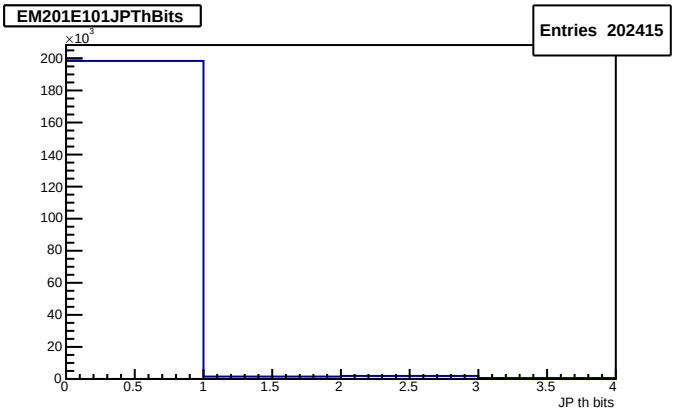

EMC L2 Input - BHT1 bits Diff
Entries 0
BHT1 Diff
1
0
-1
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

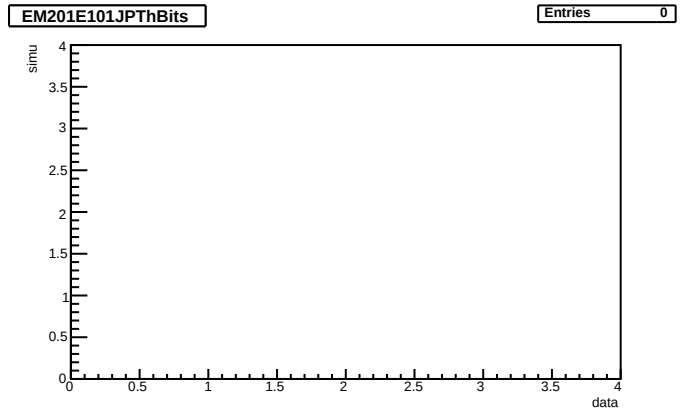

EMC L2 Input - BHT2 bits
Entries 1214490
BHT2
1
0
1033
1714
1533
1650
1843
1610
201332
200651
200832
200765
200572
200805
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel
10^5
10^4

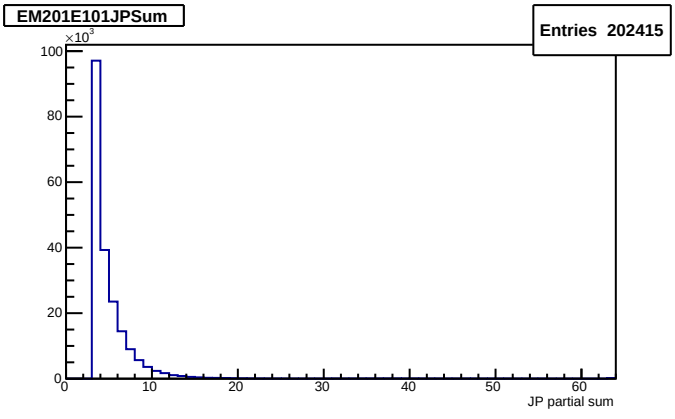

EMC L2 Input - BHT2 bits Diff
Entries 0
BHT2 Diff
1
0
-1
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

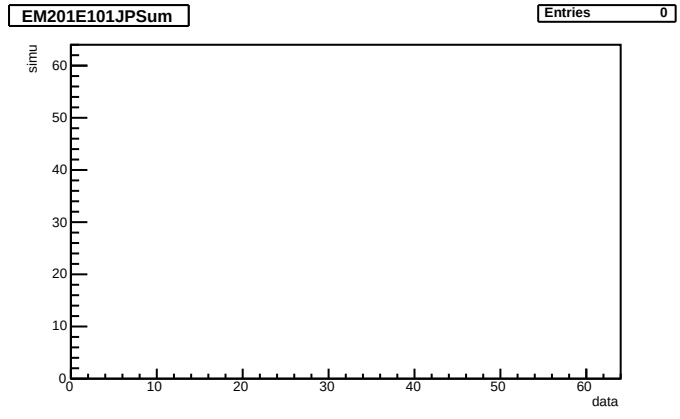

EMC L2 Input - Barrel HT.TP bits
Entries 1214490
Barrel HT.TP bits
1
0
32034
44457
44695
43865
44949
44434
170381
157868
157720
158560
157466
157881
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel
10^5

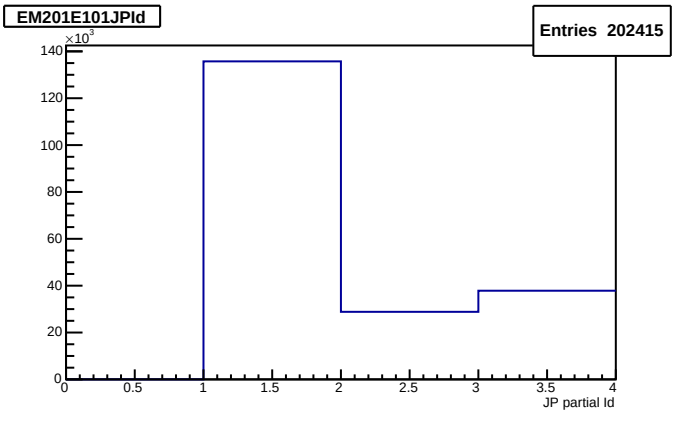

EMC L2 Input - Barrel HT.TP bits Diff
Entries 0
Barrel HT.TP bits Diff
1
0
-1
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

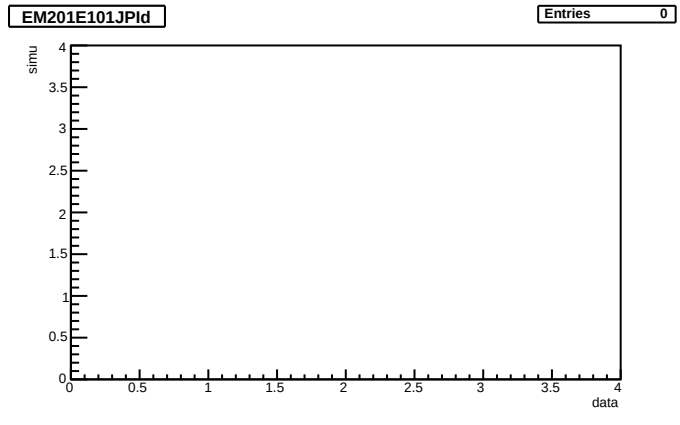

EM201E101JPThBits
Entries 202415
×10^3
JP th bits

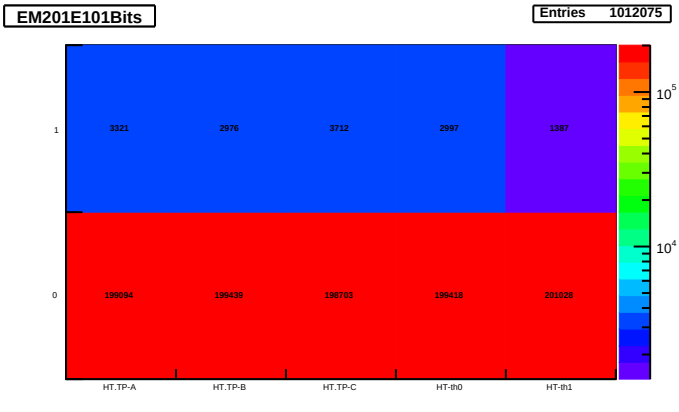

EM201E101JPThBits
Entries 0
simu
data

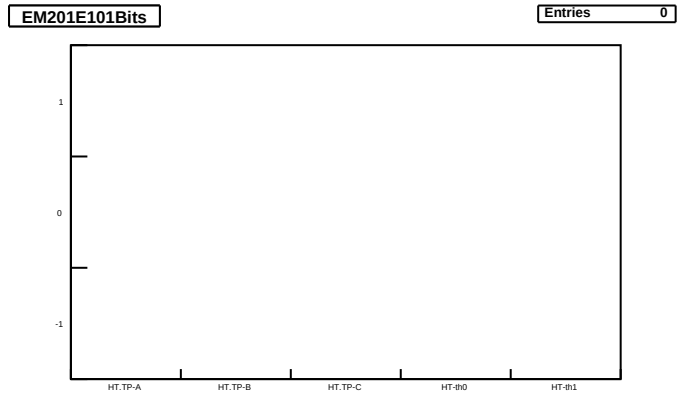

EM201E101JPSum
Entries 202415
×10^3
JP partial sum

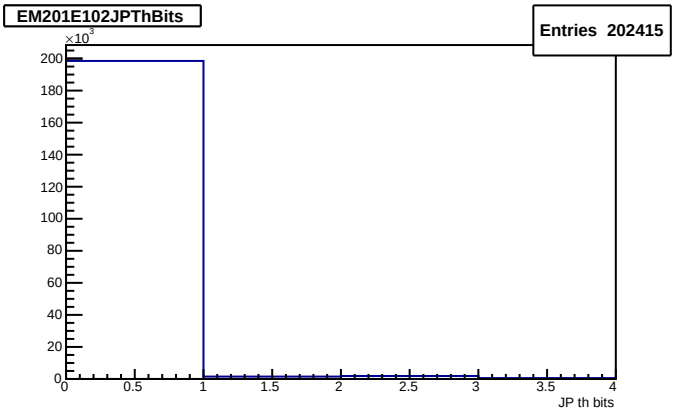

EM201E101JPSum
Entries 0
simu
data

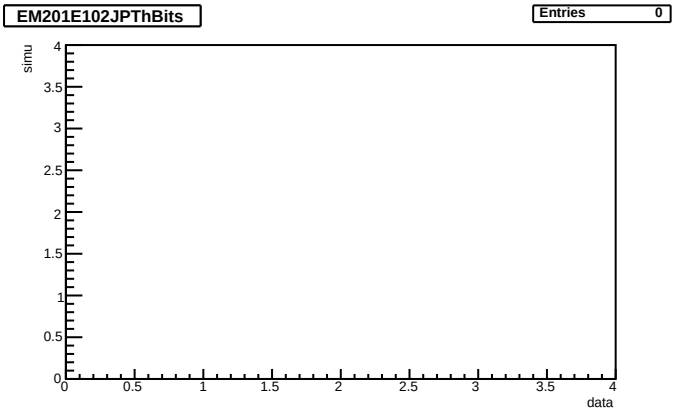

EM201E101JPId
Entries 202415
×10^3
JP partial Id

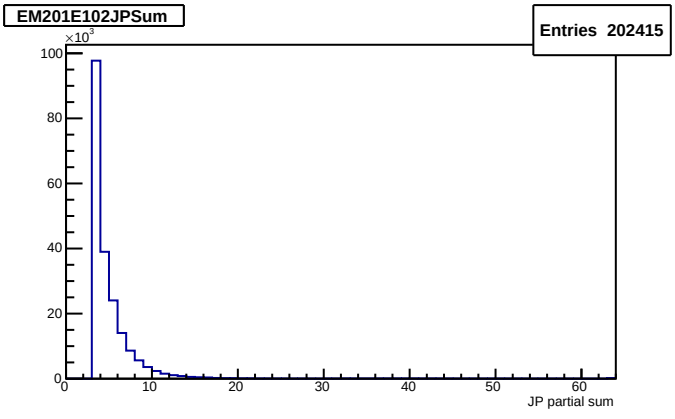

EM201E101JPId
Entries 0
simu
data

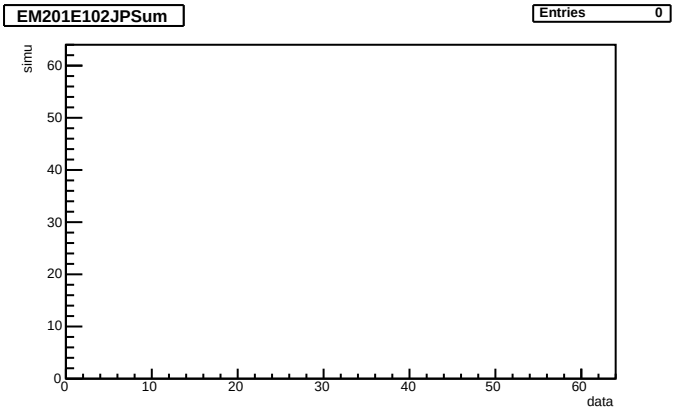

EM201E101Bits
Entries 1012075
3321
2976
3712
2997
1387
199094
199439
198703
199418
201028
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

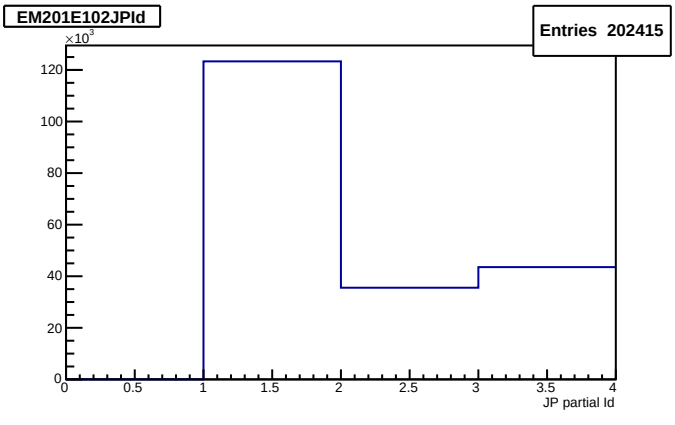

EM201E101Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

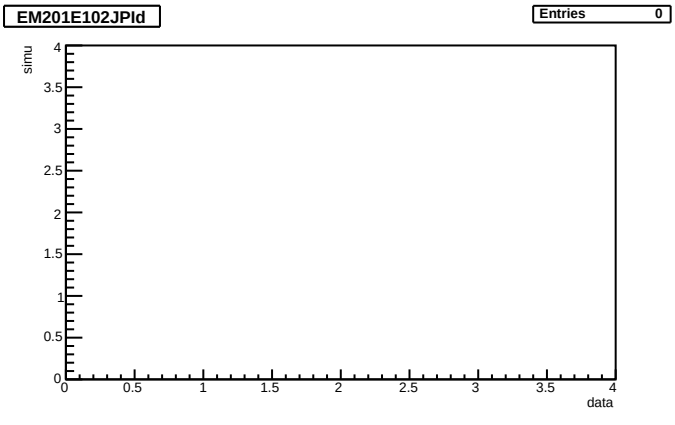

EM201E102JPThBits
Entries 202415
×10^3
JP th bits

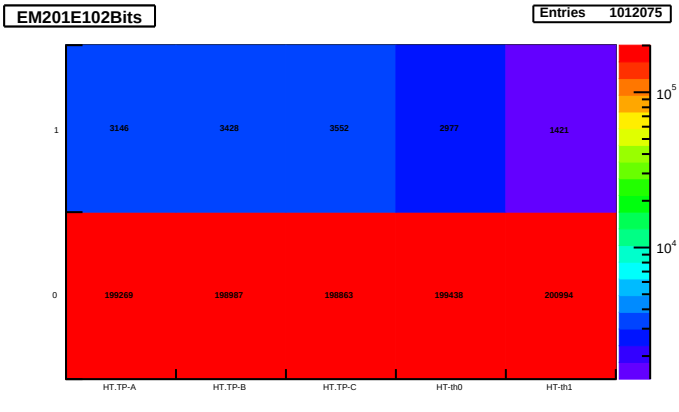

EM201E102JPThBits
Entries 0
simu
data

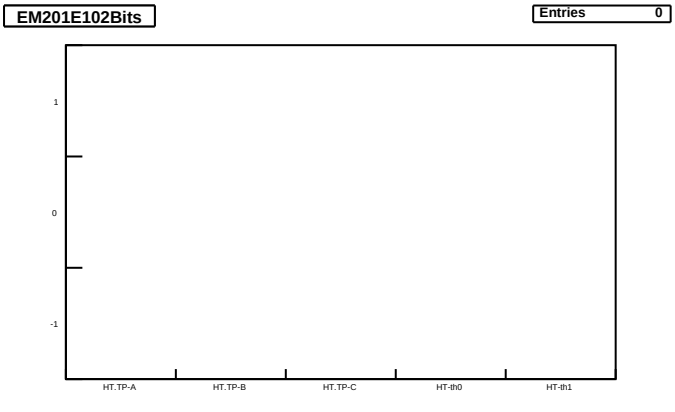

EM201E102JPSum
Entries 202415
×10^3
JP partial sum

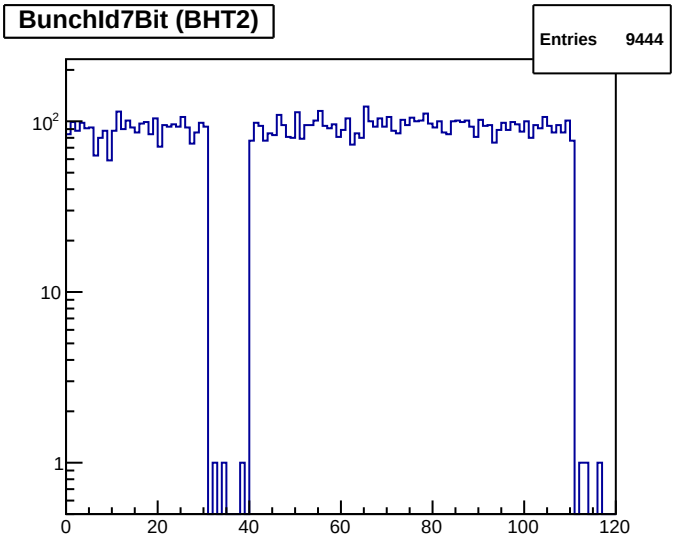

EM201E102JPSum
Entries 0
simu
data

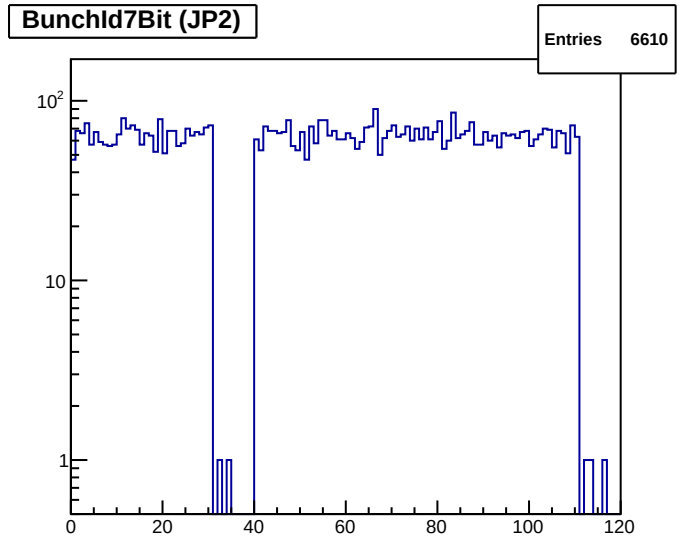

EM201E102JPId
Entries 202415
×10^3
JP partial Id

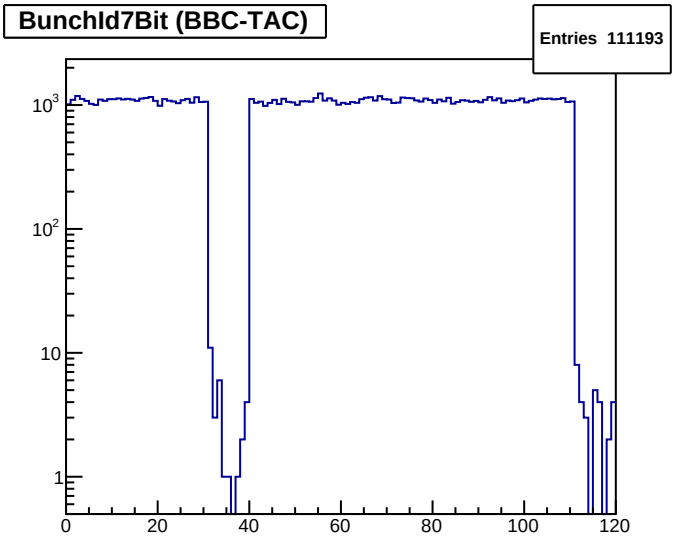

EM201E102JPId
Entries 0
simu
data

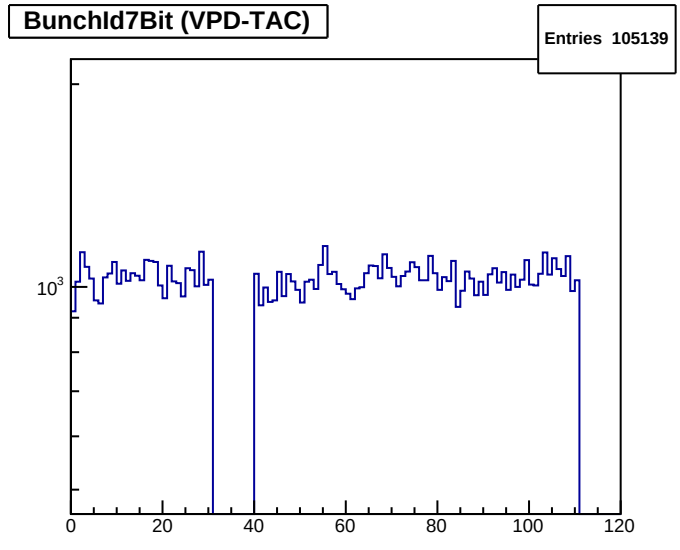

EM201E102Bits
Entries 1012075
3146
3428
3552
2977
1421
199269
198987
198863
199438
200994
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

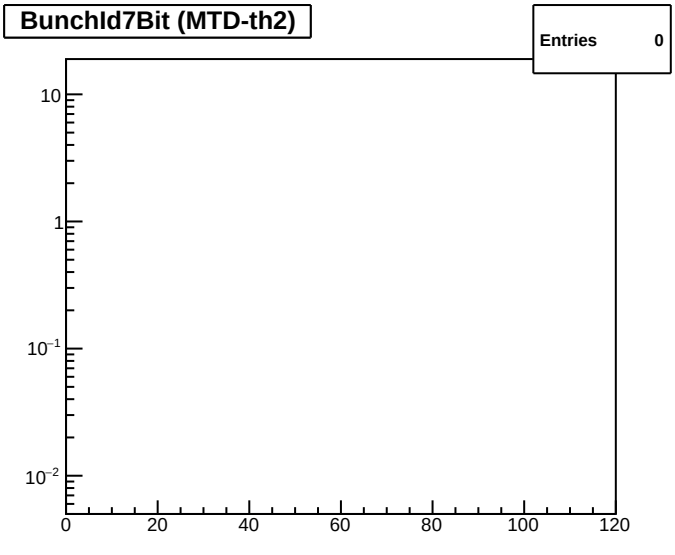

EM201E102Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

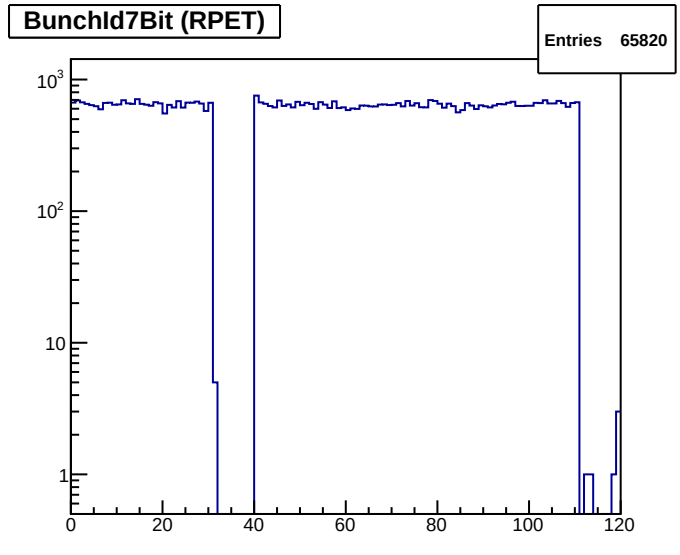

BunchId7Bit (BHT2)
Entries 9444

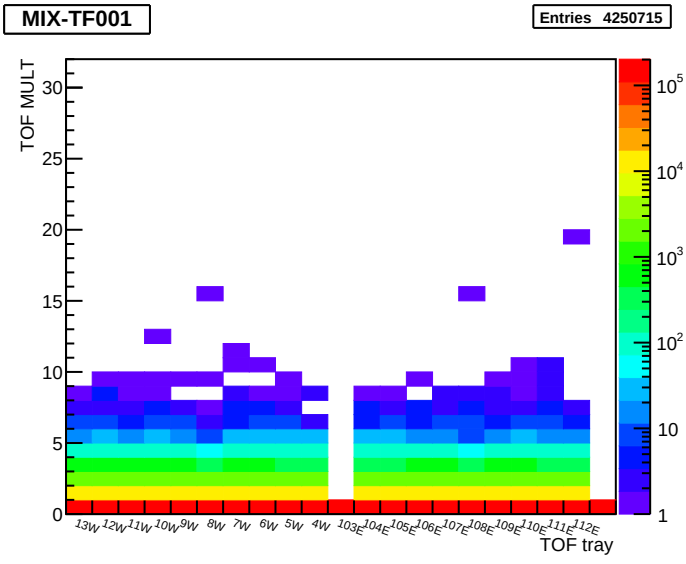

BunchId7Bit (JP2)
Entries 6610

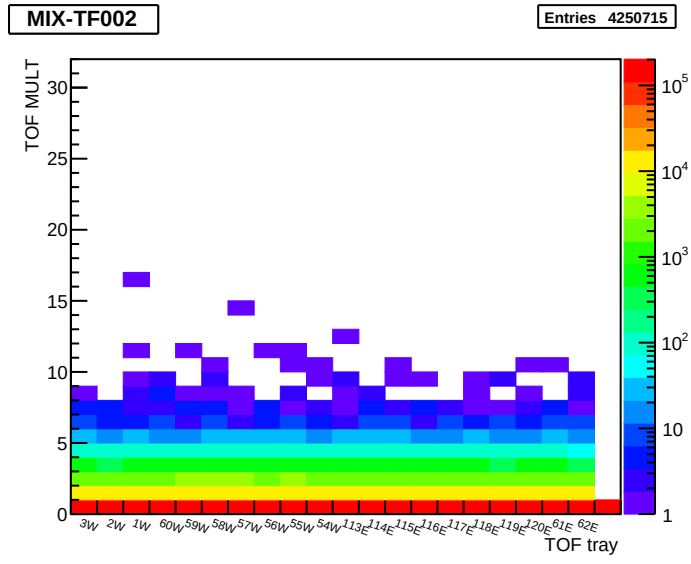

BunchId7Bit (BBC-TAC)
Entries 111193

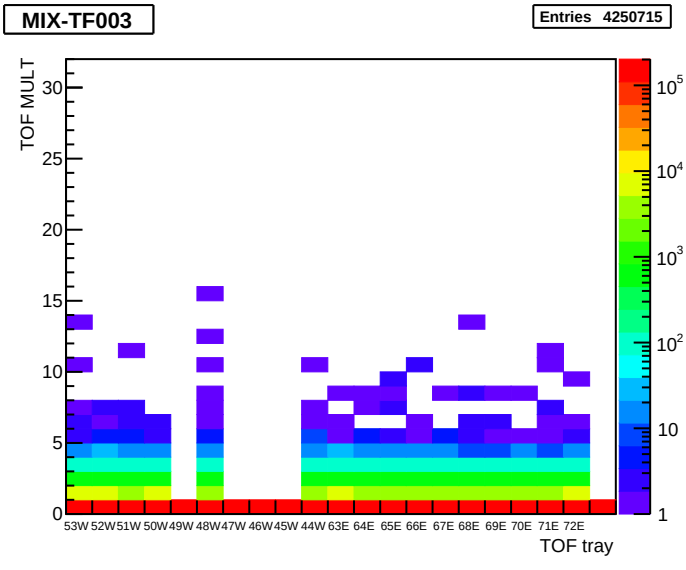

BunchId7Bit (VPD-TAC)
Entries 105139

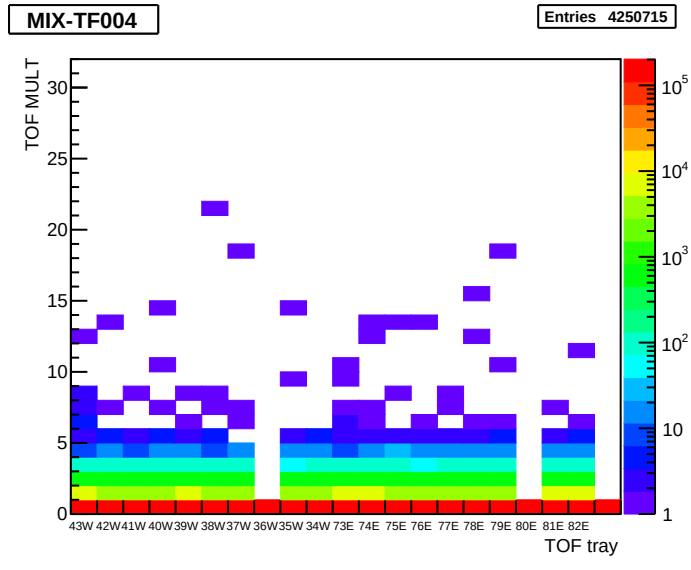

BunchId7Bit (MTD-th2)
Entries 0

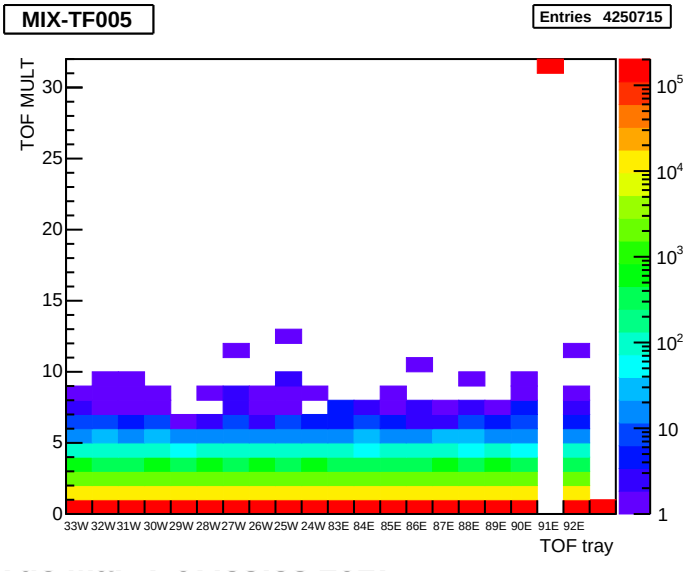

BunchId7Bit (RPET)
Entries 65820

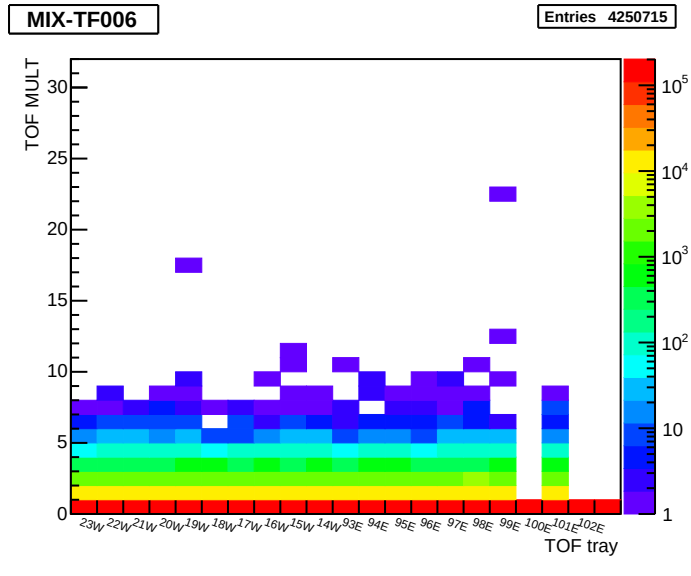

MIX-TF001
Entries 4250715
TOF MULT
TOF tray

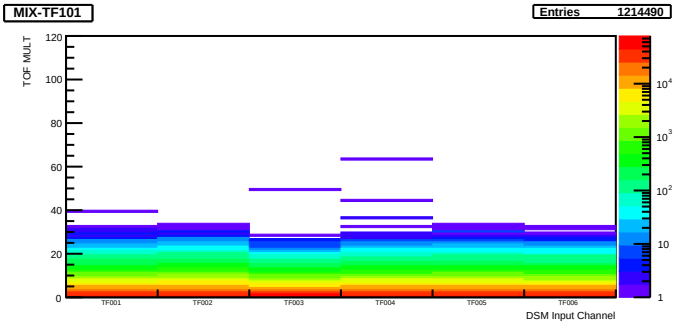

MIX-TF002
Entries 4250715
TOF MULT
TOF tray

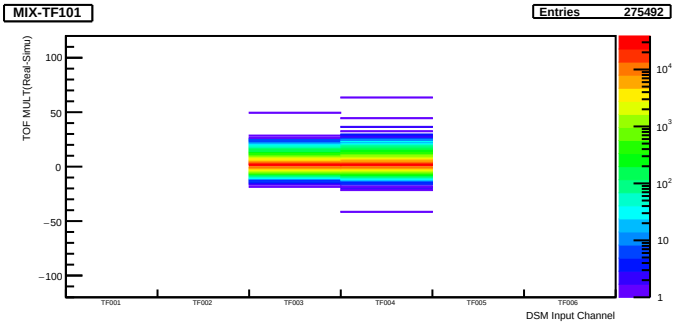

MIX-TF003
Entries 4250715
TOF MULT
TOF tray

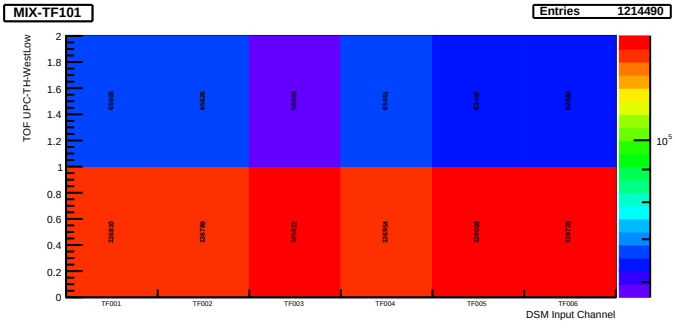

MIX-TF004
Entries 4250715
TOF MULT
TOF tray

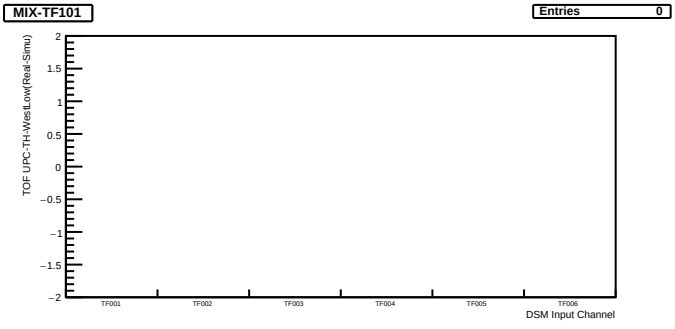

MIX-TF005
Entries 4250715
TOF MULT
TOF tray

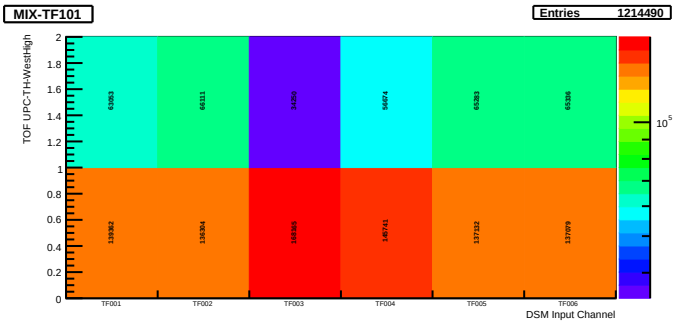

MIX-TF006
Entries 4250715
TOF MULT
TOF tray

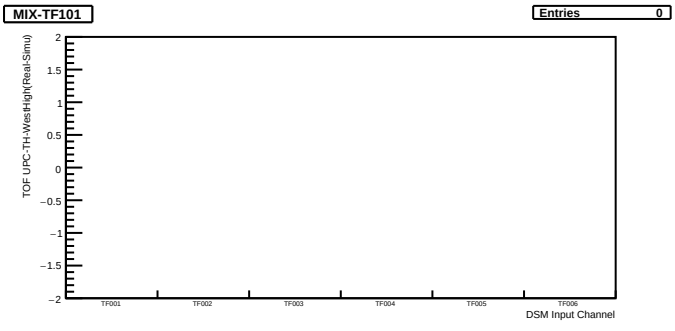

MIX-TF101
Entries 1214490
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

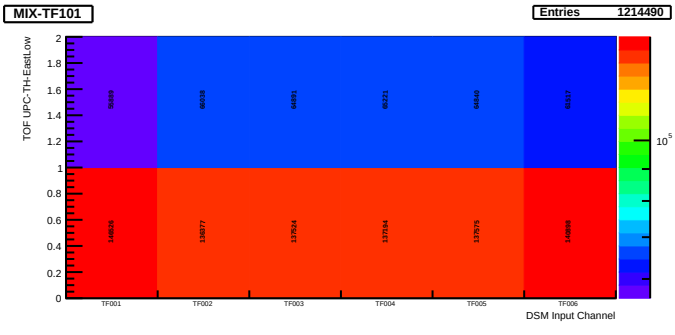

MIX-TF101
Entries 275492
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

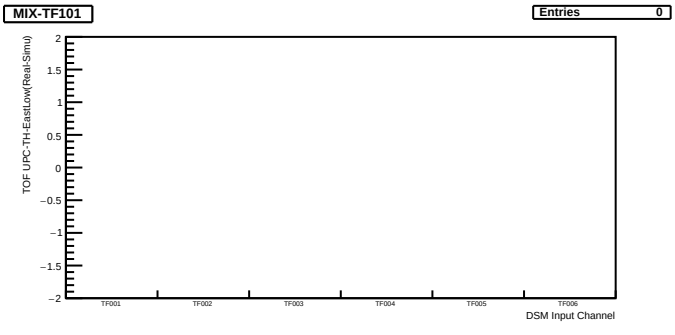

MIX-TF101
Entries 1214490
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

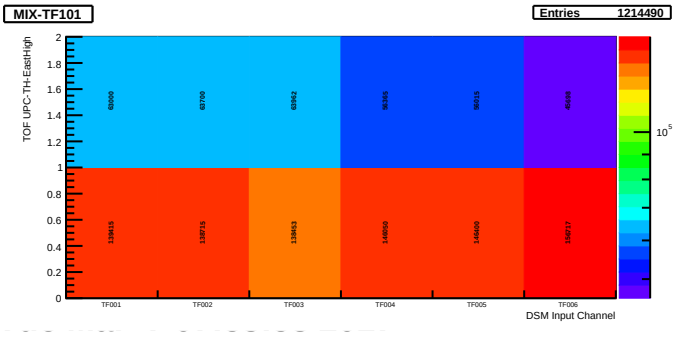

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

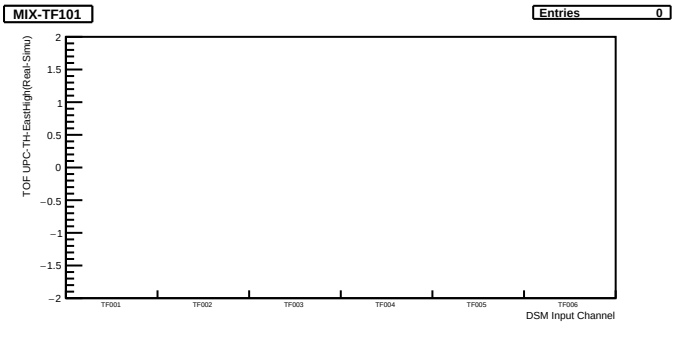

MIX-TF101
Entries 1214490
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

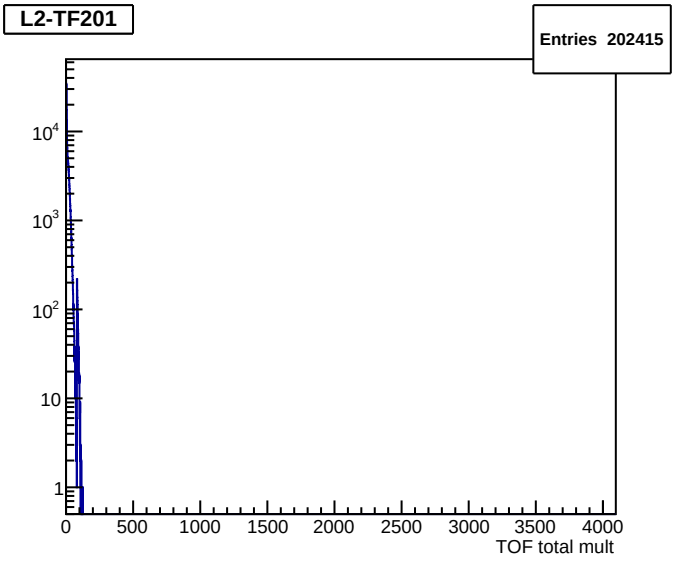

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

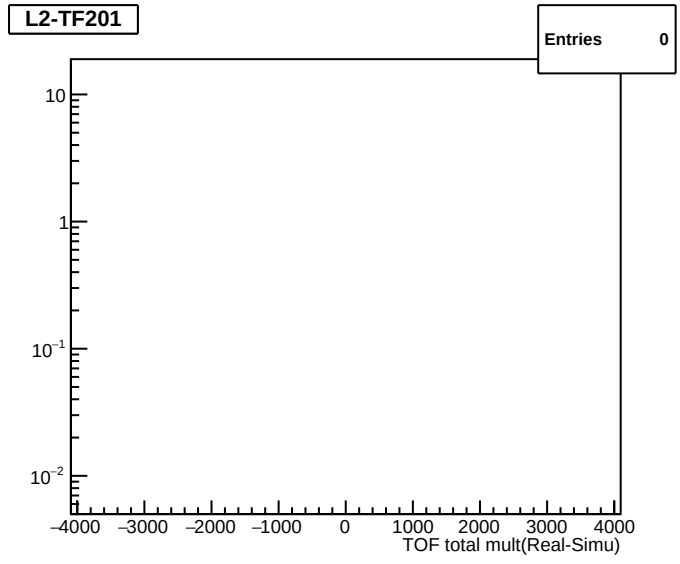

MIX-TF101
Entries 1214490
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

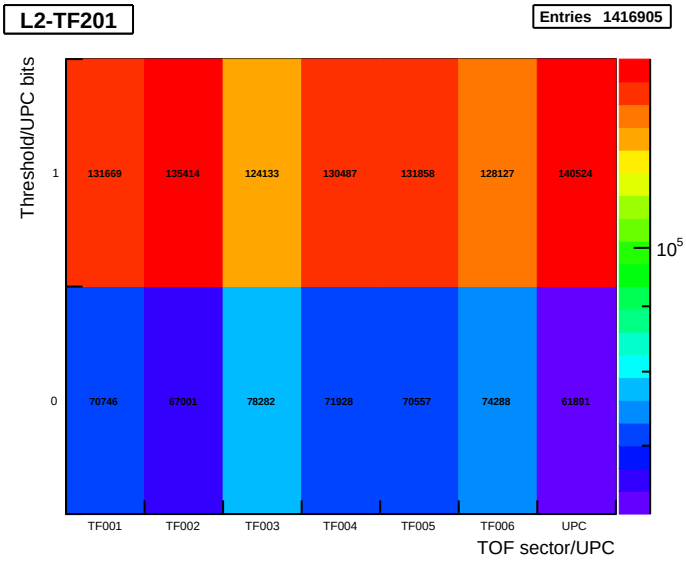

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

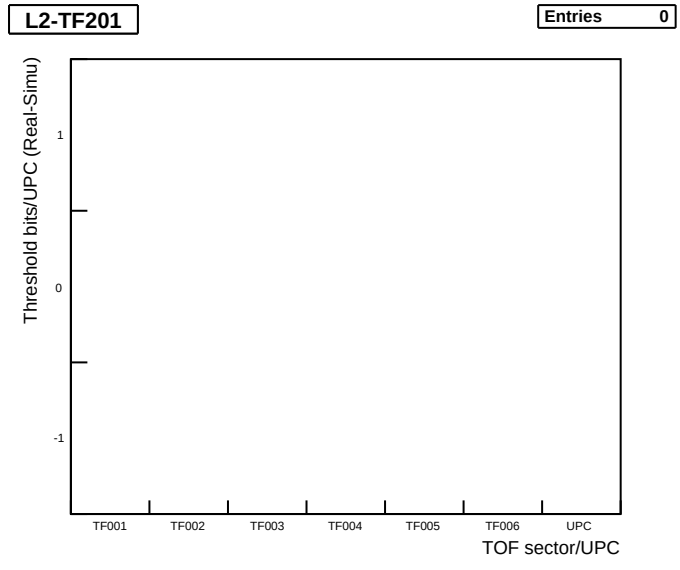

MIX-TF101
Entries 1214490
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

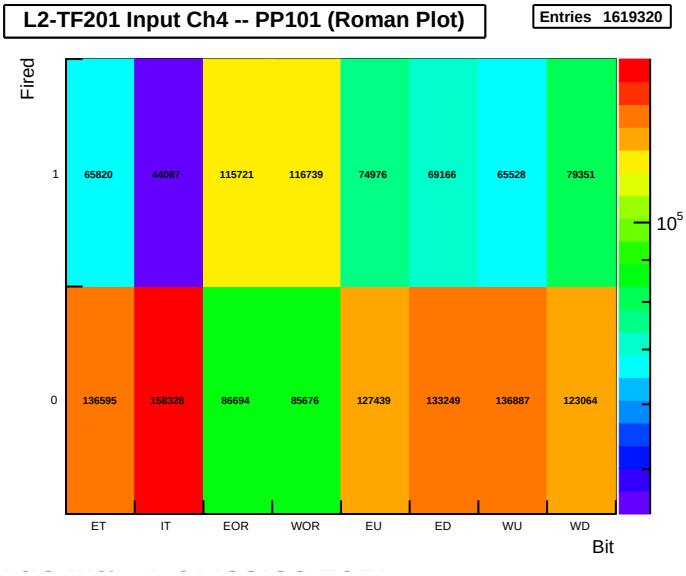

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

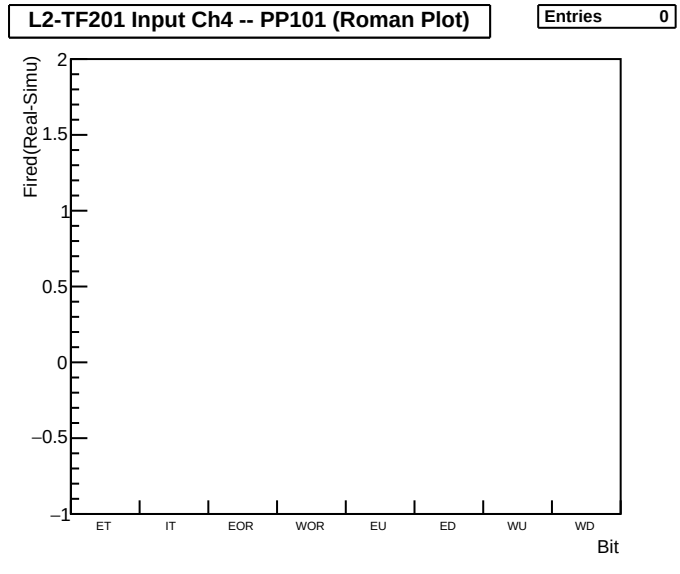

L2-TF201
Entries 202415
TOF total mult

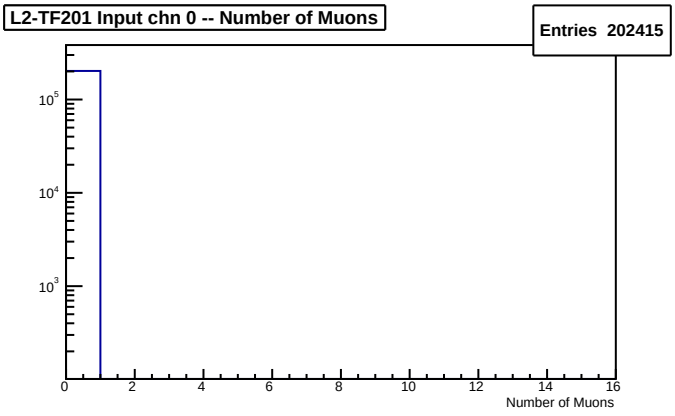

L2-TF201
Entries 0
TOF total mult(Real-Simu)

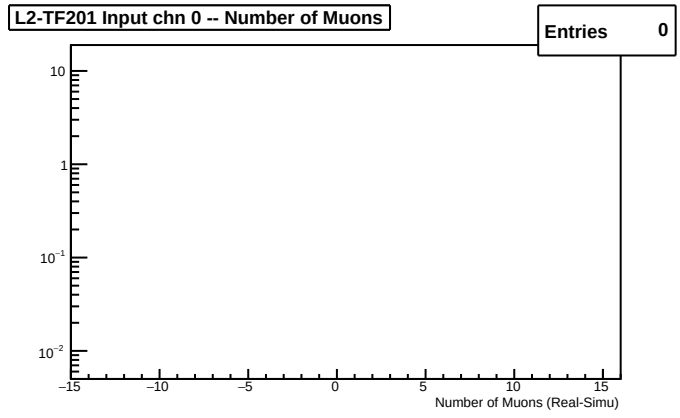

L2-TF201
Entries 1416905
Threshold/UPC bits
131669
135414
124133
130487
131858
128127
140524
70746
67001
78282
71928
70557
74288
61891
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

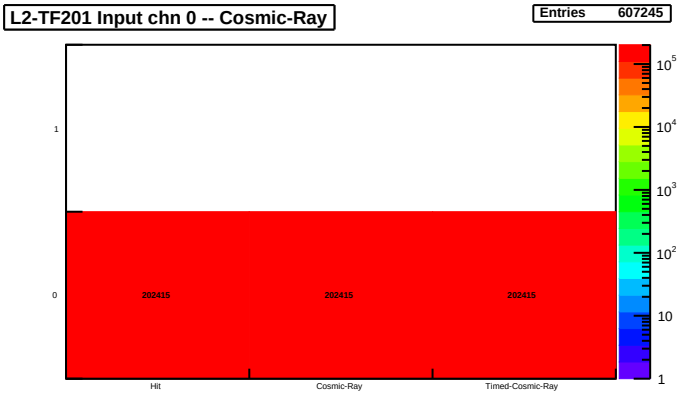

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001
TF002
TF003
TF004
TF005
TF006
UPC
TOF sector/UPC

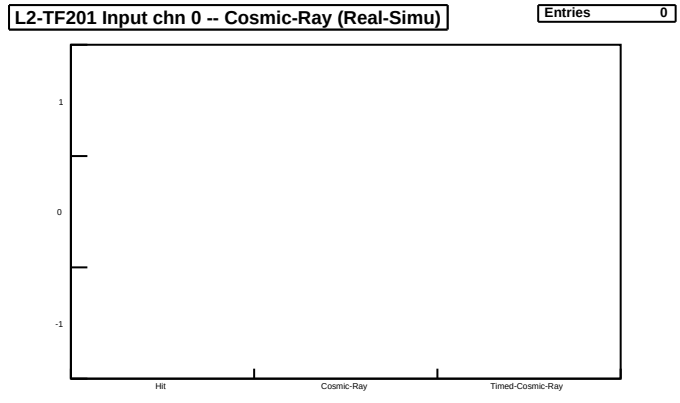

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 1619320
Fired
65820
44087
115721
116739
74976
69166
65528
79351
136595
158328
86694
85676
127439
133249
136887
123064
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

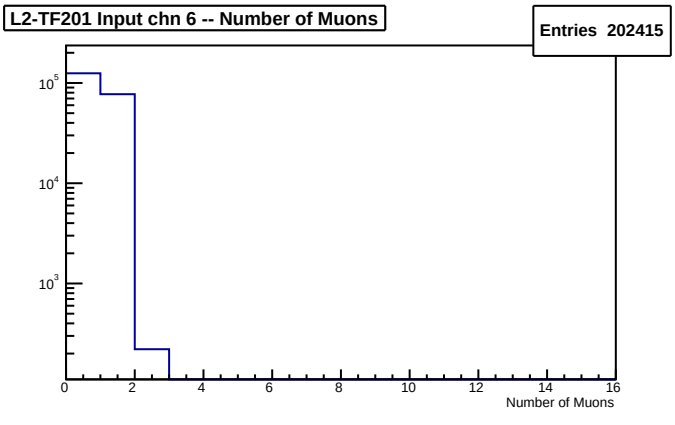

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 0
Fired(Real-Simu)
ET
IT
EOR
WOR
EU
ED
WU
WD
Bit

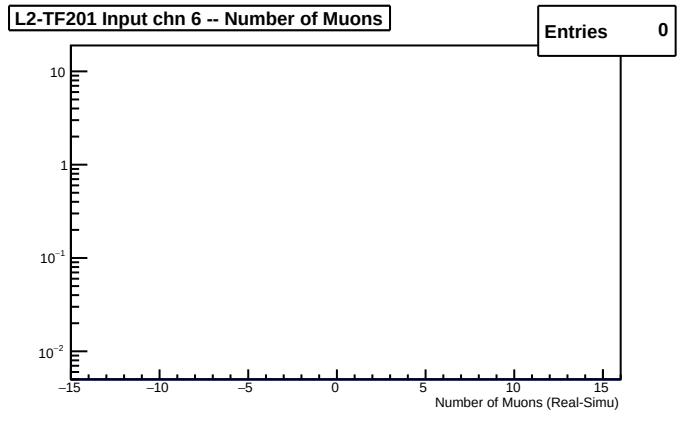

L2-TF201 Input chn 0 -- Number of Muons
Entries 202415
Number of Muons

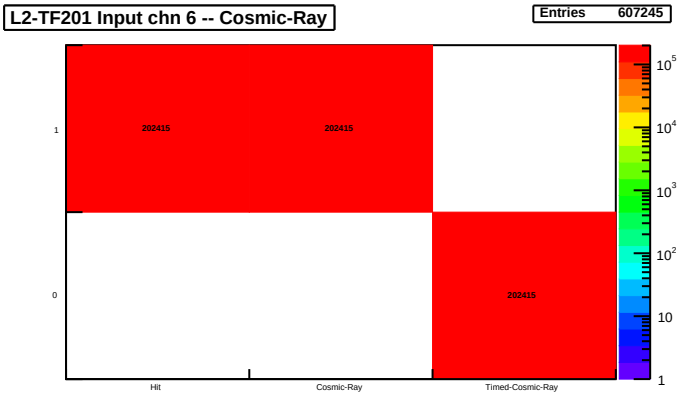

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

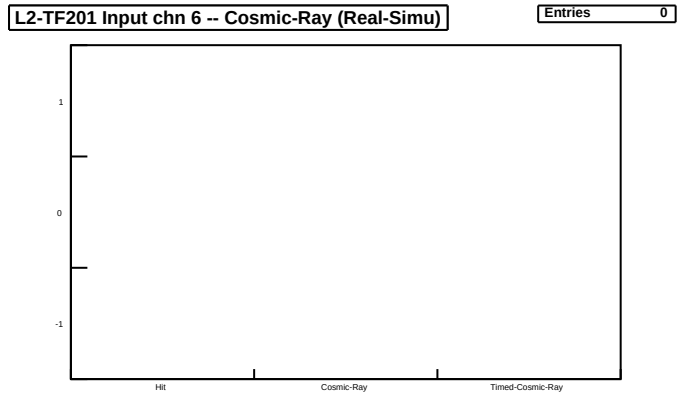

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 607245
202415
202415
202415
Hit
Cosmic-Ray
Timed-Cosmic-Ray

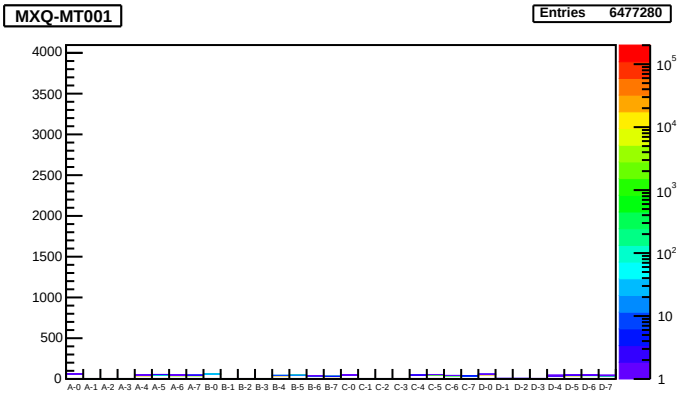

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

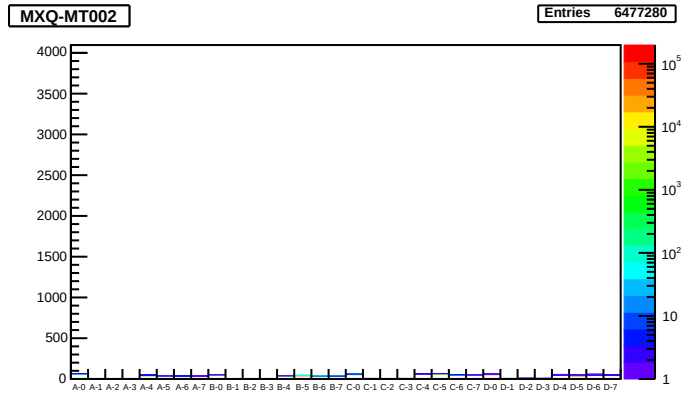

L2-TF201 Input chn 6 -- Number of Muons
Entries 202415
Number of Muons

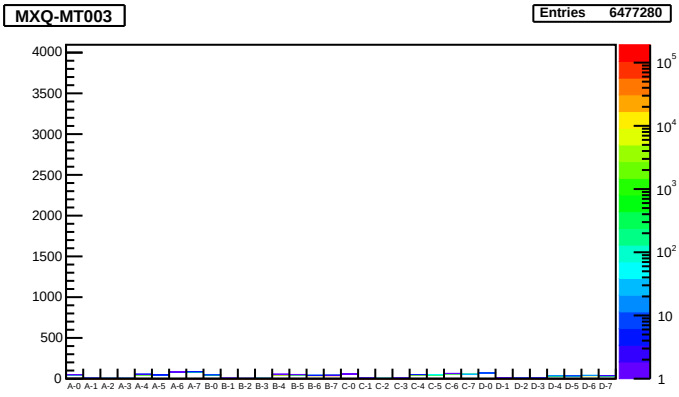

L2-TF201 Input chn 6 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

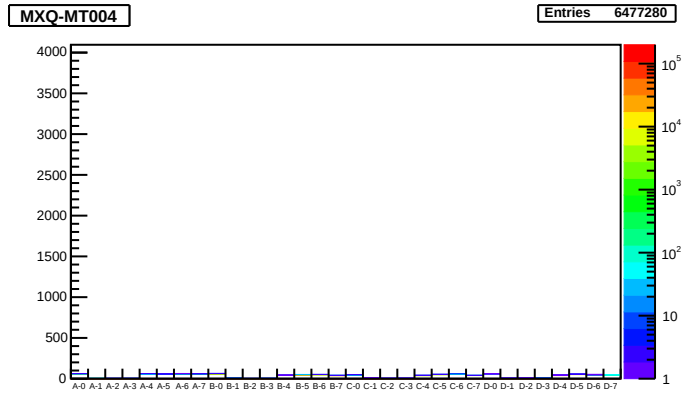

L2-TF201 Input chn 6 -- Cosmic-Ray
Entries 607245
202415
202415
202415
Hit
Cosmic-Ray
Timed-Cosmic-Ray

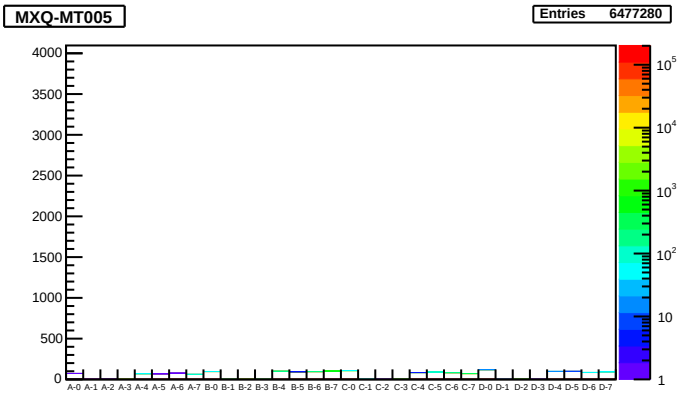

L2-TF201 Input chn 6 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

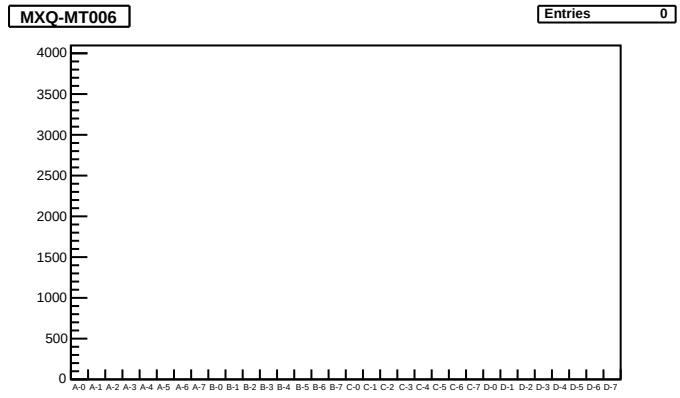

MXQ-MT001
Entries 6477280

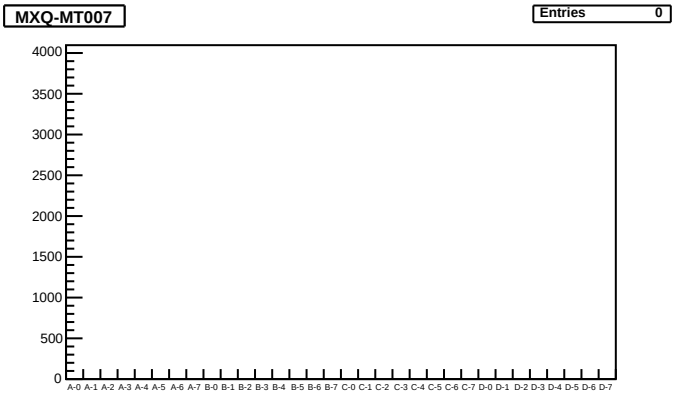

MXQ-MT002
Entries 6477280

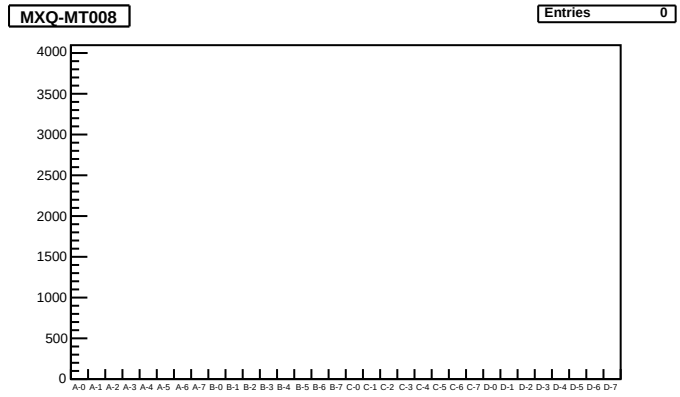

MXQ-MT003
Entries 6477280

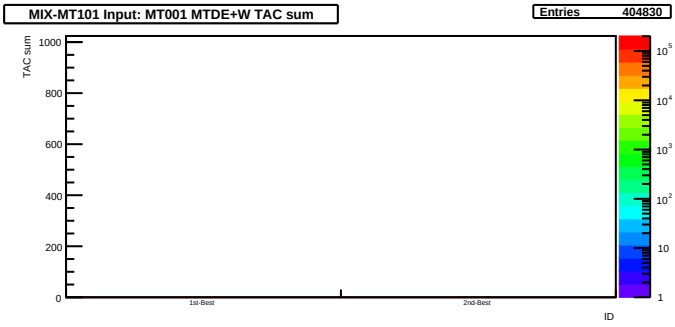

MXQ-MT004
Entries 6477280

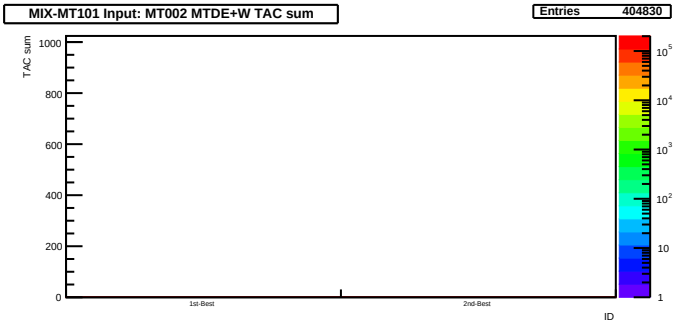

MXQ-MT005
Entries 6477280

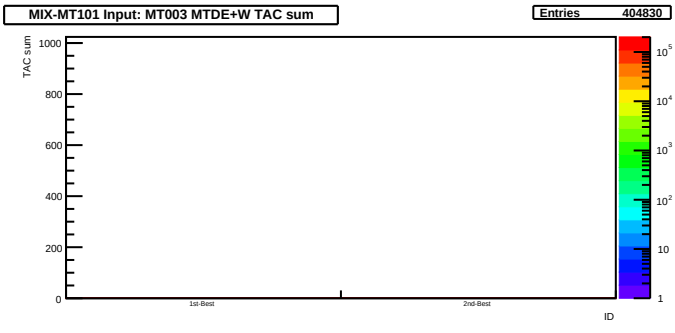

MXQ-MT006
Entries 0

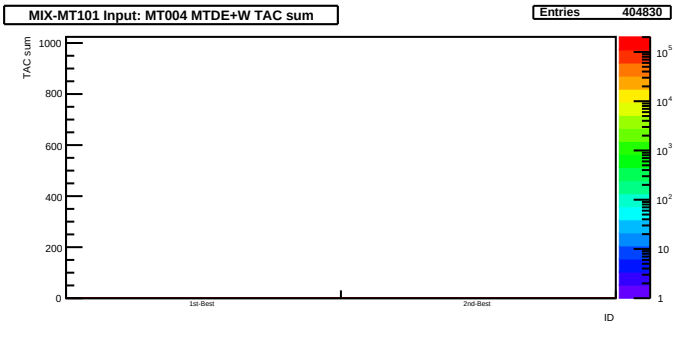

MXQ-MT007
Entries 0

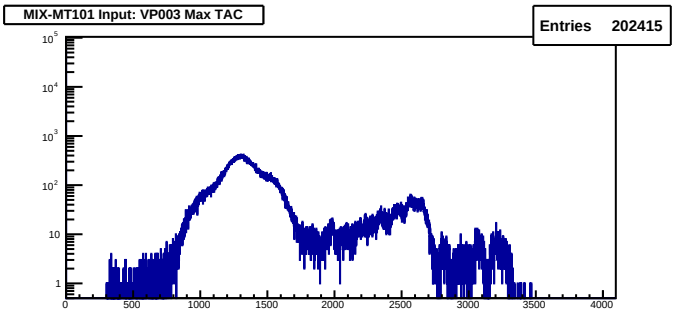

MXQ-MT008
Entries 0

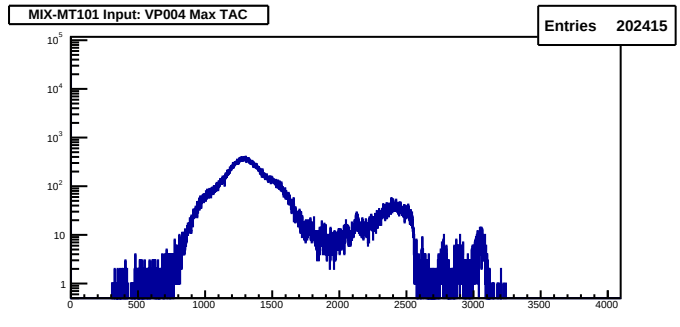

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 404830
TAC sum
1st-Best
2nd-Best
ID

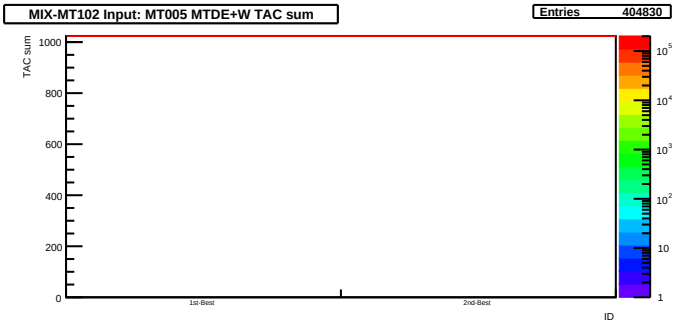

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 404830
TAC sum
1st-Best
2nd-Best
ID

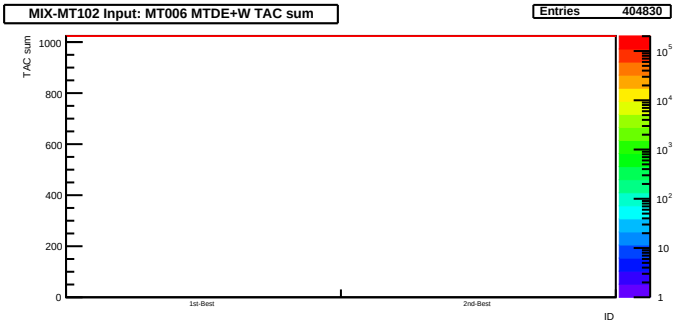

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 404830
TAC sum
1st-Best
2nd-Best
ID

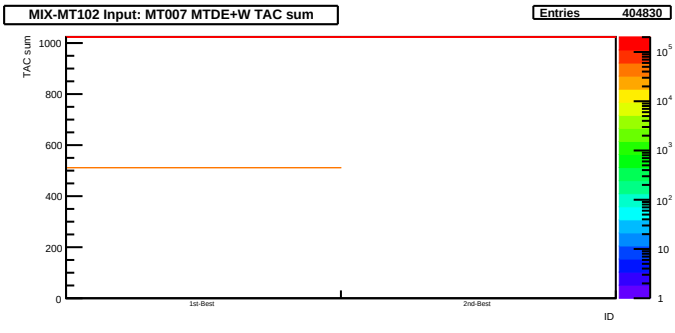

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 404830
TAC sum
1st-Best
2nd-Best
ID

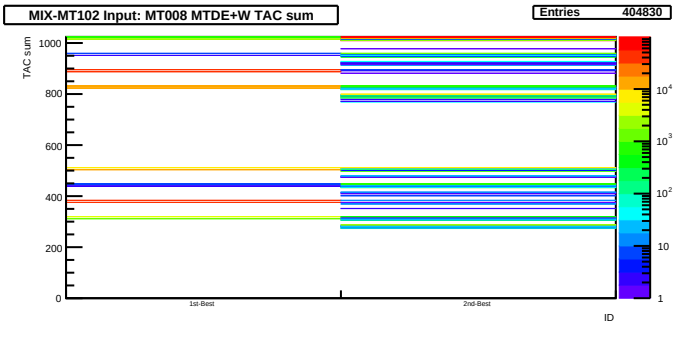

MIX-MT101 Input: VP003 Max TAC
Entries 202415

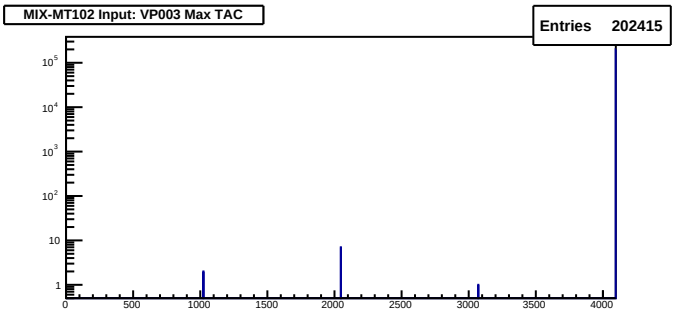

MIX-MT101 Input: VP004 Max TAC
Entries 202415

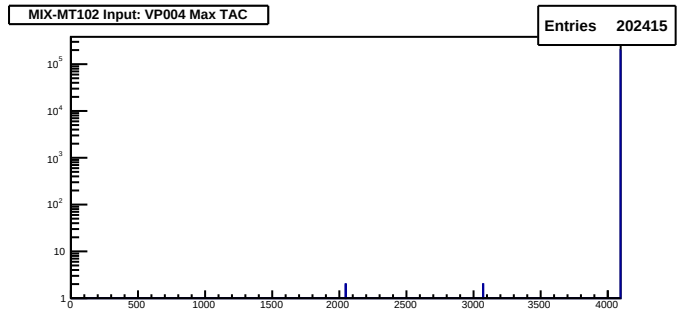

MIX-MT102 Input: MT005 MTDE+W TAC sum
Entries 404830
TAC sum
1st-Best
2nd-Best
ID

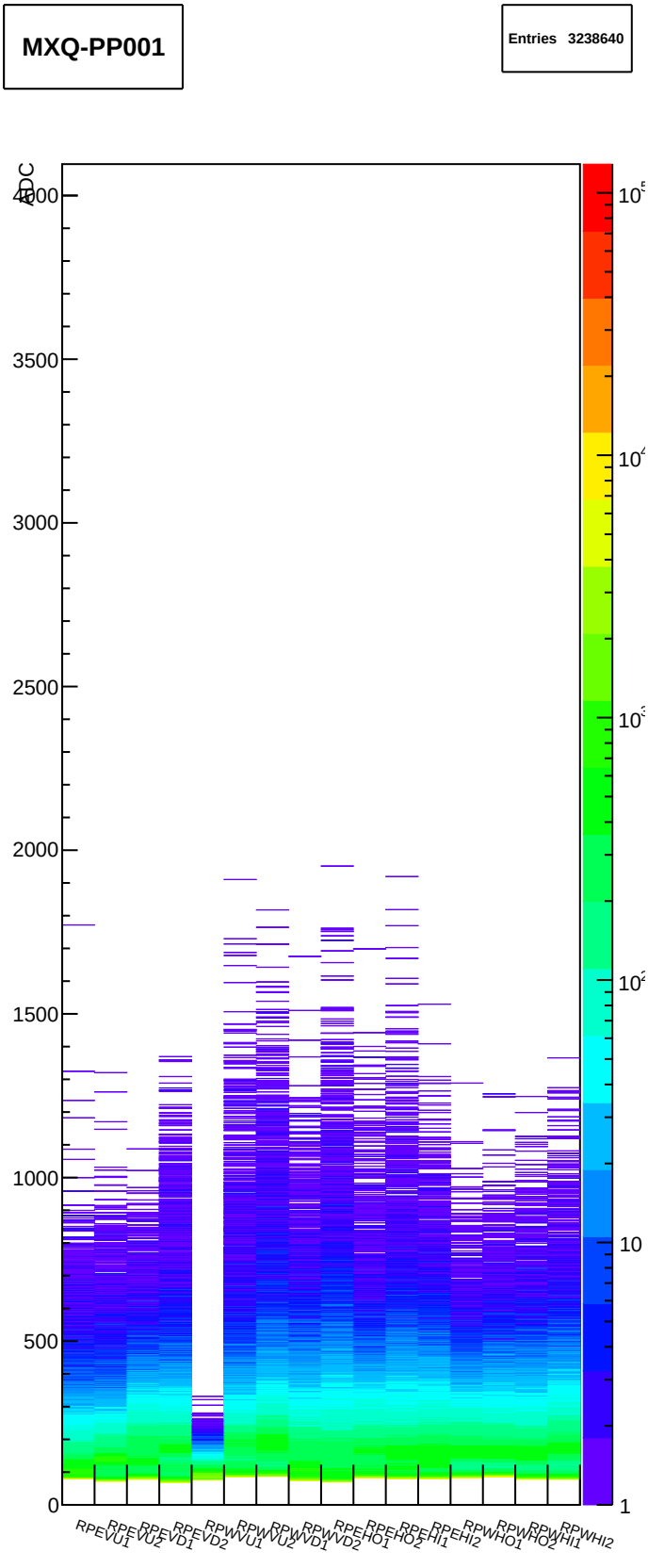

MIX-MT102 Input: MT006 MTDE+W TAC sum
Entries 404830
TAC sum
1st-Best
2nd-Best
ID

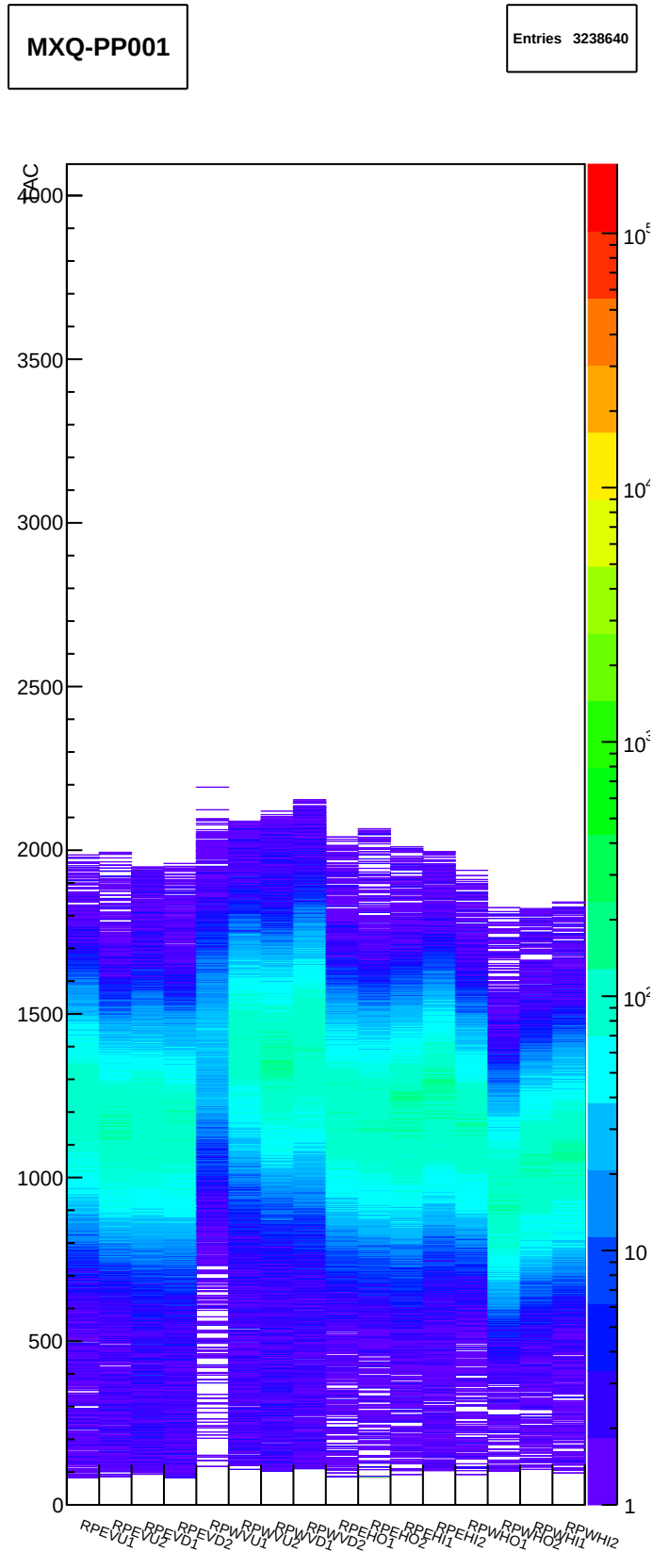

MIX-MT102 Input: MT007 MTDE+W TAC sum
Entries 404830
TAC sum
1st-Best
2nd-Best
ID

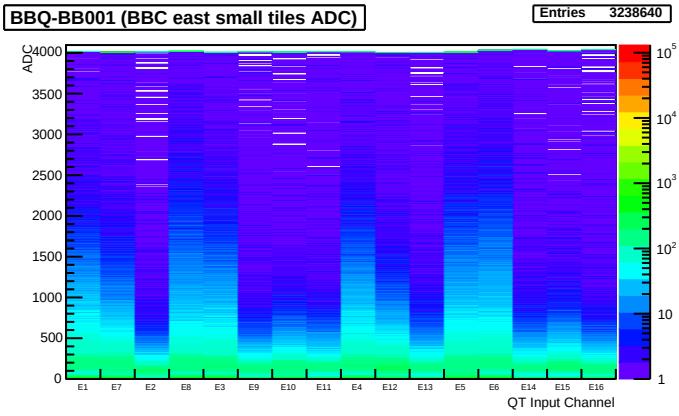

MIX-MT102 Input: MT008 MTDE+W TAC sum
Entries 404830
TAC sum
1st-Best
2nd-Best
ID

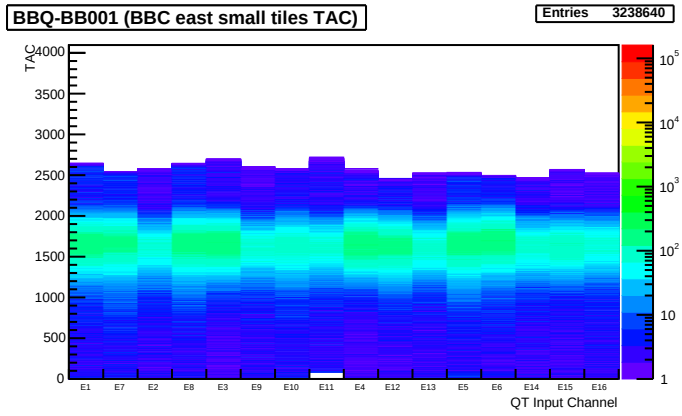

MIX-MT102 Input: VP003 Max TAC
Entries 202415

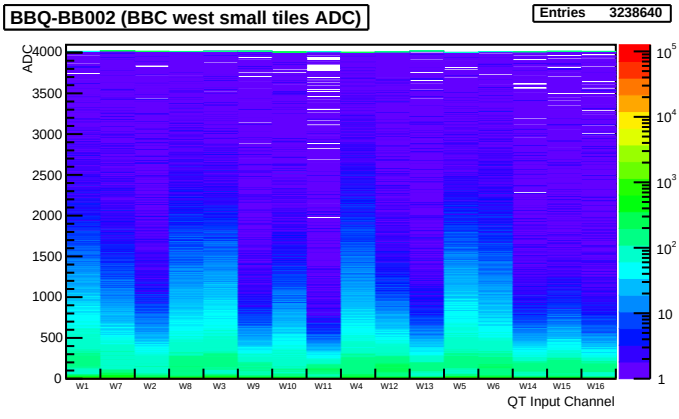

MIX-MT102 Input: VP004 Max TAC
Entries 202415

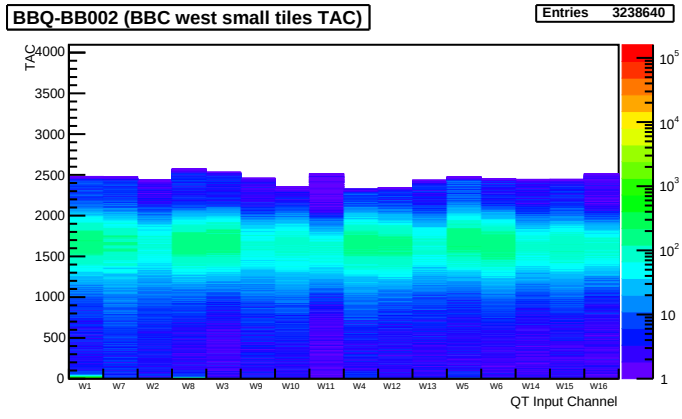

MXQ-PP001
Entries 3238640
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
1
10

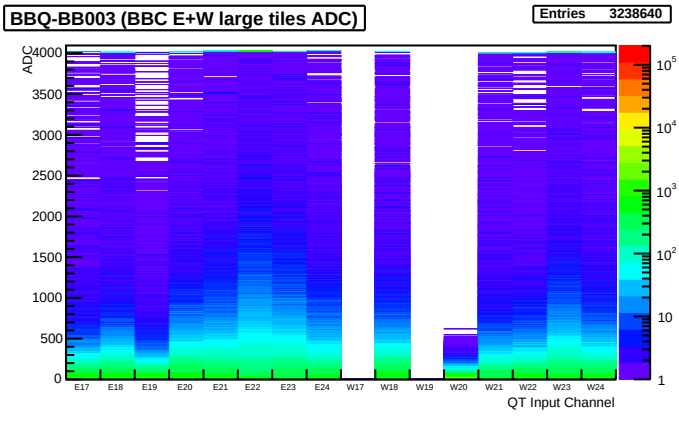

MXQ-PP001
Entries 3238640
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
1
10

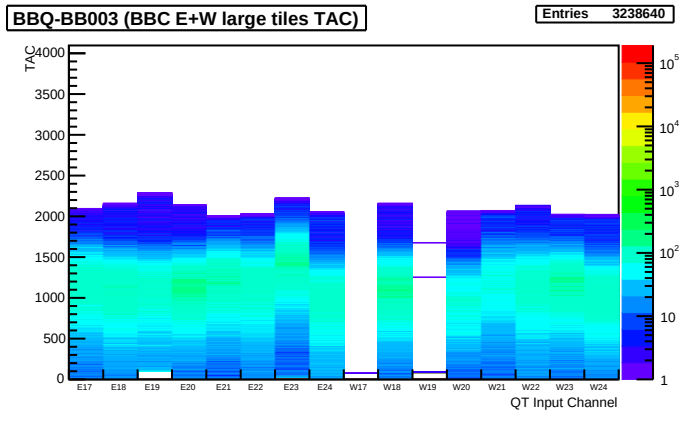

BBQ-BB001 (BBC east small tiles ADC)
Entries 3238640
ADC
QT Input Channel

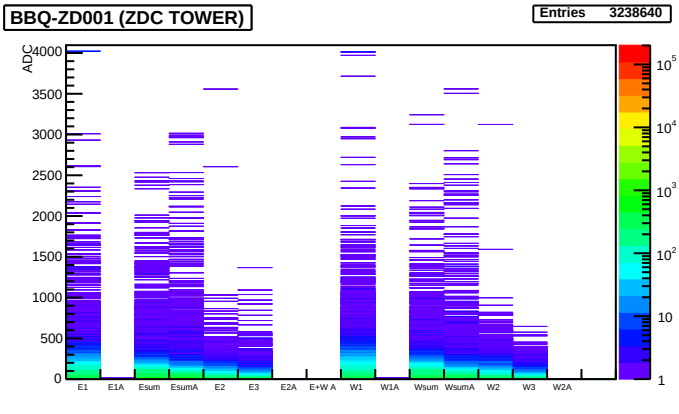

BBQ-BB001 (BBC east small tiles TAC)
Entries 3238640
TAC
QT Input Channel

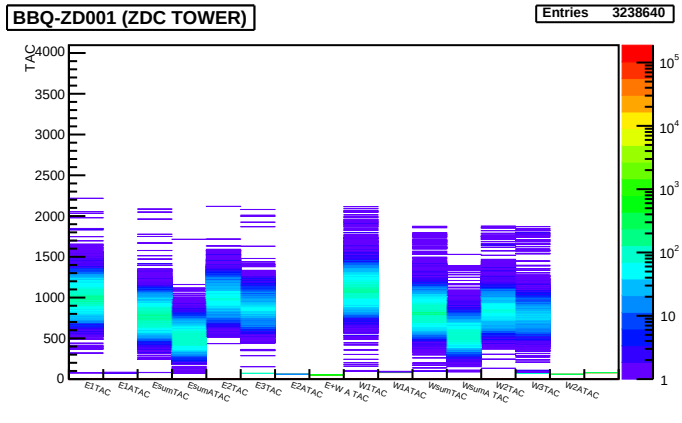

BBQ-BB002 (BBC west small tiles ADC)
Entries 3238640
ADC
QT Input Channel

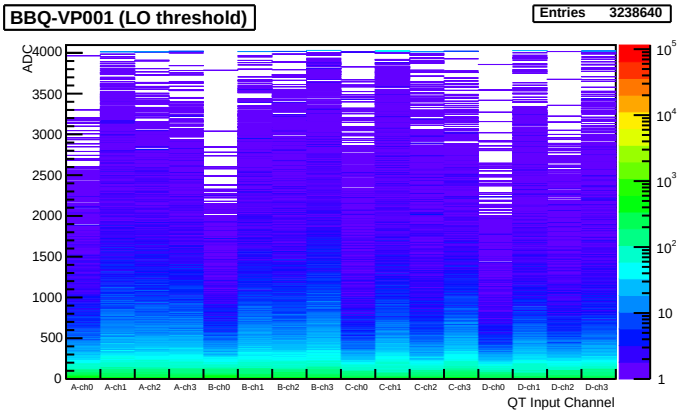

BBQ-BB002 (BBC west small tiles TAC)
Entries 3238640
TAC
QT Input Channel

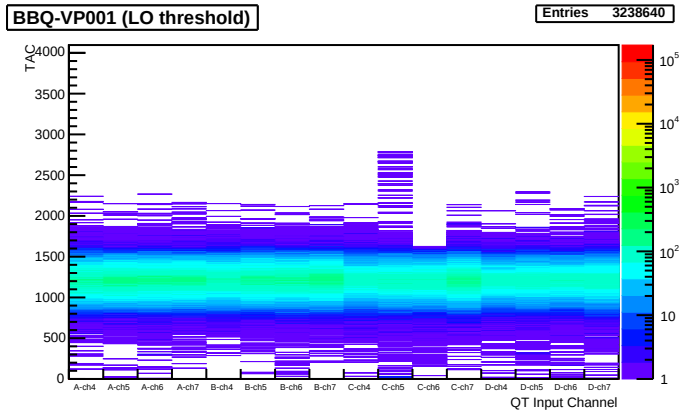

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3238640
ADC
QT Input Channel

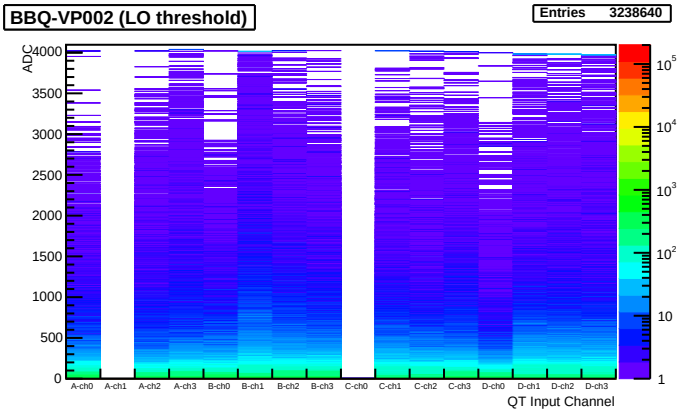

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3238640
TAC
QT Input Channel

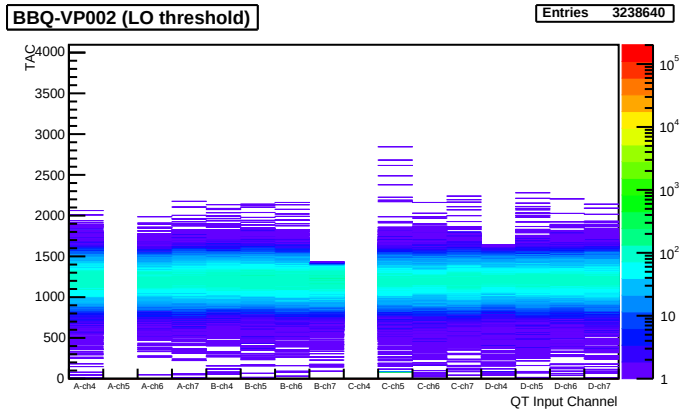

BBQ-ZD001 (ZDC TOWER)
Entries 3238640
ADC

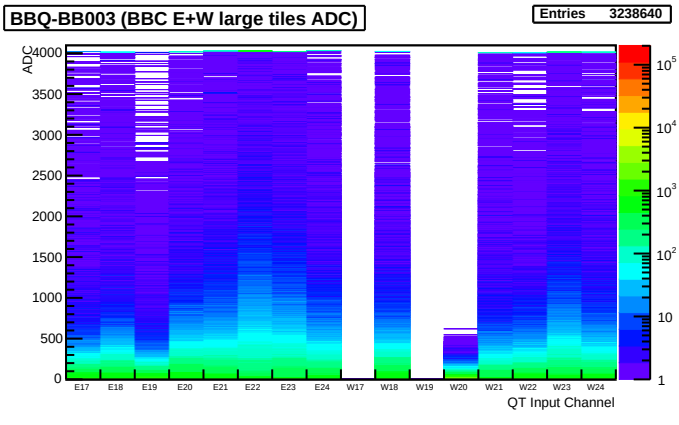

BBQ-ZD001 (ZDC TOWER)
Entries 3238640
TAC

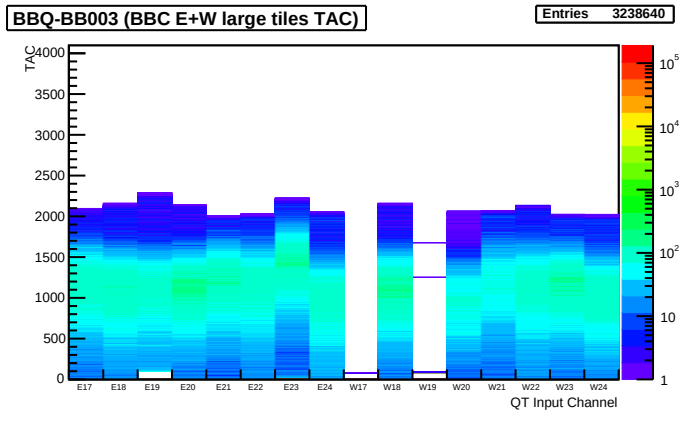

BBQ-VP001 (LO threshold)
Entries 3238640
ADC
QT Input Channel

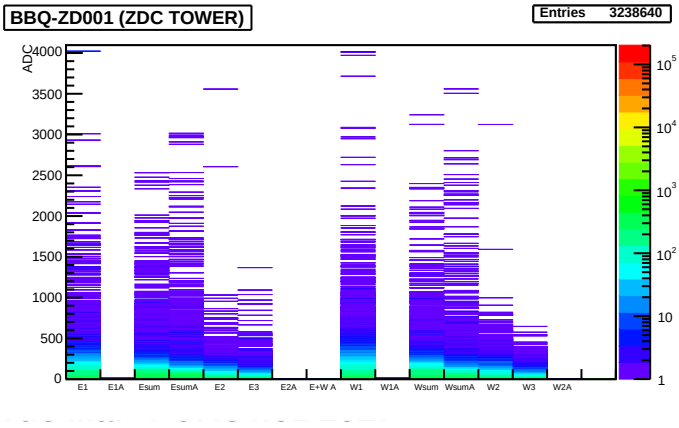

BBQ-VP001 (LO threshold)
Entries 3238640
TAC
QT Input Channel

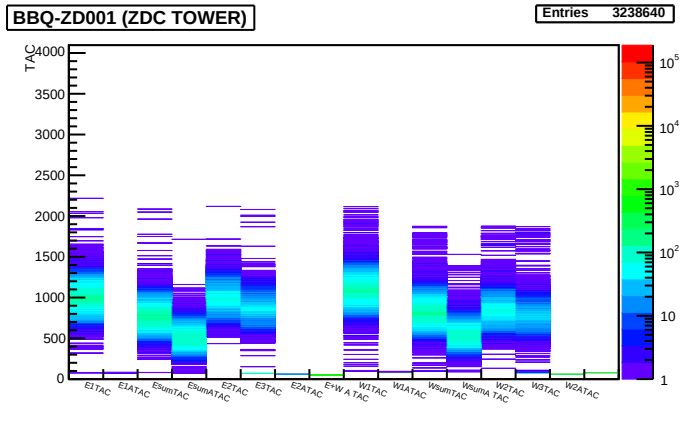

BBQ-VP002 (LO threshold)
Entries 3238640
ADC
QT Input Channel

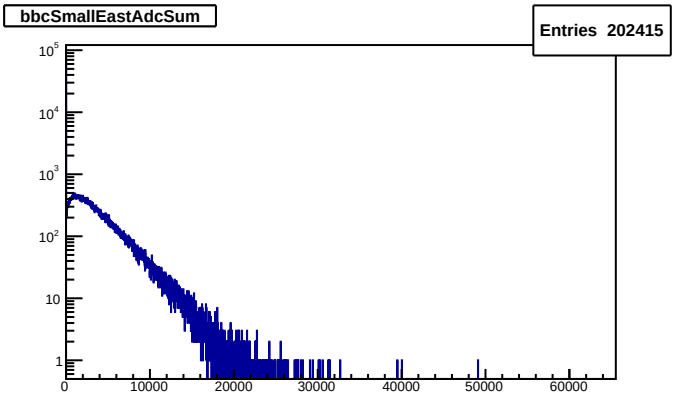

BBQ-VP002 (LO threshold)
Entries 3238640
TAC
QT Input Channel

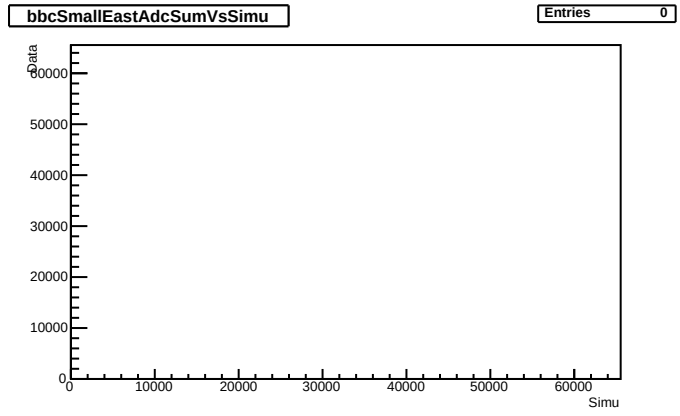

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3238640
ADC
QT Input Channel

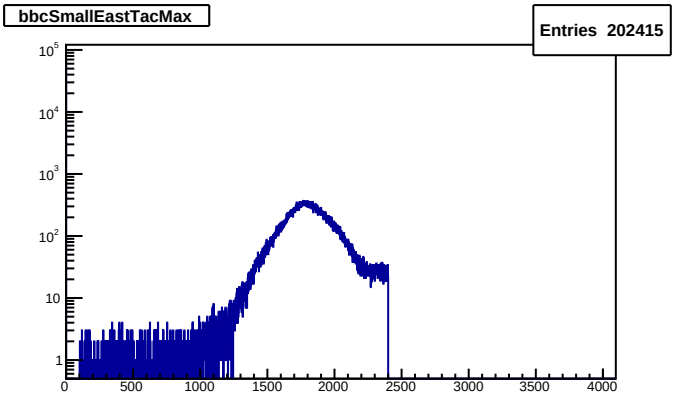

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3238640
TAC
QT Input Channel

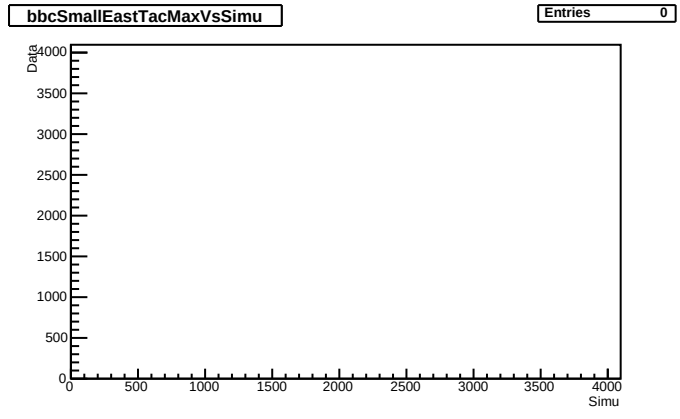

BBQ-ZD001 (ZDC TOWER)
Entries 3238640
ADC

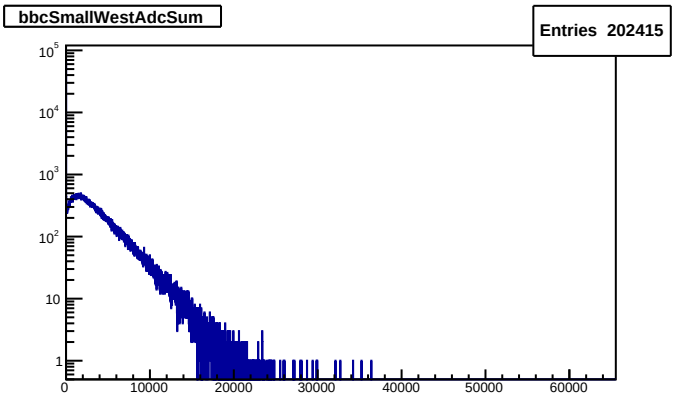

BBQ-ZD001 (ZDC TOWER)
Entries 3238640
TAC

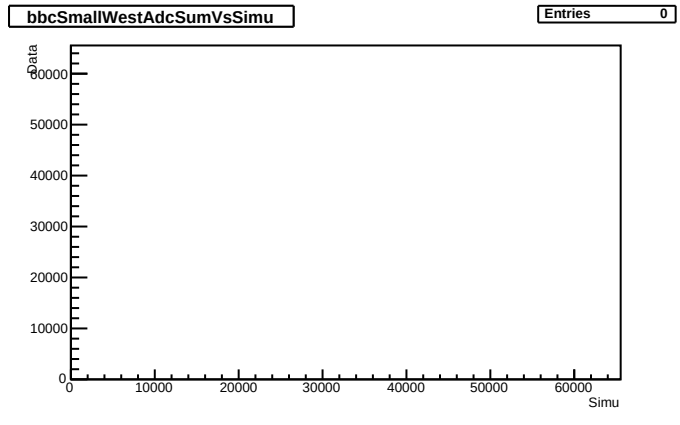

bbcSmallEastAdcSum
Entries 202415

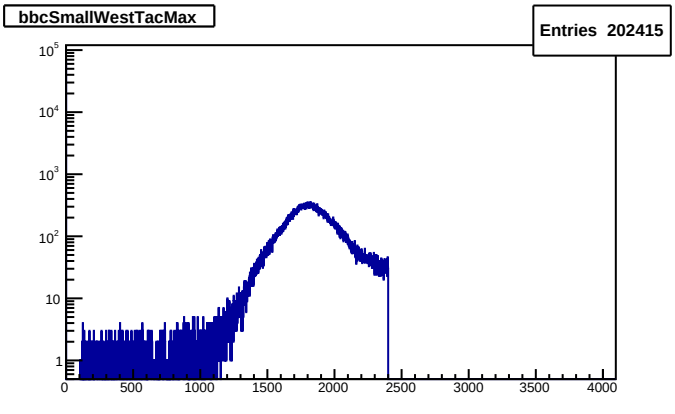

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

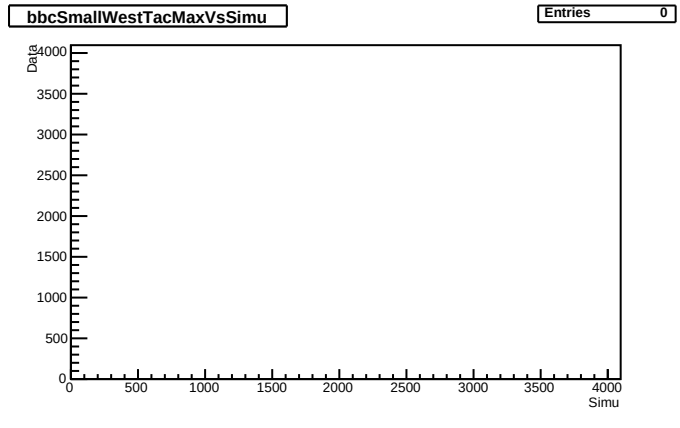

bbcSmallEastTacMax
Entries 202415

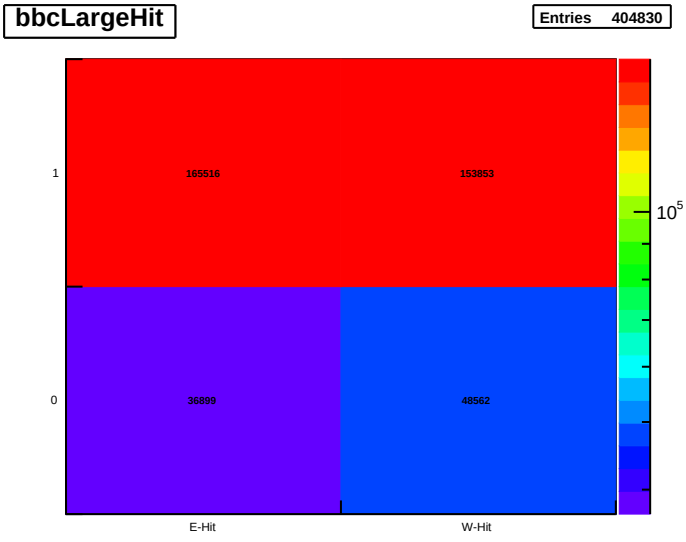

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

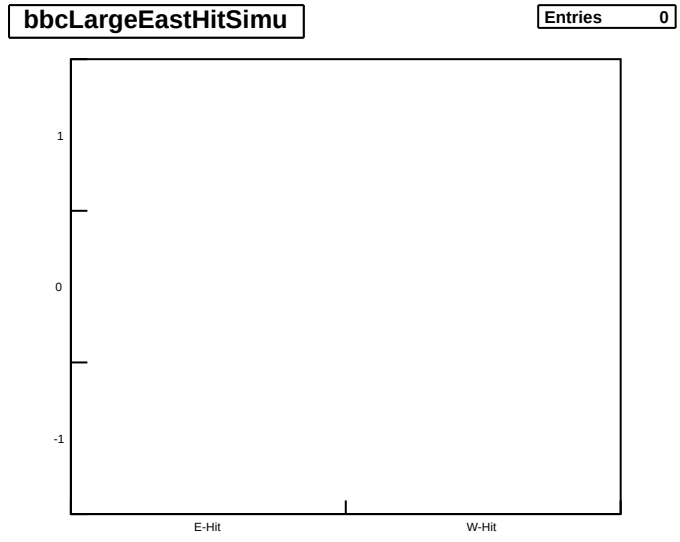

bbcSmallWestAdcSum
Entries 202415

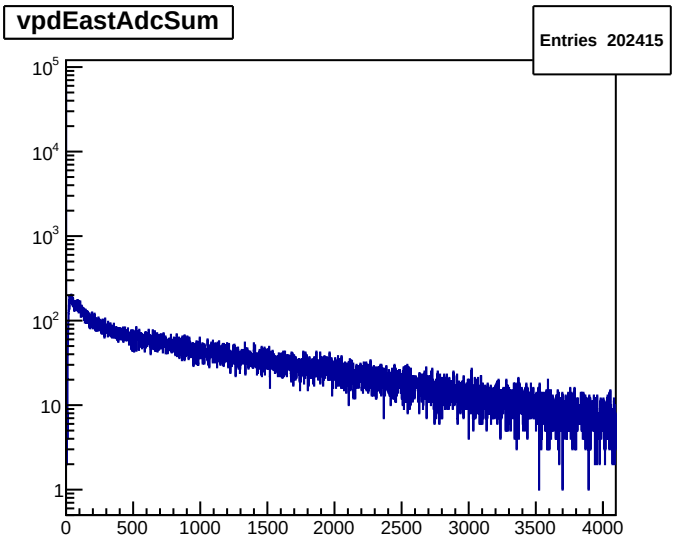

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

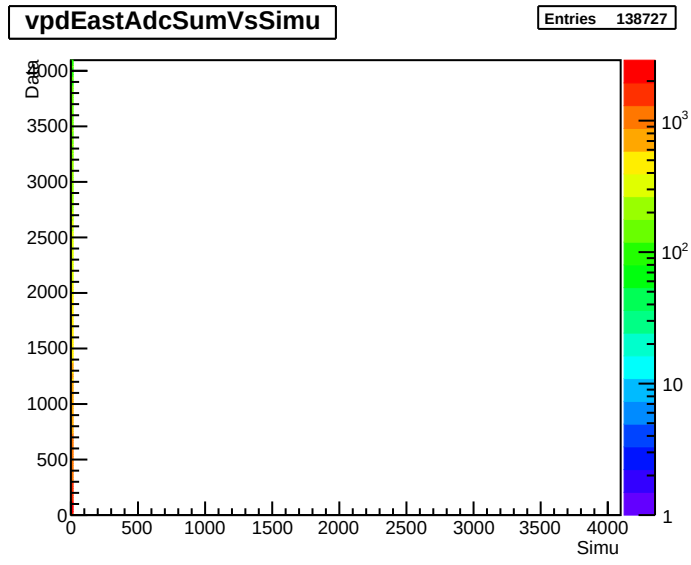

bbcSmallWestTacMax
Entries 202415

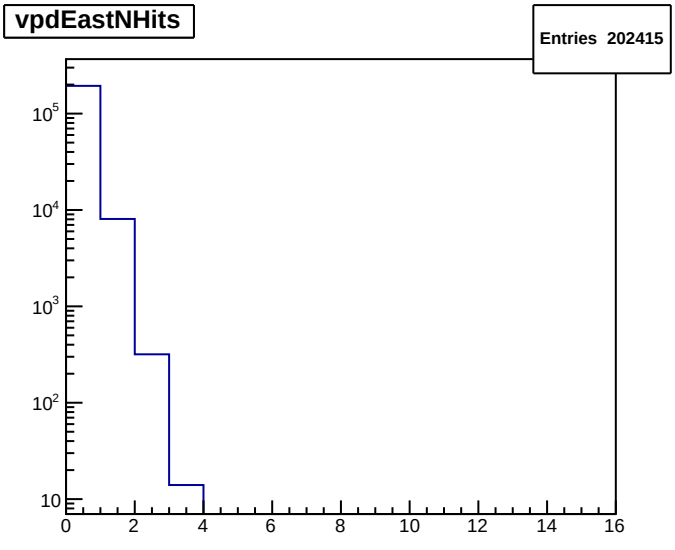

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

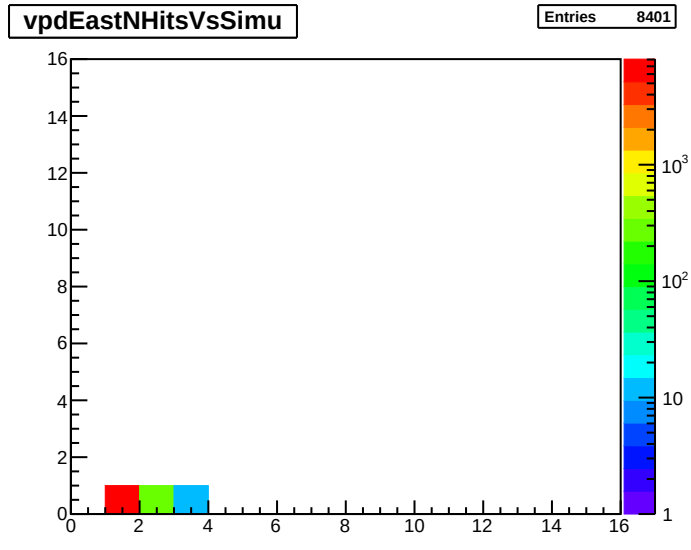

bbcLargeHit
Entries 404830
1
0
165516
153853
36899
48562
E-Hit
W-Hit
10^5

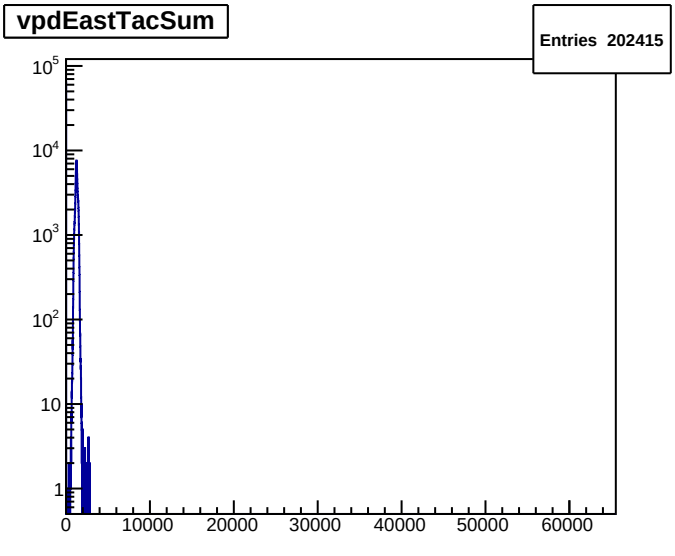

bbcLargeEastHitSimu
Entries 0
1
0
-1
E-Hit
W-Hit

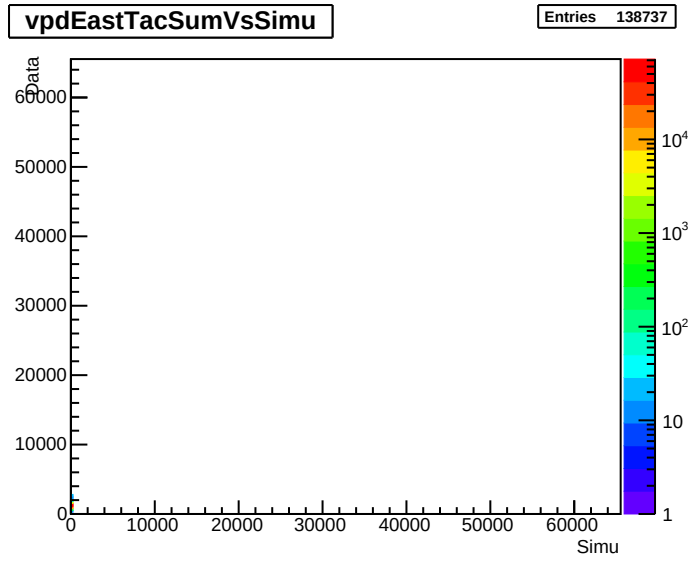

vpdEastAdcSum
Entries 202415

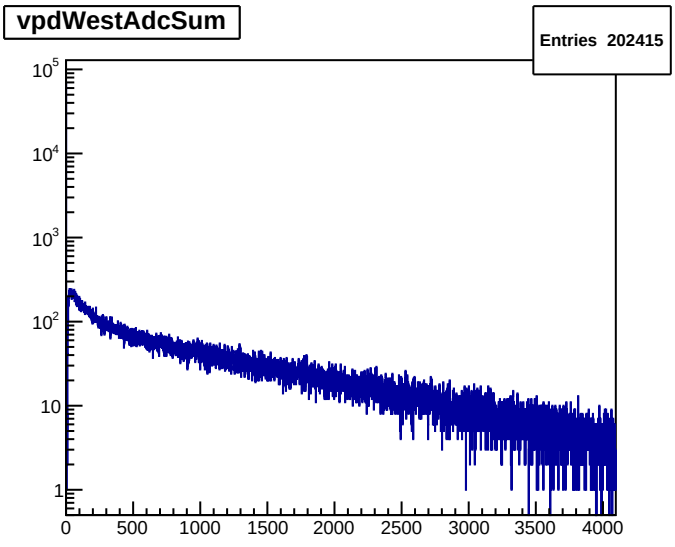

vpdEastAdcSumVsSimu
Entries 138727
Data
Simu

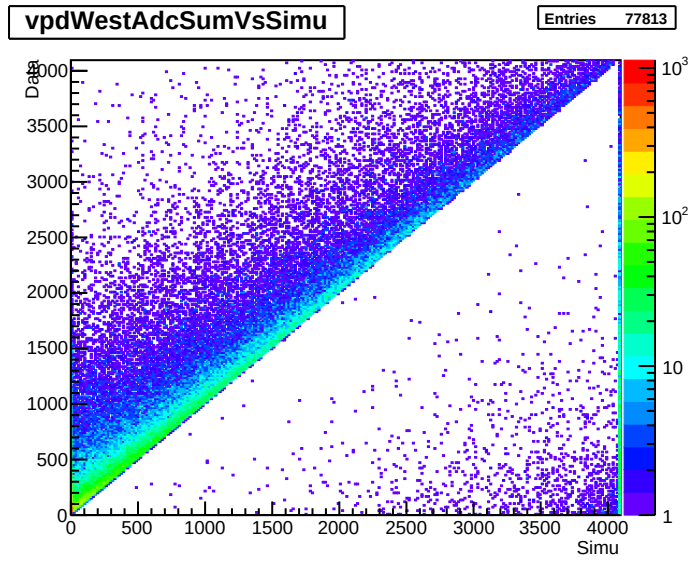

vpdEastNHits
Entries 202415

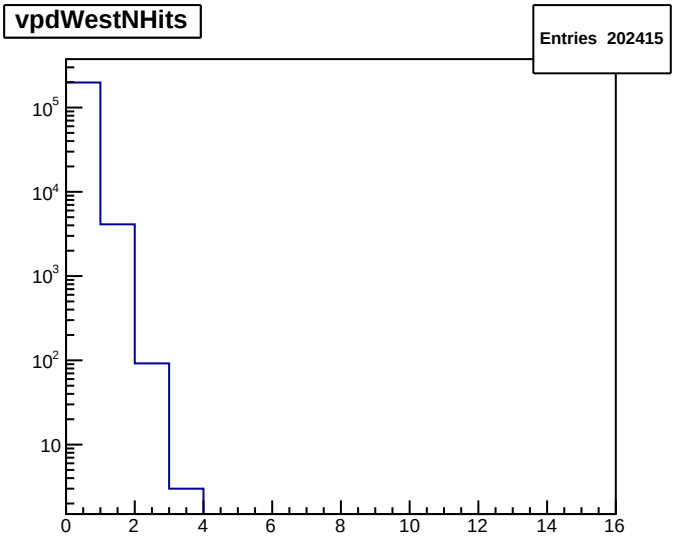

vpdEastNHitsVsSimu
Entries 8401

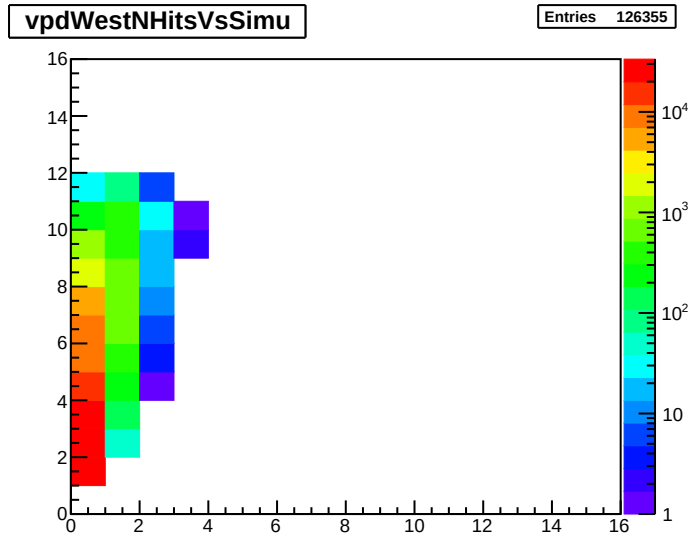

vpdEastTacSum
Entries 202415

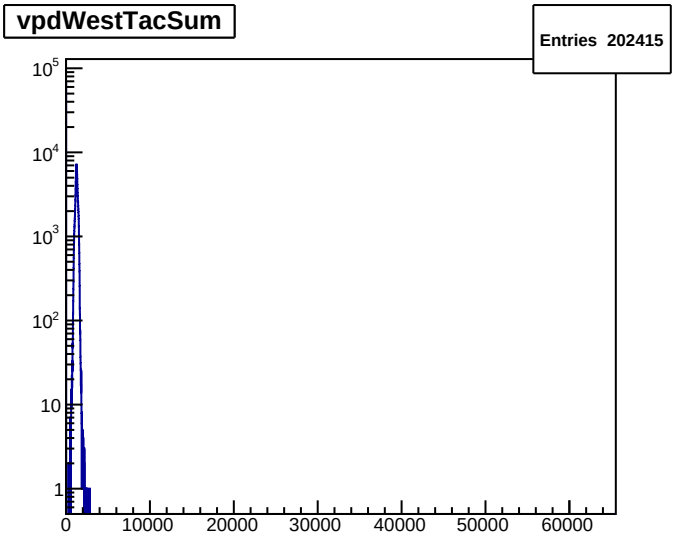

vpdEastTacSumVsSimu
Entries 138737
Data
Simu

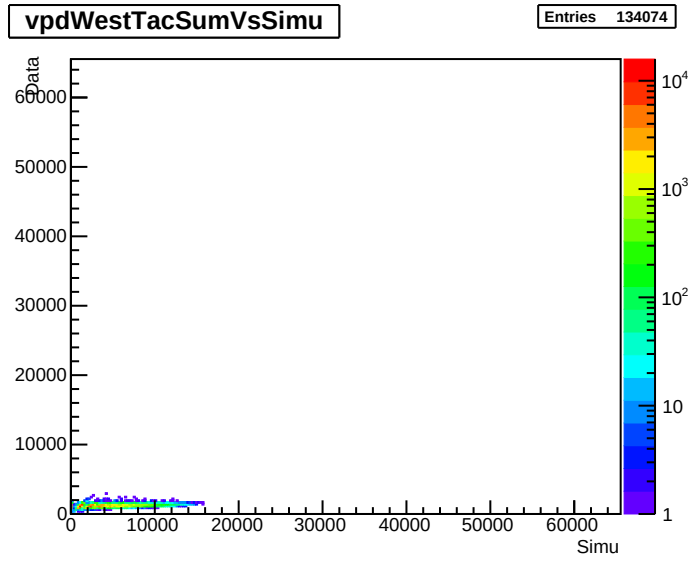

vpdWestAdcSum
Entries 202415

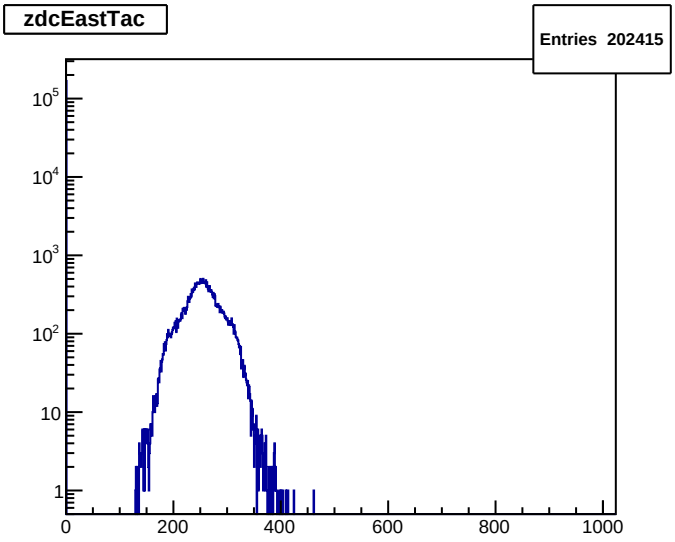

vpdWestAdcSumVsSimu
Entries 77813
Data
Simu

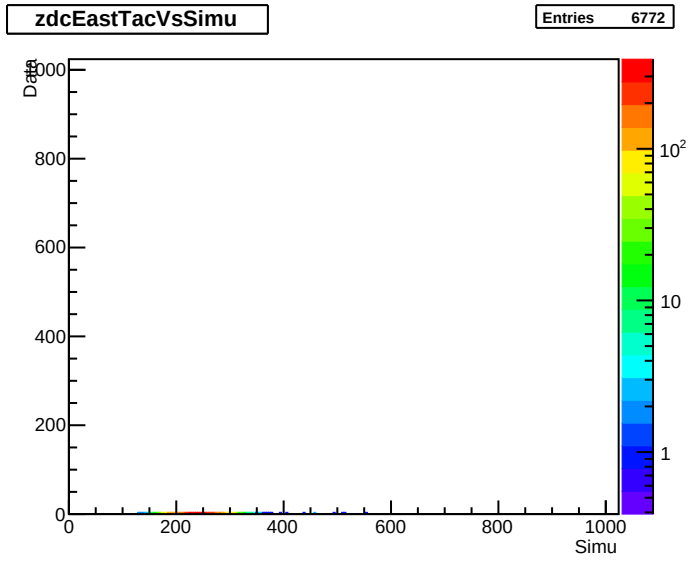

vpdWestNHits
Entries 202415

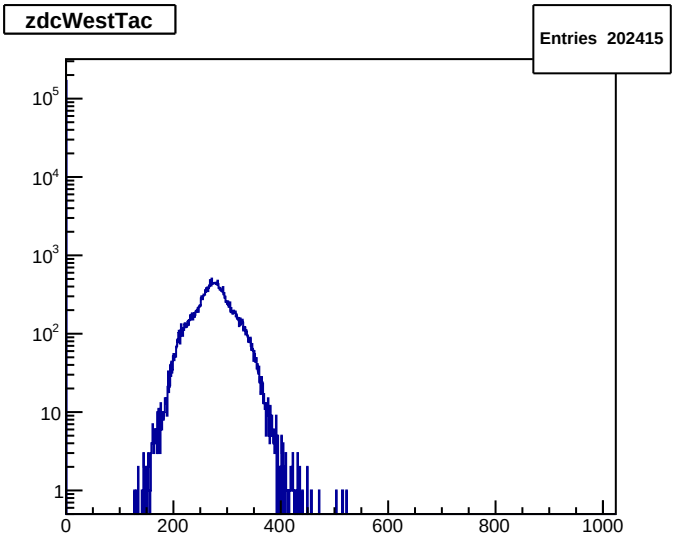

vpdWestNHitsVsSimu
Entries 126355

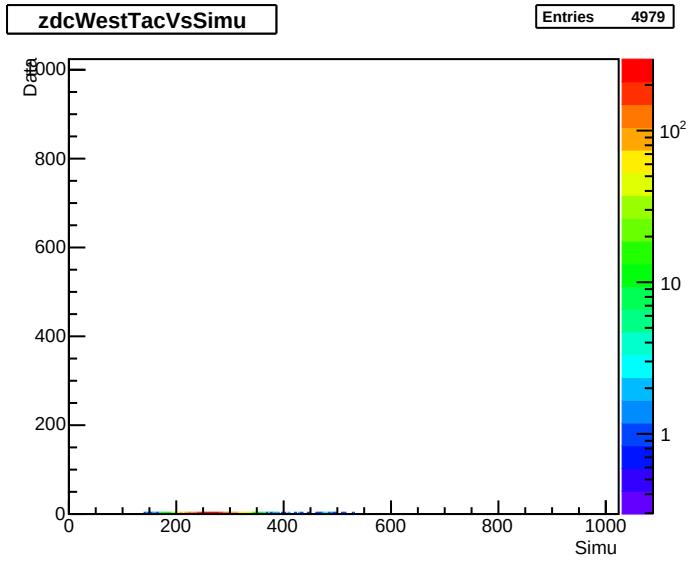

vpdWestTacSum
Entries 202415

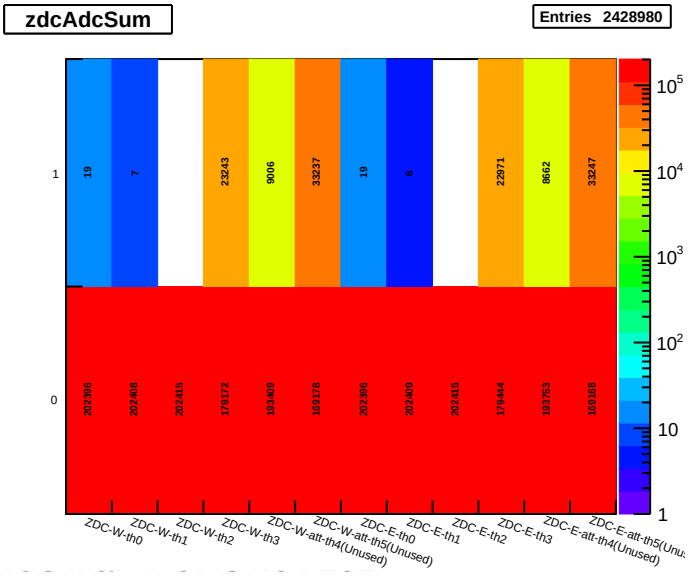

vpdWestTacSumVsSimu
Entries 134074
Data
Simu

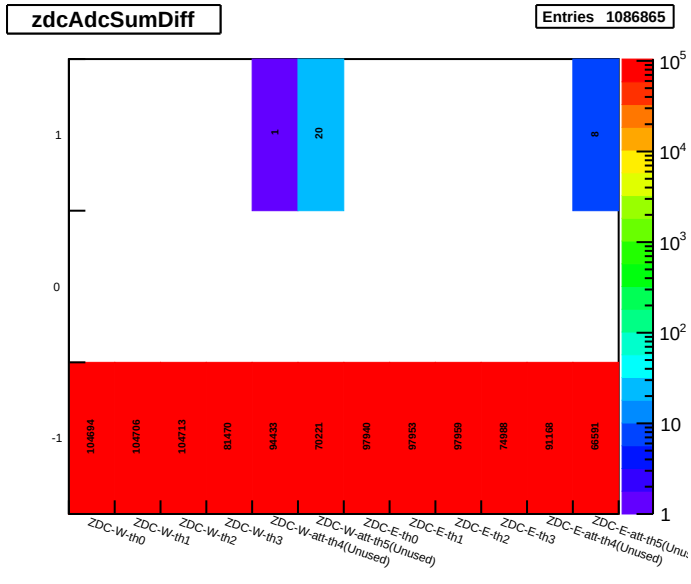

zdcEastTac
Entries 202415

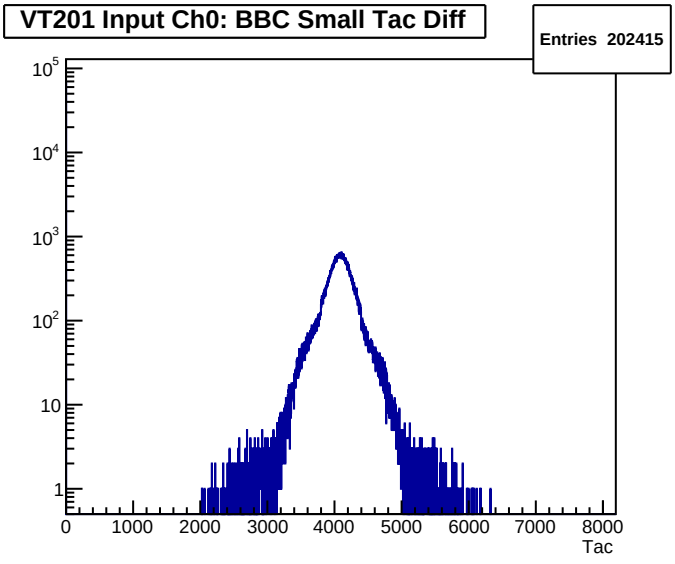

zdcEastTacVsSimu
Entries 6772
Data
Simu

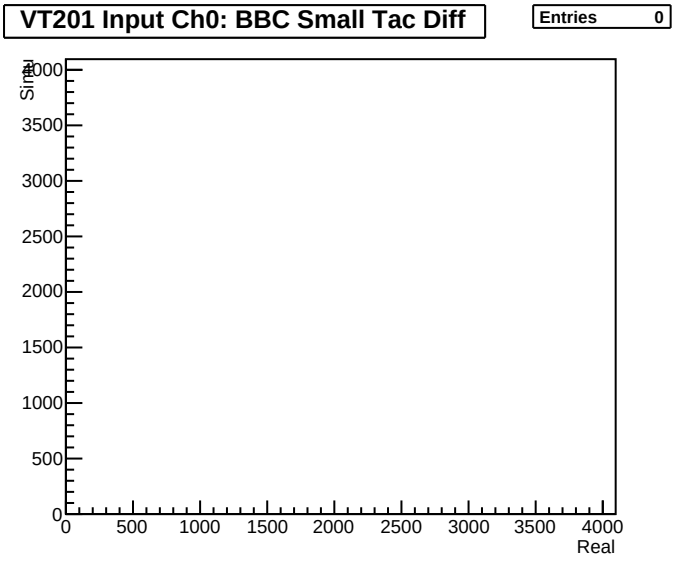

zdcWestTac
Entries 202415

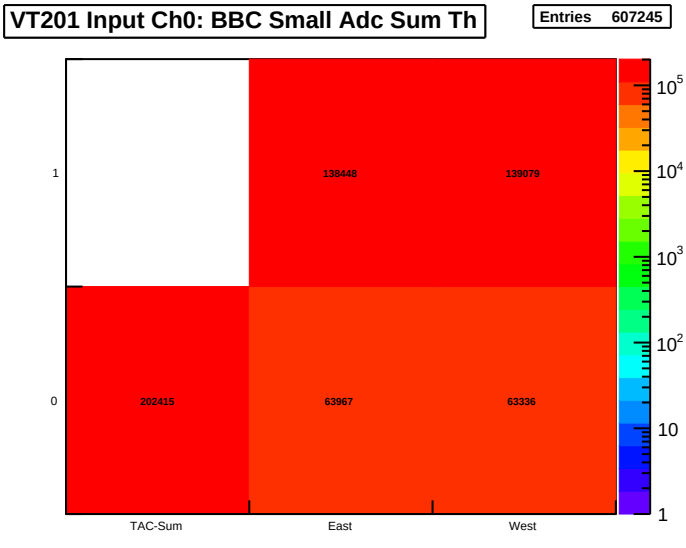

zdcWestTacVsSimu
Entries 4979
Data
Simu

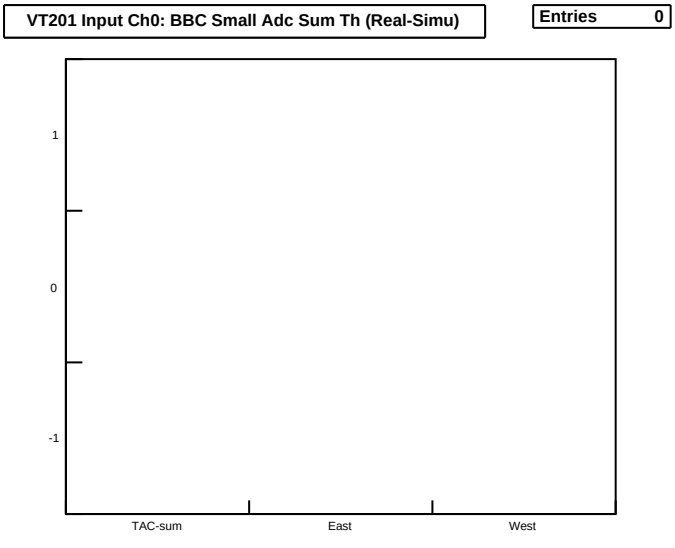

zdcAdcSum
Entries 2428980
19
7
23243
9006
33237
19
6
22871
8662
33247
202396
202408
202415
179172
193409
169178
202396
202409
202415
179444
193753
169168
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

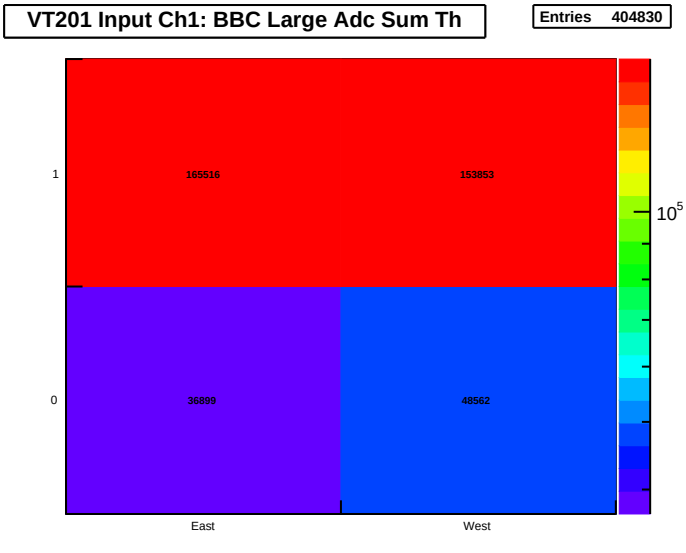

zdcAdcSumDiff
Entries 1086865
1
20
8
104694
104706
104713
81470
94433
70221
97940
97953
97959
74988
91168
66591
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

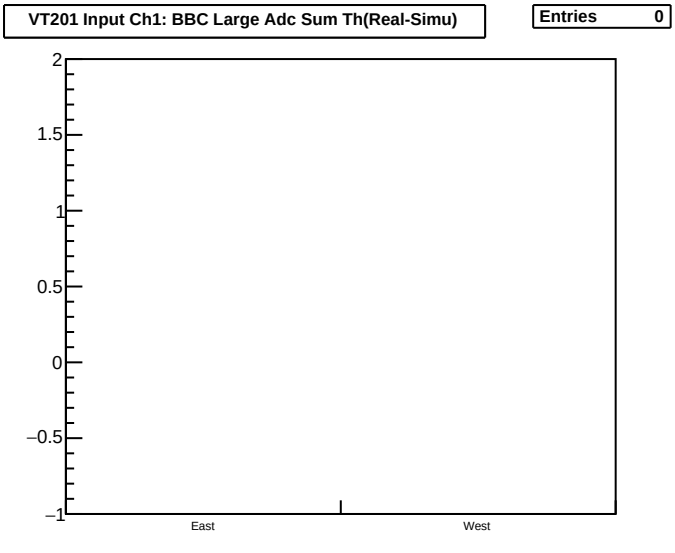

VT201 Input Ch0: BBC Small Tac Diff
Entries 202415
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

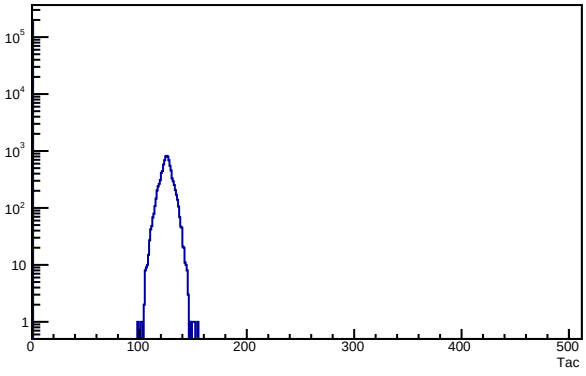

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 607245
138448
139079
202415
63967
63336
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

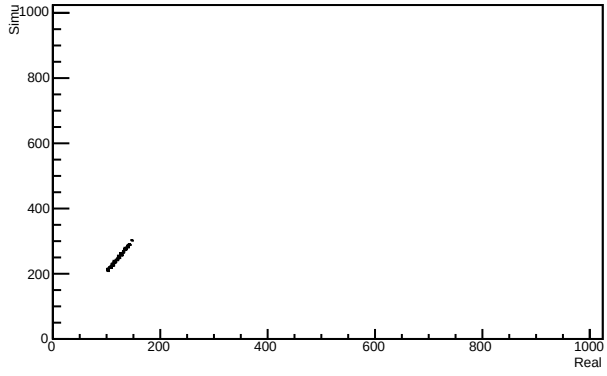

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 404830
165516
153853
36899
48562
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

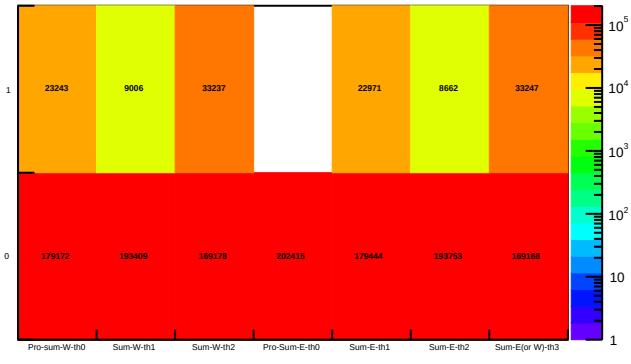
VT201 Input Ch2: ZDC Tac Diff



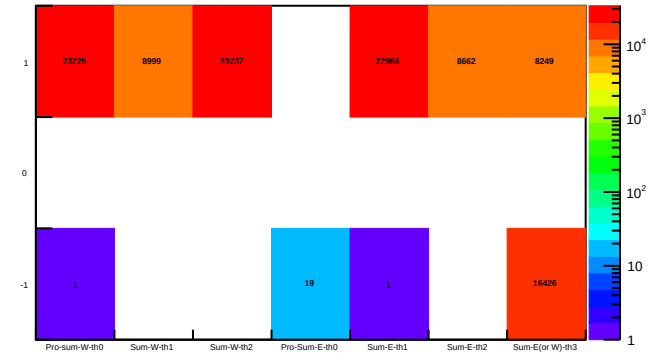
VT201 Input Ch2: ZDC Tac Diff



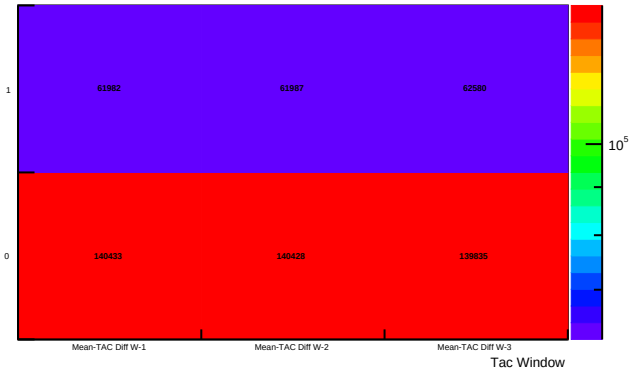
VT201 Input Ch2: ZDC Adc Sum Th



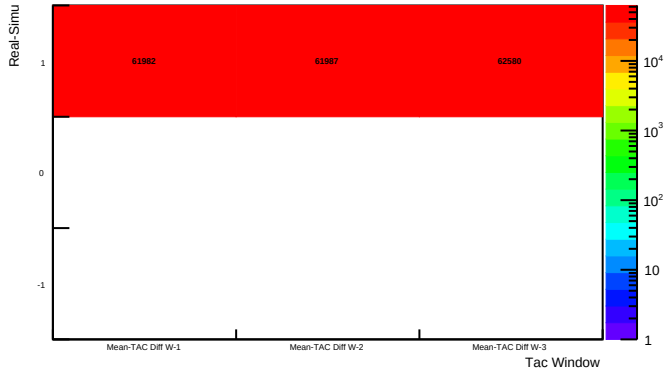
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



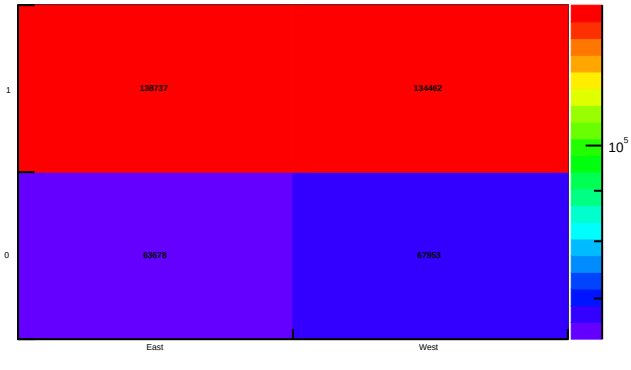
VT201 Input Ch4: VPD Tac Diff



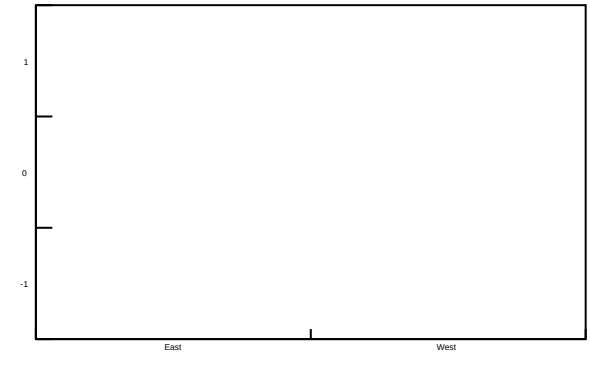
VT201 Input Ch4: VPD Tac Diff

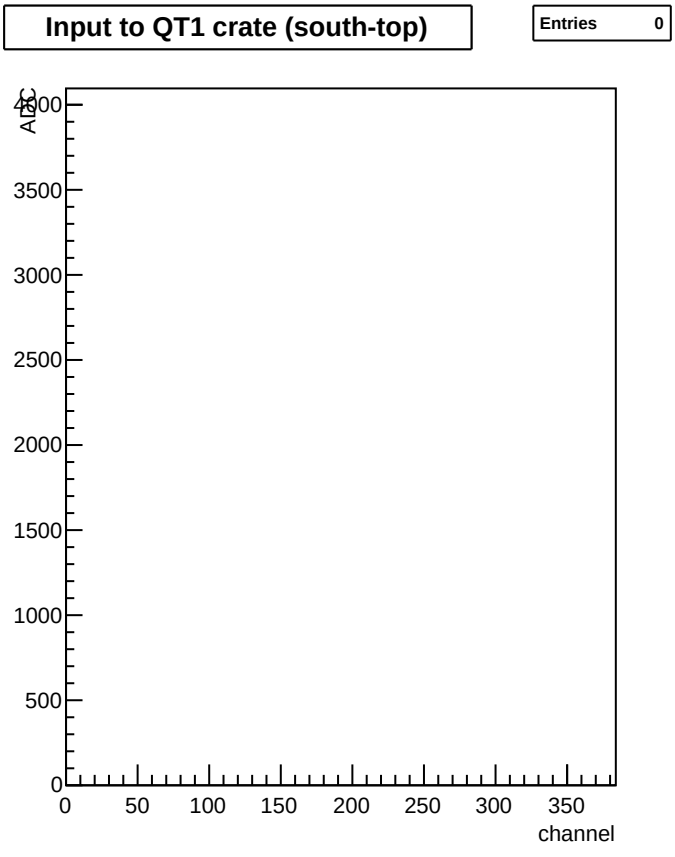


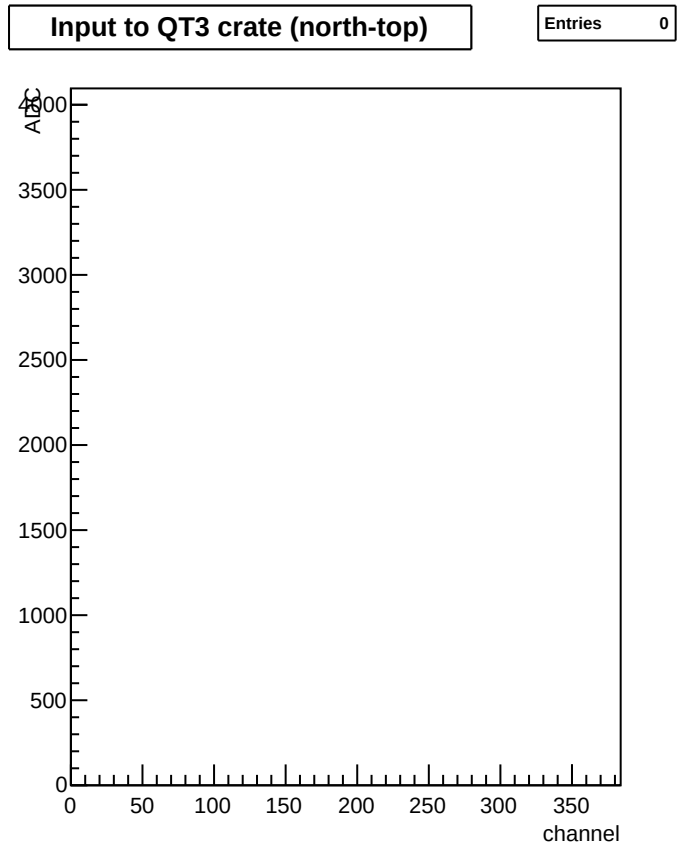
VT201 Input Ch4: VPD Adc Sum Th

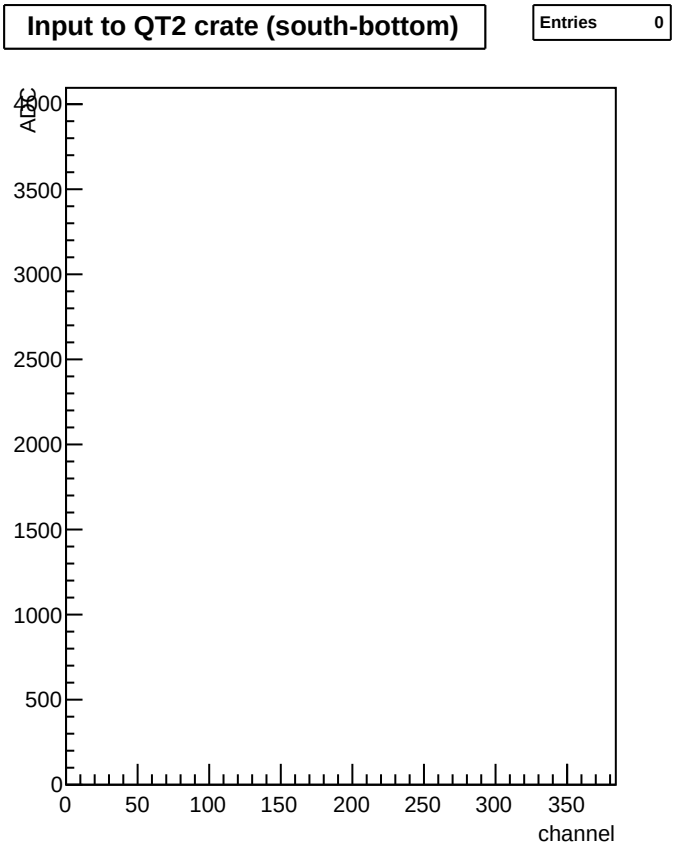


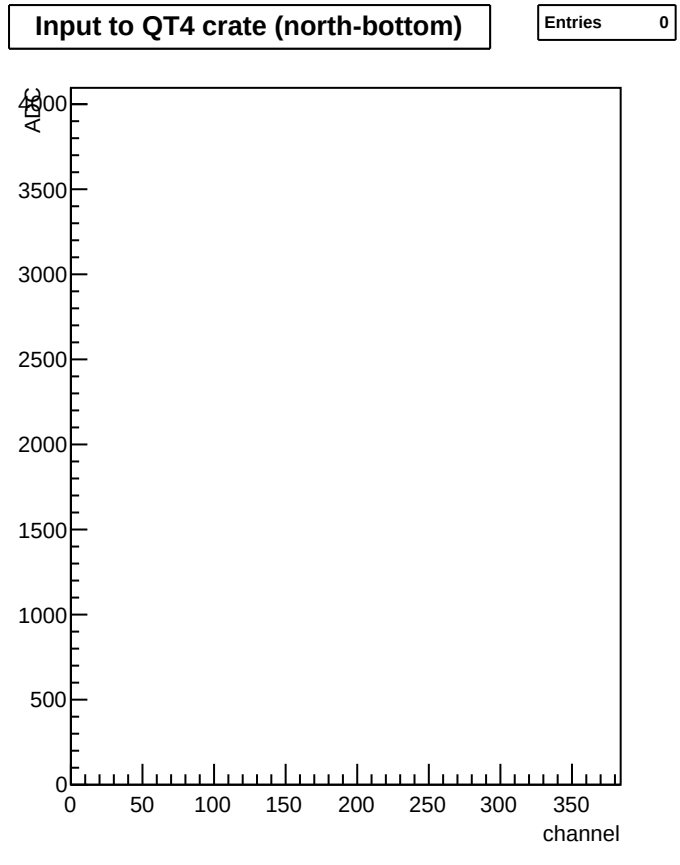
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

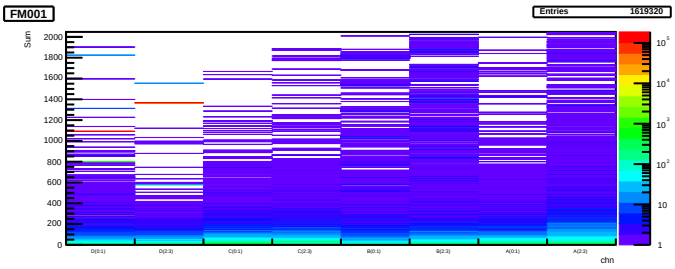


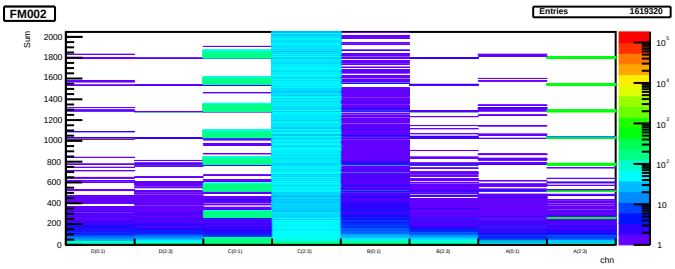

Input to QT1 crate (south-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

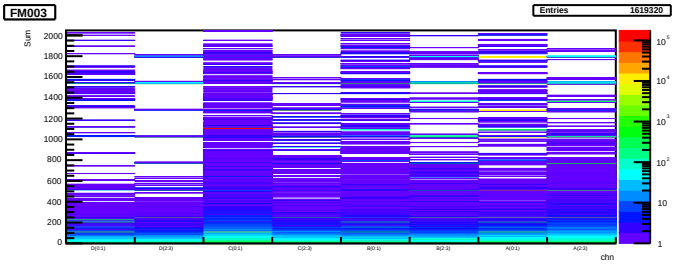

Input to QT3 crate (north-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

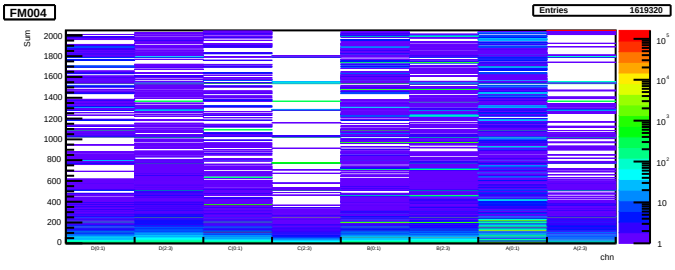

Input to QT2 crate (south-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

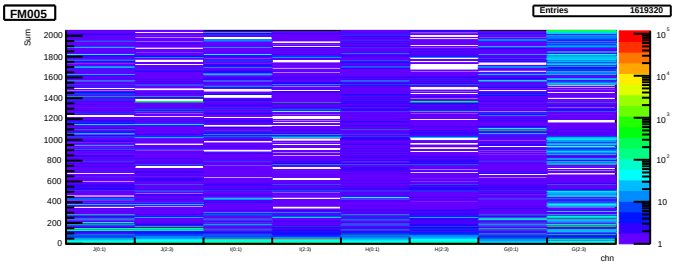

Input to QT4 crate (north-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

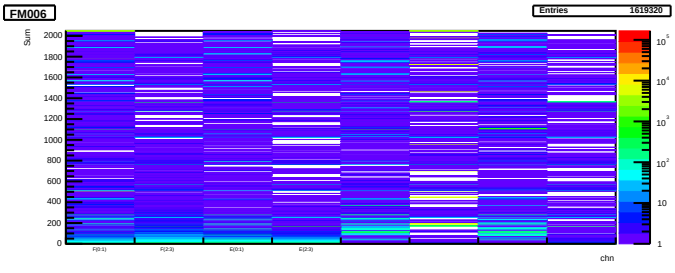

FM001
Entries 1619320
Sum
chn

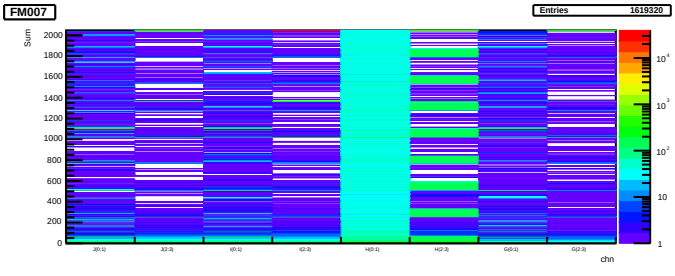

FM002
Entries 1619320
Sum
chn

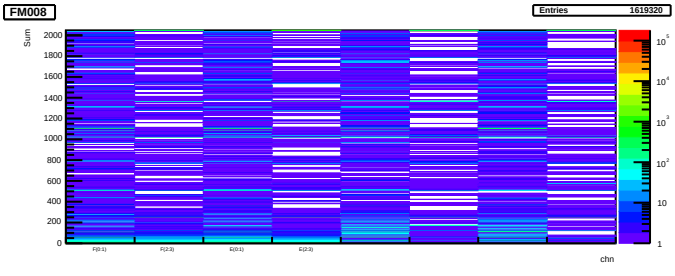

FM003
Entries 1619320
Sum
chn

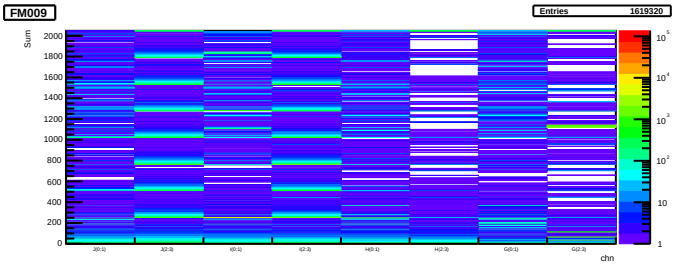

FM004
Entries 1619320
Sum
chn

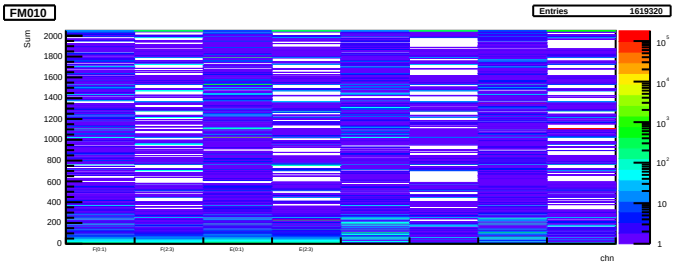

FM005
Entries 1619320
Sum
chn

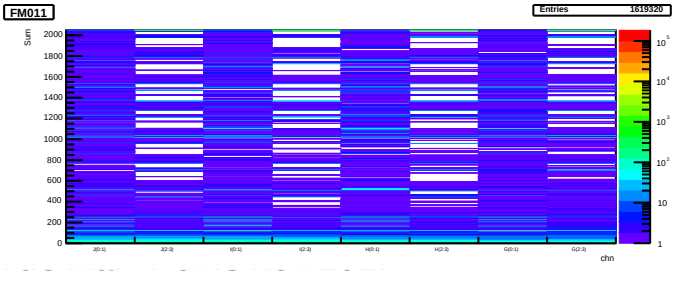

FM006
Entries 1619320
Sum
chn

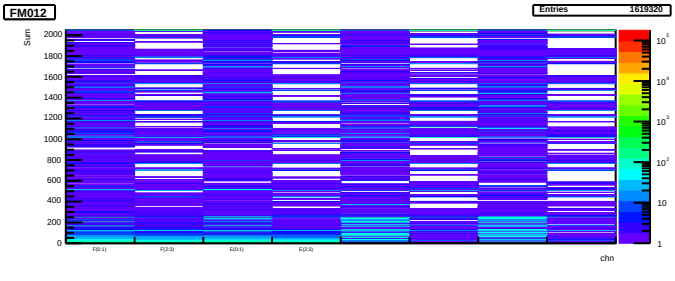

FM007
Entries 1619320
Sum
chn

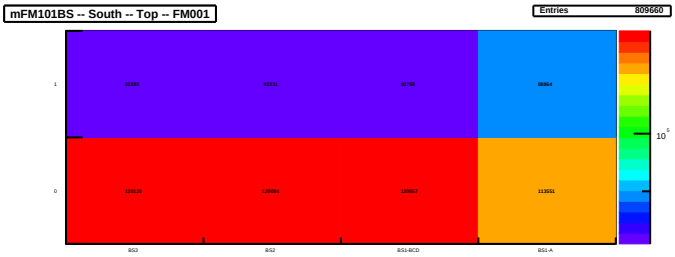

FM008
Entries 1619320
Sum
chn

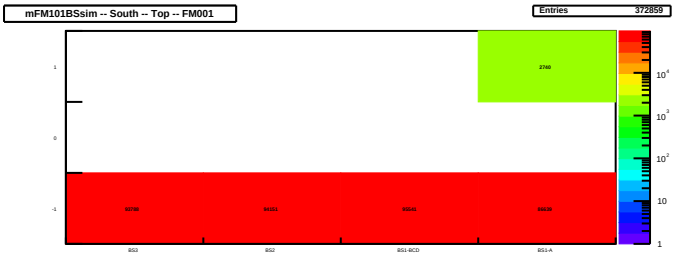

FM009
Entries 1619320
Sum
chn

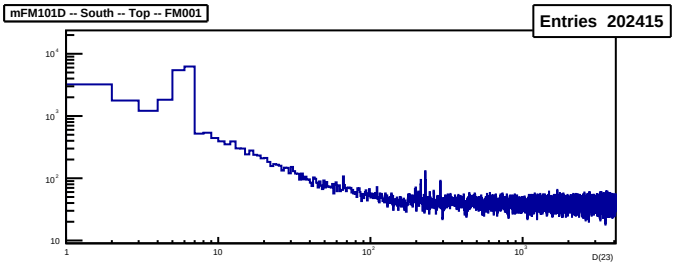

FM010
Entries 1619320
Sum
chn

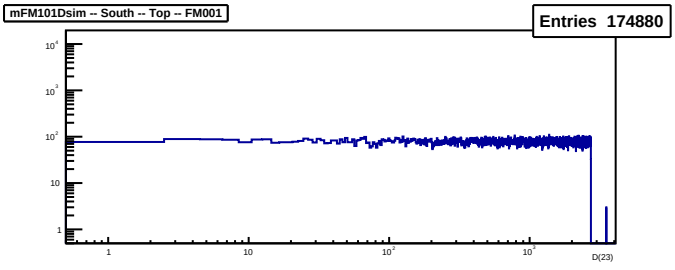

FM011
Entries 1619320
Sum
chn

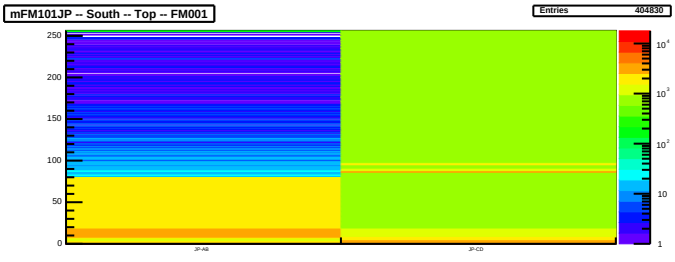

FM012
Entries 1619320
Sum
chn


mFM101BS -- South -- Top -- FM001
Entries 809660


mFM101BSsim -- South -- Top -- FM001
Entries 372859


mFM101D -- South -- Top -- FM001
Entries 202415
D(23)


mFM101Dsim -- South -- Top -- FM001
Entries 174880
D(23)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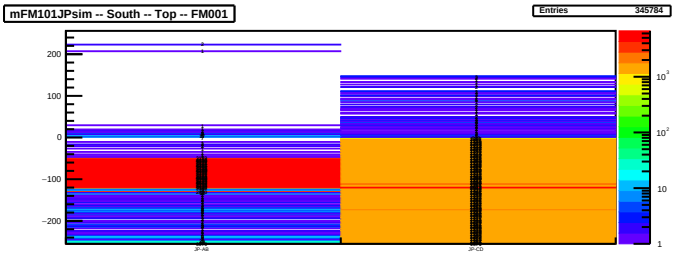
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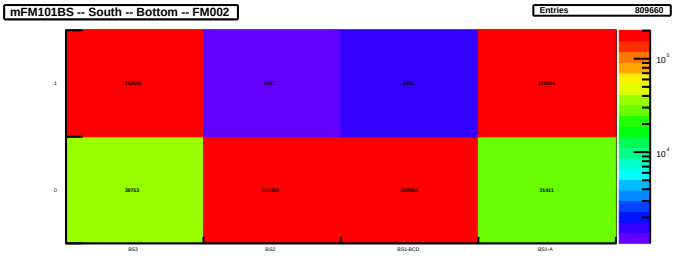
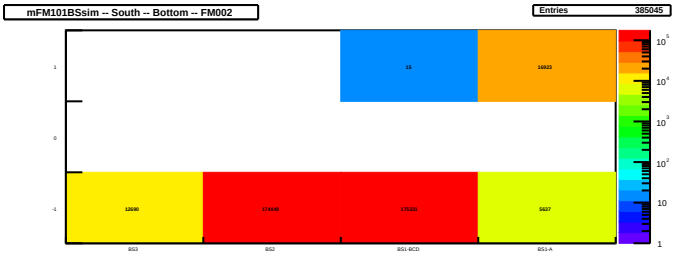

mFM101BS -- South -- Bottom -- FM002
Entries 809660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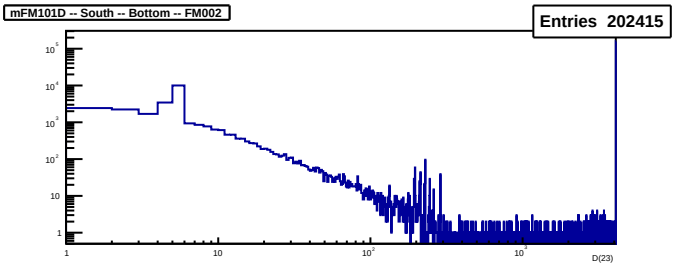
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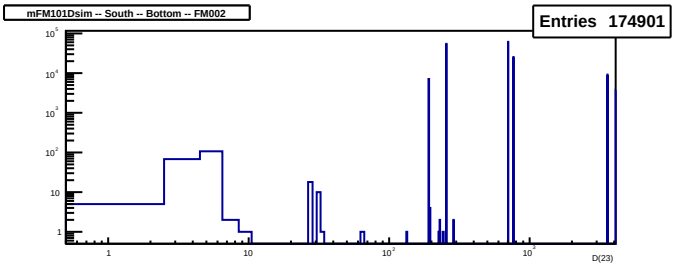
Entries 202415
D(23)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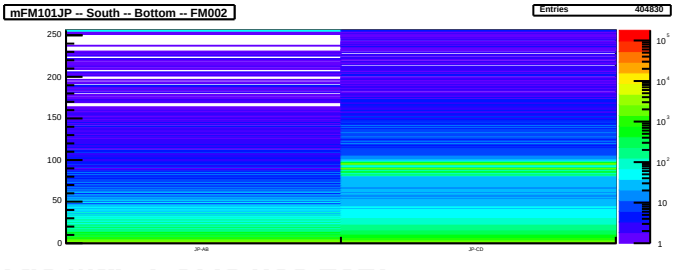
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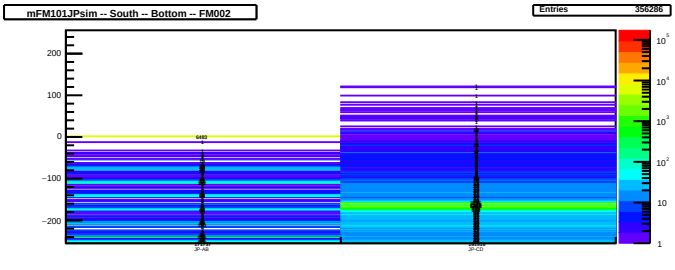
Entries 404830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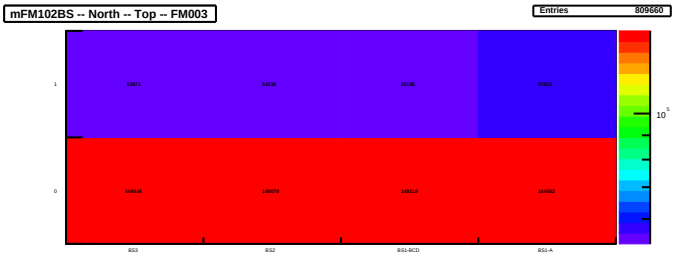
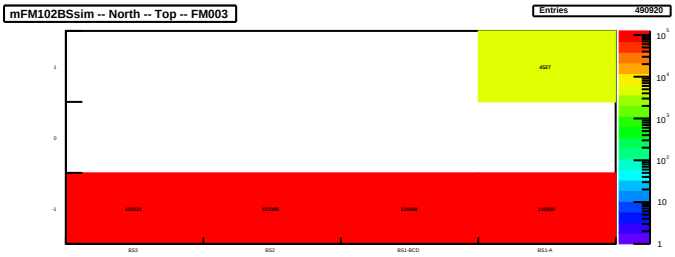

mFM102BS -- North -- Top -- FM003
Entries 809660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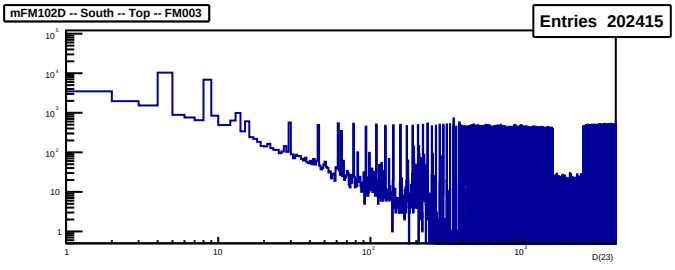
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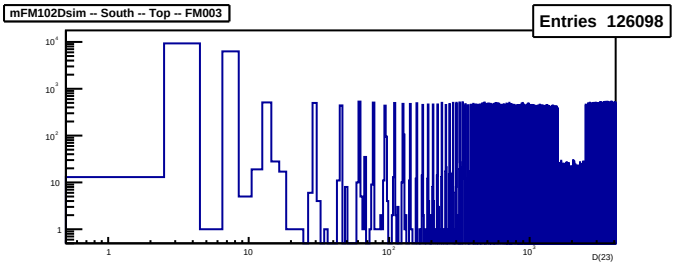
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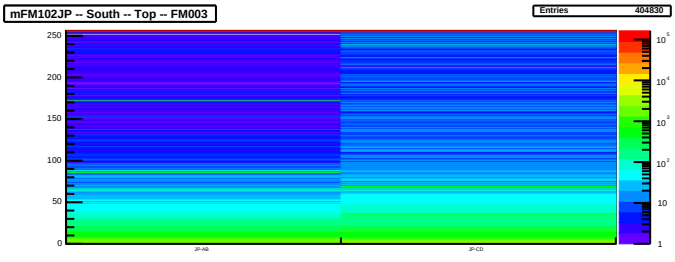
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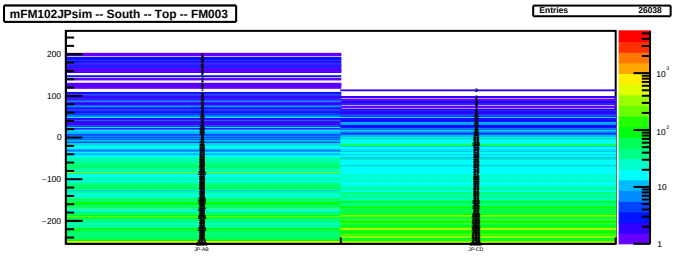
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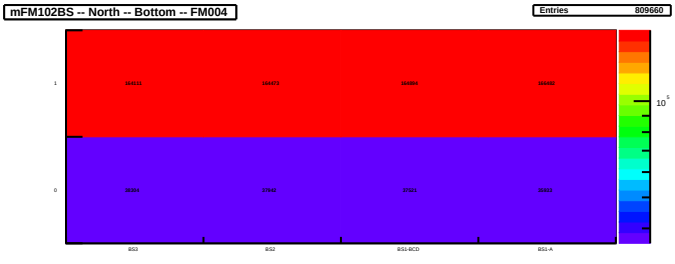
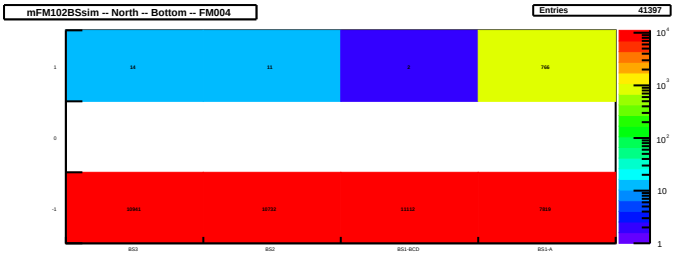

mFM102BS -- North -- Bottom -- FM004
Entries 809660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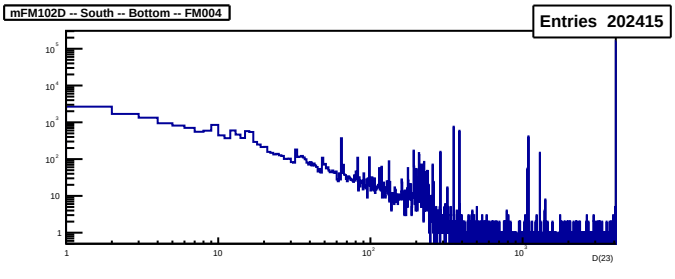
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Entries 202415
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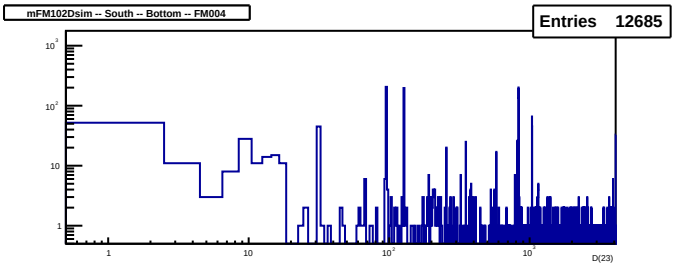
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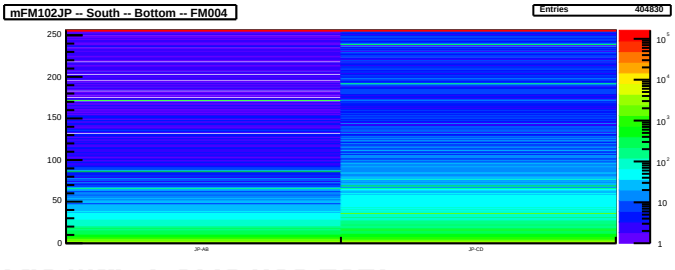
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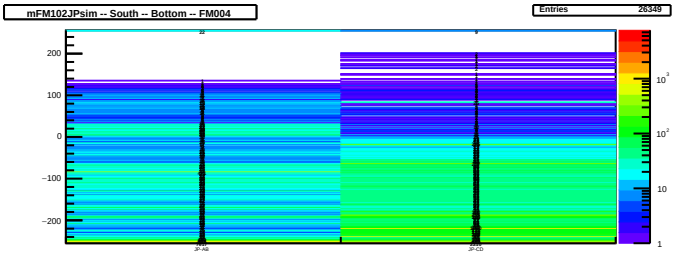
Entries 404830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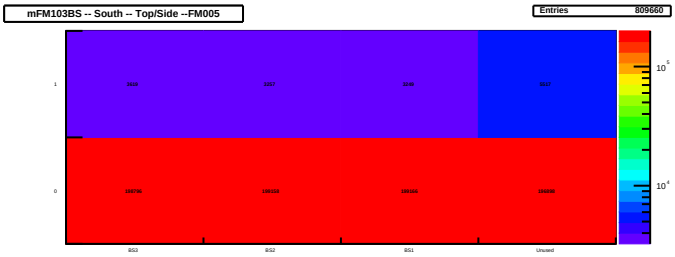
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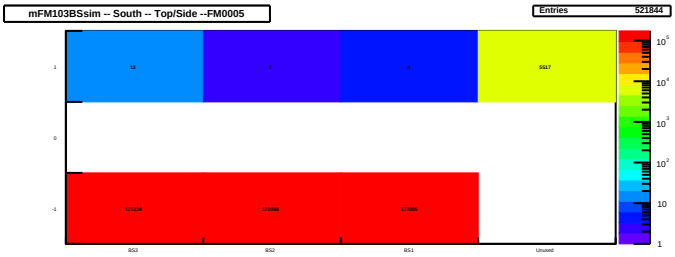
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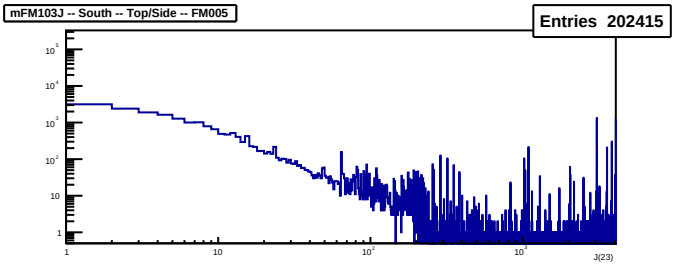
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Entries 202415
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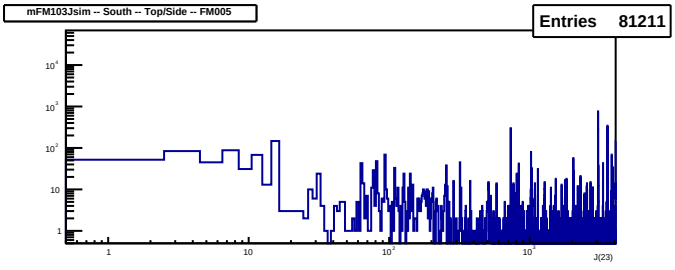
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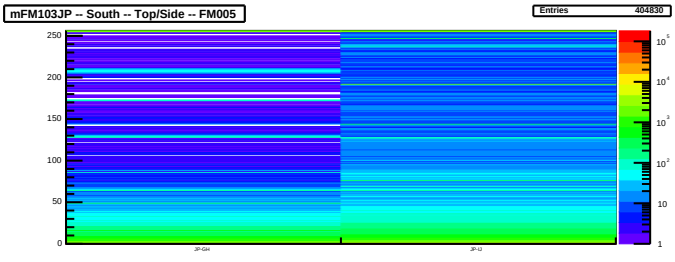
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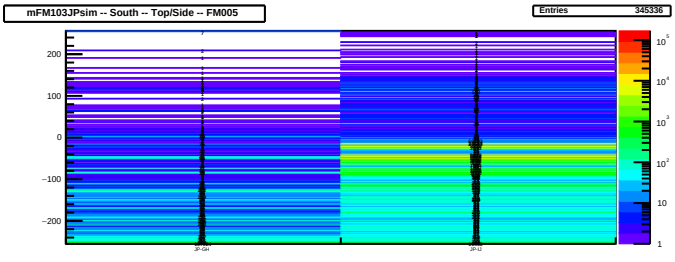
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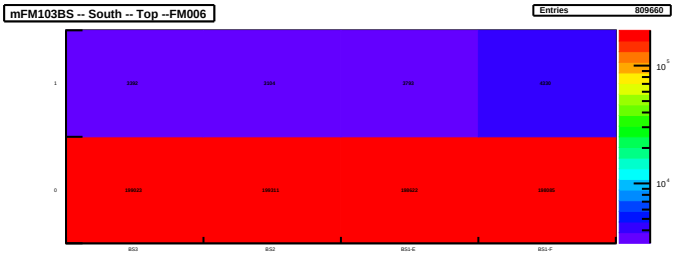
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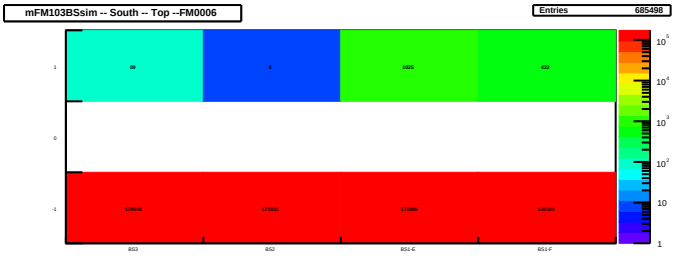
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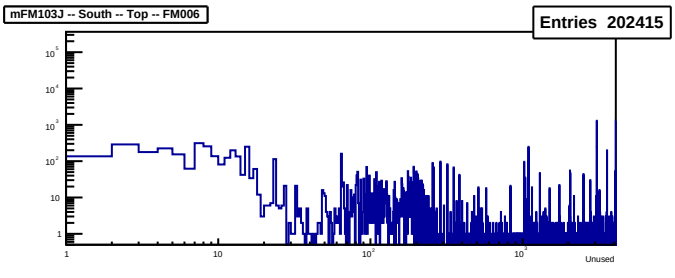
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Entries 202415
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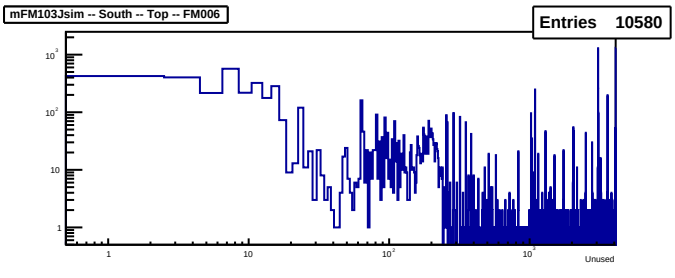
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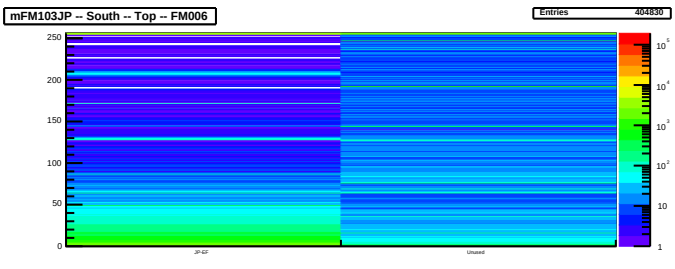
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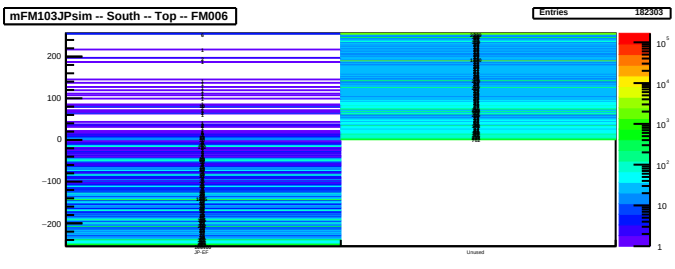
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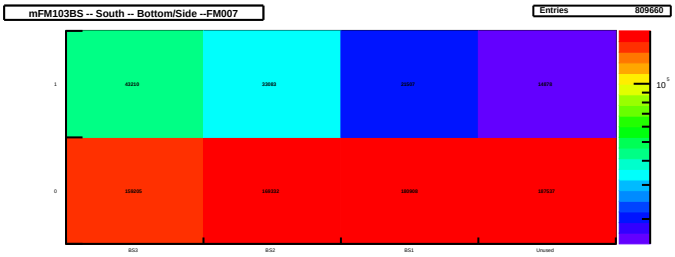
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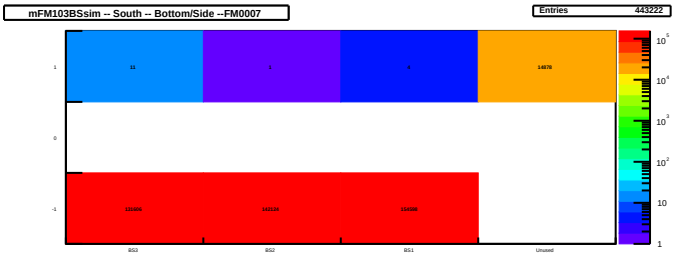
Entries 809660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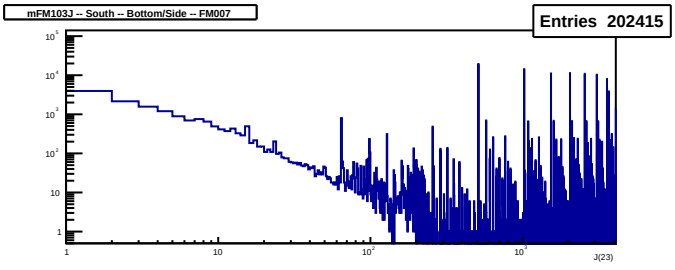
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Entries 202415
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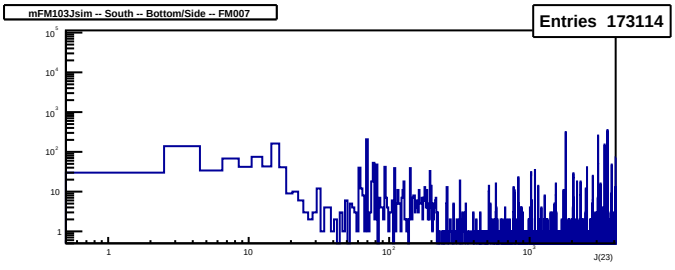
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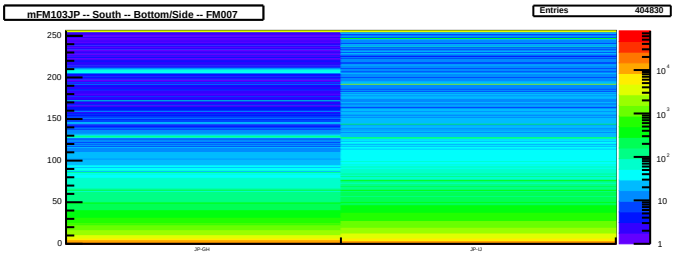
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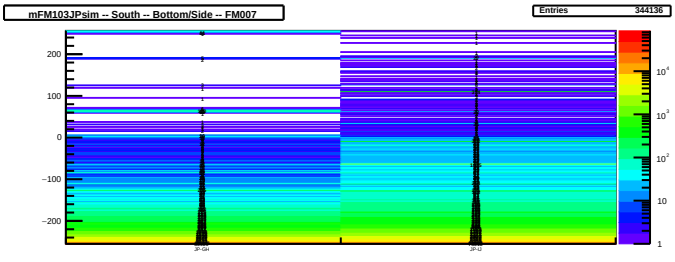
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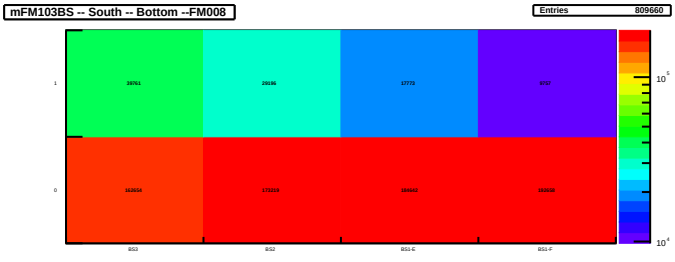
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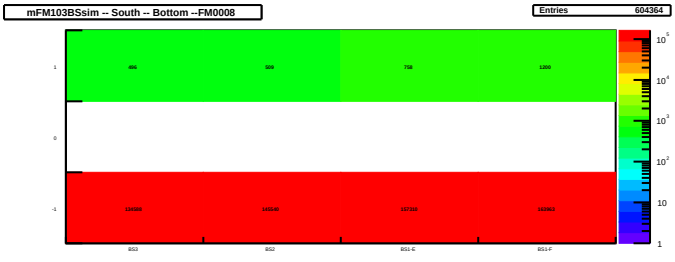
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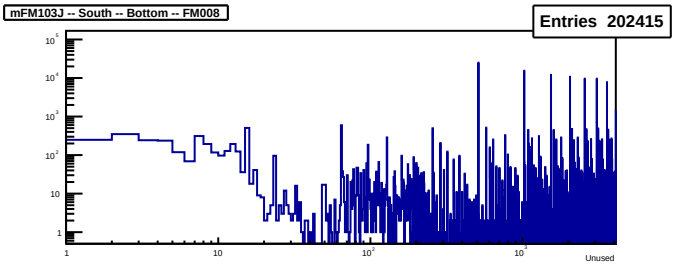
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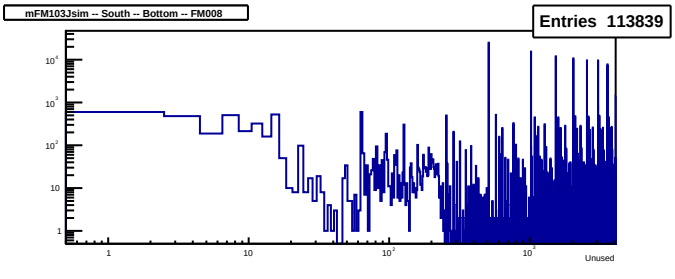
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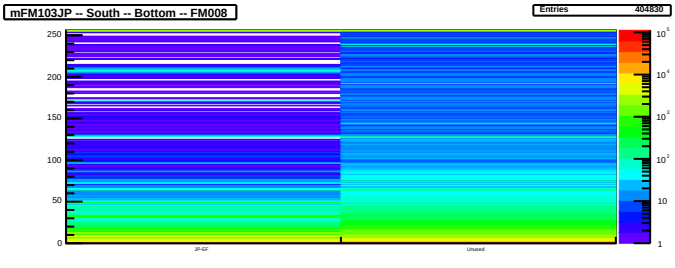
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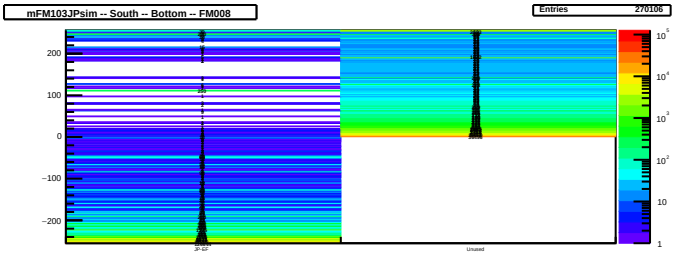
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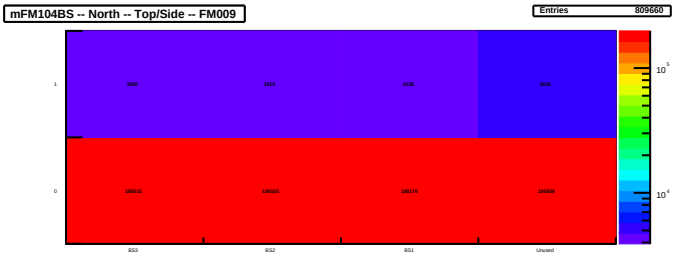
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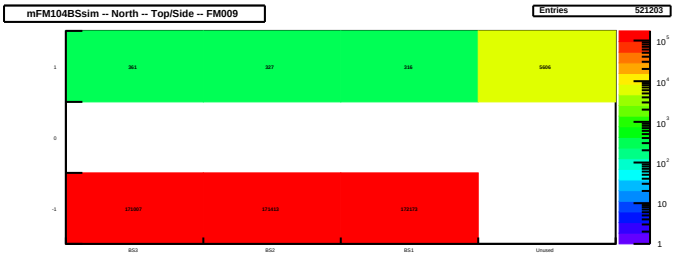
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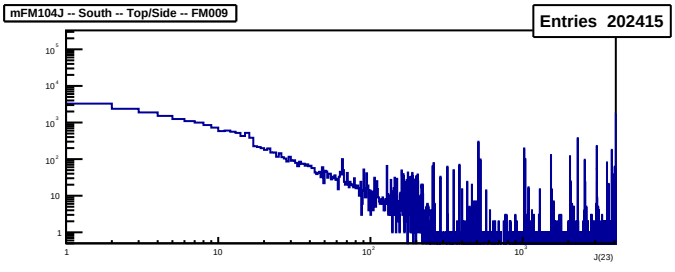
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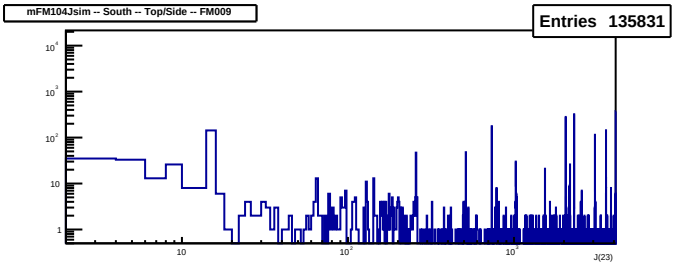
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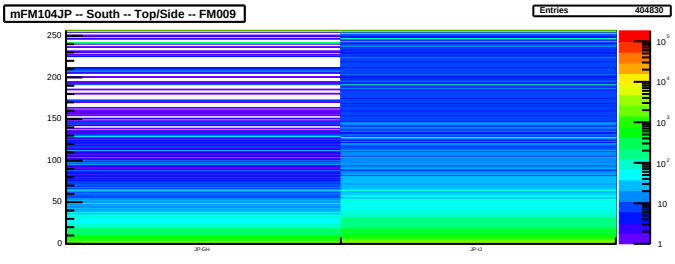
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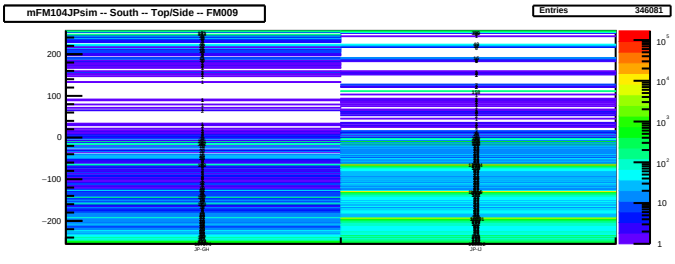
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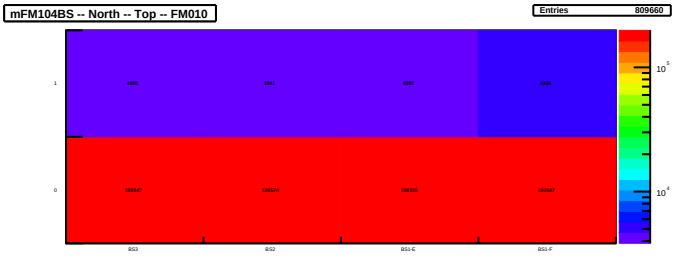
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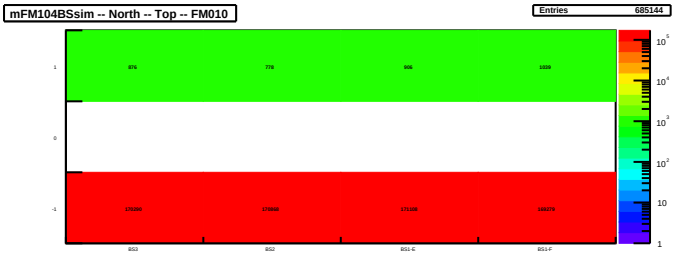
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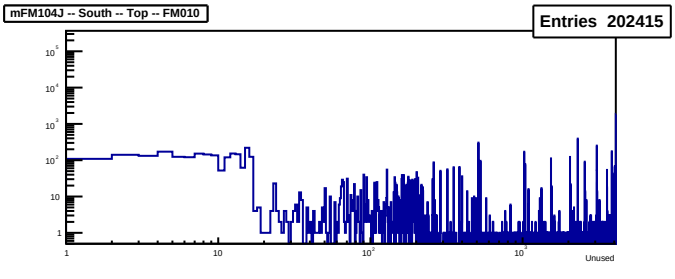
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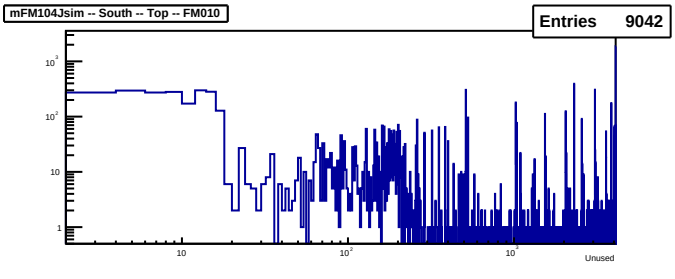
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Entries 9042
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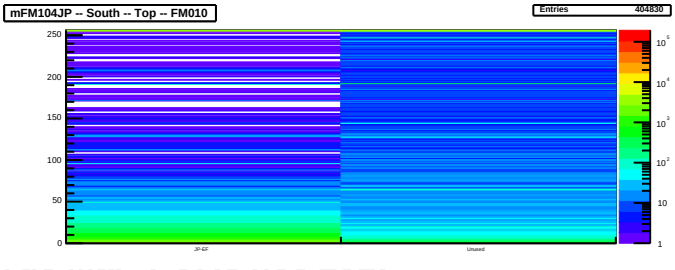
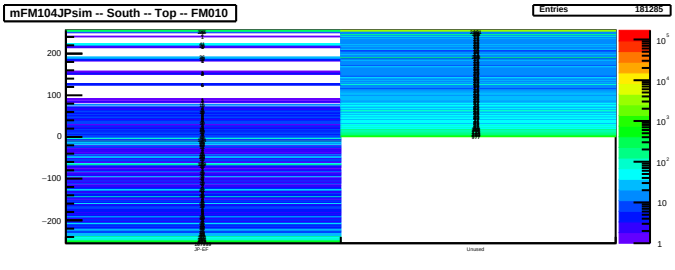
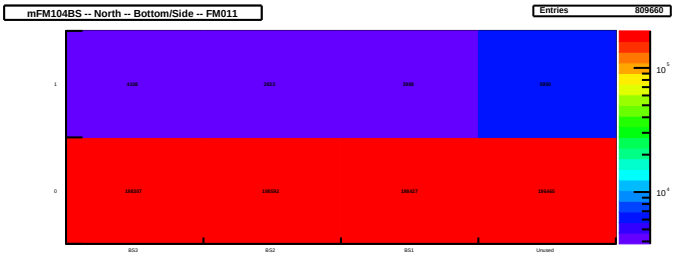
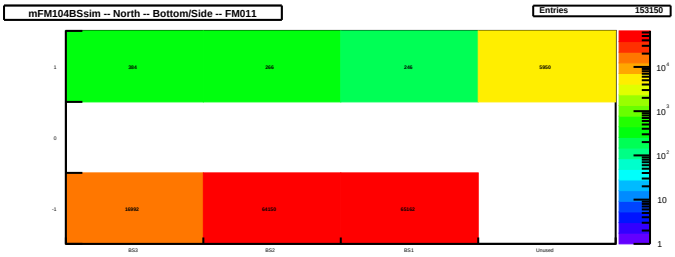
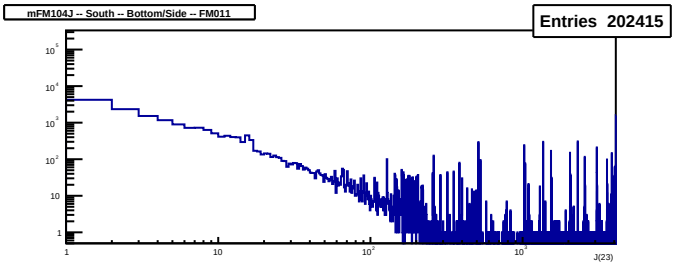
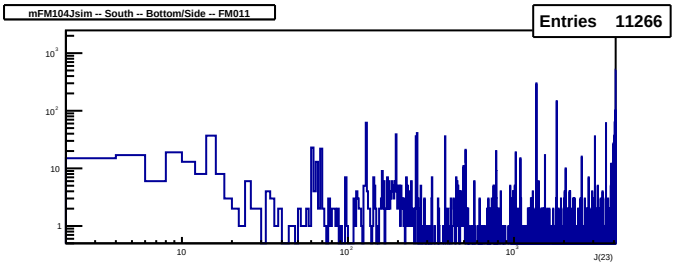

mFM104JP -- South -- Top -- FM010
Entries 404830

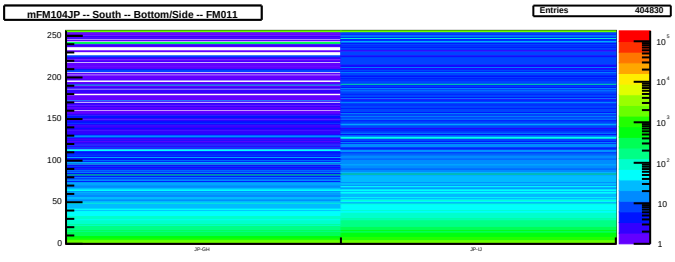

mFM104JPsim -- South -- Top -- FM010
Entries 181285

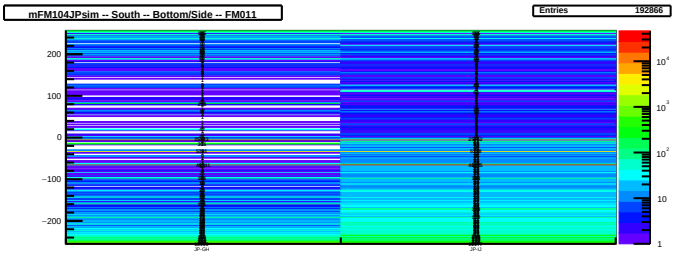

mFM104BS -- North -- Bottom/Side -- FM011
Entries 809660

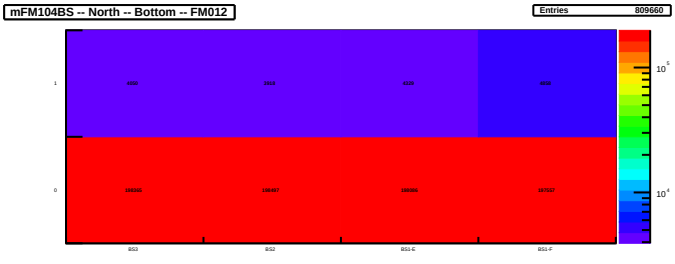

mFM104BSsim -- North -- Bottom/Side -- FM011
Entries 153150

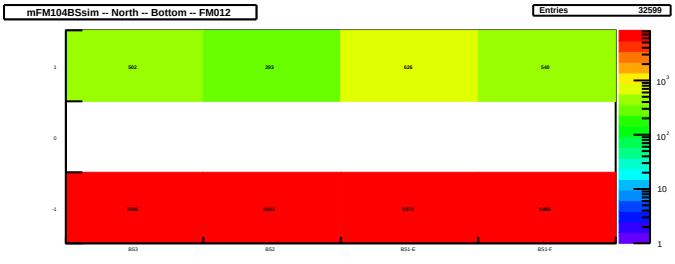

mFM104J -- South -- Bottom/Side -- FM011
Entries 202415
J(23)

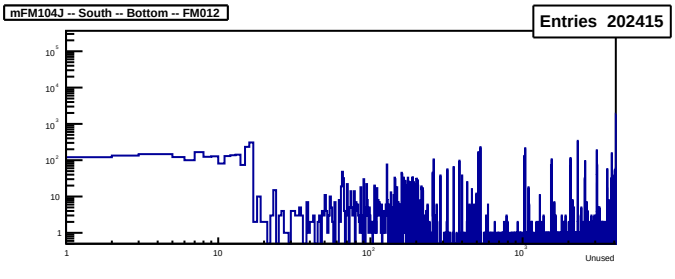

mFM104Jsim -- South -- Bottom/Side -- FM011
Entries 11266
J(23)

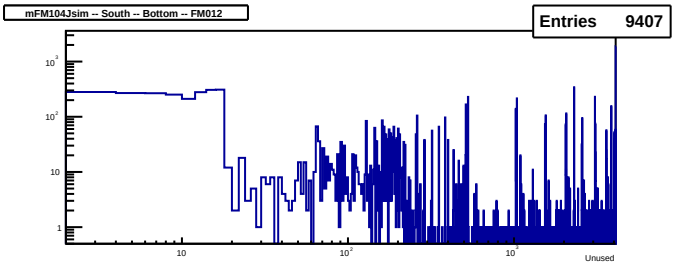

mFM104JP -- South -- Bottom/Side -- FM011
Entries 404830


mFM104JPsim -- South -- Bottom/Side -- FM011
Entries 192866


mFM104BS -- North -- Bottom -- FM012
Entries 809660


mFM104BSsim -- North -- Bottom -- FM012
Entries 32599


mFM104J -- South -- Bottom -- FM012
Entries 202415
Unused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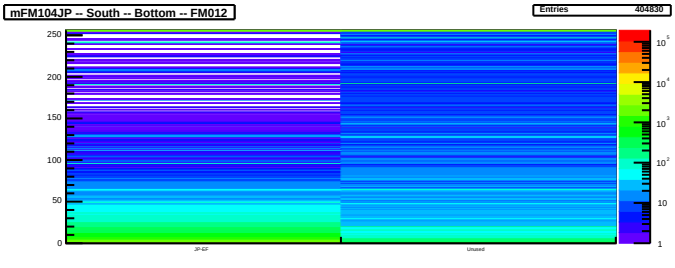
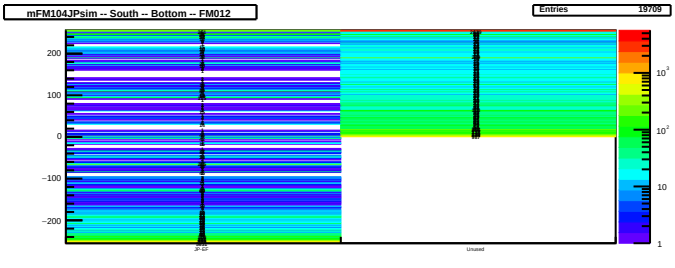
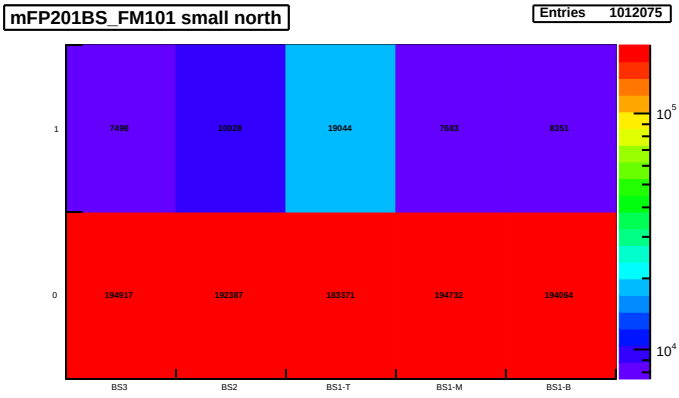
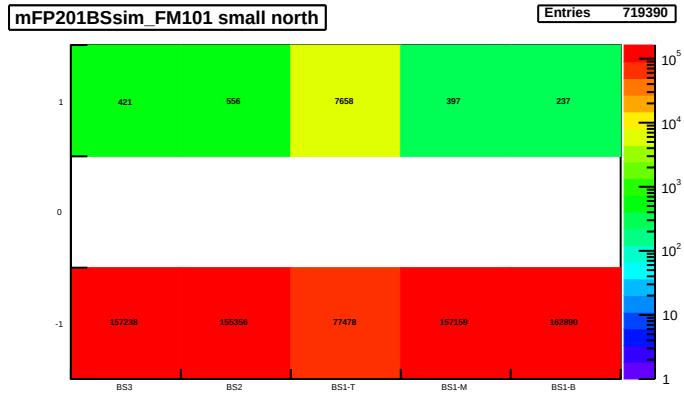
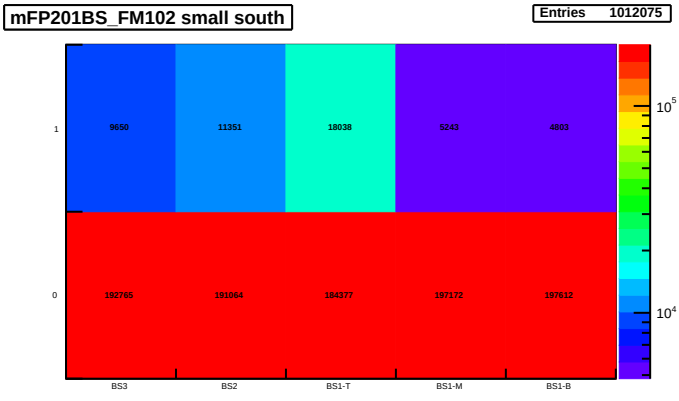
Entries 9407
Unused

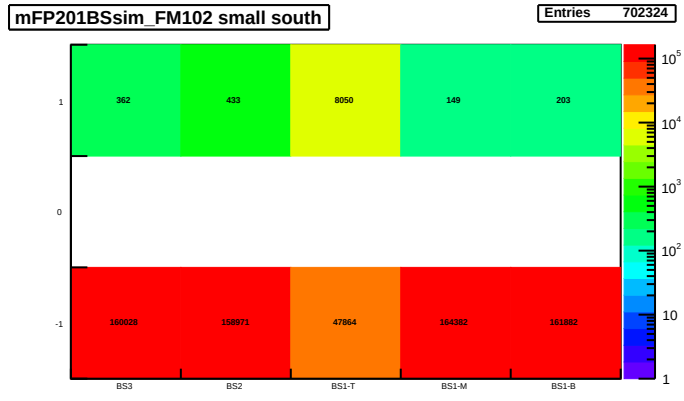

mFM104JP -- South -- Bottom -- FM012
Entries 404830

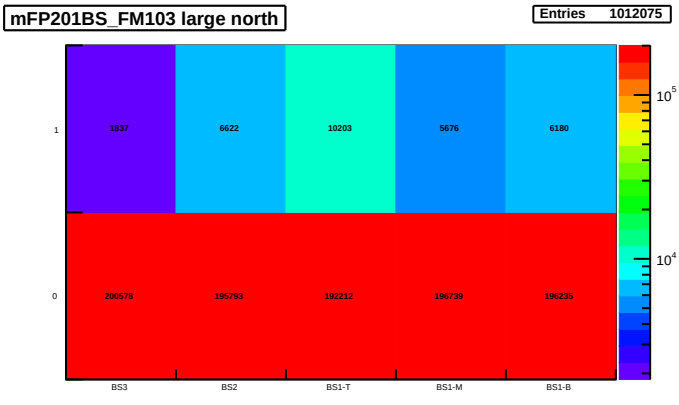

mFM104JPsim -- South -- Bottom -- FM012
Entries 19709

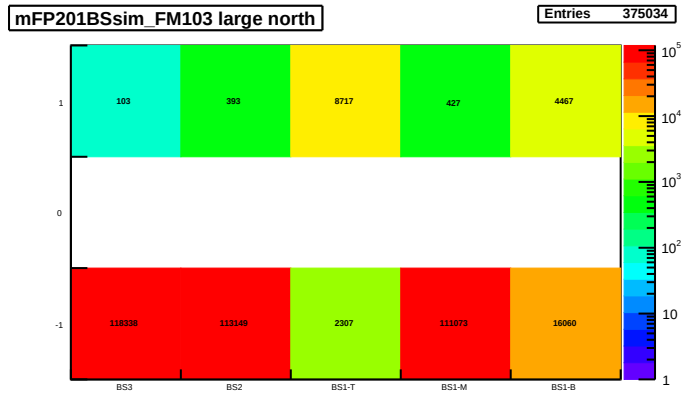

mFP201BS_FM101 small north
Entries 1012075
7498
10028
19044
7683
8351
194917
192387
183371
194732
194064
BS3
BS2
BS1-T
BS1-M
BS1-B

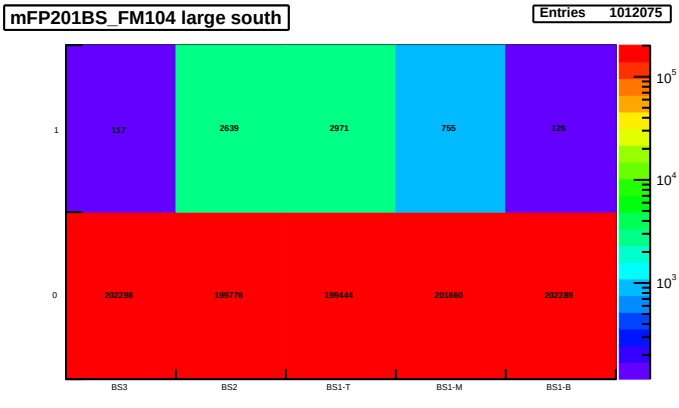

mFP201BSsim_FM101 small north
Entries 719390
421
556
7658
397
237
157238
155356
77478
157159
162890
BS3
BS2
BS1-T
BS1-M
BS1-B

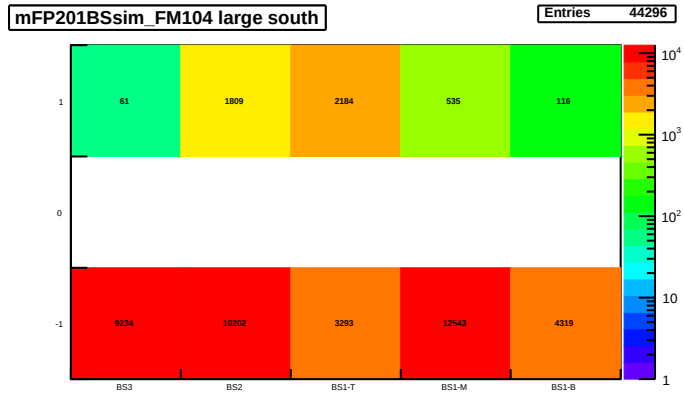

mFP201BS_FM102 small south
Entries 1012075
9650
11351
18038
5243
4803
192765
191064
184377
197172
197612
BS3
BS2
BS1-T
BS1-M
BS1-B

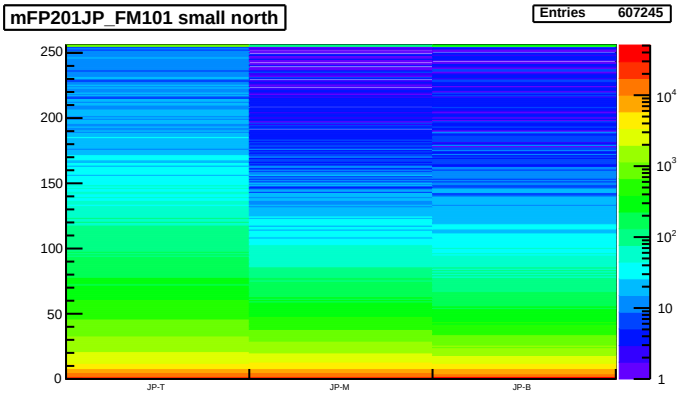

mFP201BSsim_FM102 small south
Entries 702324
362
433
8050
149
203
160028
158971
47864
164382
161882
BS3
BS2
BS1-T
BS1-M
BS1-B

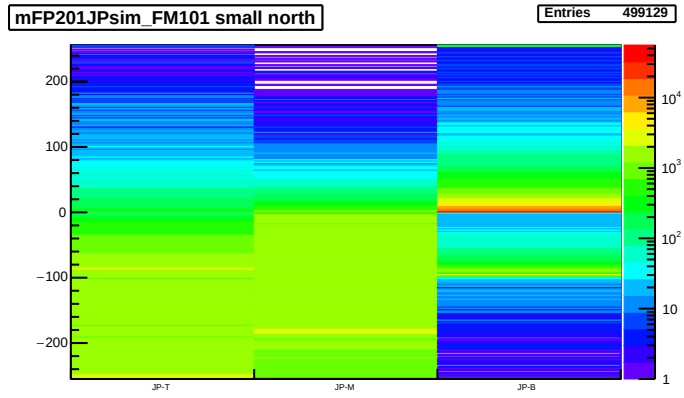

mFP201BS_FM103 large north
Entries 1012075
1837
6622
10203
5676
6180
200578
195793
192212
196739
196235
BS3
BS2
BS1-T
BS1-M
BS1-B

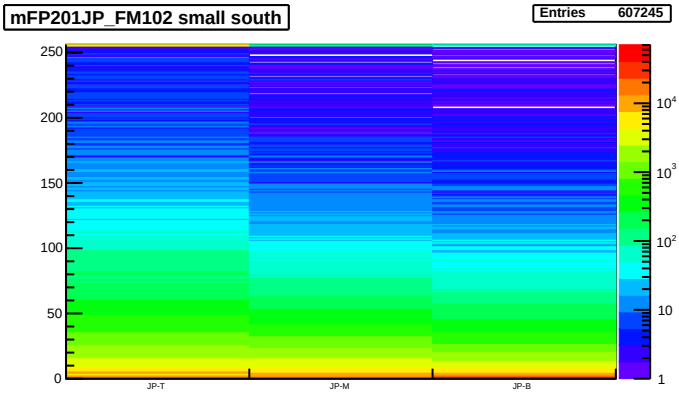

mFP201BSsim_FM103 large north
Entries 375034
103
393
8717
427
4467
118338
113149
2307
111073
16060
BS3
BS2
BS1-T
BS1-M
BS1-B

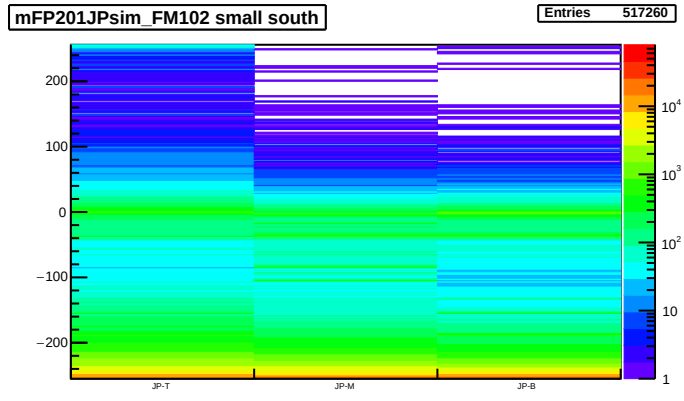

mFP201BS_FM104 large south
Entries 1012075
117
2639
2971
755
126
202298
199776
199444
201660
202289
BS3
BS2
BS1-T
BS1-M
BS1-B

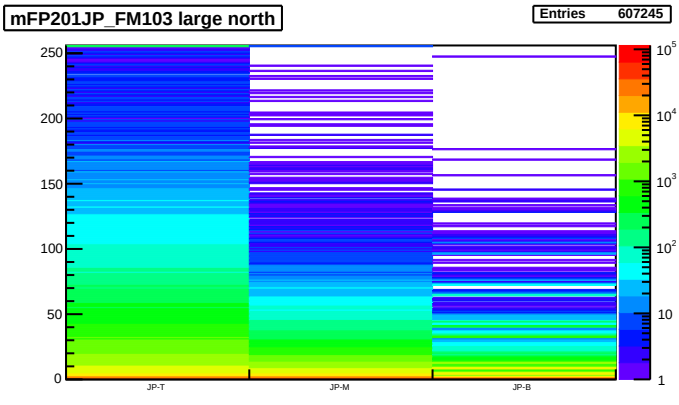

mFP201BSsim_FM104 large south
Entries 44296
61
1809
2184
535
116
9234
10202
3293
12543
4319
BS3
BS2
BS1-T
BS1-M
BS1-B

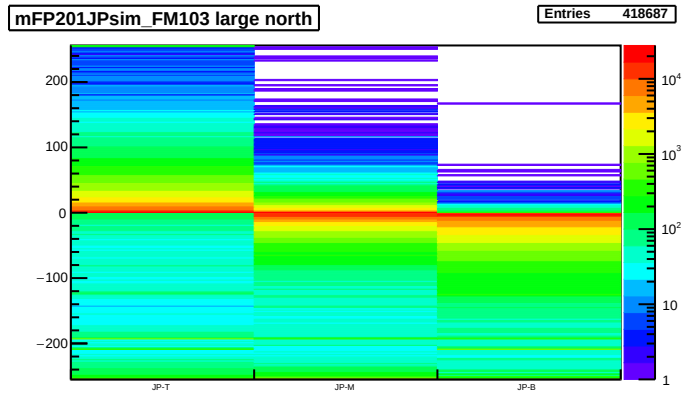

mFP201JP_FM101 small north
Entries 607245
JP-T
JP-M
JP-B

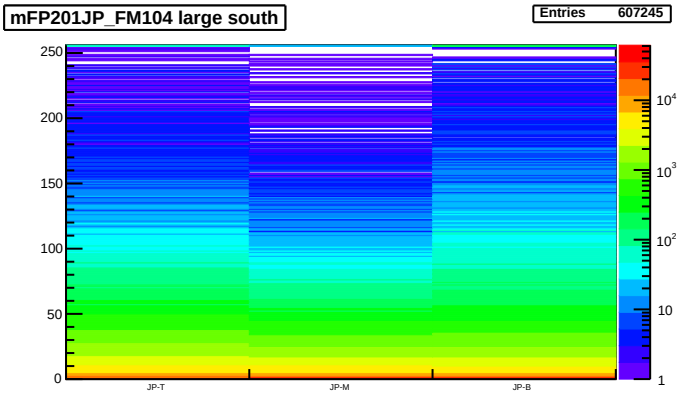

mFP201JPsim_FM101 small north
Entries 499129
JP-T
JP-M
JP-B

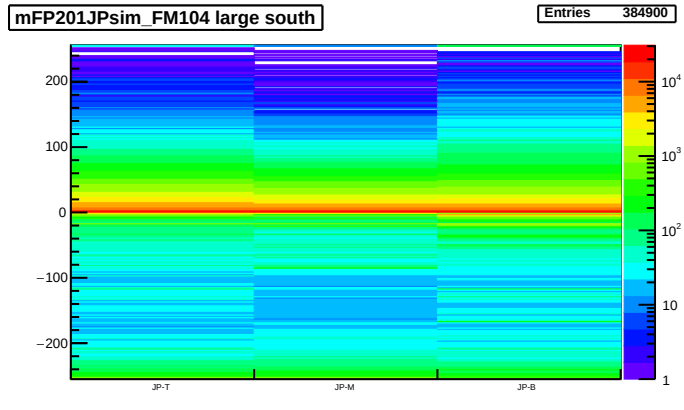

mFP201JP_FM102 small south
Entries 607245
JP-T
JP-M
JP-B

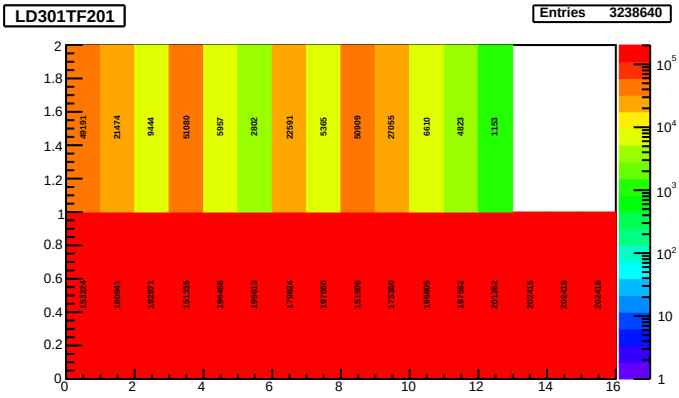

mFP201JPsim_FM102 small south
Entries 517260
JP-T
JP-M
JP-B

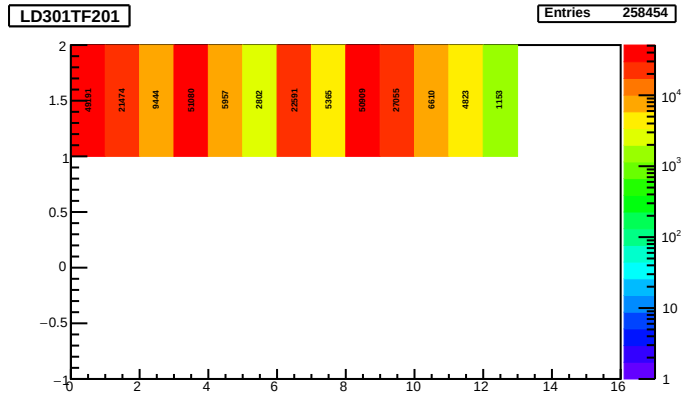

mFP201JP_FM103 large north
Entries 607245
JP-T
JP-M
JP-B

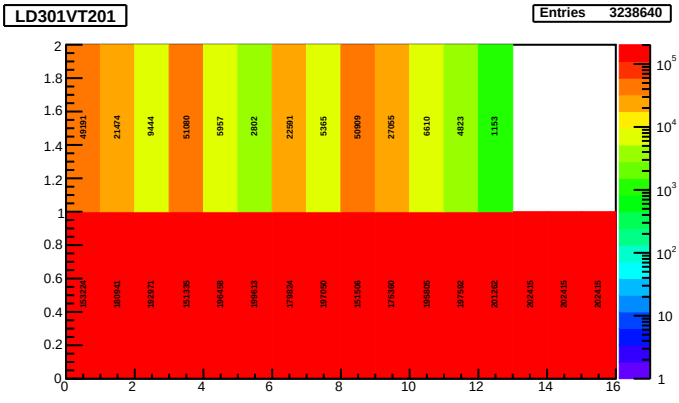

mFP201JPsim_FM103 large north
Entries 418687
JP-T
JP-M
JP-B

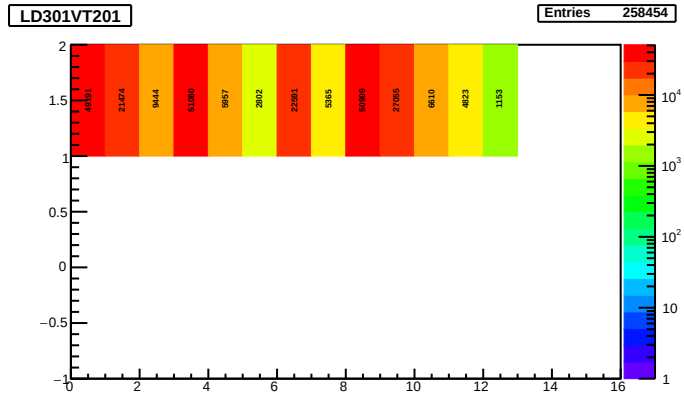

mFP201JP_FM104 large south
Entries 607245
JP-T
JP-M
JP-B

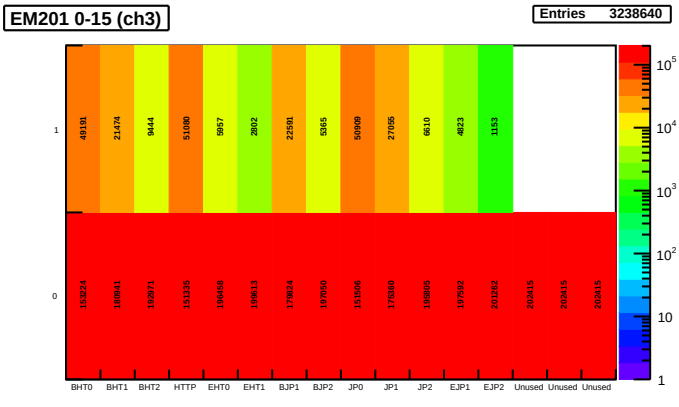

mFP201JPsim_FM104 large south
Entries 384900
JP-T
JP-M
JP-B

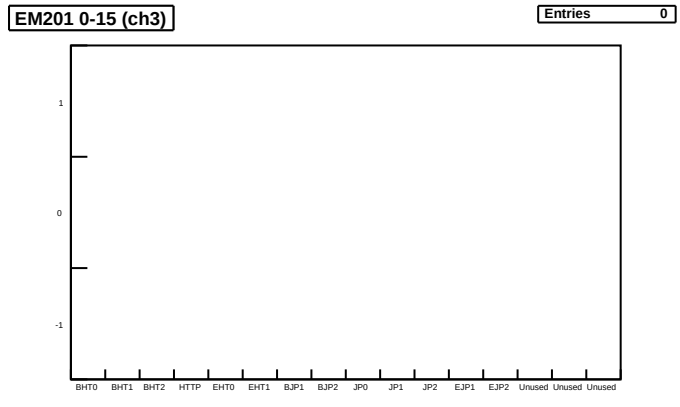

LD301TF201
Entries 3238640
49191 21474 9444 51080 5957 2802 22591 5365 50909 27055 6610 4823 1153
153224 180941 192371 151335 196458 199613 179824 197050 151506 175360 195805 197592 201262 202415 202415 202415

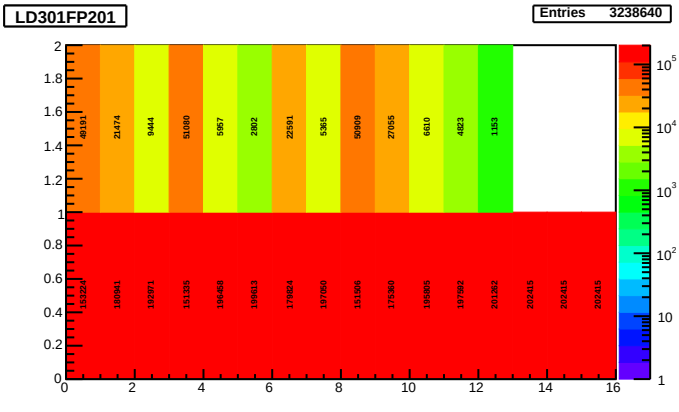

LD301TF201
Entries 258454
49191 21474 9444 51080 5957 2802 22591 5365 50909 27055 6610 4823 1153

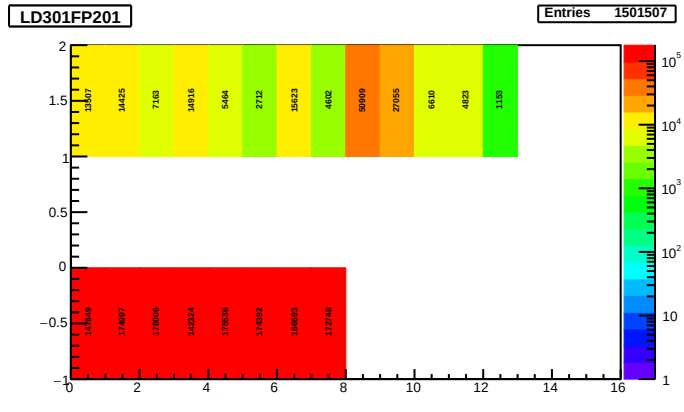

LD301VT201
Entries 3238640
49191 21474 9444 51080 5957 2802 22591 5365 50909 27055 6610 4823 1153
153224 180941 192371 151335 196458 199613 179824 197050 151506 175360 195805 197592 201262 202415 202415 202415


LD301VT201
Entries 258454
49191 21474 9444 51080 5957 2802 22591 5365 50909 27055 6610 4823 1153


EM201 0-15 (ch3)
Entries 3238640
49191 21474 9444 51080 5957 2802 22591 5365 50909 27055 6610 4823 1153
153224 180941 192371 151335 196458 199613 179824 197050 151506 175360 195805 197592 201262 202415 202415 202415
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


LD301FP201
Entries 3238640
49191 21474 9444 51080 5957 2802 22591 5365 50909 27055 6610 4823 1153
153224 180941 192371 151335 196458 199613 179824 197050 151506 175360 195805 197592 201262 202415 202415 202415


LD301FP201
Entries 1501507
13507 14425 7183 14916 5484 2712 15623 4602 50909 27055 6610 4823 1153
147849 174897 178006 142224 175536 174392 166633 172748