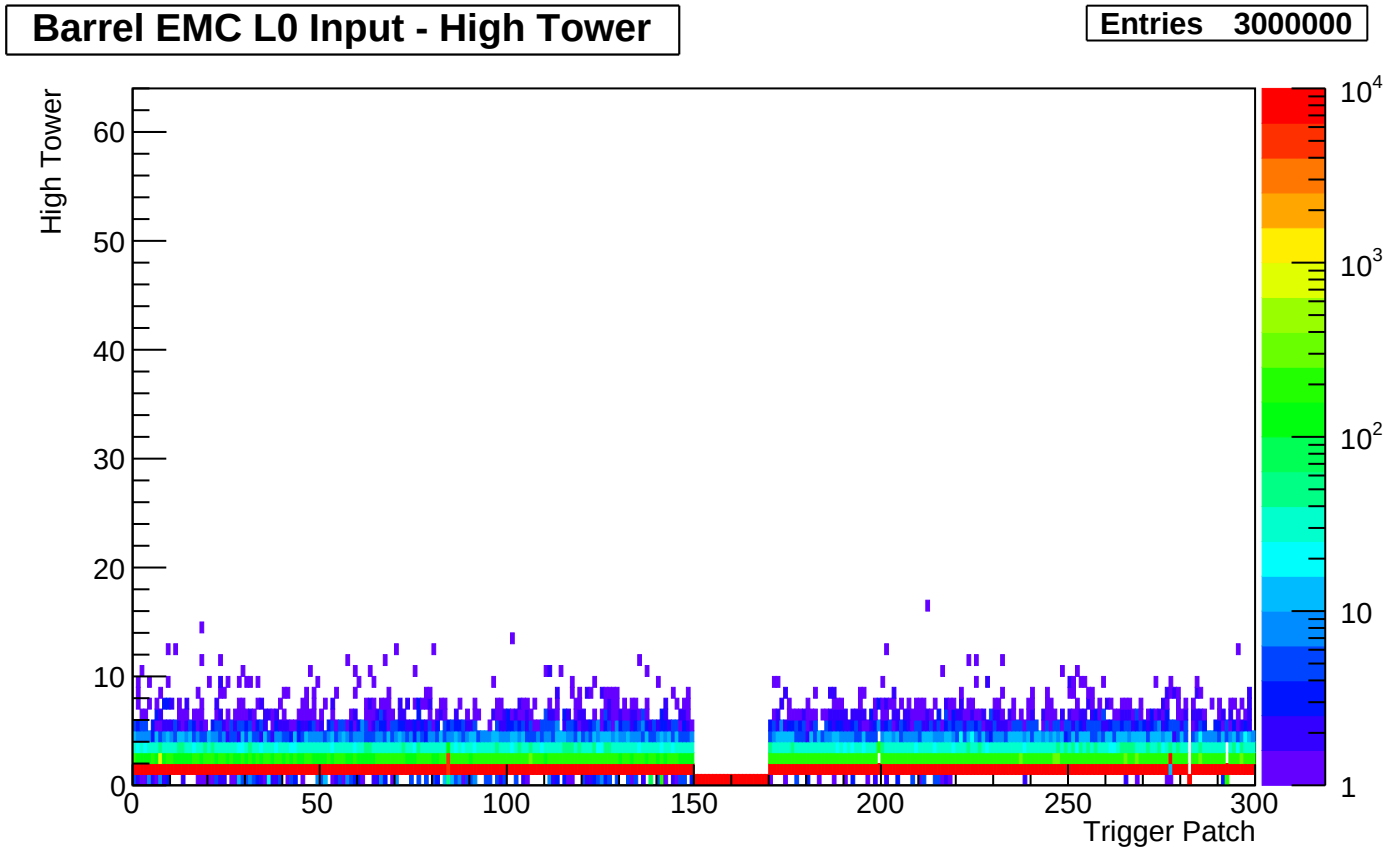
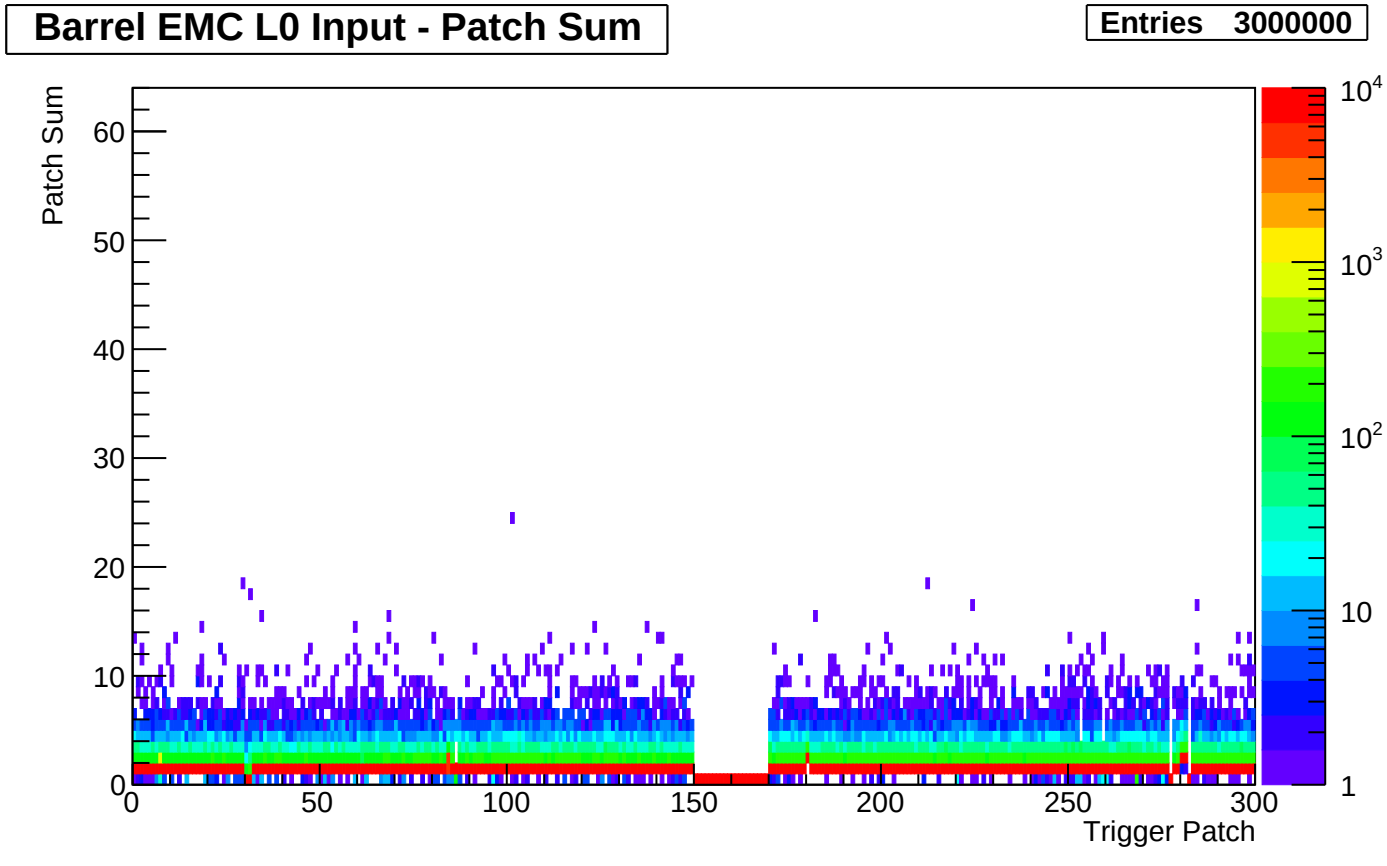
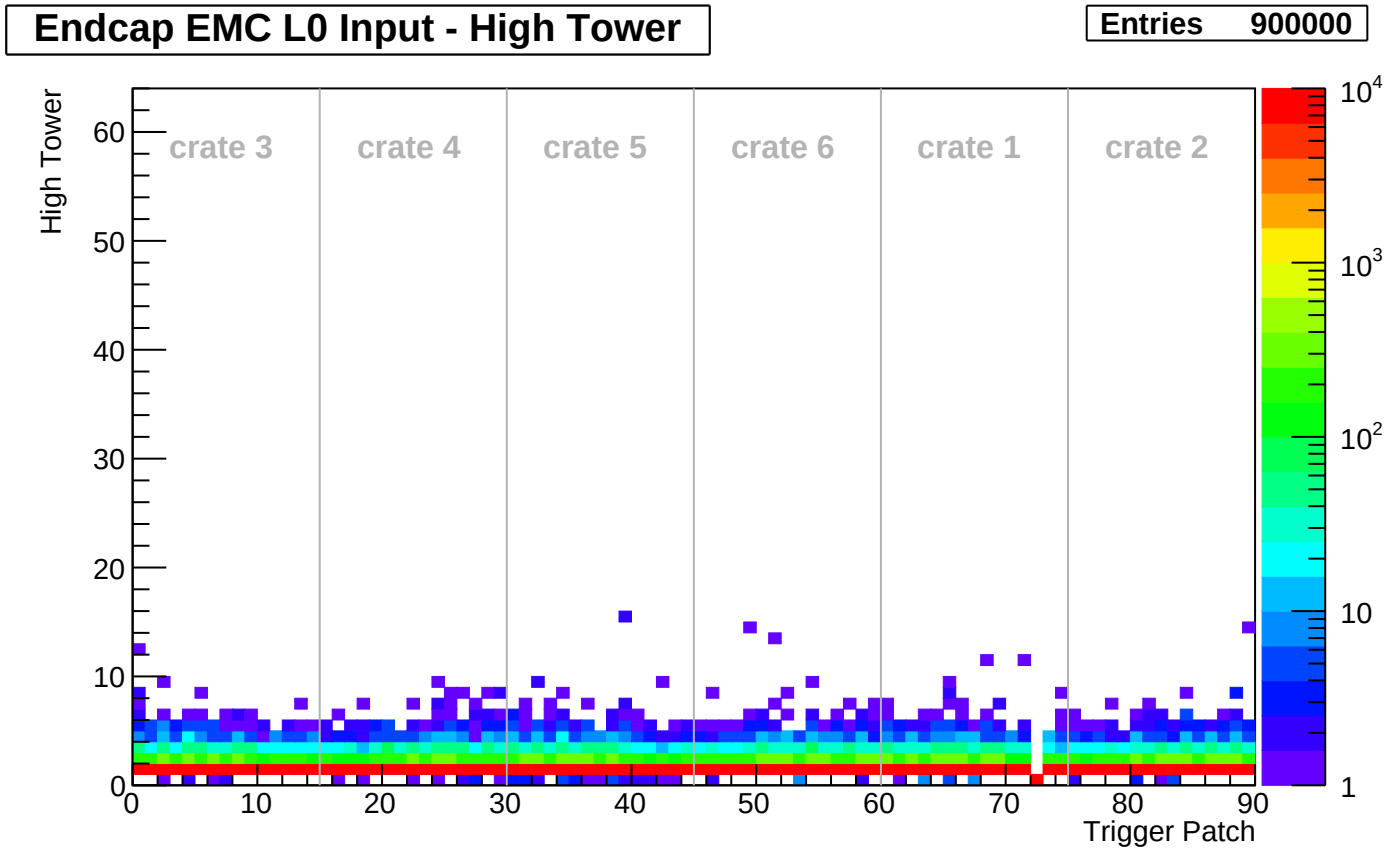
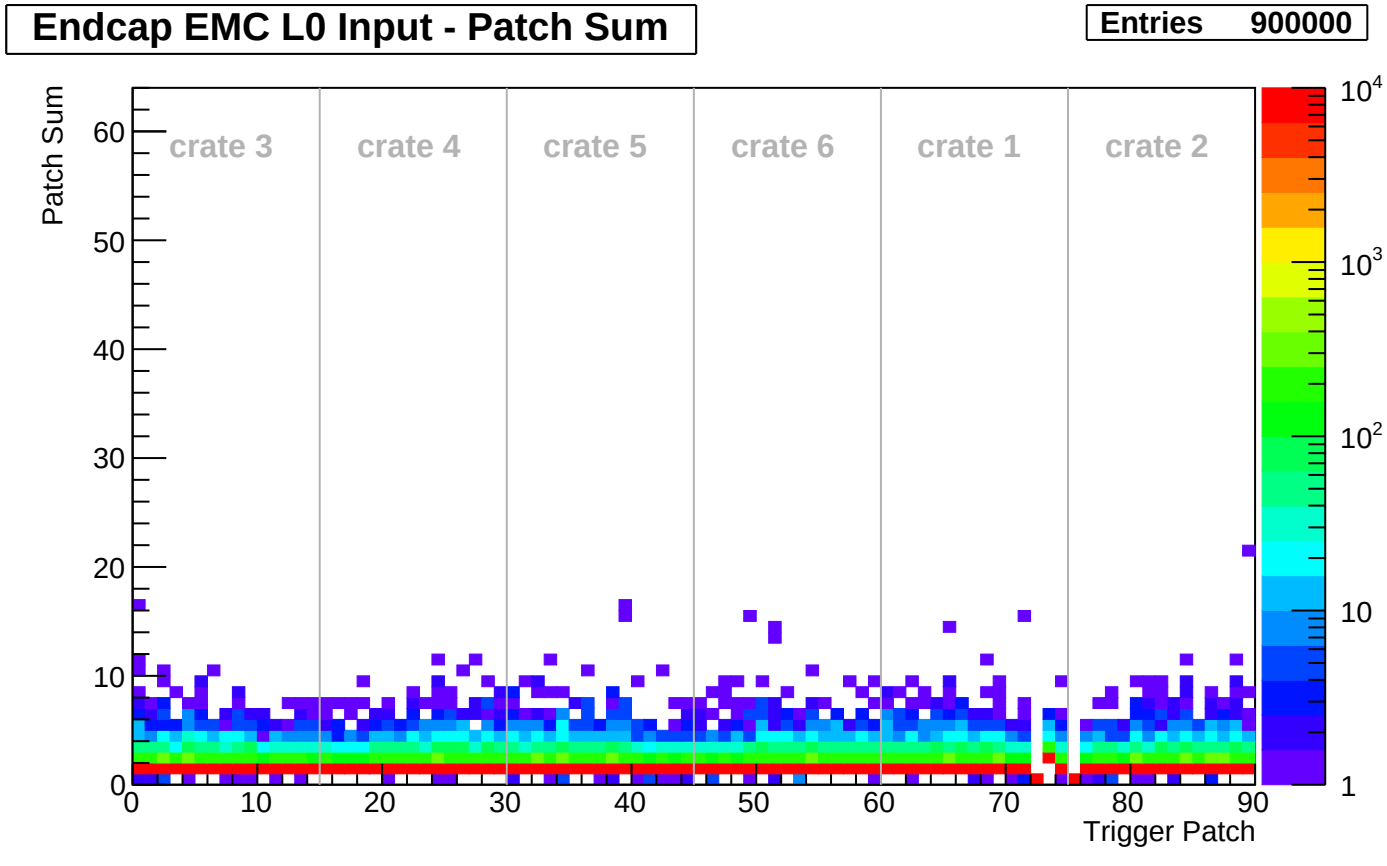
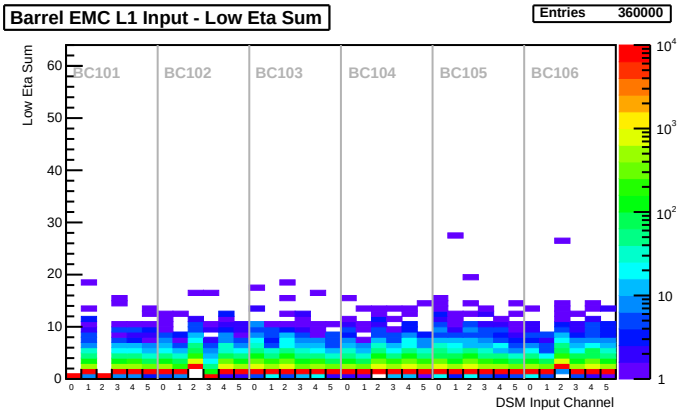
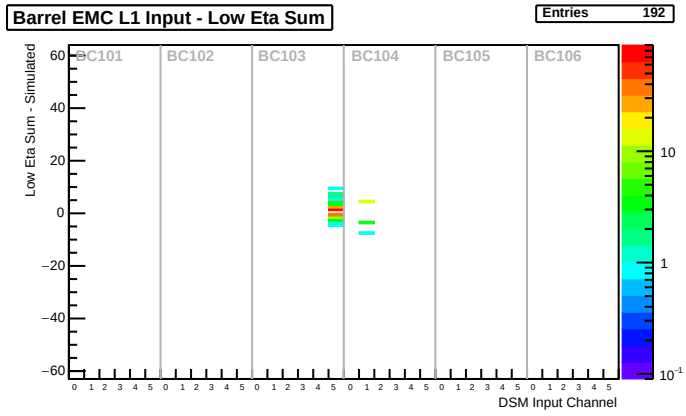

Barrel EMC L0 Input - High Tower
Entries 3000000
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

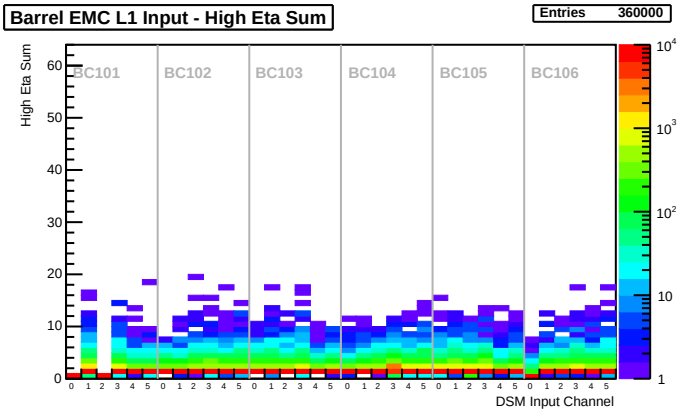

Barrel EMC L0 Input - Patch Sum
Entries 3000000
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

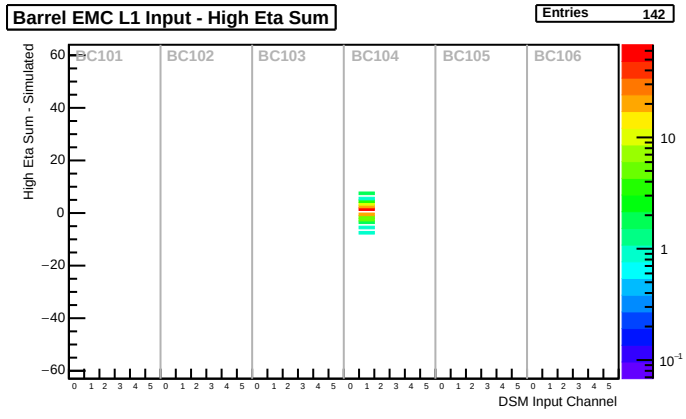

Endcap EMC L0 Input - High Tower
Entries 900000
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3
10^4

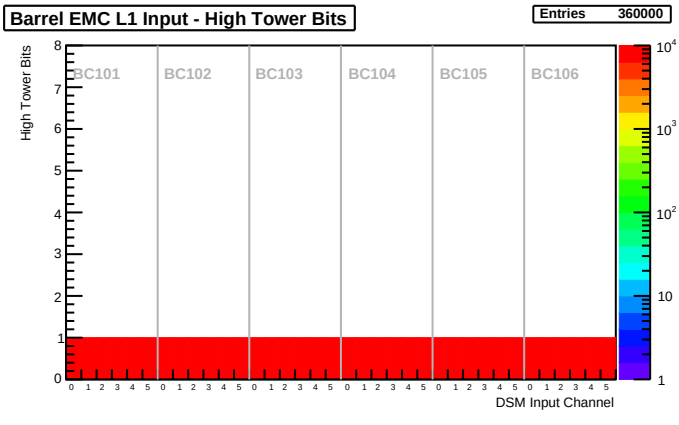

Endcap EMC L0 Input - Patch Sum
Entries 900000
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3
10^4

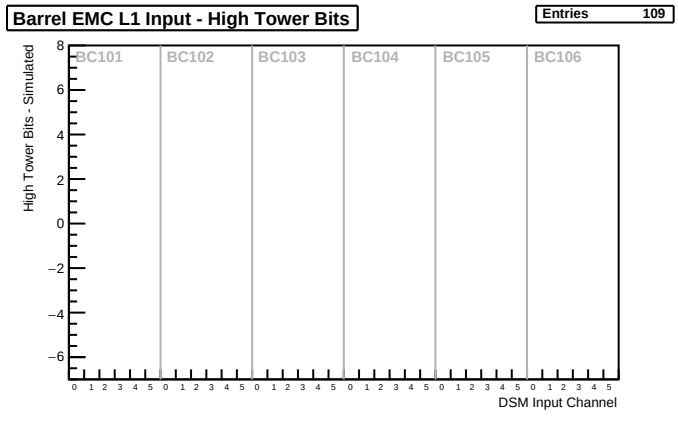

Barrel EMC L1 Input - Low Eta Sum
Entries 360000
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

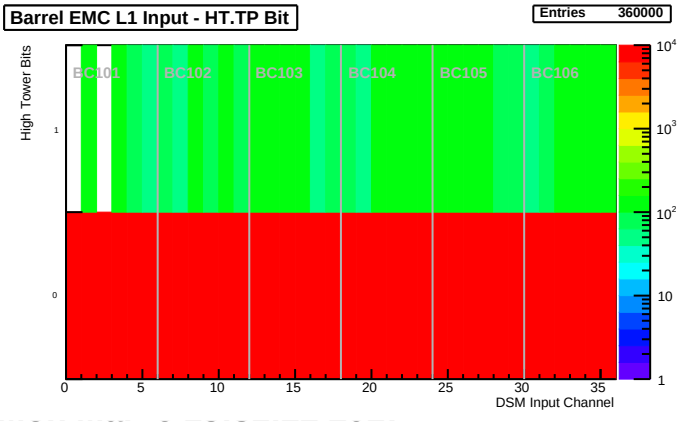

Barrel EMC L1 Input - Low Eta Sum
Entries 192
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

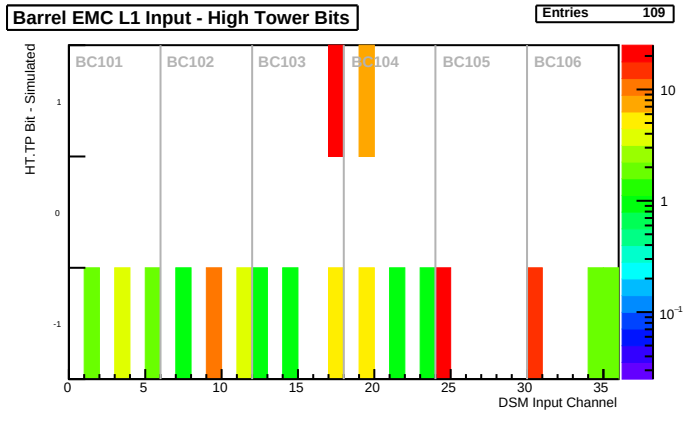

Barrel EMC L1 Input - High Eta Sum
Entries 360000
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

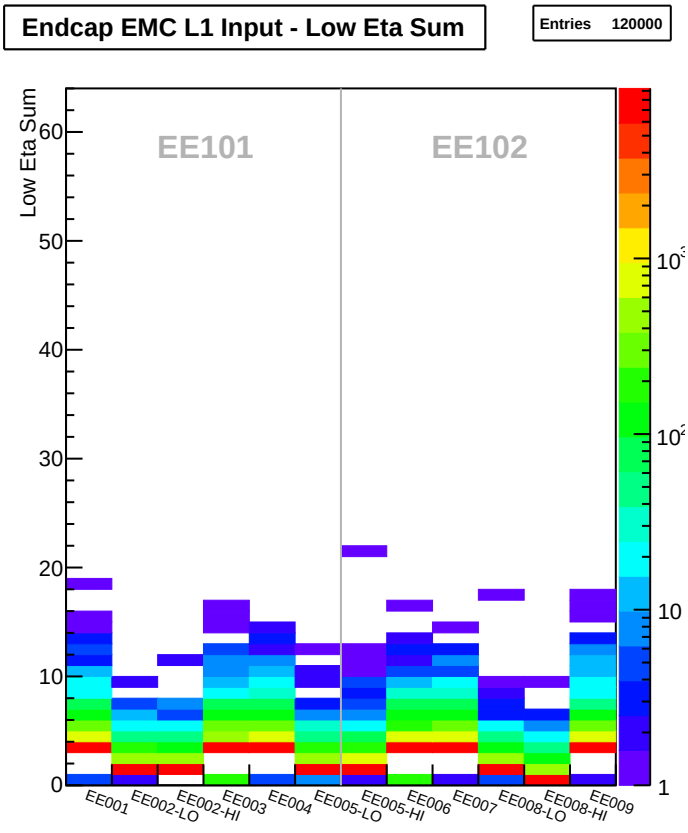

Barrel EMC L1 Input - High Eta Sum
Entries 142
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

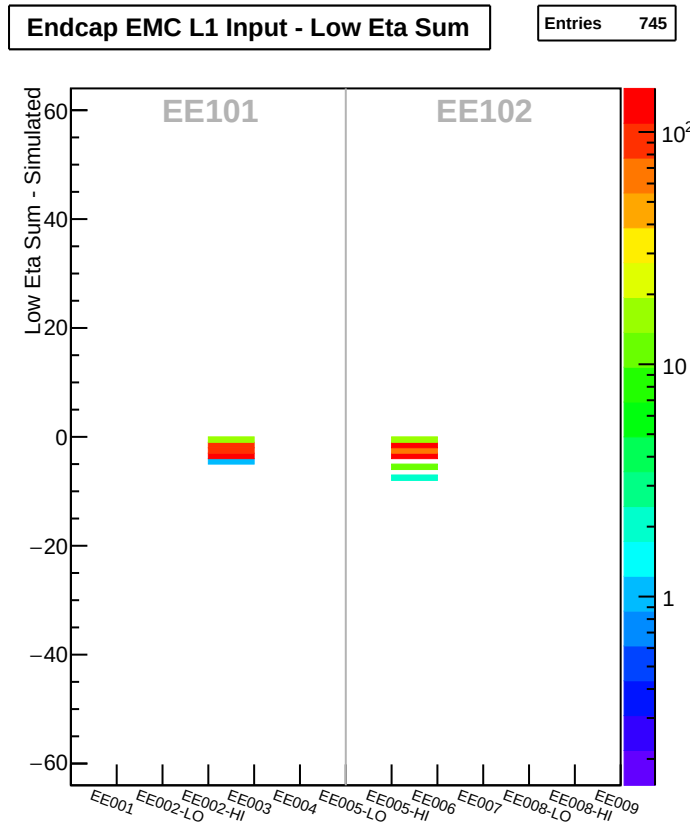

Barrel EMC L1 Input - High Tower Bits
Entries 360000
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

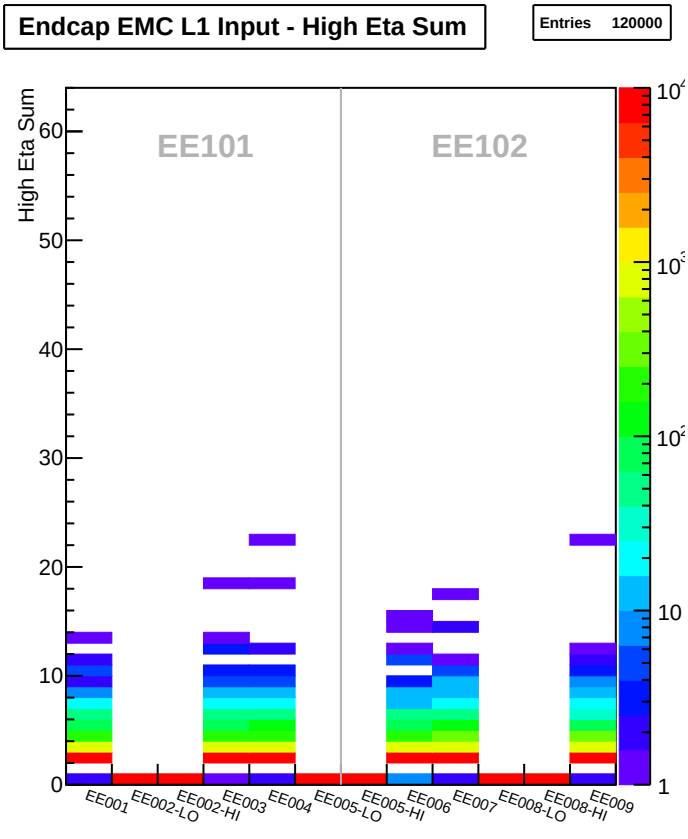

Barrel EMC L1 Input - High Tower Bits
Entries 109
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

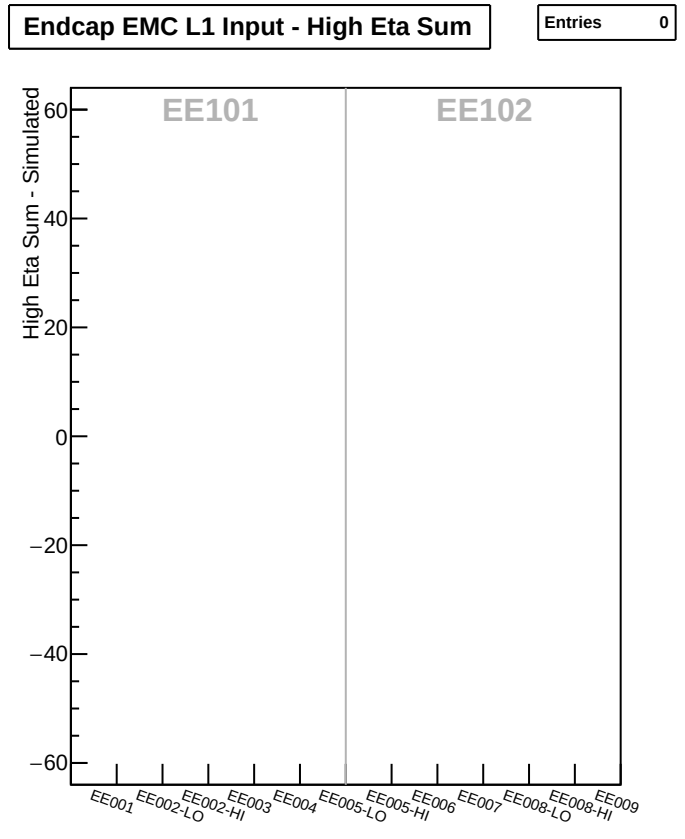

Barrel EMC L1 Input - HT.TP Bit
Entries 360000
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

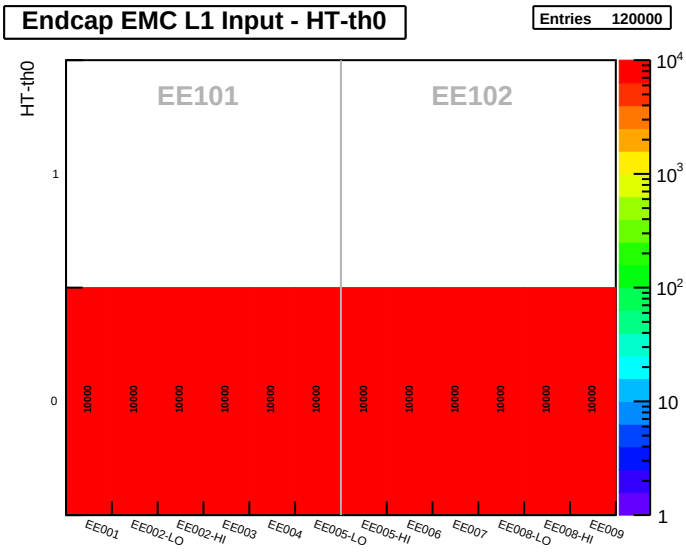

Barrel EMC L1 Input - High Tower Bits
Entries 109
HT.TP Bit - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

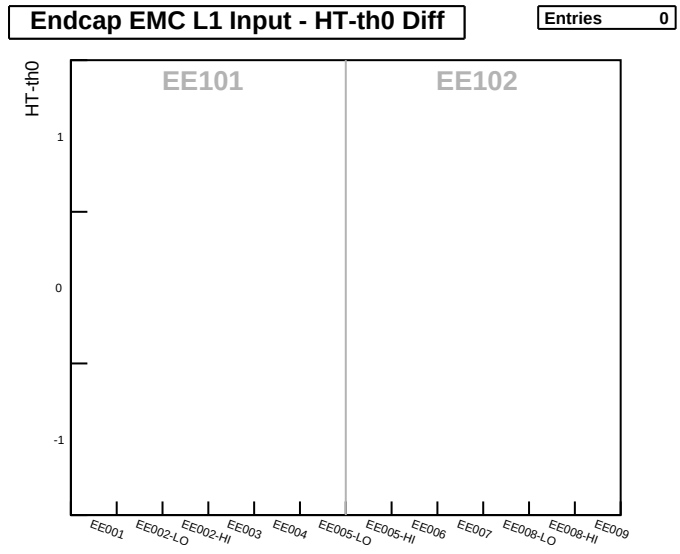

Endcap EMC L1 Input - Low Eta Sum
Entries 120000
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

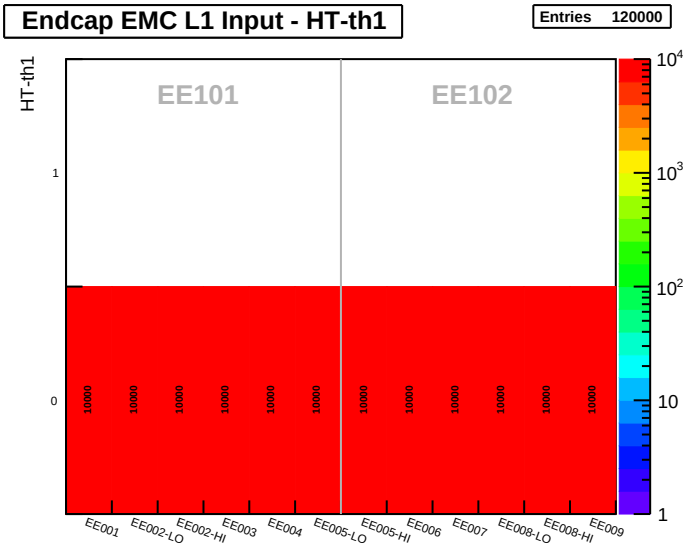

Endcap EMC L1 Input - Low Eta Sum
Entries 745
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

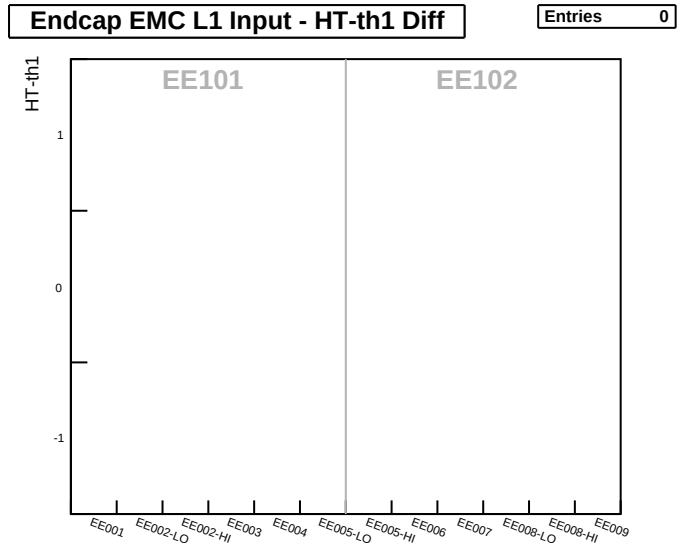

Endcap EMC L1 Input - High Eta Sum
Entries 120000
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

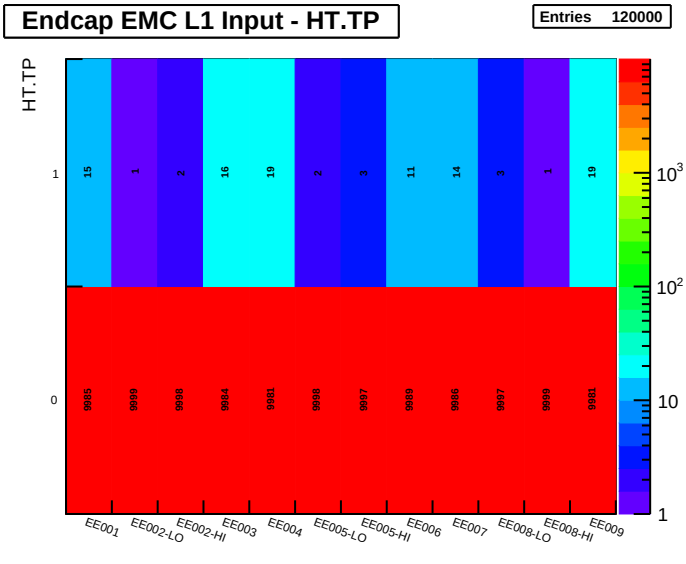

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

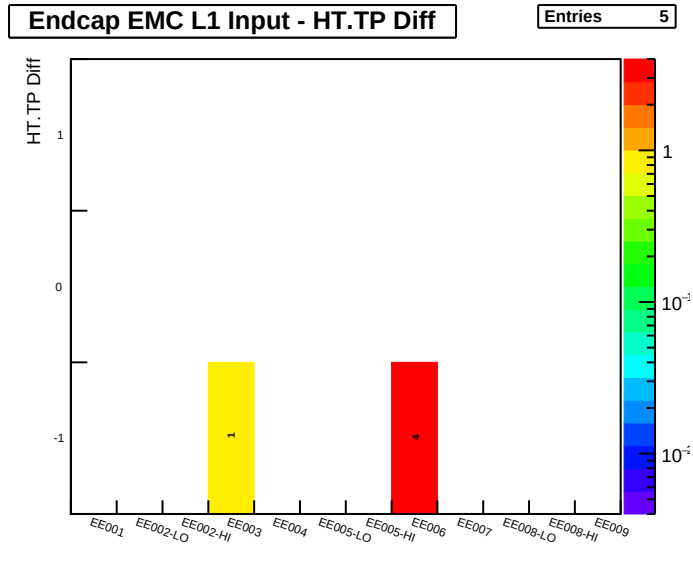

Endcap EMC L1 Input - HT-th0
Entries 120000
HT-th0
EE101
EE102
1
0
10000
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009
10^4
10^3
10^2
10
1

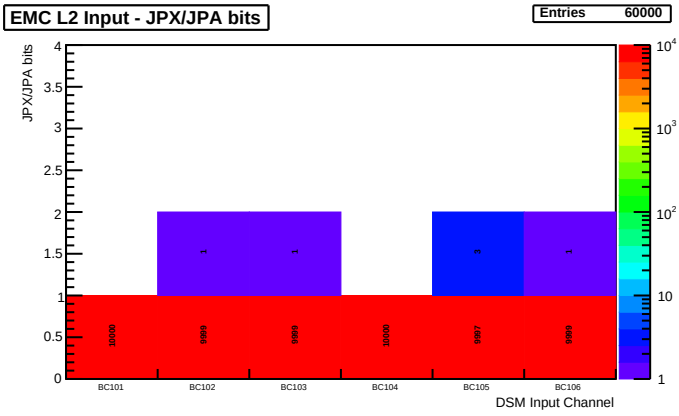

Endcap EMC L1 Input - HT-th0 Diff
Entries 0
HT-th0
EE101
EE102
1
0
-1
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

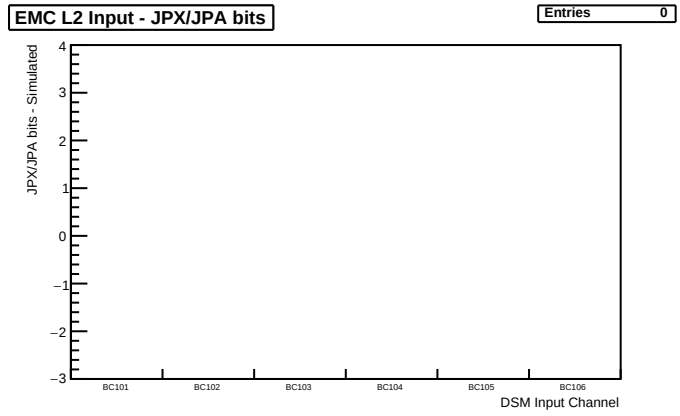

Endcap EMC L1 Input - HT-th1
Entries 120000
HT-th1
EE101
EE102
1
0
10000
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009
10^4
10^3
10^2
10
1

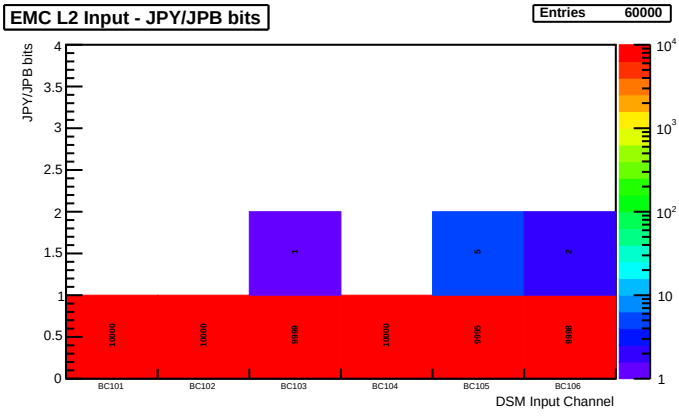

Endcap EMC L1 Input - HT-th1 Diff
Entries 0
HT-th1
EE101
EE102
1
0
-1
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

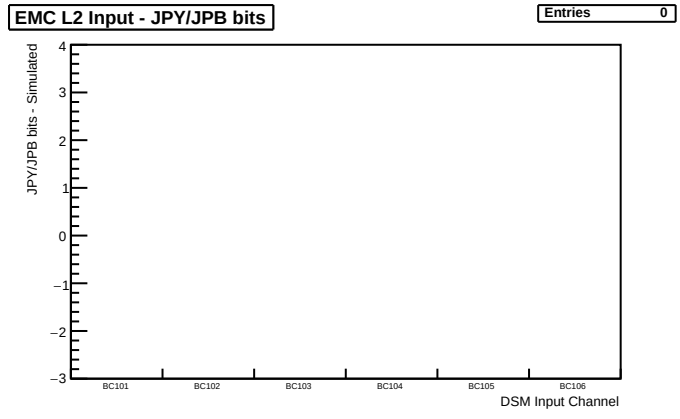

Endcap EMC L1 Input - HT.TP
Entries 120000
HT.TP
1
0
15
1
2
16
19
2
3
11
14
3
1
19
9985
9999
9998
9984
9981
9998
9997
9989
9986
9997
9999
9981
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009
10^3
10^2
10
1

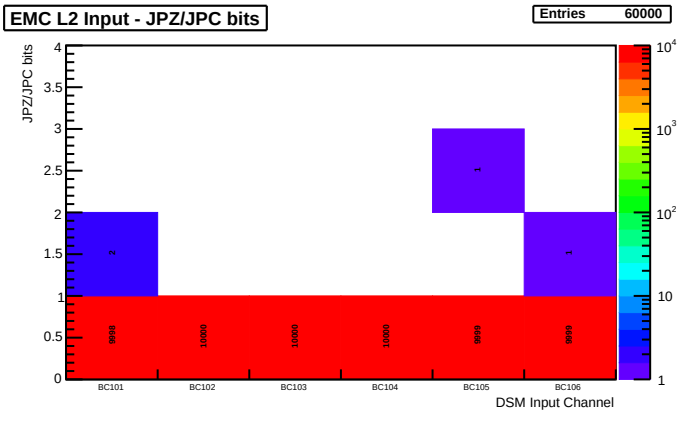

Endcap EMC L1 Input - HT.TP Diff
Entries 5
HT.TP Diff
1
0
-1
1
4
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009
1

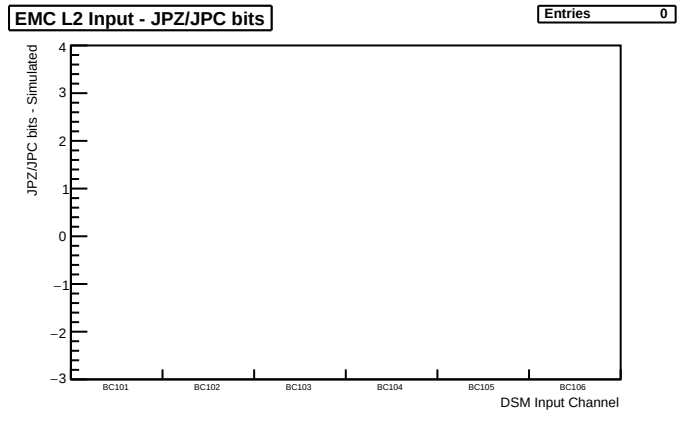

EMC L2 Input - JPX/JPA bits
Entries 60000
JPX/JPA bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

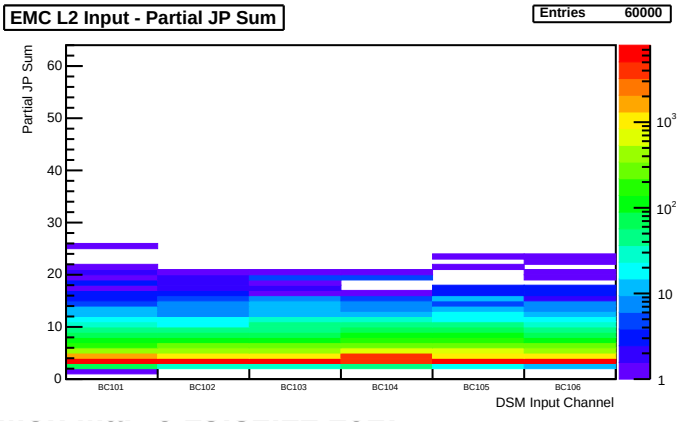

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

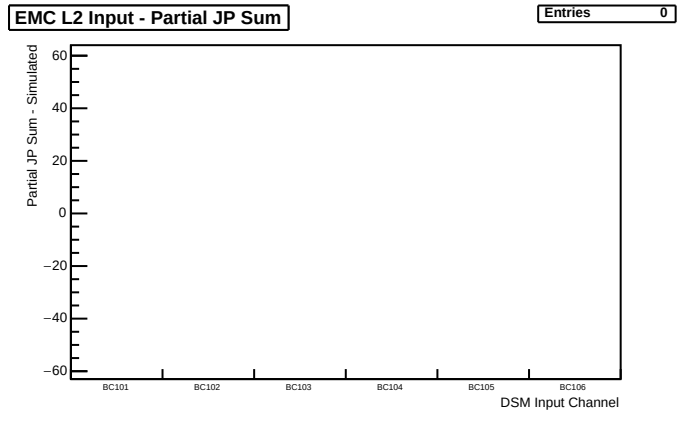

EMC L2 Input - JPY/JPB bits
Entries 60000
JPY/JPB bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

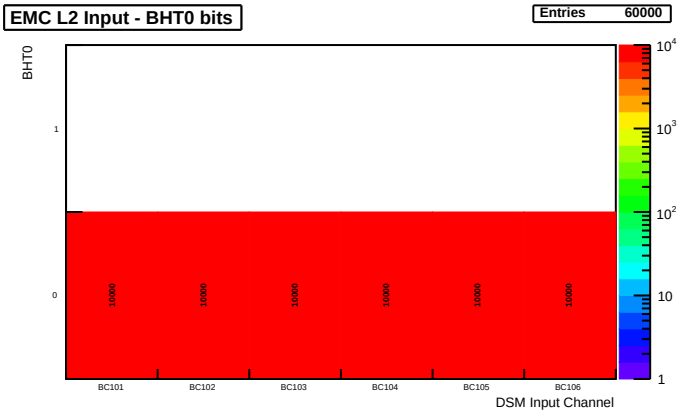

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

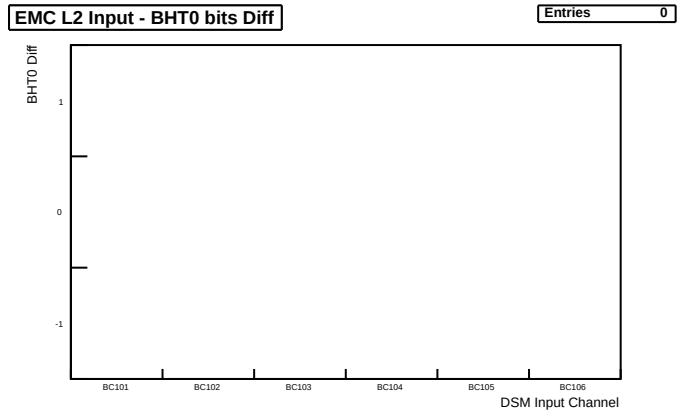

EMC L2 Input - JPZ/JPC bits
Entries 60000
JPZ/JPC bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

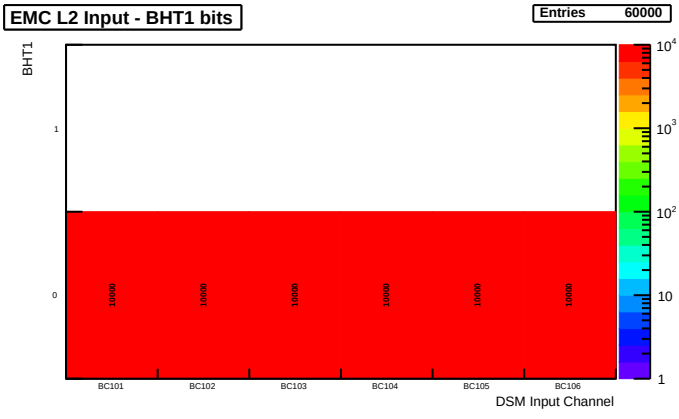

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

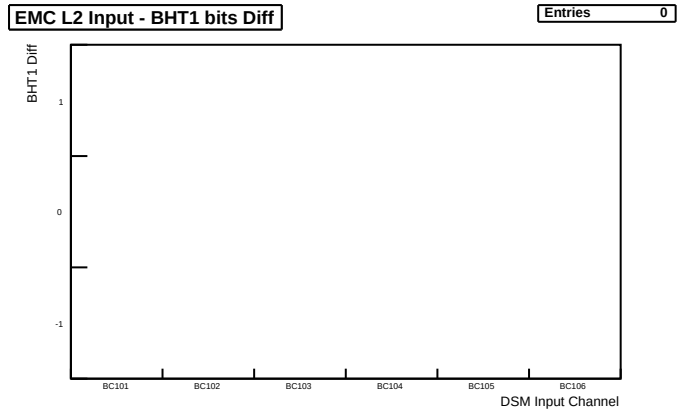

EMC L2 Input - Partial JP Sum
Entries 60000
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

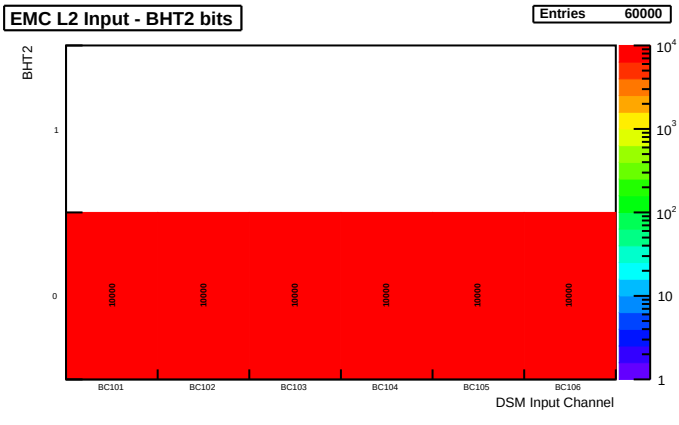

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

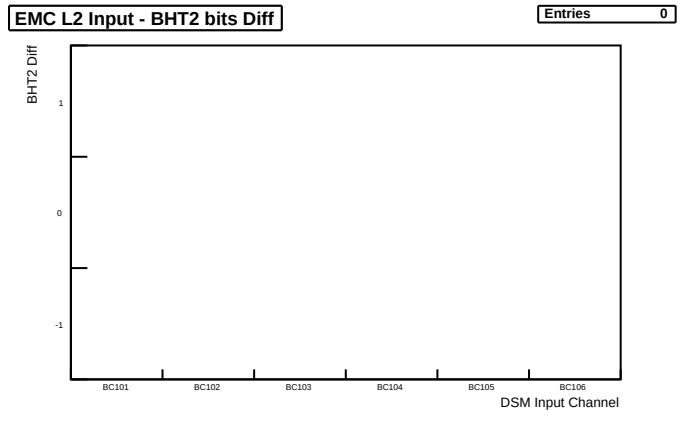

EMC L2 Input - BHT0 bits
Entries 60000
BHT0
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
10000

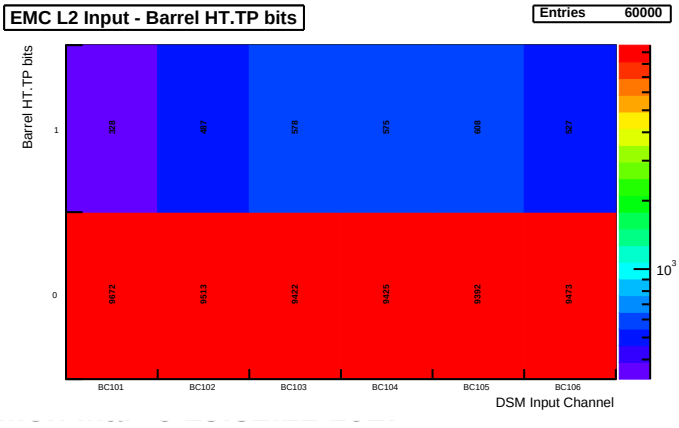

EMC L2 Input - BHT0 bits Diff
Entries 0
BHT0 Diff
DSM Input Channel

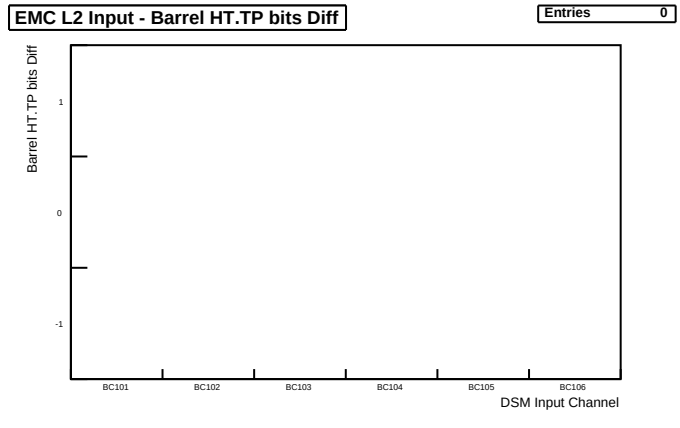

EMC L2 Input - BHT1 bits
Entries 60000
BHT1
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
10000

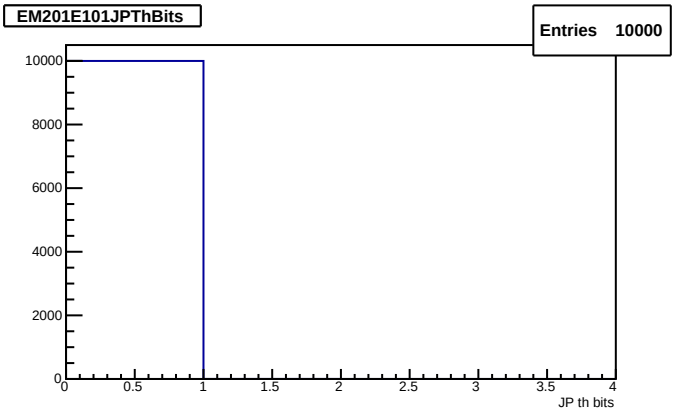

EMC L2 Input - BHT1 bits Diff
Entries 0
BHT1 Diff
DSM Input Channel

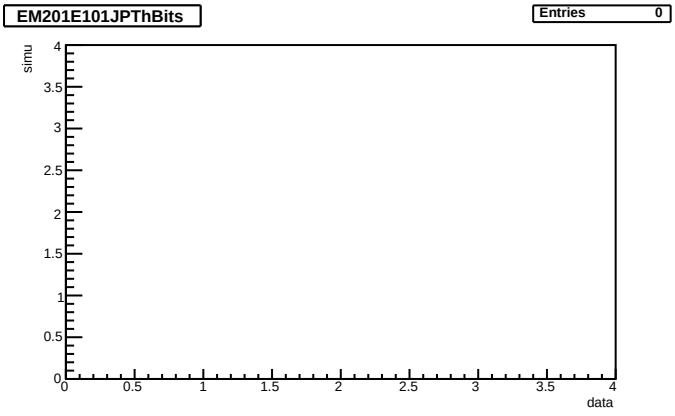

EMC L2 Input - BHT2 bits
Entries 60000
BHT2
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
10000

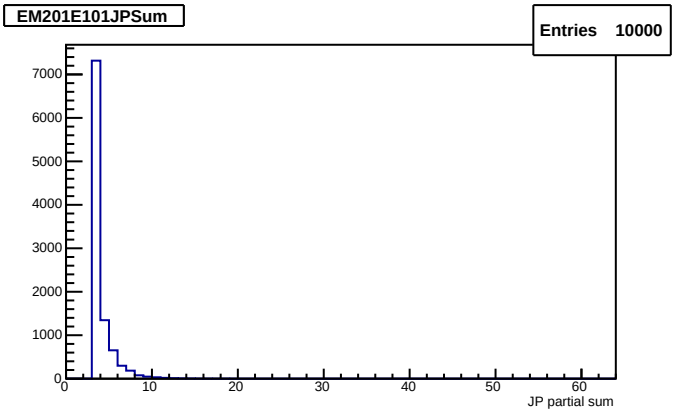

EMC L2 Input - BHT2 bits Diff
Entries 0
BHT2 Diff
DSM Input Channel

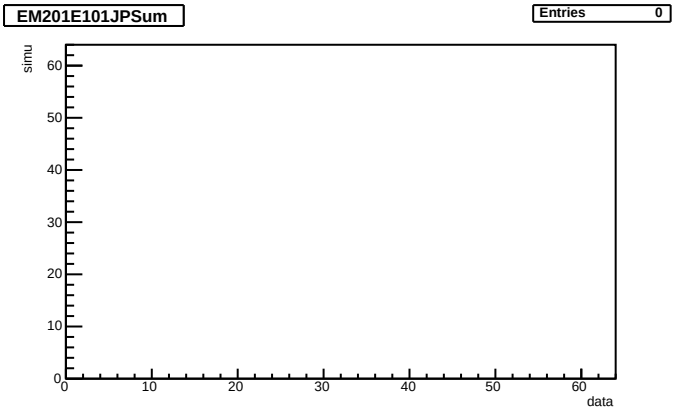

EMC L2 Input - Barrel HT.TP bits
Entries 60000
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
326
497
578
575
608
527
9672
9513
9422
9425
9392
9473

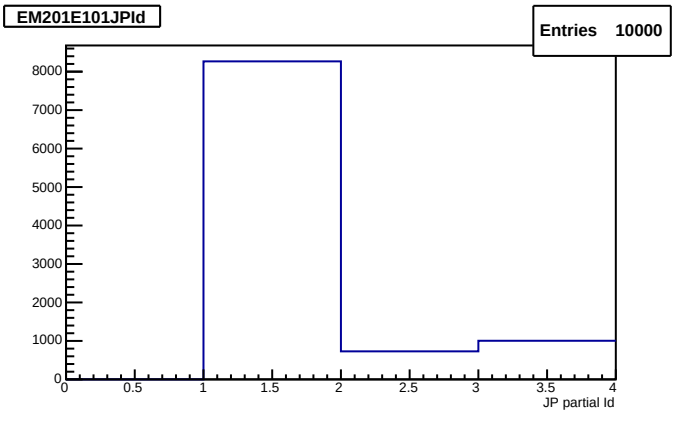

EMC L2 Input - Barrel HT.TP bits Diff
Entries 0
Barrel HT.TP bits Diff
DSM Input Channel

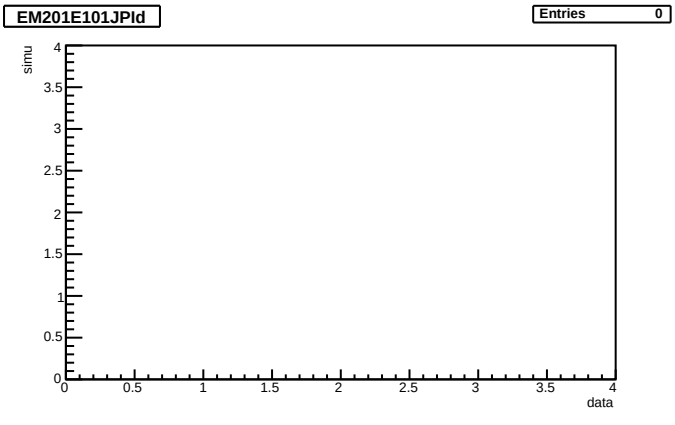

EM201E101JPThBits
Entries 10000
JP th bits

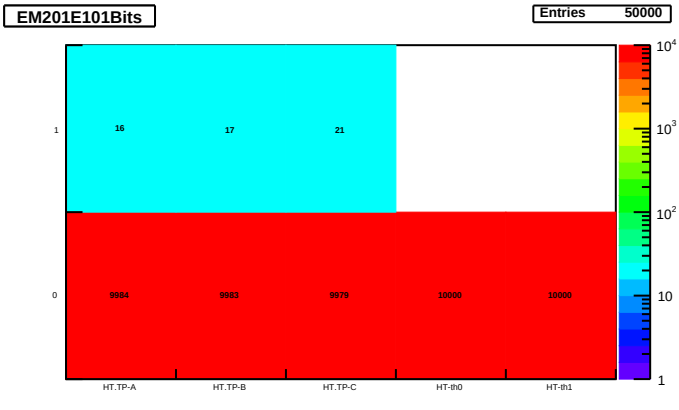

EM201E101JPThBits
Entries 0
simu
data

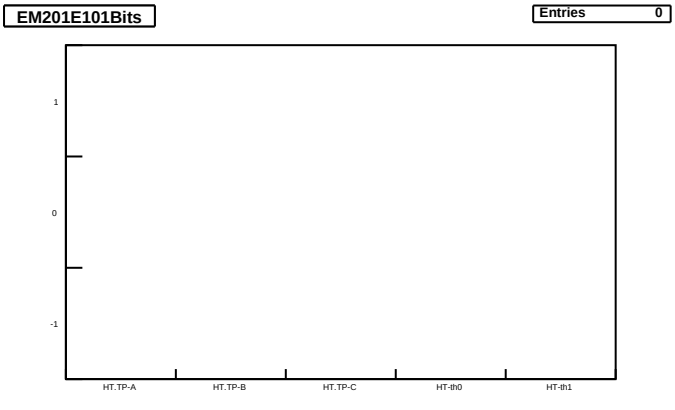

EM201E101JPSum
Entries 10000
JP partial sum

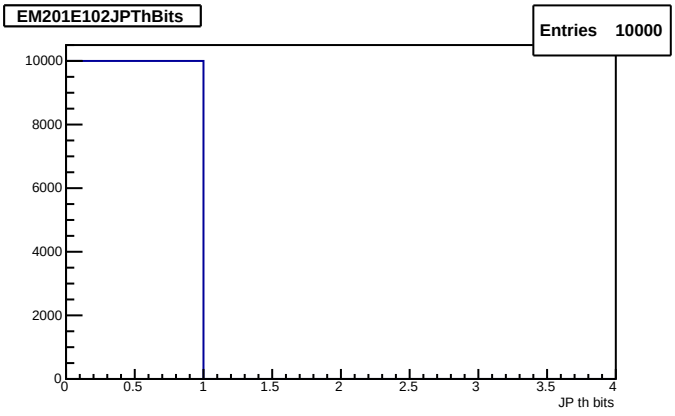

EM201E101JPSum
Entries 0
simu
data

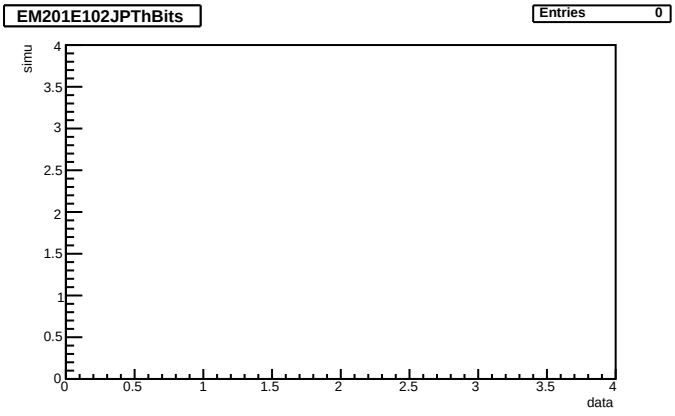

EM201E101JPId
Entries 10000
JP partial Id

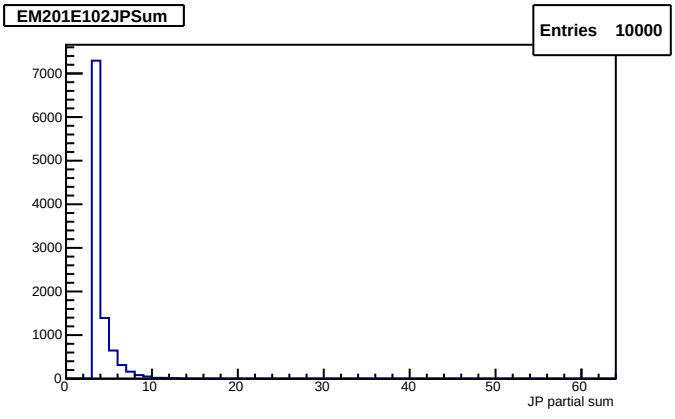

EM201E101JPId
Entries 0
simu
data

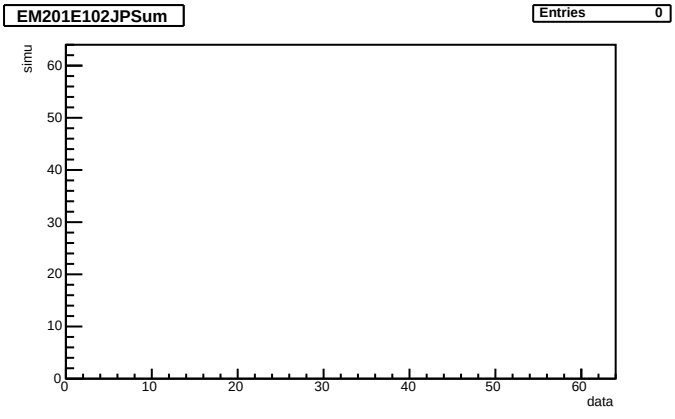

EM201E101Bits
Entries 50000
16
17
21
9984
9983
9979
10000
10000
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

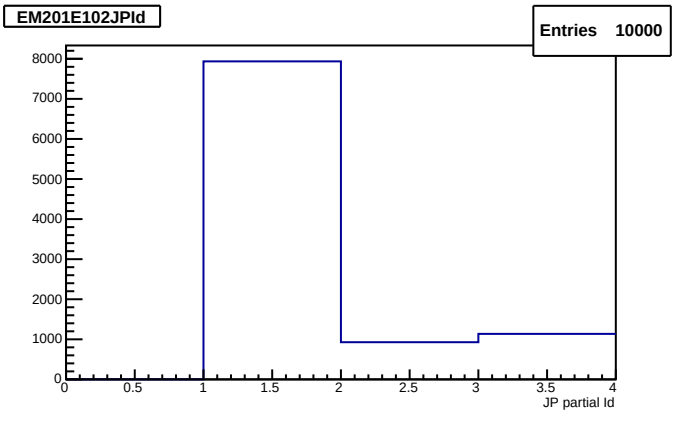

EM201E101Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

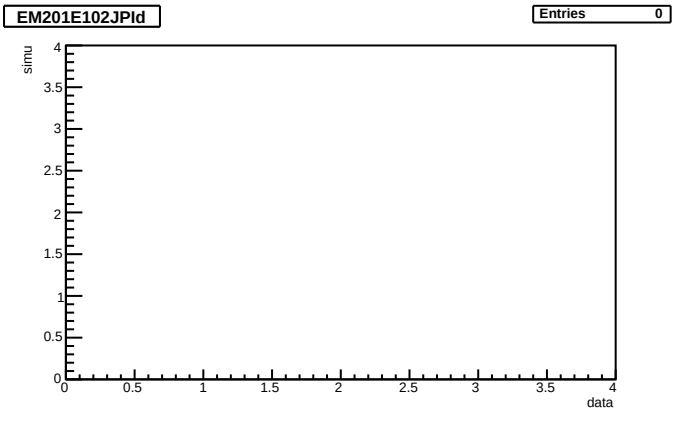

EM201E102JPThBits
Entries 10000
JP th bits

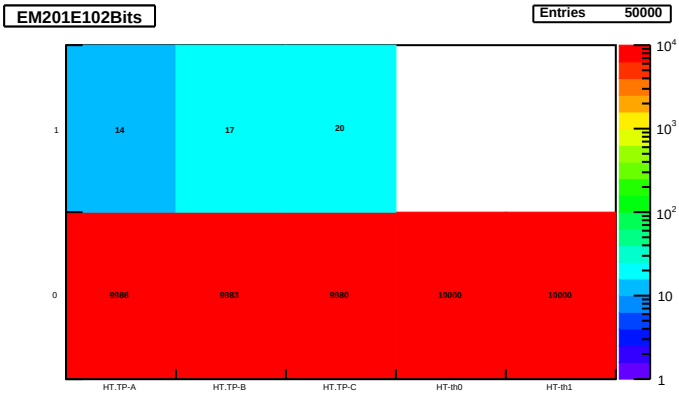

EM201E102JPThBits
Entries 0
simu
data

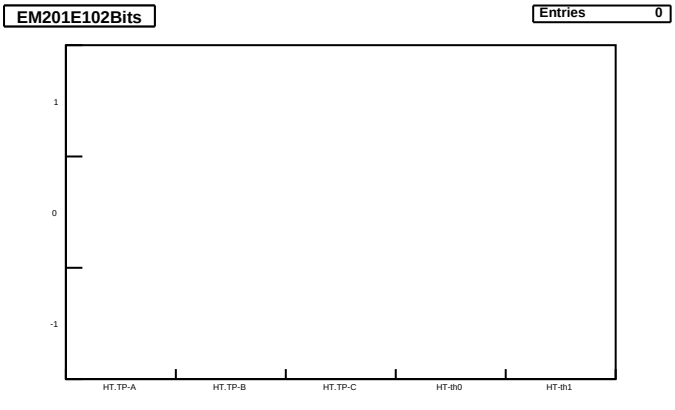

EM201E102JPSum
Entries 10000
JP partial sum

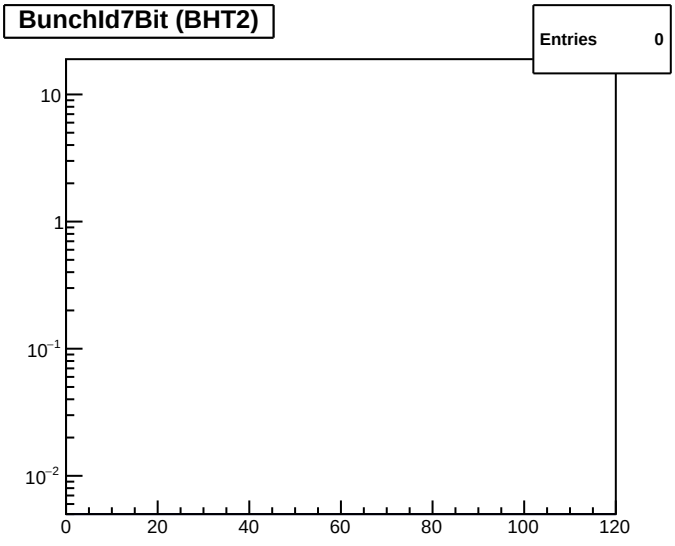

EM201E102JPSum
Entries 0
simu
data

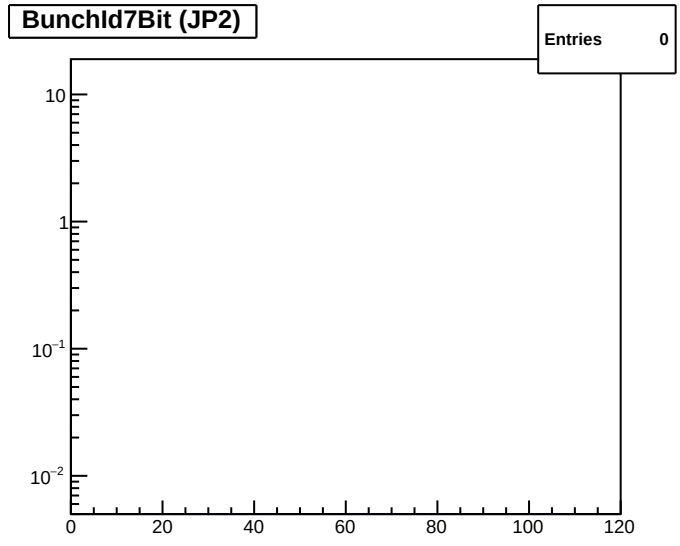

EM201E102JPId
Entries 10000
JP partial Id

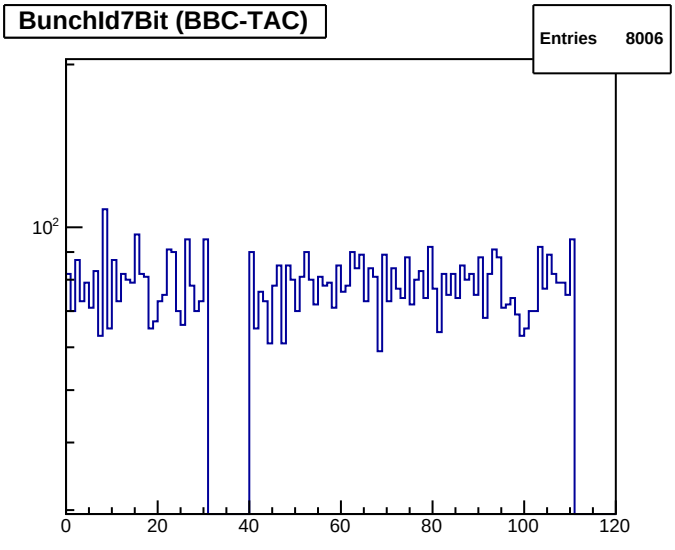

EM201E102JPId
Entries 0
simu
data

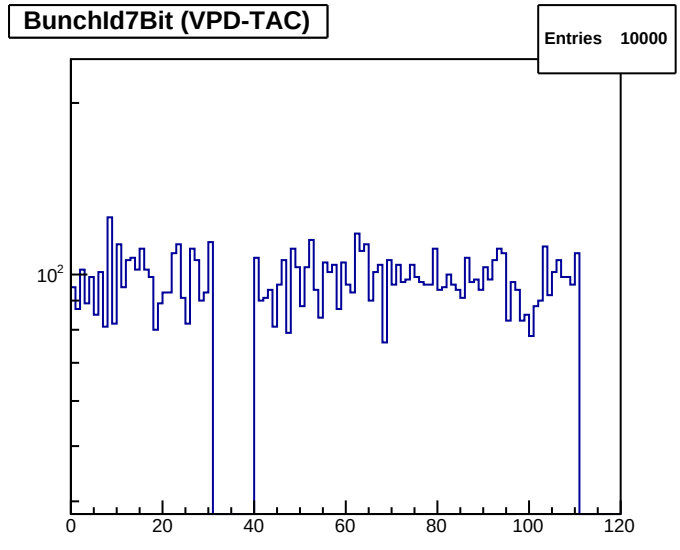

EM201E102Bits
Entries 50000
14
17
20
9986
9983
9980
10000
10000
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

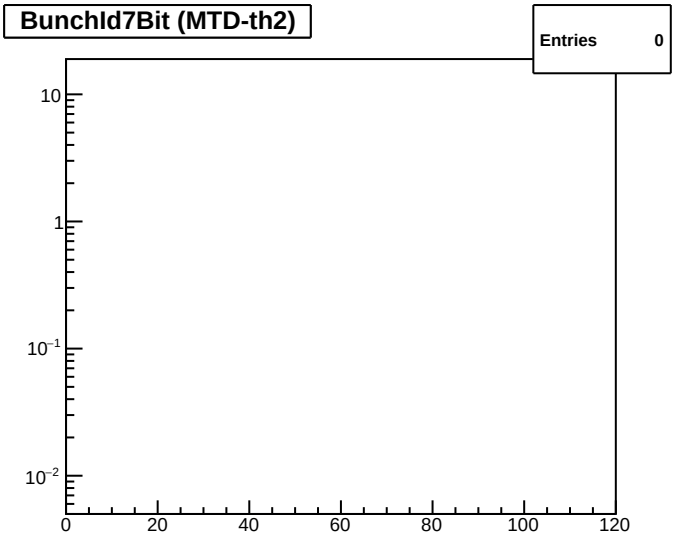

EM201E102Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

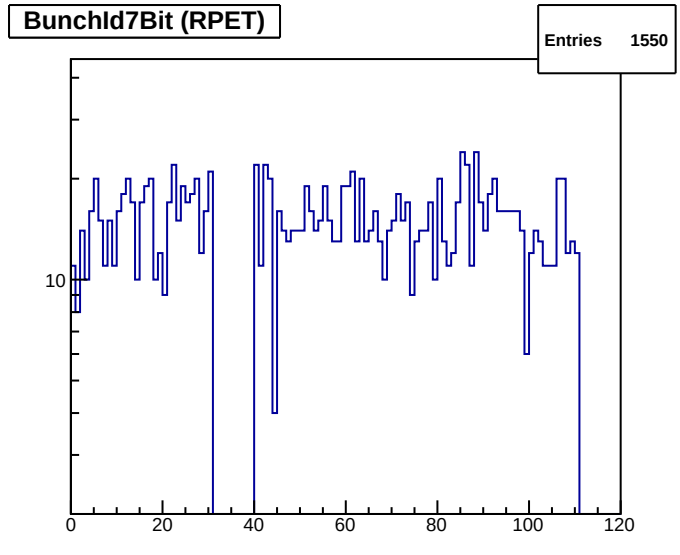

BunchId7Bit (BHT2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

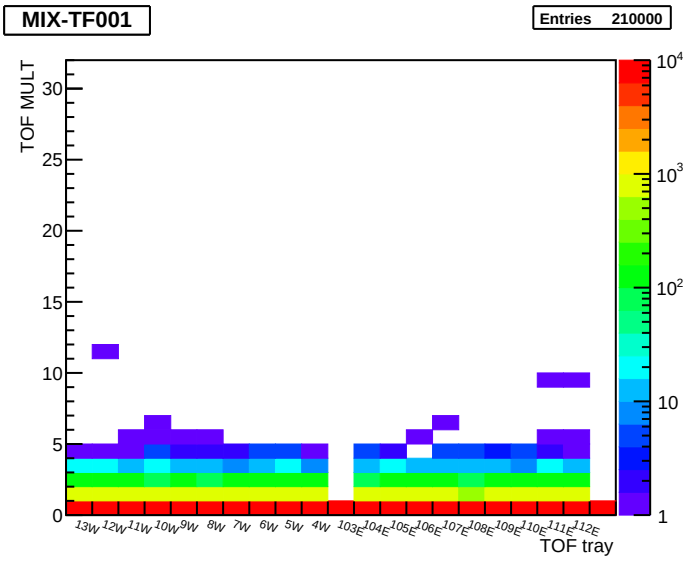

BunchId7Bit (JP2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

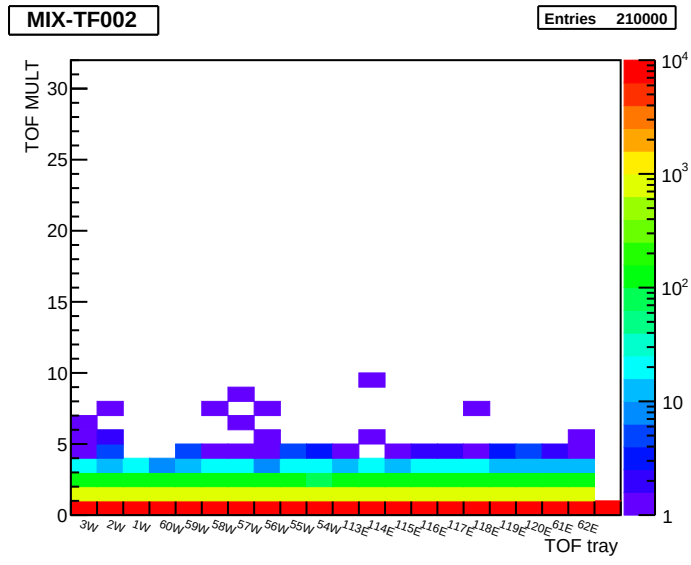

BunchId7Bit (BBC-TAC)
Entries 8006
10^{2}
0 20 40 60 80 100 120

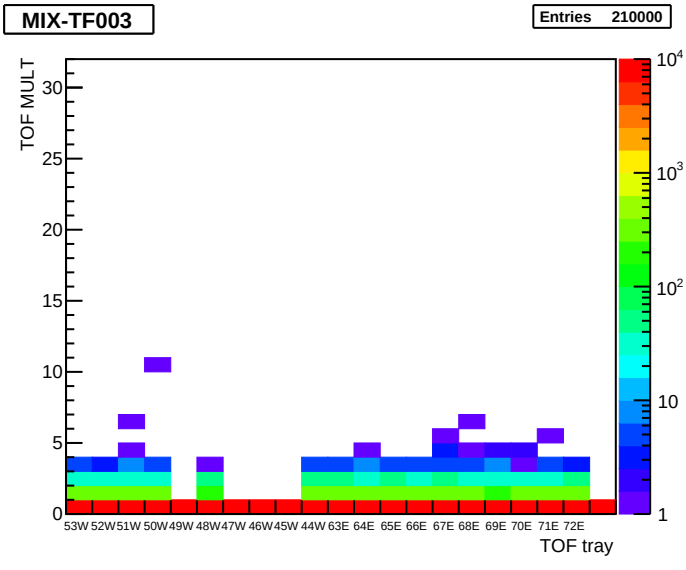

BunchId7Bit (VPD-TAC)
Entries 10000
10^{2}
0 20 40 60 80 100 120

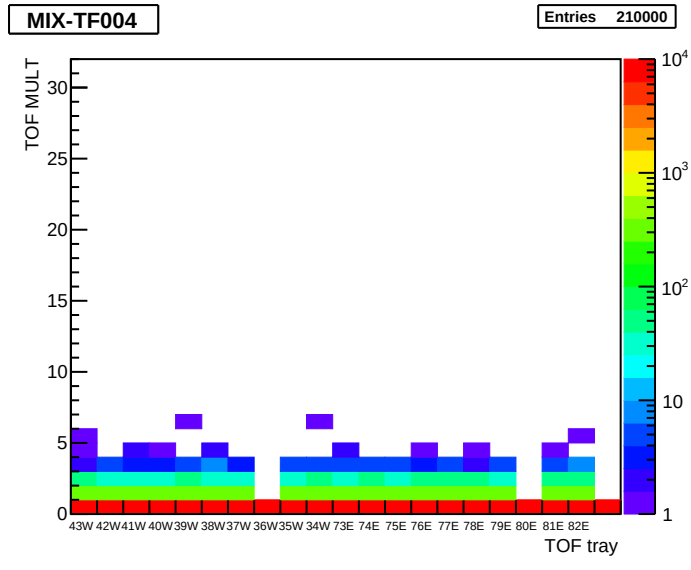

BunchId7Bit (MTD-th2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

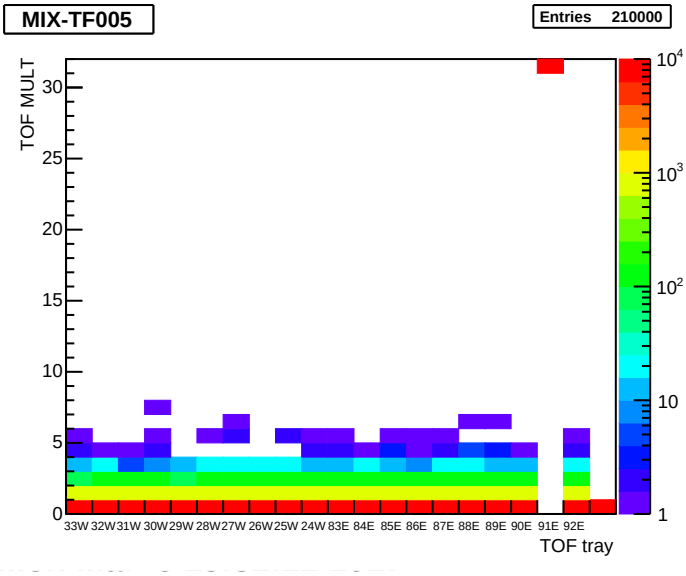

BunchId7Bit (RPET)
Entries 1550
10
0 20 40 60 80 100 120

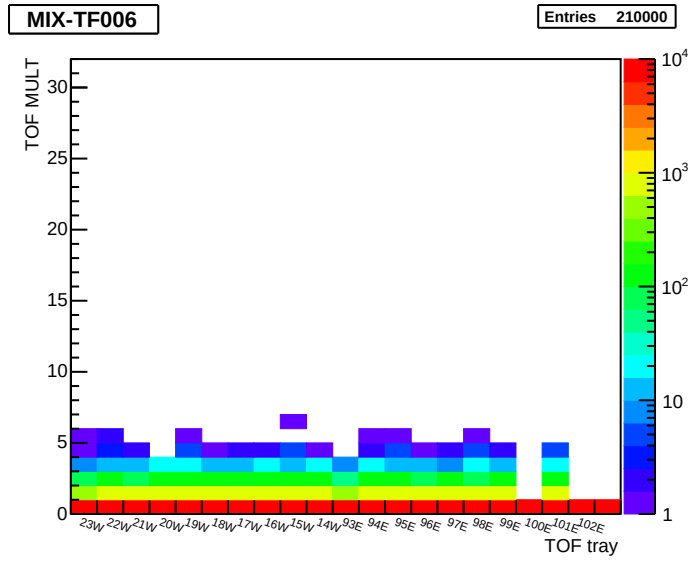

MIX-TF001
Entries 210000
TOF MULT
TOF tray

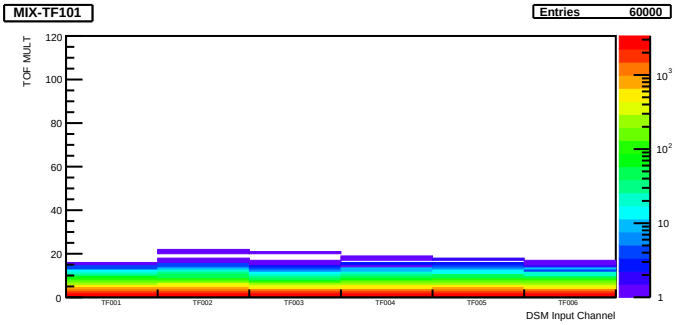

MIX-TF002
Entries 210000
TOF MULT
TOF tray

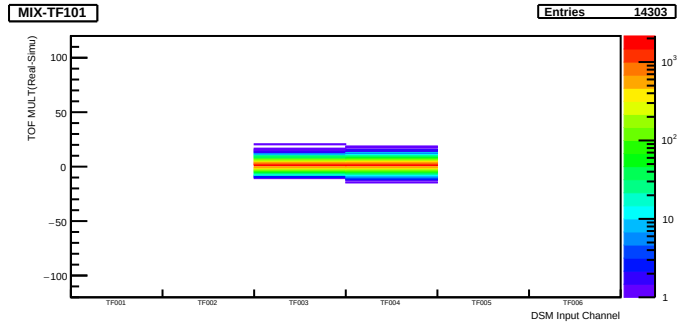

MIX-TF003
Entries 210000
TOF MULT
TOF tray

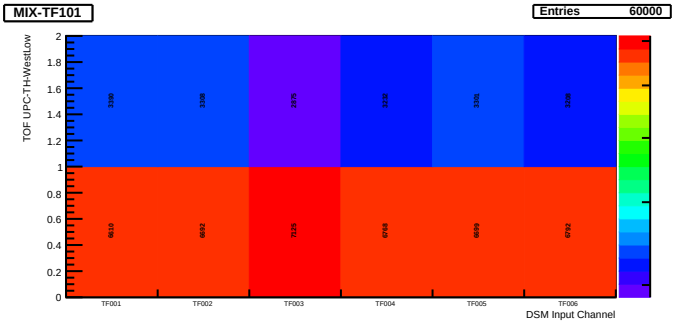

MIX-TF004
Entries 210000
TOF MULT
TOF tray

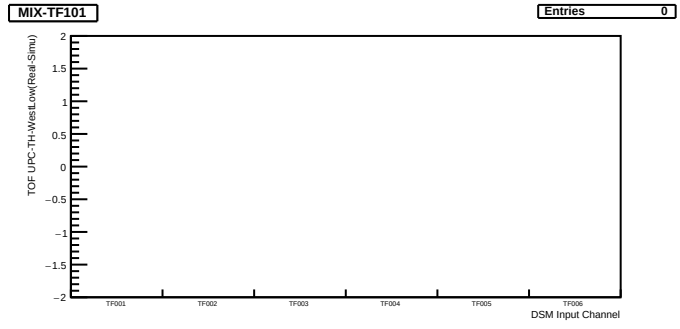

MIX-TF005
Entries 210000
TOF MULT
TOF tray

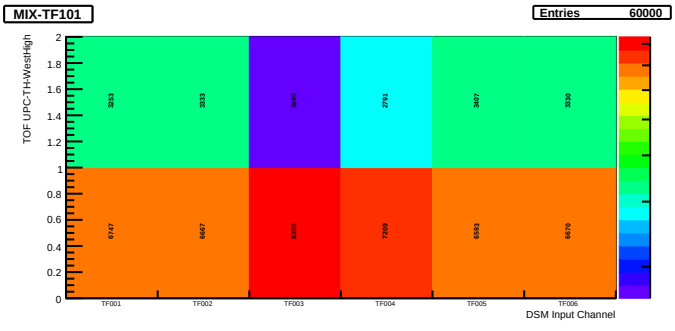

MIX-TF006
Entries 210000
TOF MULT
TOF tray

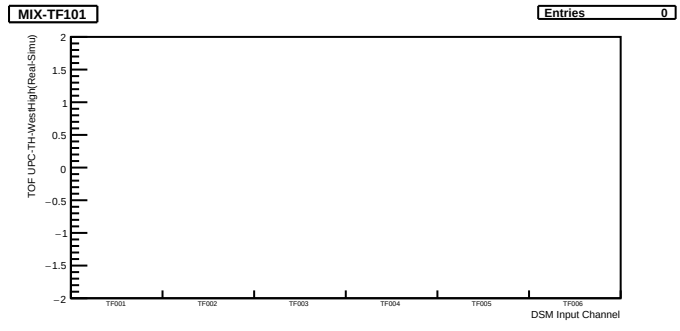

MIX-TF101
Entries 60000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

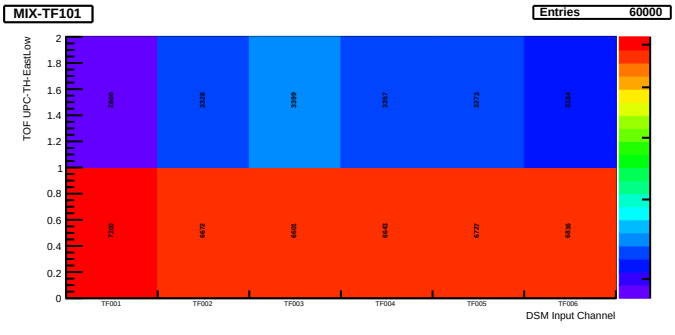

MIX-TF101
Entries 14303
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

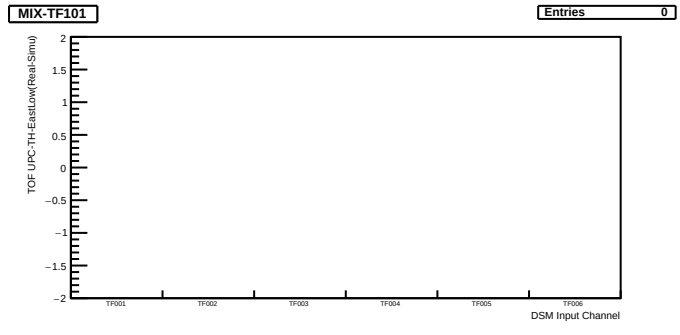

MIX-TF101
Entries 60000
TOF UPC-TH-WestLow
3330
3338
2975
3322
3331
3328
6810
6692
7125
6748
6699
6792
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

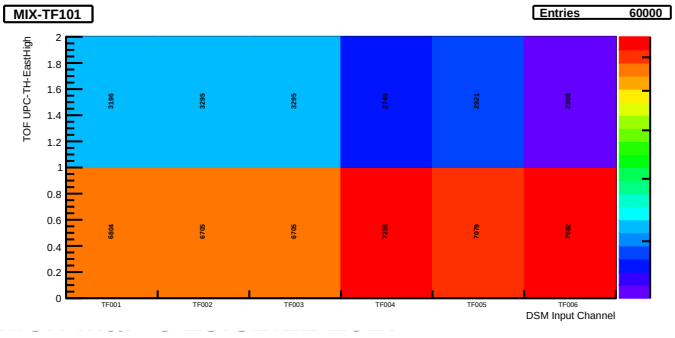

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

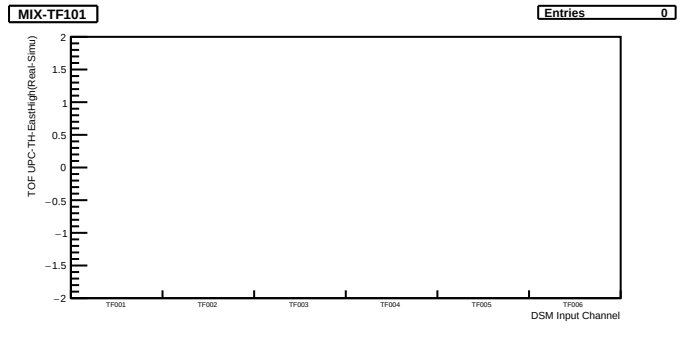

MIX-TF101
Entries 60000
TOF UPC-TH-WestHigh
3253
3333
3545
2791
3407
3330
6747
6667
6355
7299
6693
6670
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

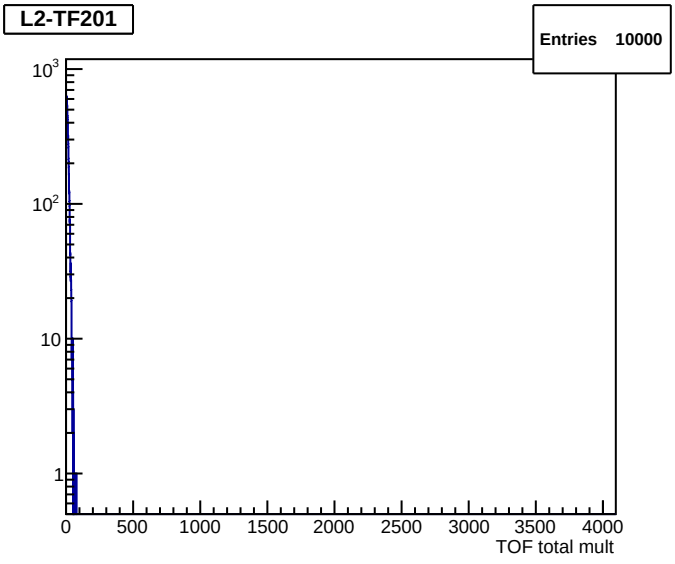

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

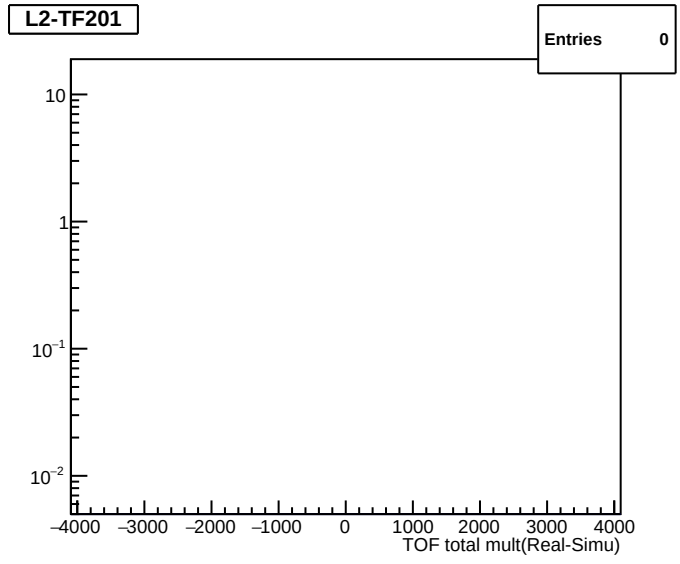

MIX-TF101
Entries 60000
TOF UPC-TH-EastLow
2300
3328
3399
3357
3273
3344
7230
6672
6681
6643
6727
6836
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

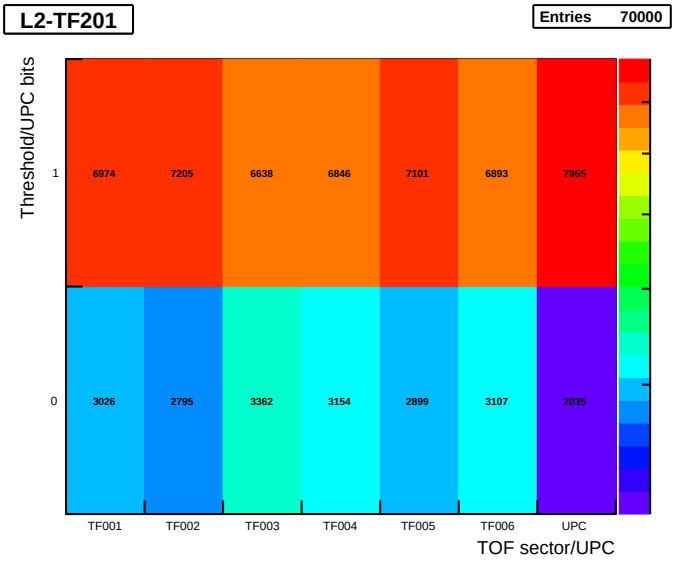

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

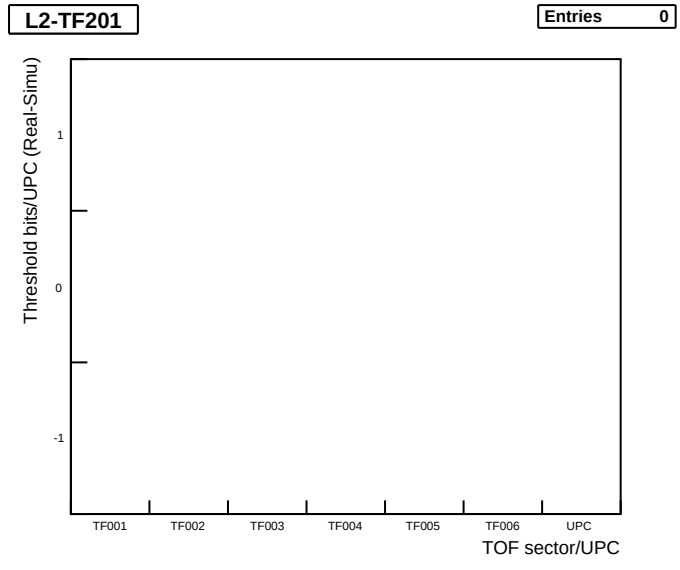

MIX-TF101
Entries 60000
TOF UPC-TH-EastHigh
3194
3295
3295
2745
2921
2508
6804
6705
6705
7235
7079
7492
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

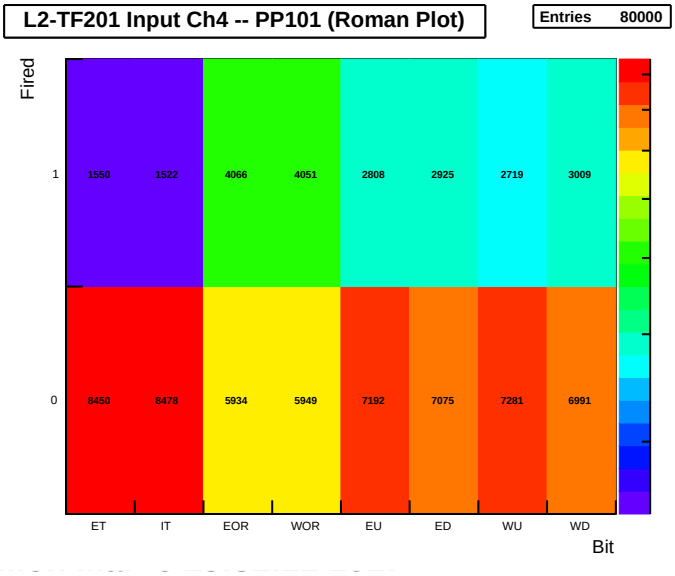

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

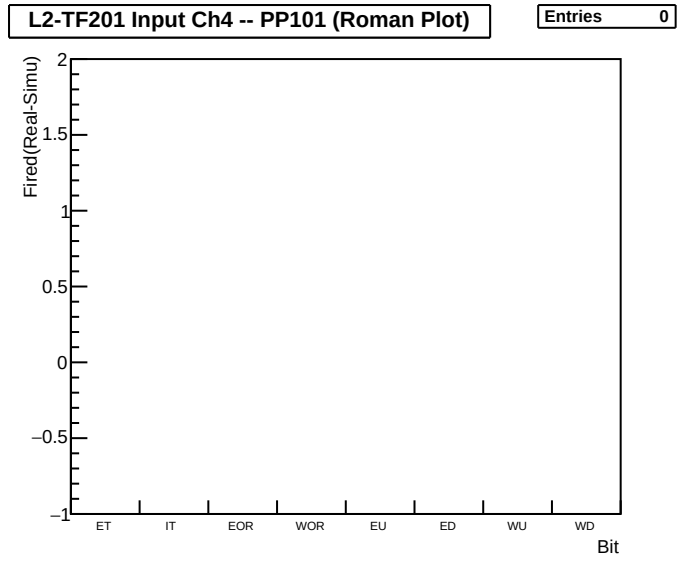

L2-TF201
Entries 10000
TOF total mult

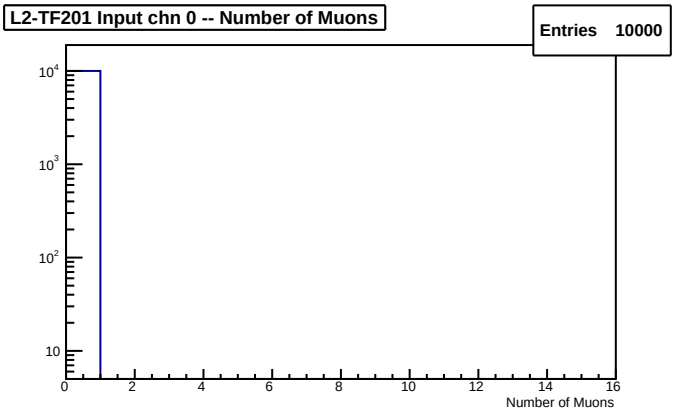

L2-TF201
Entries 0
TOF total mult(Real-Simu)

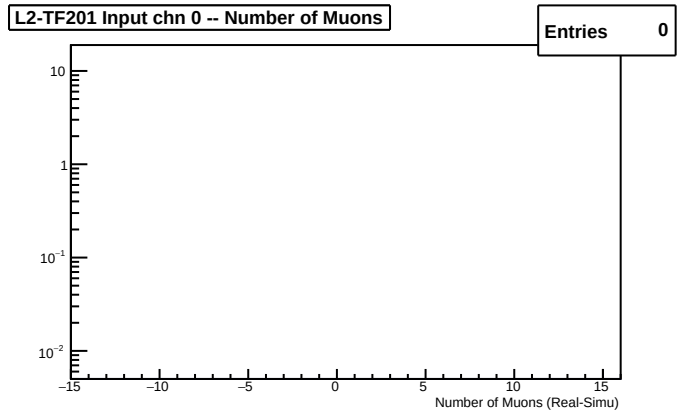

L2-TF201
Entries 70000
Threshold/UPC bits
6974 7205 6638 6846 7101 6893 7965
3026 2795 3362 3154 2899 3107 2035
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

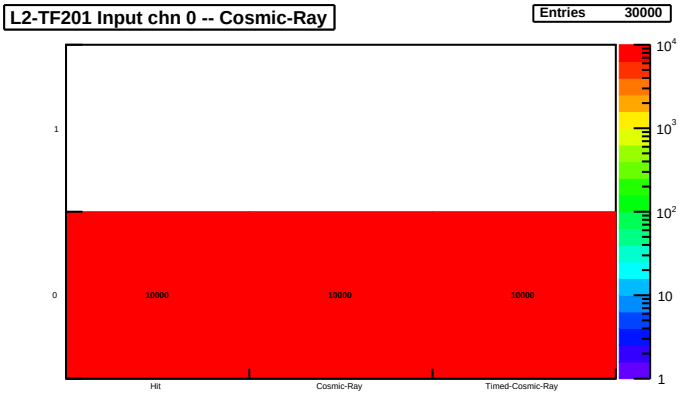

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

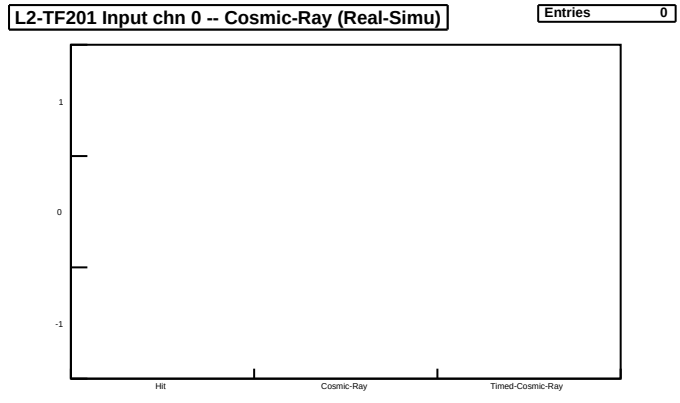

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 80000
Fired
1550 1522 4066 4051 2808 2925 2719 3009
8450 8478 5934 5949 7192 7075 7281 6991
ET IT EOR WOR EU ED WU WD
Bit

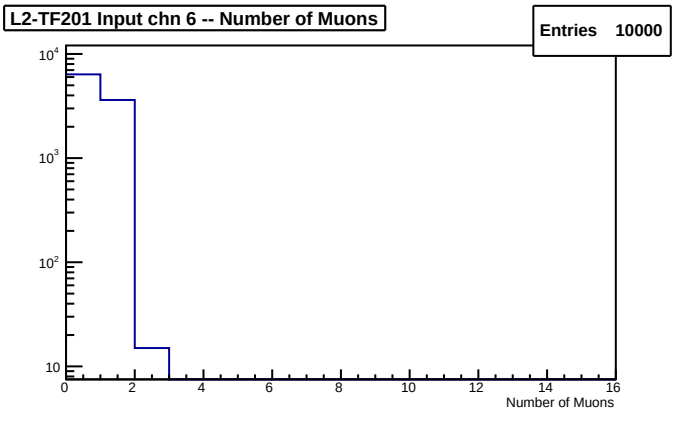

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 0
Fired(Real-Simu)
ET IT EOR WOR EU ED WU WD
Bit

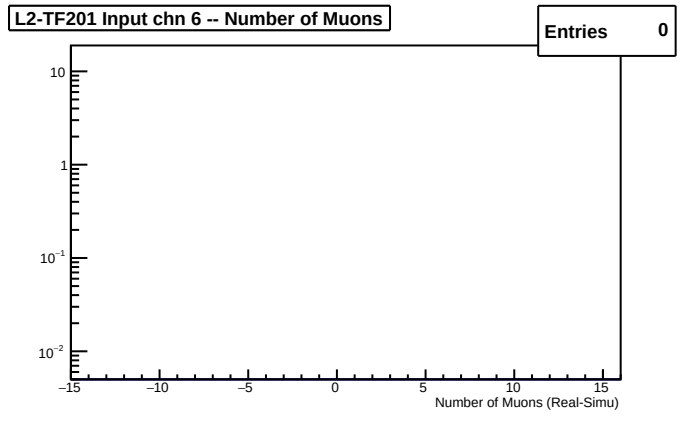

L2-TF201 Input chn 0 -- Number of Muons
Entries 10000
Number of Muons

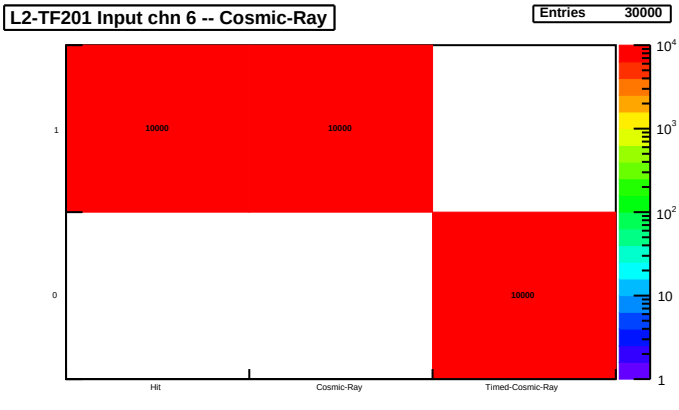

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

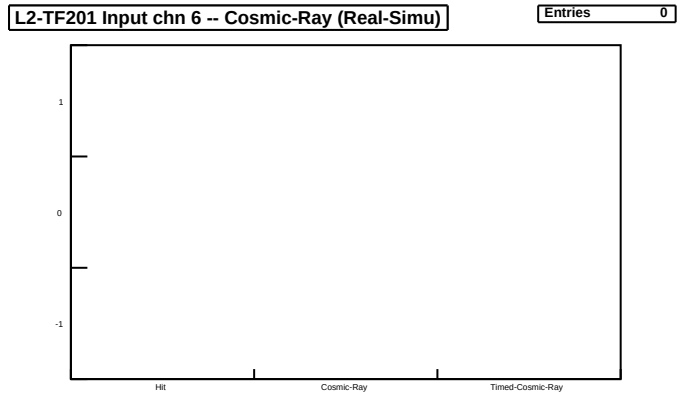

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 30000
10000
10000
10000
Hit
Cosmic-Ray
Timed-Cosmic-Ray

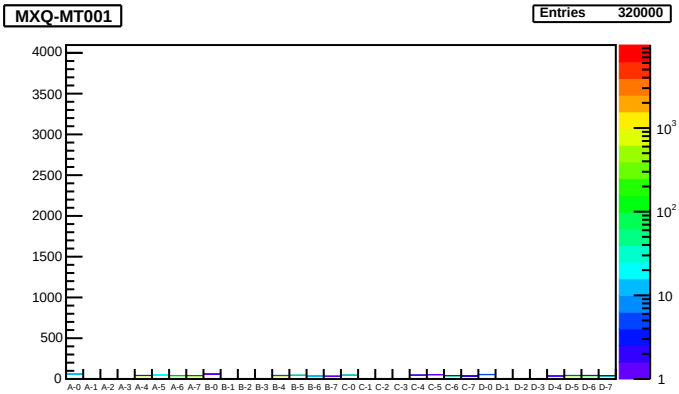

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

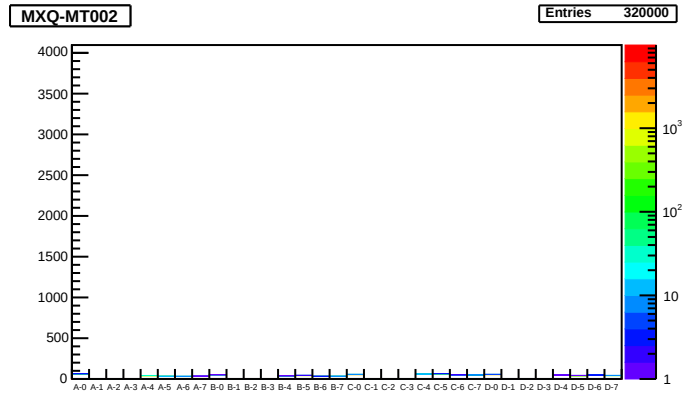

L2-TF201 Input chn 6 -- Number of Muons
Entries 10000
Number of Muons

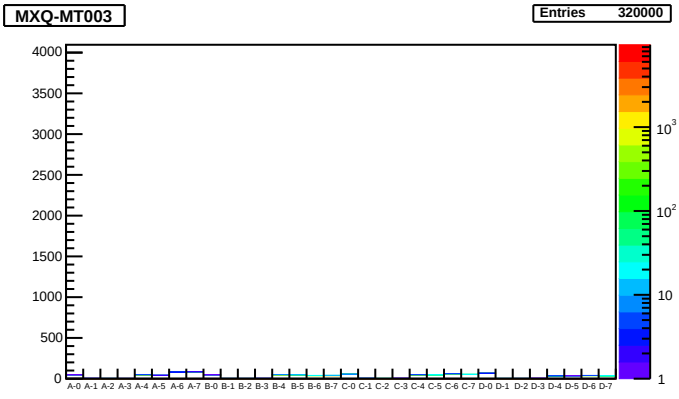

L2-TF201 Input chn 6 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

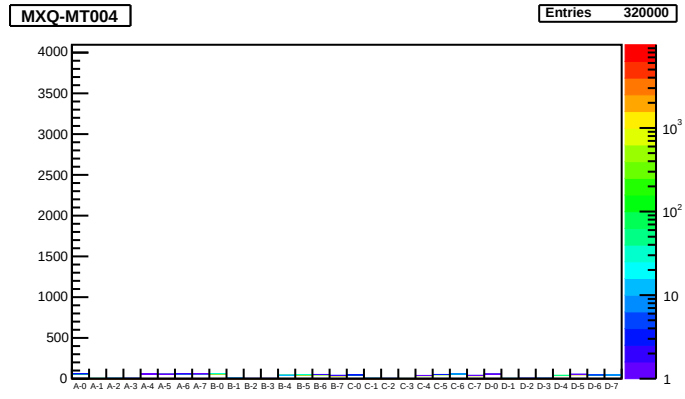

L2-TF201 Input chn 6 -- Cosmic-Ray
Entries 30000
10000
10000
10000
Hit
Cosmic-Ray
Timed-Cosmic-Ray

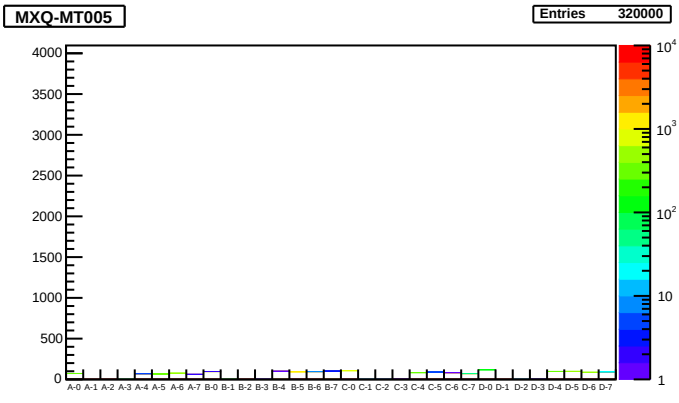

L2-TF201 Input chn 6 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

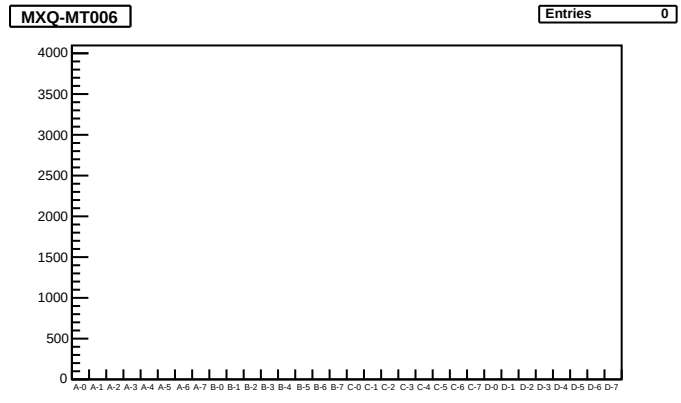

MXQ-MT001
Entries 320000

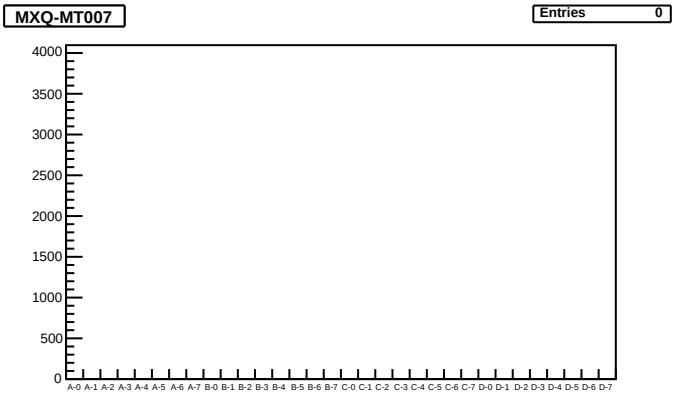

MXQ-MT002
Entries 320000

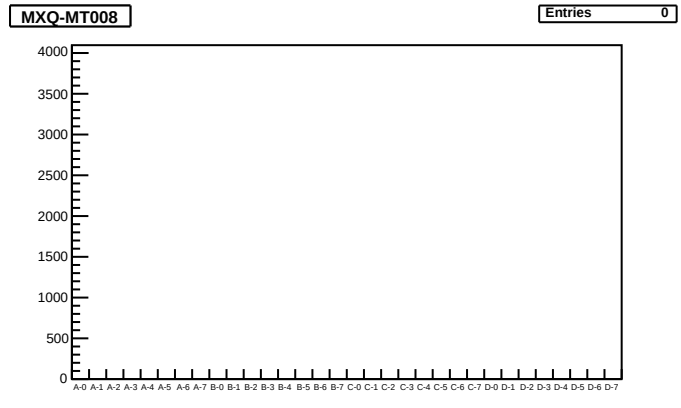

MXQ-MT003
Entries 320000

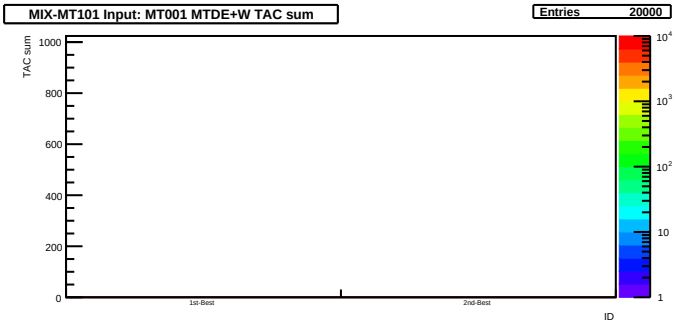

MXQ-MT004
Entries 320000

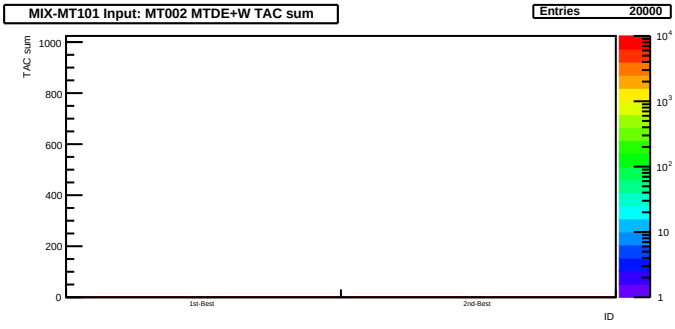

MXQ-MT005
Entries 320000

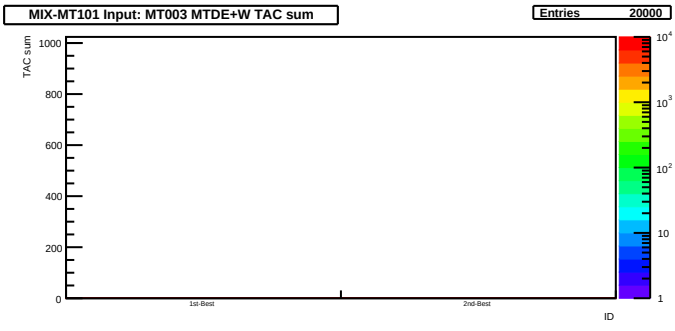

MXQ-MT006
Entries 0

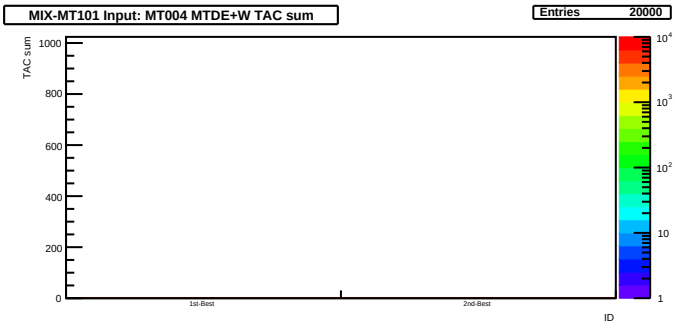

MXQ-MT007
Entries 0

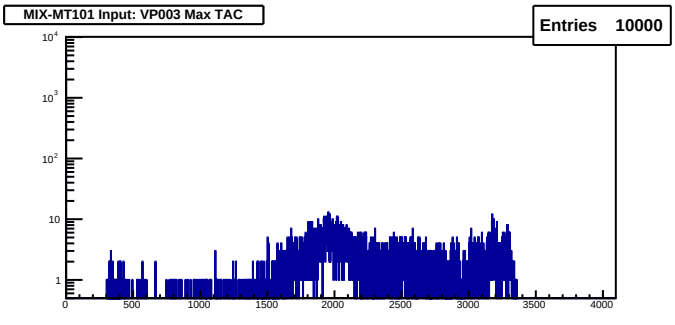

MXQ-MT008
Entries 0

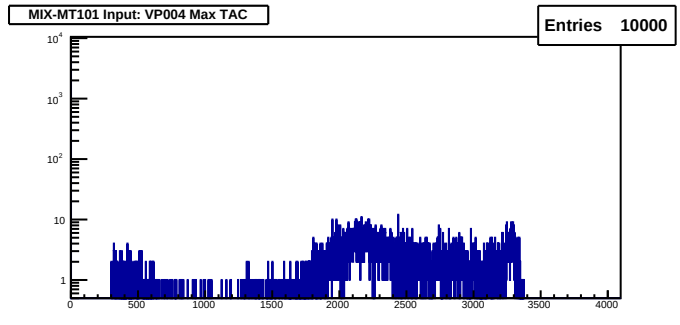

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

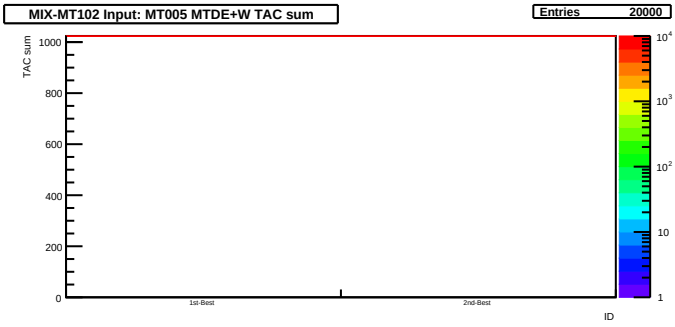

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

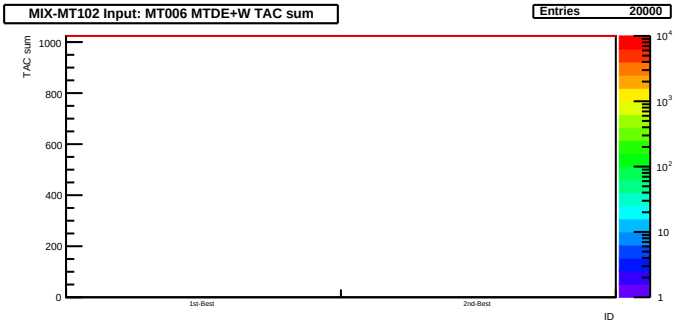

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

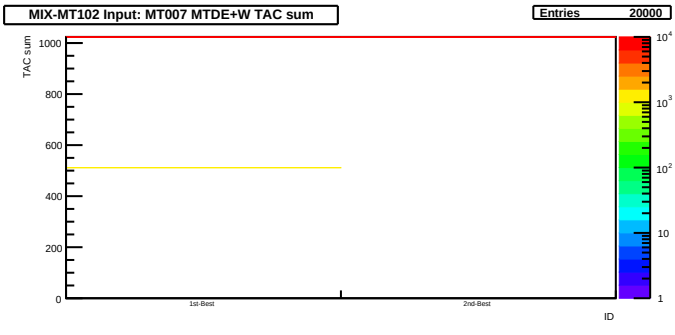

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

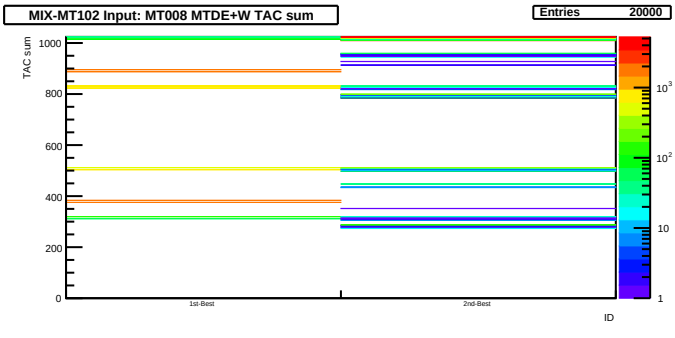

MIX-MT101 Input: VP003 Max TAC
Entries 10000

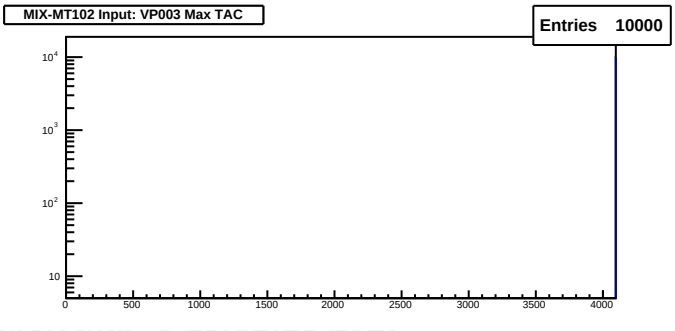

MIX-MT101 Input: VP004 Max TAC
Entries 10000

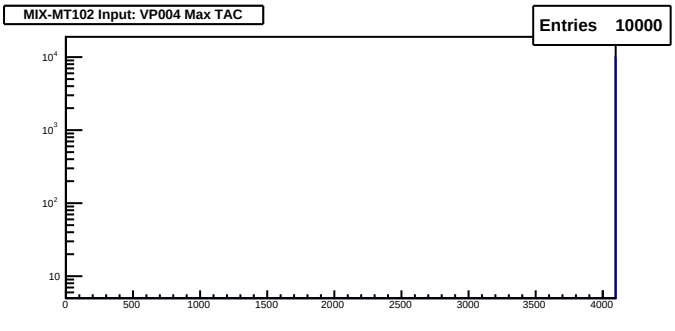

MIX-MT102 Input: MT005 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

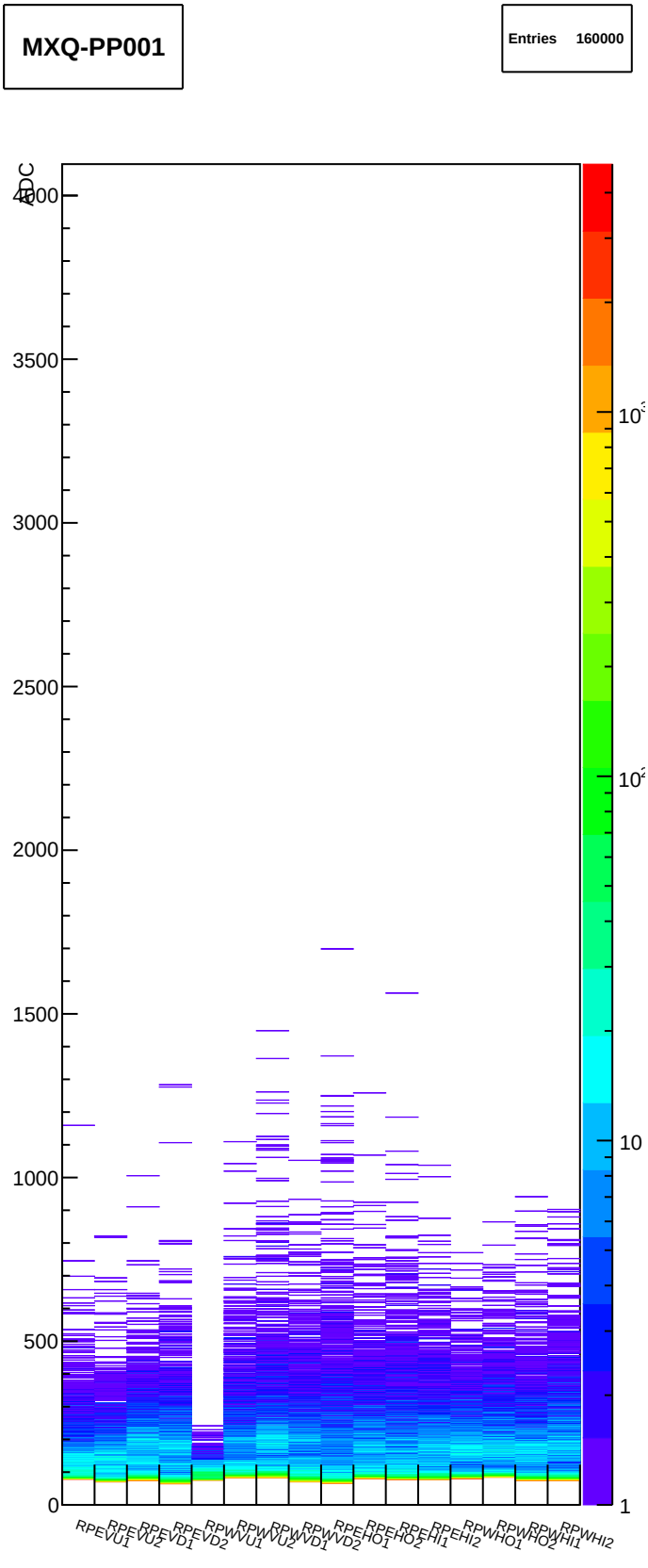

MIX-MT102 Input: MT006 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

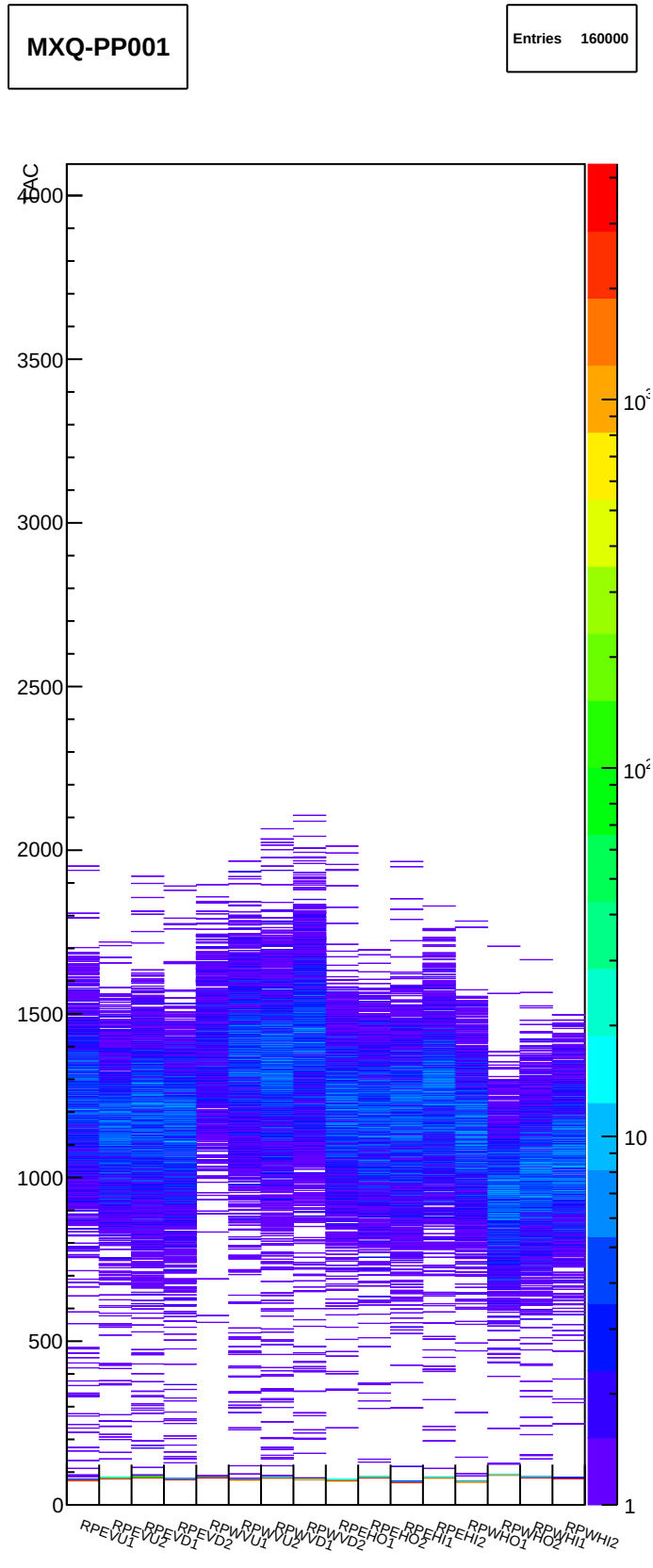

MIX-MT102 Input: MT007 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

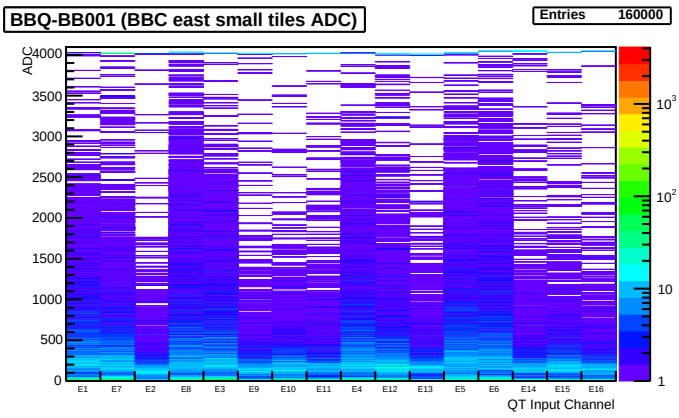

MIX-MT102 Input: MT008 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

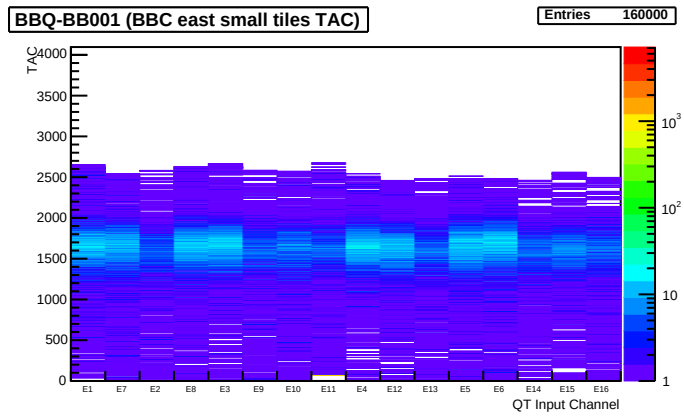

MIX-MT102 Input: VP003 Max TAC
Entries 10000

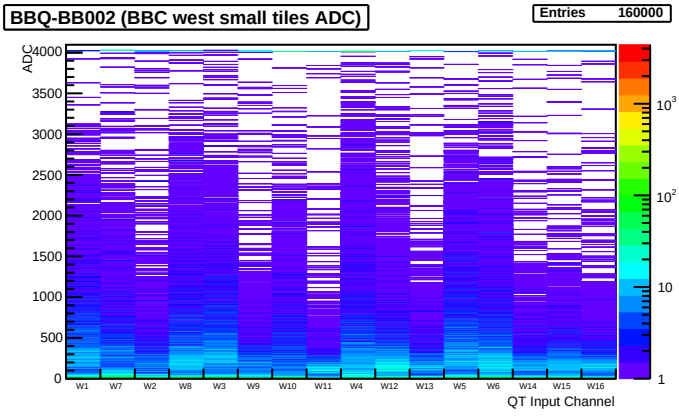

MIX-MT102 Input: VP004 Max TAC
Entries 10000

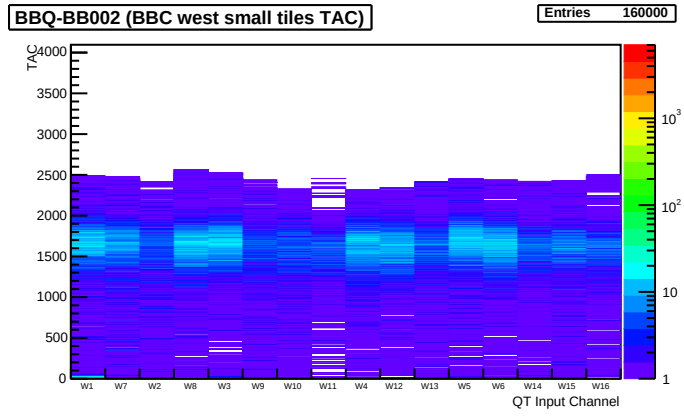

MXQ-PP001
Entries 160000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

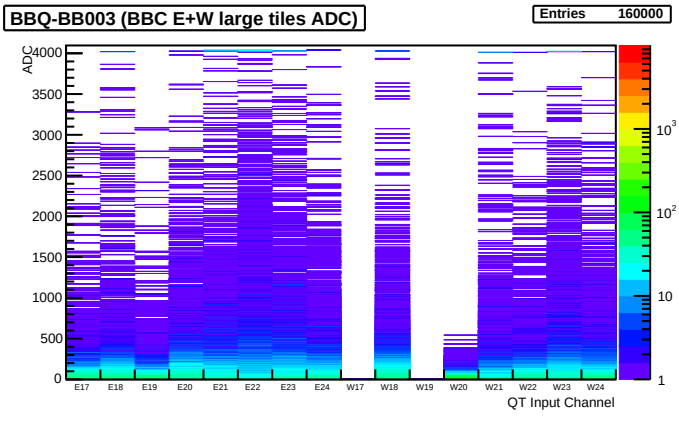

MXQ-PP001
Entries 160000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

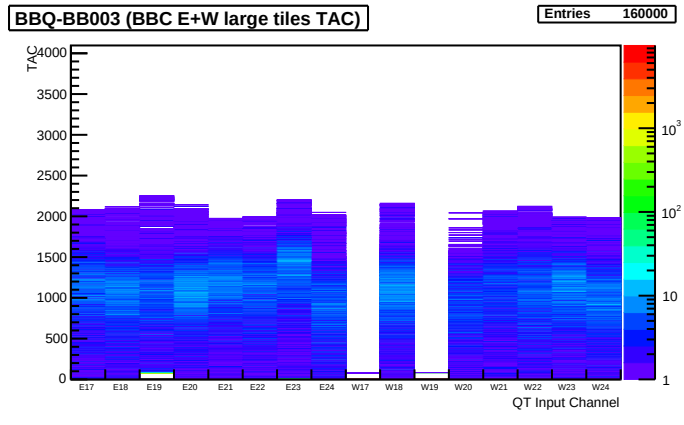

BBQ-BB001 (BBC east small tiles ADC)
Entries 160000
ADC
QT Input Channel

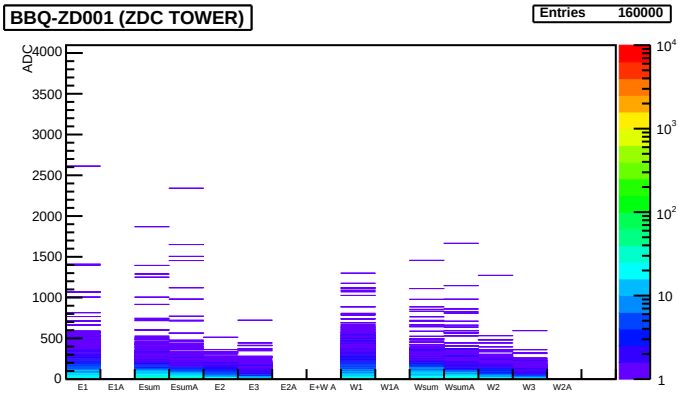

BBQ-BB001 (BBC east small tiles TAC)
Entries 160000
TAC
QT Input Channel

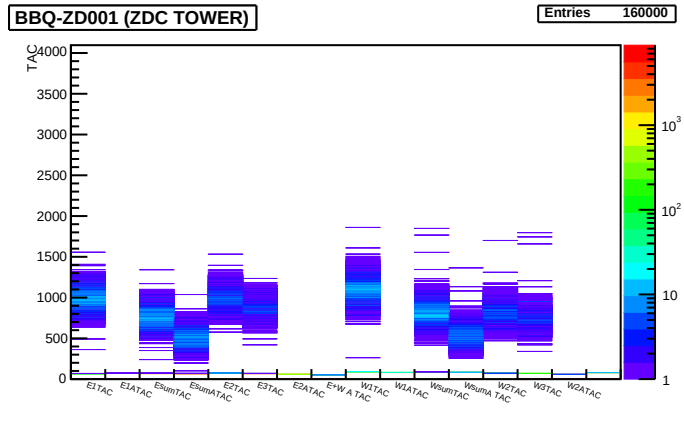

BBQ-BB002 (BBC west small tiles ADC)
Entries 160000
ADC
QT Input Channel

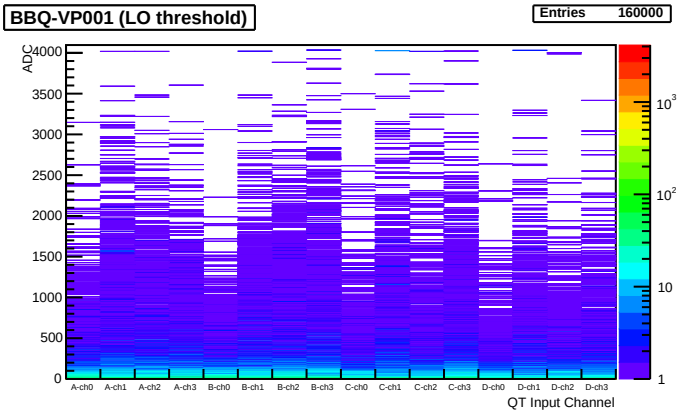

BBQ-BB002 (BBC west small tiles TAC)
Entries 160000
TAC
QT Input Channel

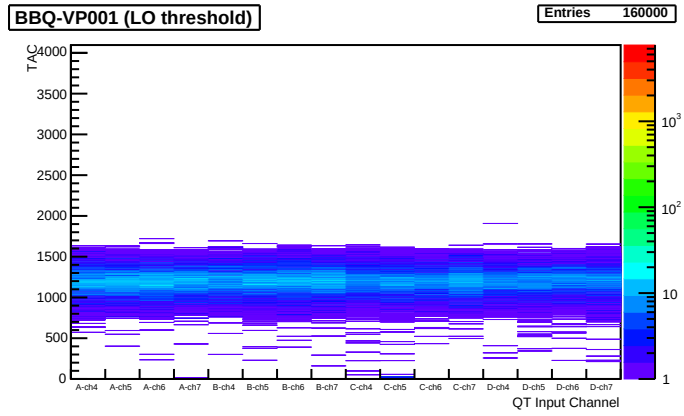

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 160000
ADC
QT Input Channel

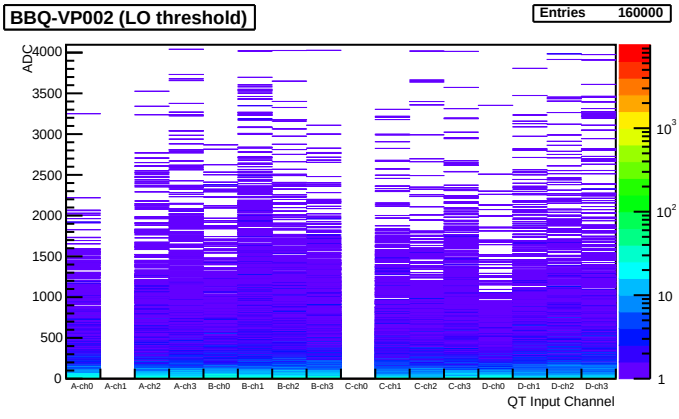

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 160000
TAC
QT Input Channel

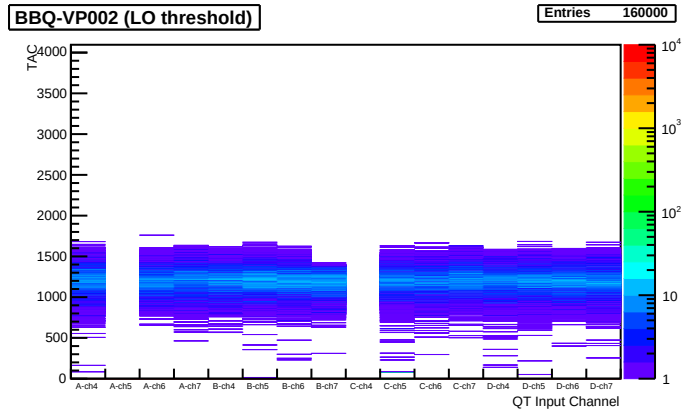

BBQ-ZD001 (ZDC TOWER)
Entries 160000
ADC

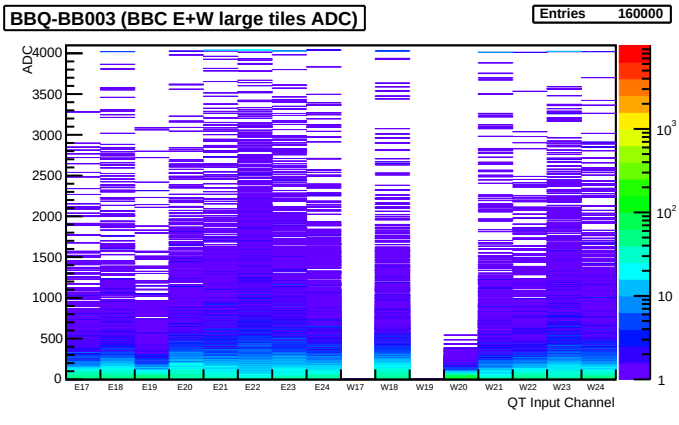

BBQ-ZD001 (ZDC TOWER)
Entries 160000
TAC

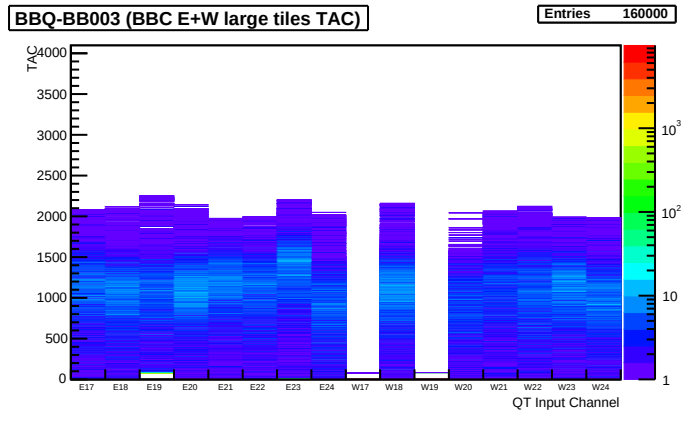

BBQ-VP001 (LO threshold)
Entries 160000
ADC
QT Input Channel

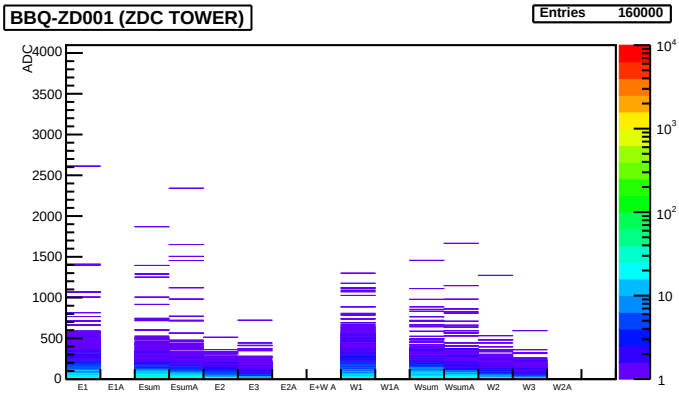

BBQ-VP001 (LO threshold)
Entries 160000
TAC
QT Input Channel

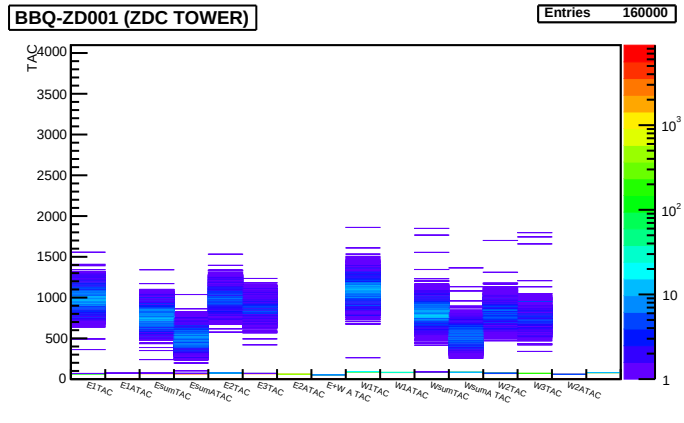

BBQ-VP002 (LO threshold)
Entries 160000
ADC
QT Input Channel

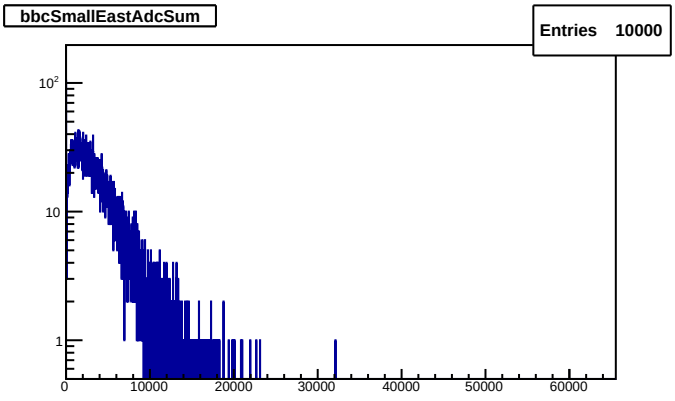

BBQ-VP002 (LO threshold)
Entries 160000
TAC
QT Input Channel

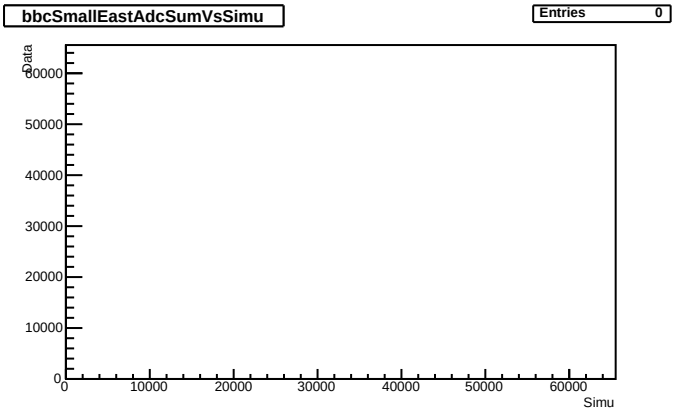

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 160000
ADC
QT Input Channel

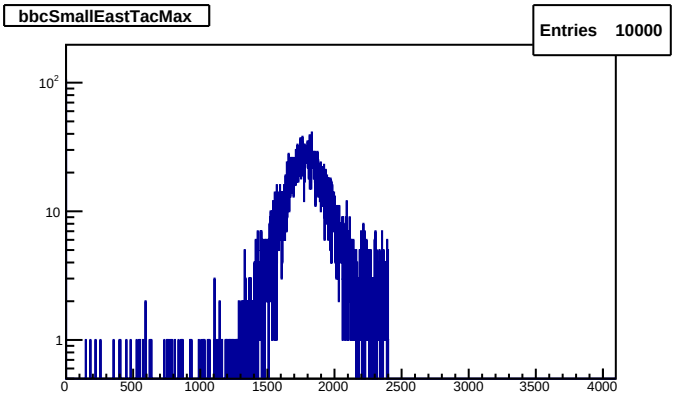

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 160000
TAC
QT Input Channel

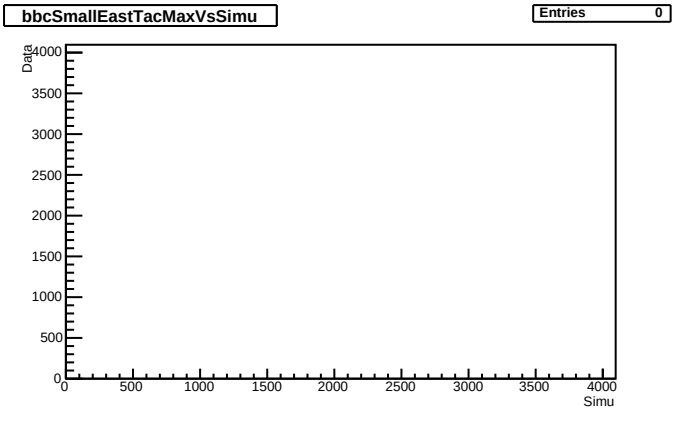

BBQ-ZD001 (ZDC TOWER)
Entries 160000
ADC

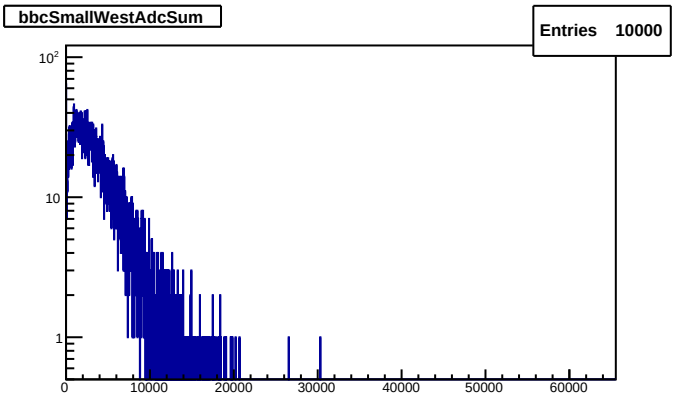

BBQ-ZD001 (ZDC TOWER)
Entries 160000
TAC

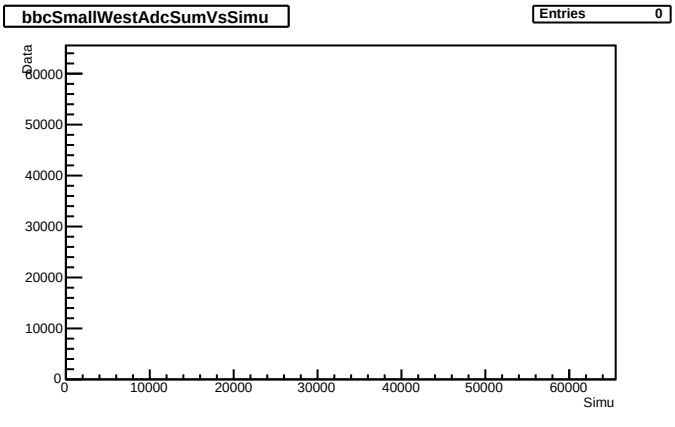

bbcSmallEastAdcSum
Entries 10000

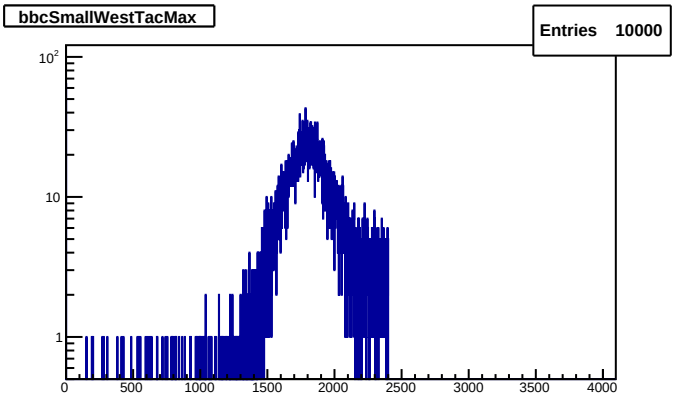

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

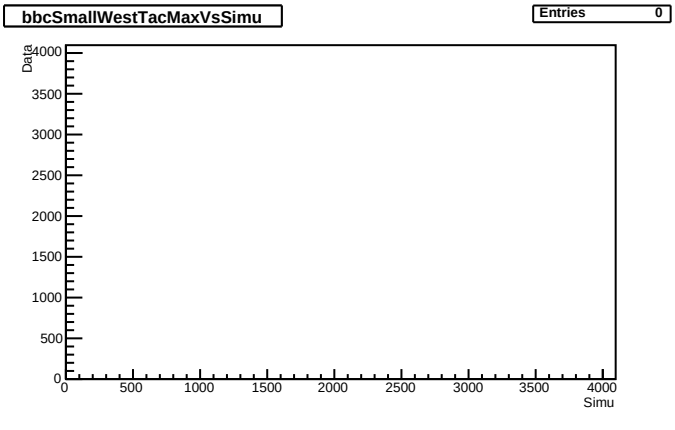

bbcSmallEastTacMax
Entries 10000

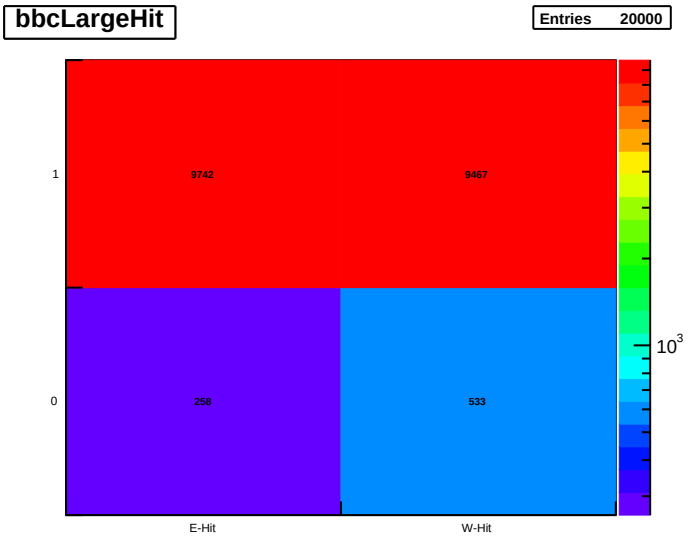

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

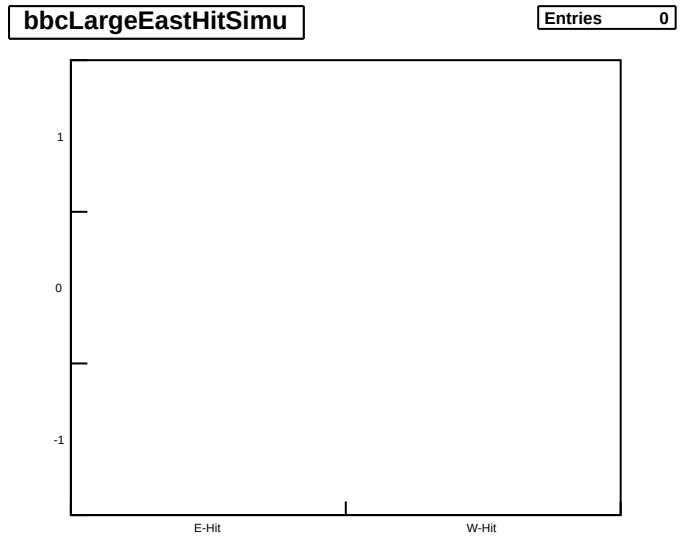

bbcSmallWestAdcSum
Entries 10000

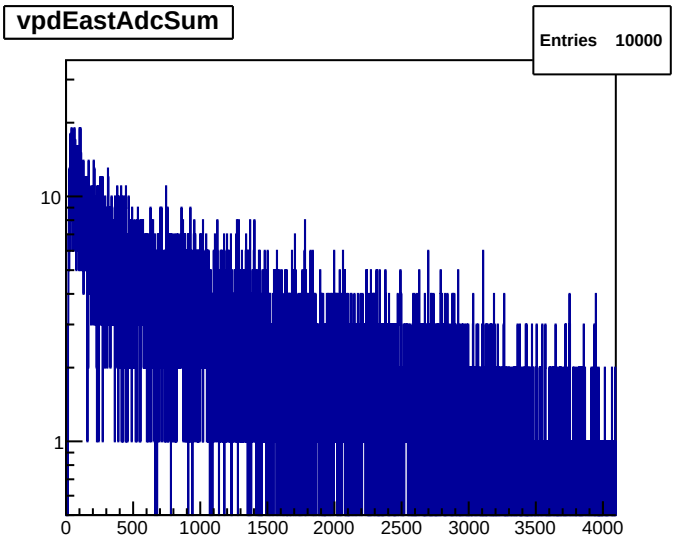

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

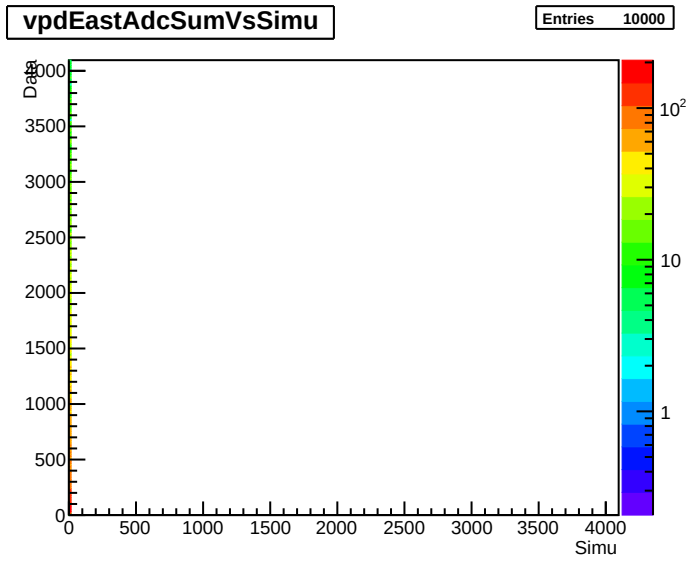

bbcSmallWestTacMax
Entries 10000

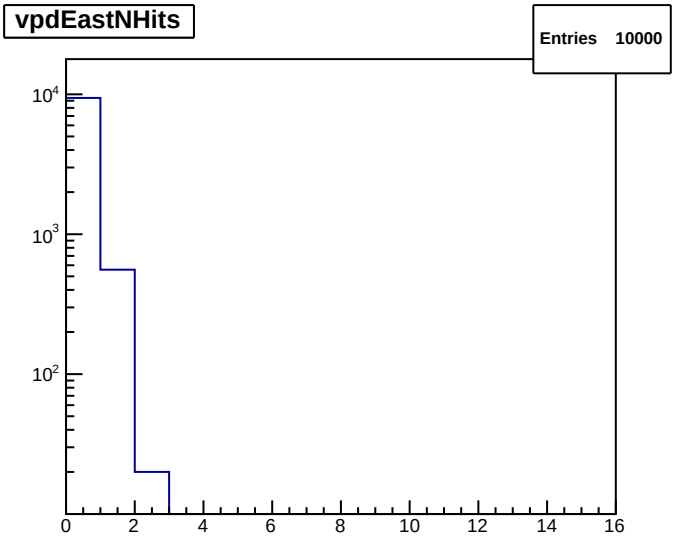

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

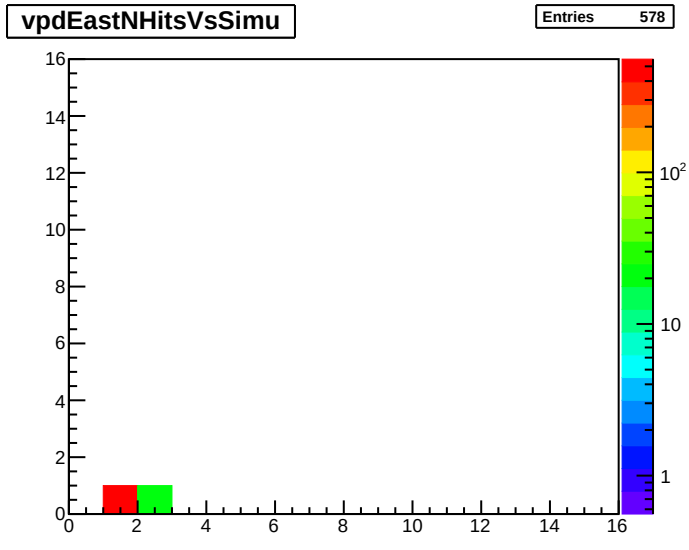

bbcLargeHit
Entries 20000
1
0
9742
9467
258
533
E-Hit
W-Hit
10^3

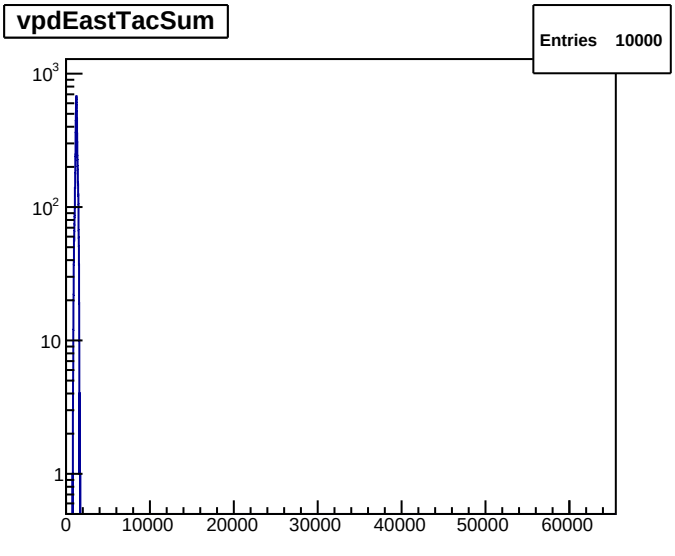

bbcLargeEastHitSimu
Entries 0
1
0
-1
E-Hit
W-Hit

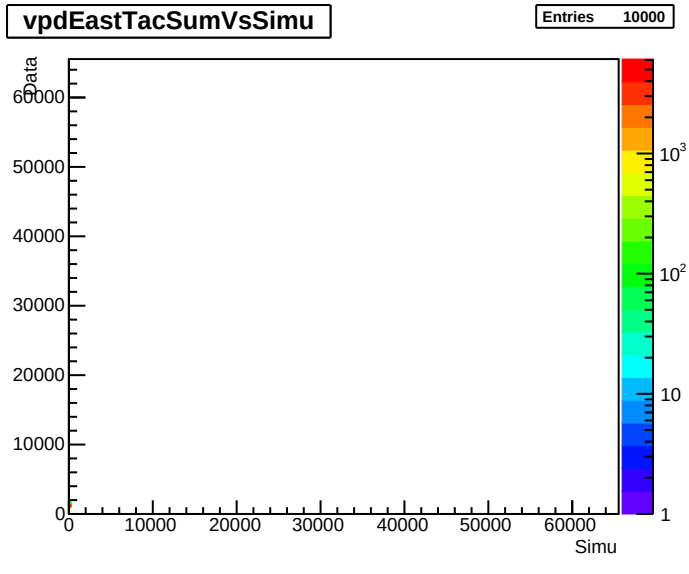

vpdEastAdcSum
Entries 10000

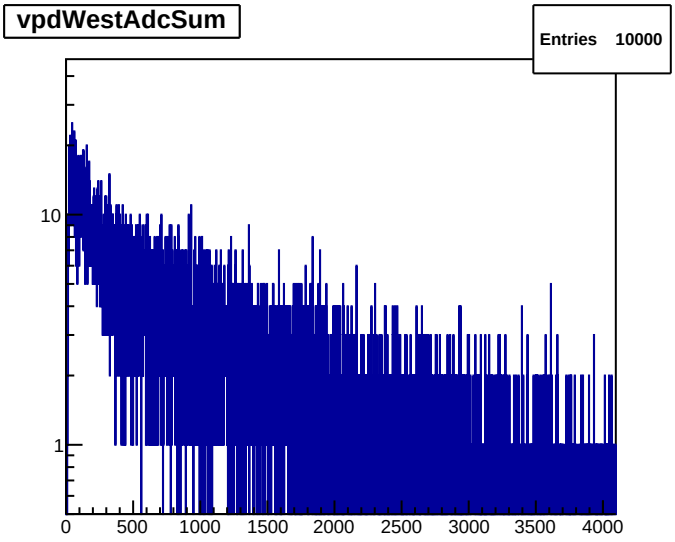

vpdEastAdcSumVsSimu
Entries 10000
Data
Simu

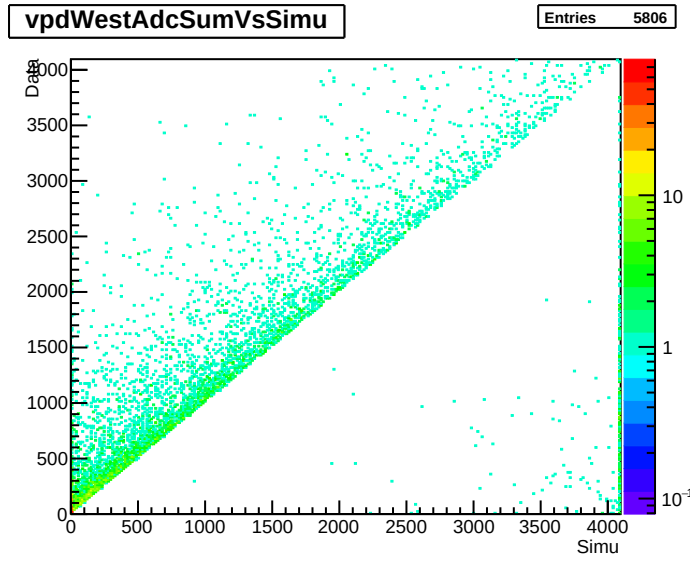

vpdEastNHits
Entries 10000

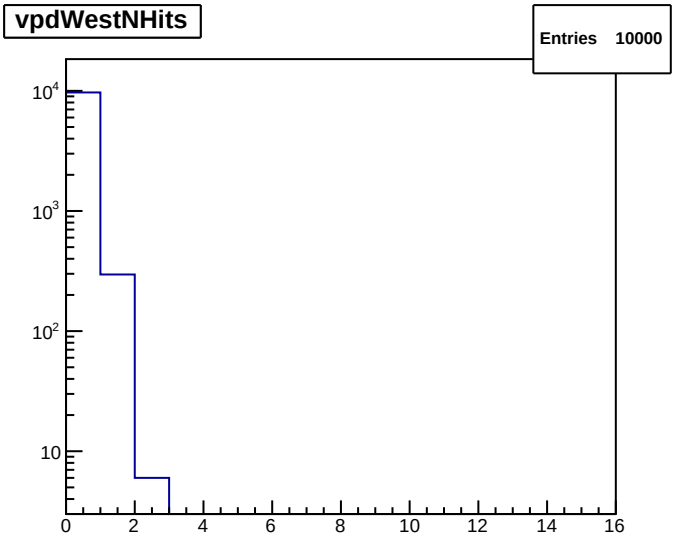

vpdEastNHitsVsSimu
Entries 578

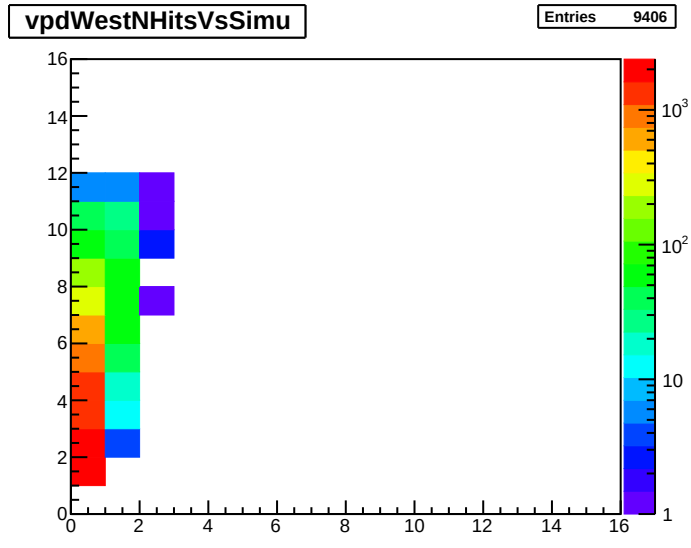

vpdEastTacSum
Entries 10000

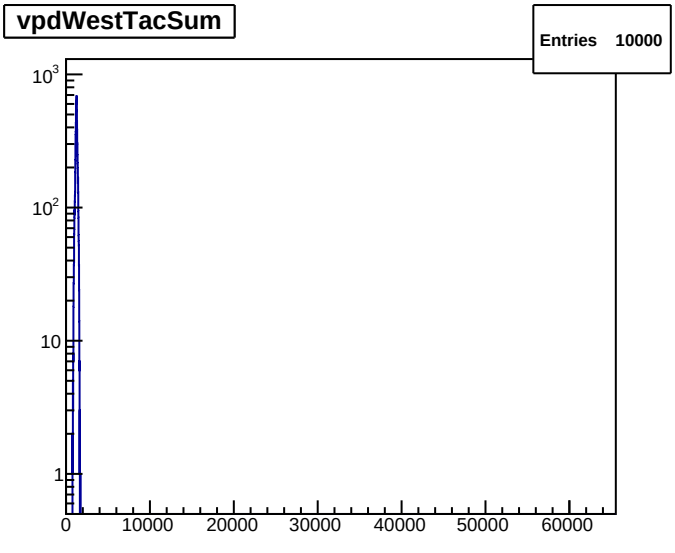

vpdEastTacSumVsSimu
Entries 10000
Data
Simu

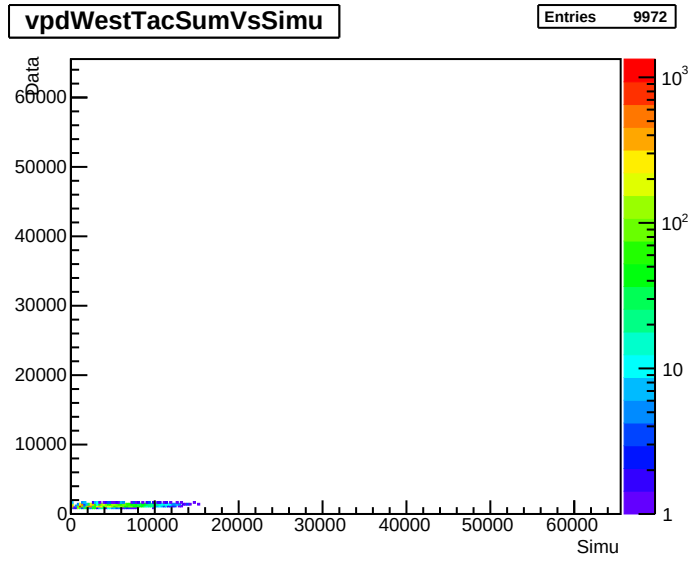

vpdWestAdcSum
Entries 10000

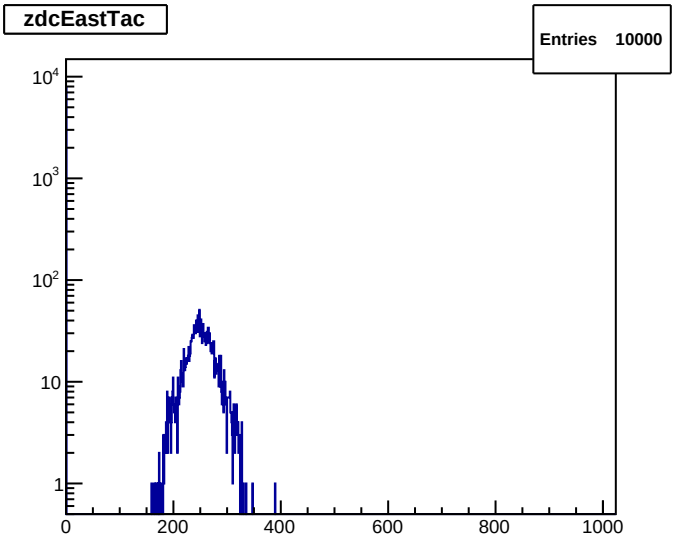

vpdWestAdcSumVsSimu
Entries 5806
Data
Simu

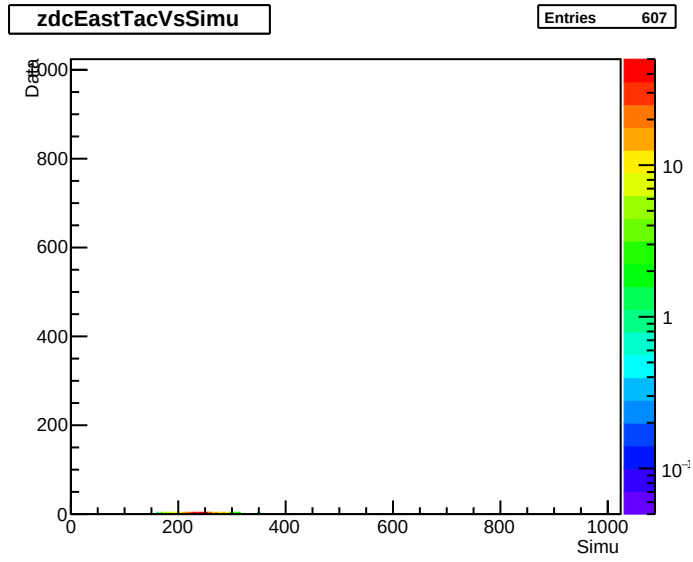

vpdWestNHits
Entries 10000

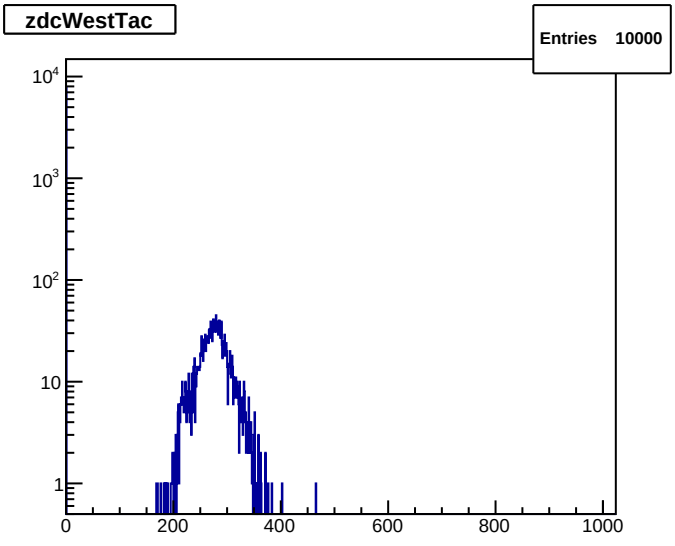

vpdWestNHitsVsSimu
Entries 9406

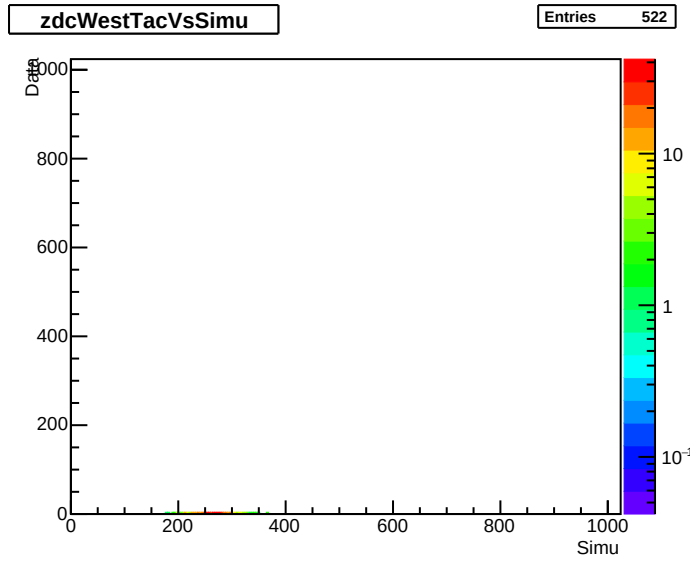

vpdWestTacSum
Entries 10000

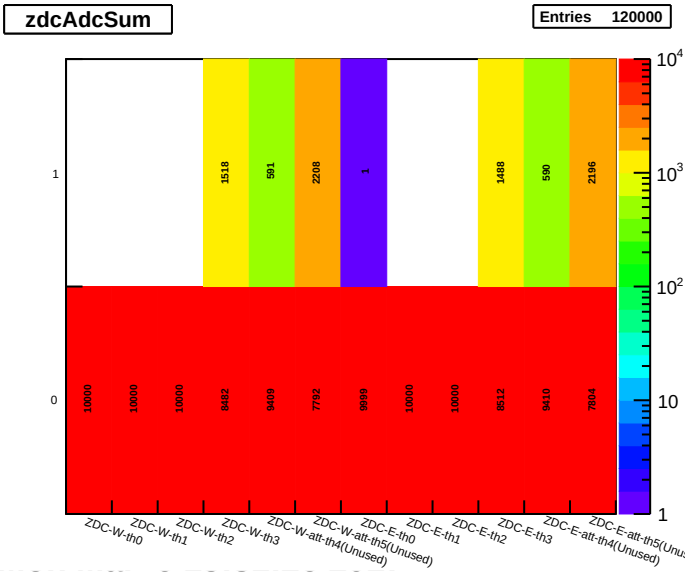

vpdWestTacSumVsSimu
Entries 9972
Data
Simu

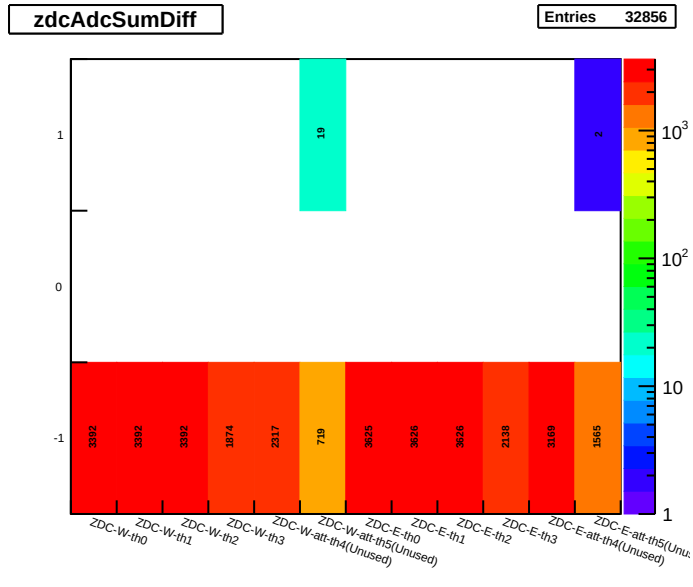

zdcEastTac
Entries 10000

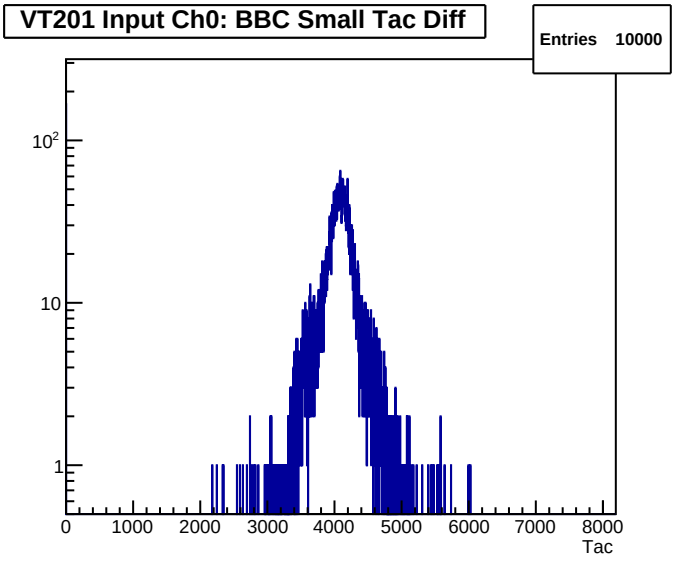

zdcEastTacVsSimu
Entries 607
Data
Simu

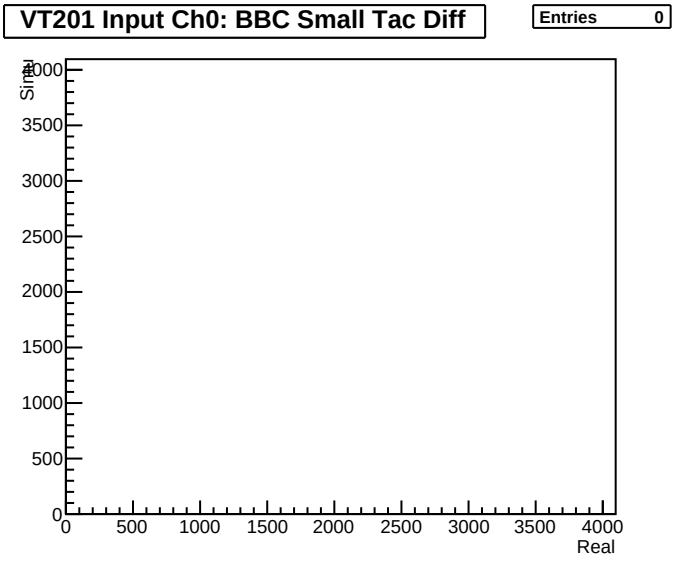

zdcWestTac
Entries 10000

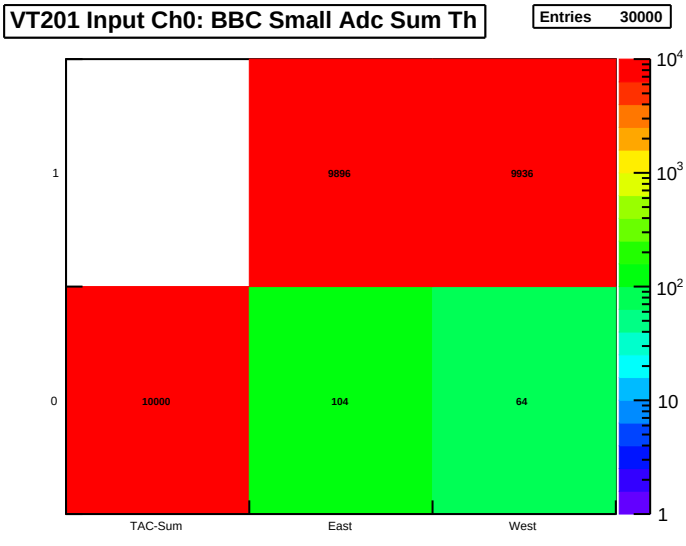

zdcWestTacVsSimu
Entries 522
Data
Simu

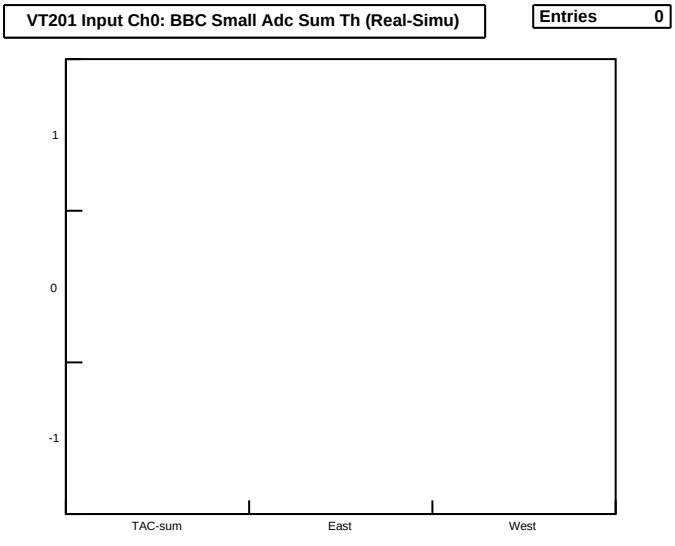

zdcAdcSum
Entries 120000
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

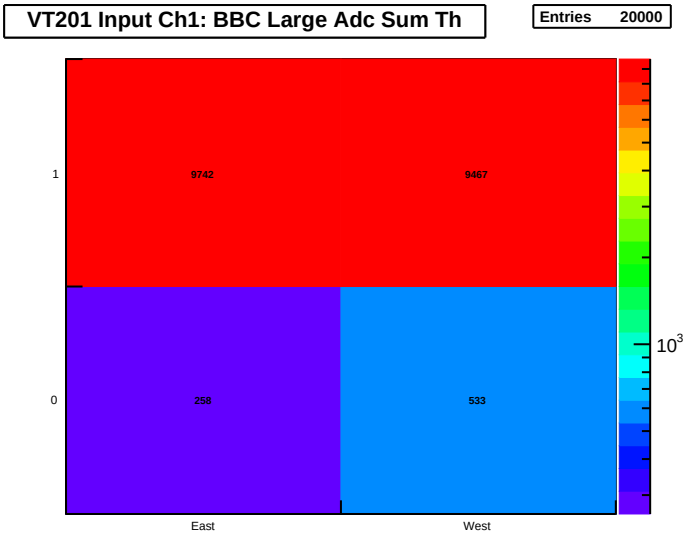

zdcAdcSumDiff
Entries 32856
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

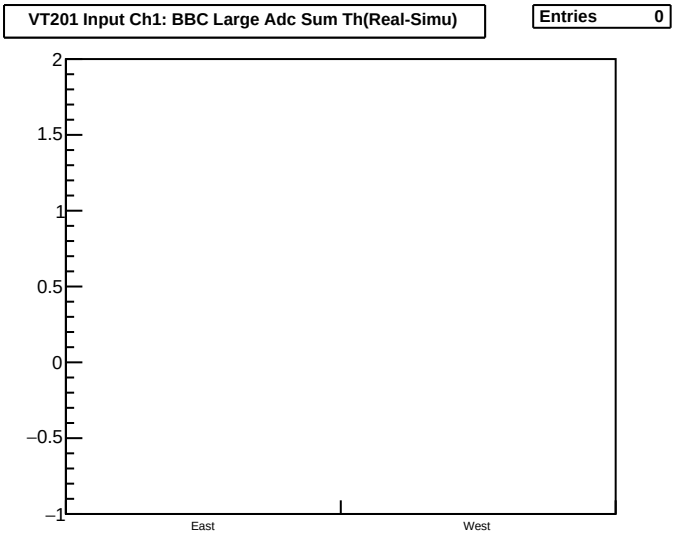

VT201 Input Ch0: BBC Small Tac Diff
Entries 10000
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

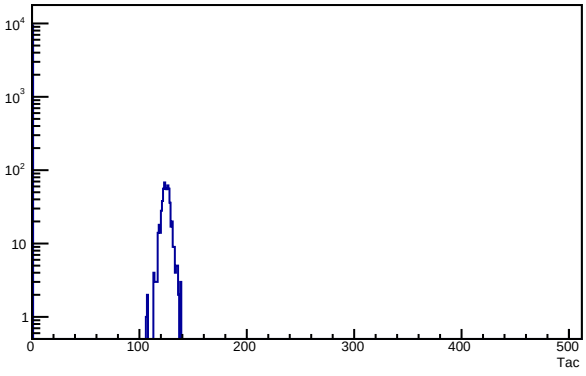

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 30000
9896
9936
10000
104
64
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

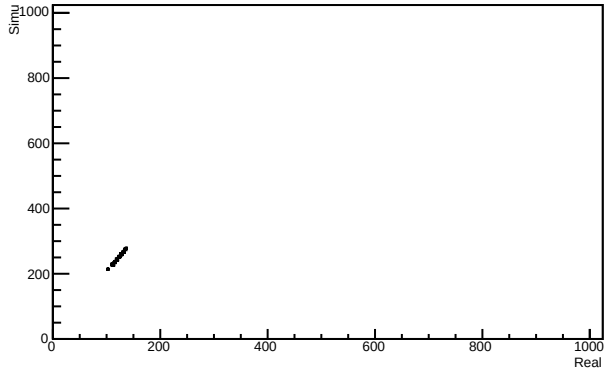

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 20000
9742
9467
258
533
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

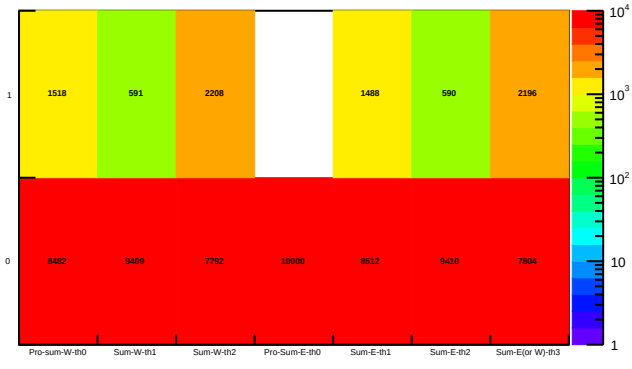
VT201 Input Ch2: ZDC Tac Diff



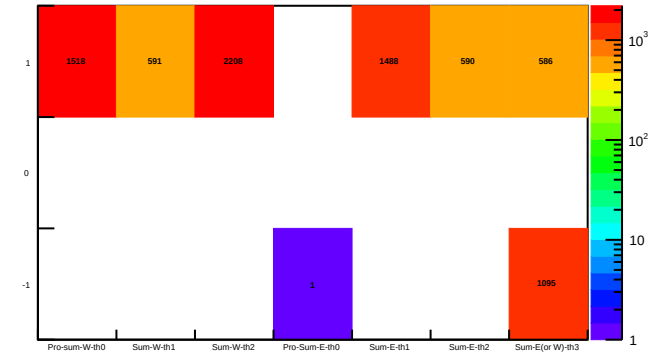
VT201 Input Ch2: ZDC Tac Diff



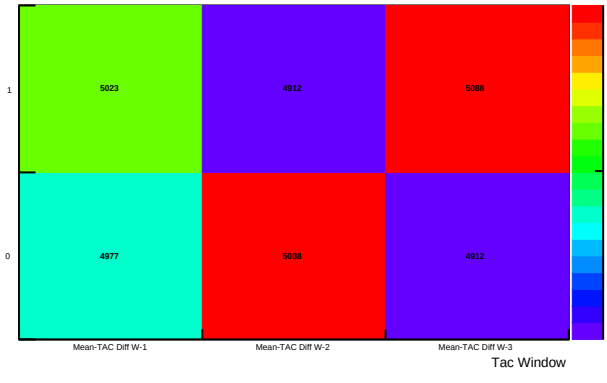
VT201 Input Ch2: ZDC Adc Sum Th



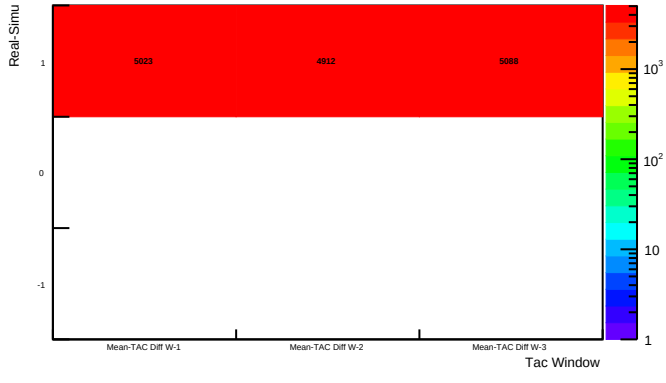
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



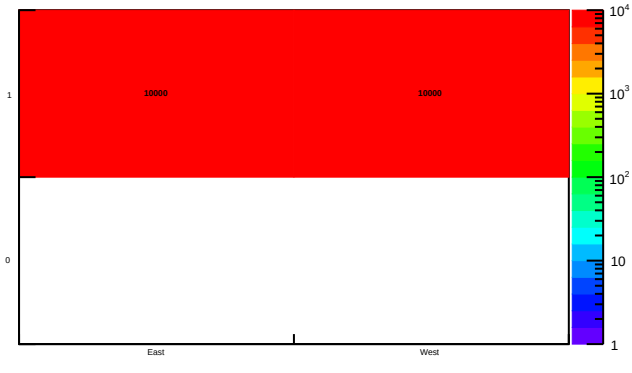
VT201 Input Ch4: VPD Tac Diff



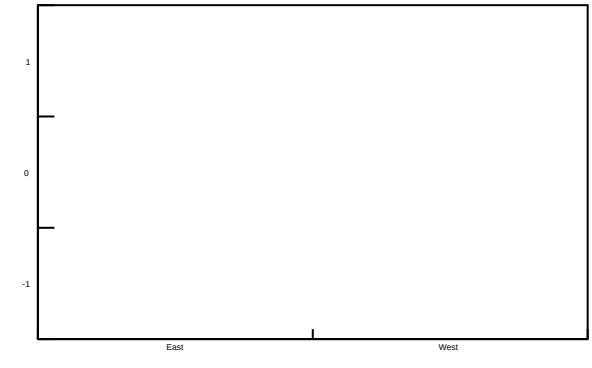
VT201 Input Ch4: VPD Tac Diff

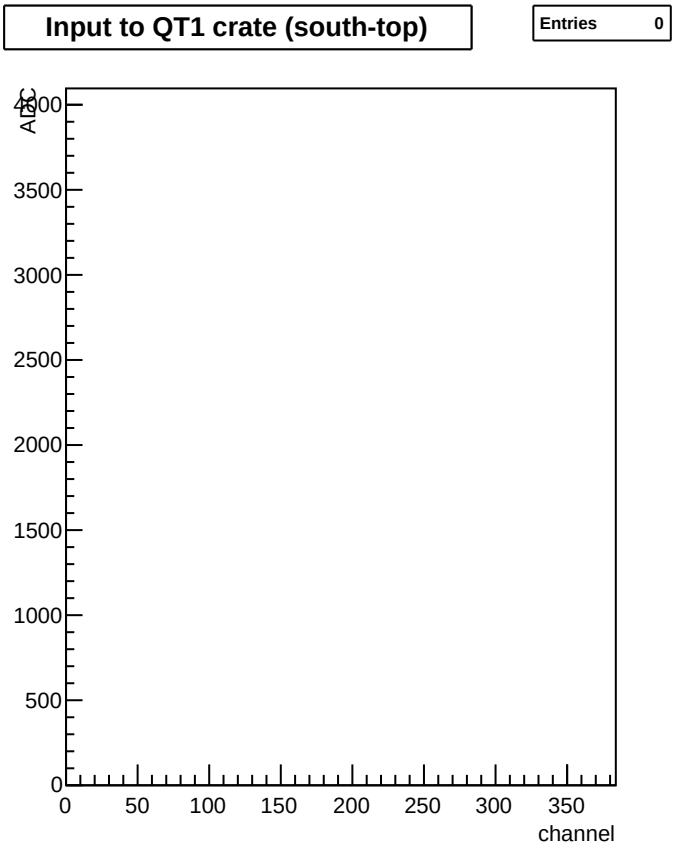


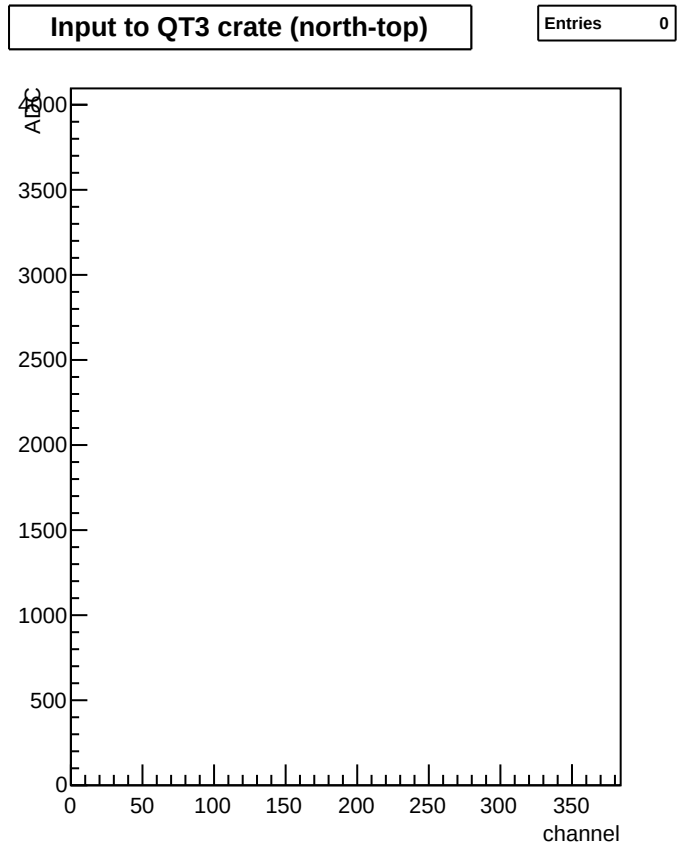
VT201 Input Ch4: VPD Adc Sum Th

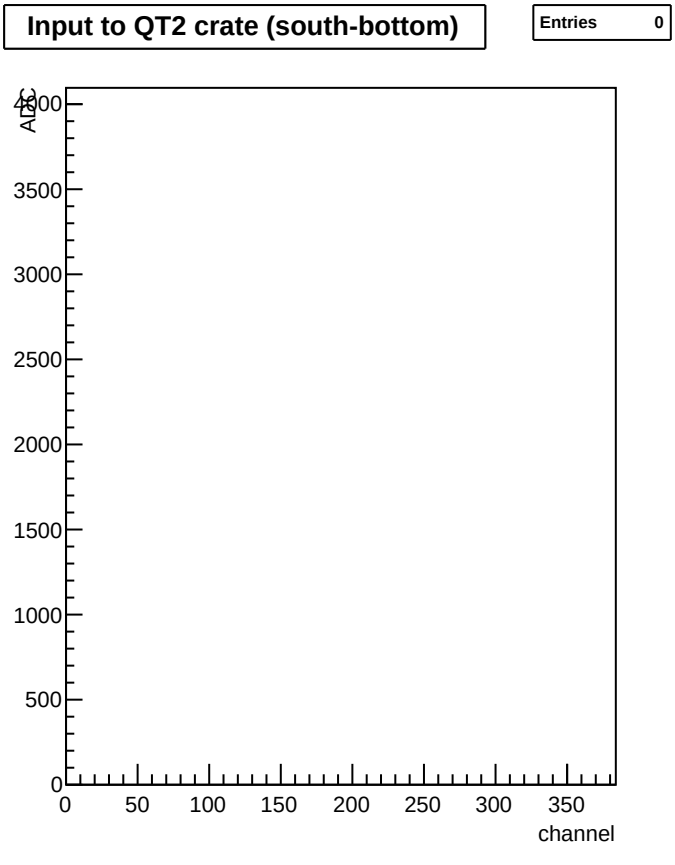


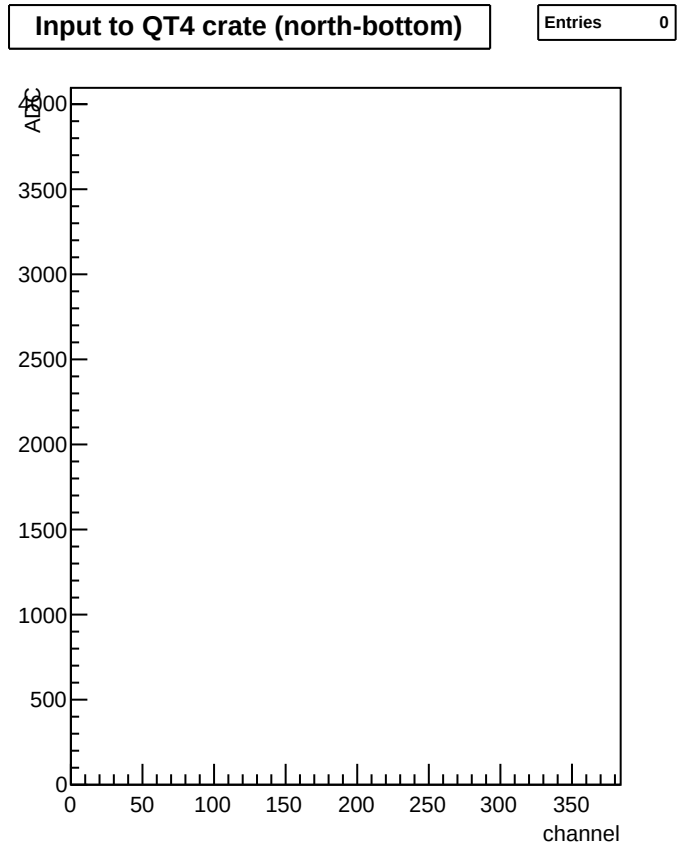
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

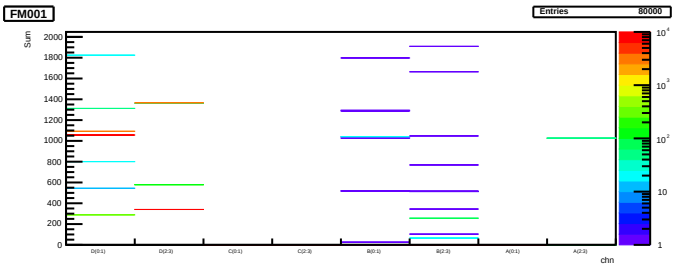


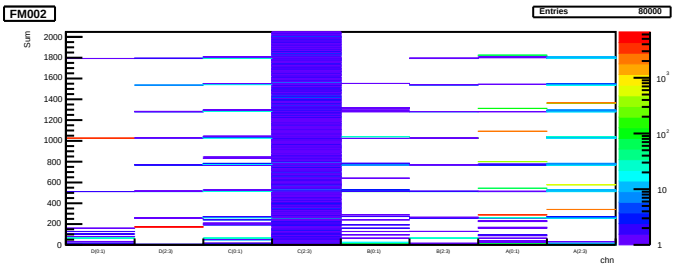

Input to QT1 crate (south-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

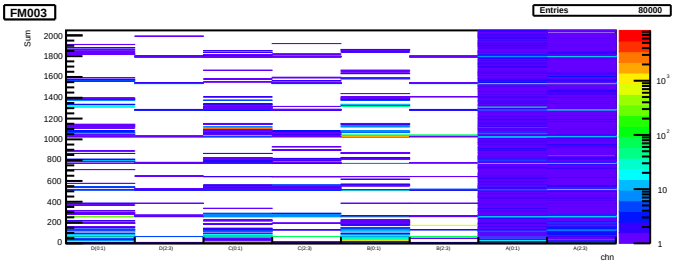

Input to QT3 crate (north-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

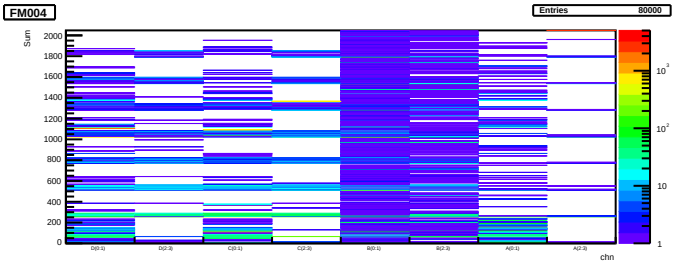

Input to QT2 crate (south-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

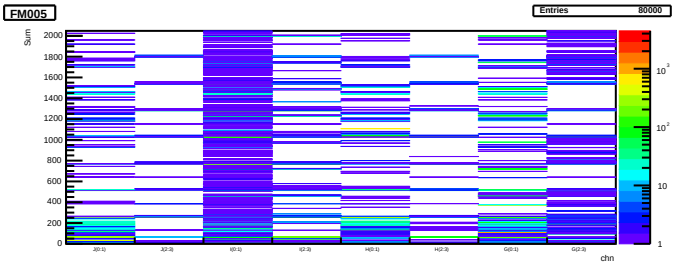

Input to QT4 crate (north-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

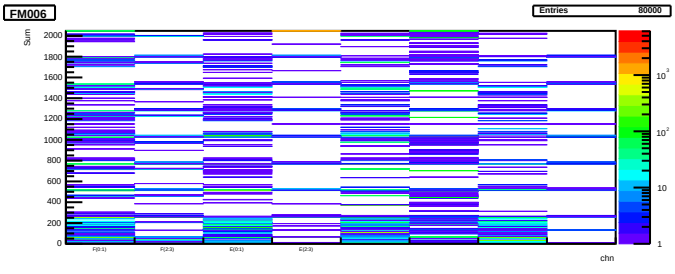

FM001
Entries 80000
Sum
chn

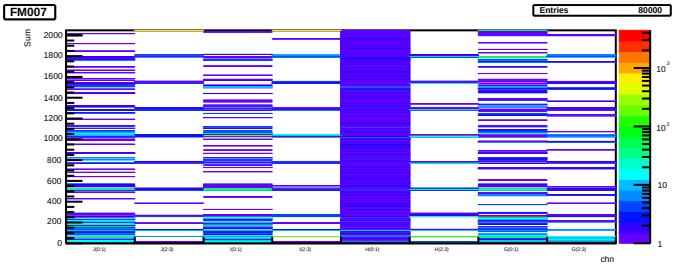

FM002
Entries 80000
Sum
chn

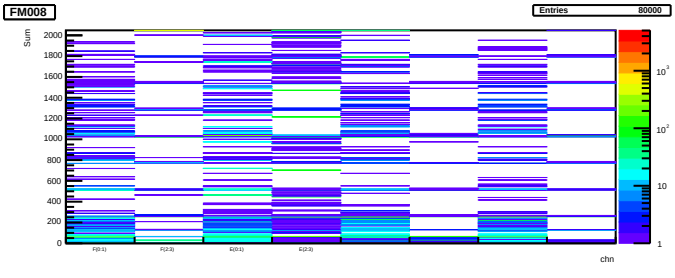

FM003
Entries 80000
Sum
chn

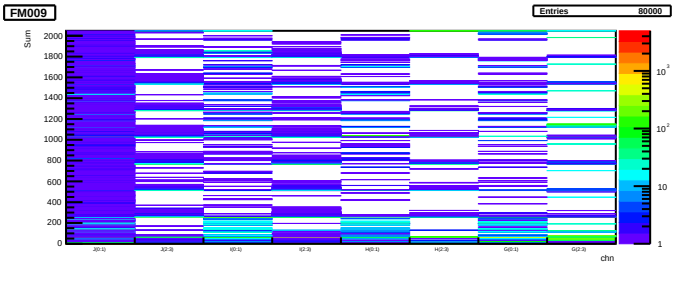

FM004
Entries 80000
Sum
chn

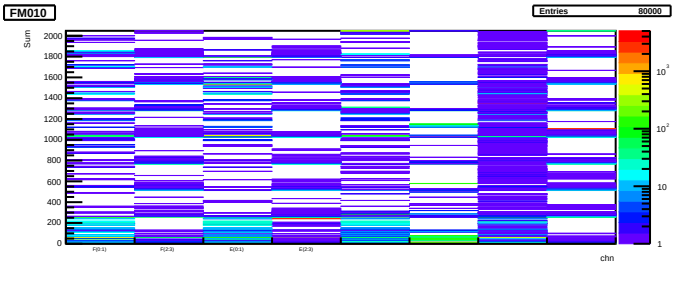

FM005
Entries 80000
Sum
chn

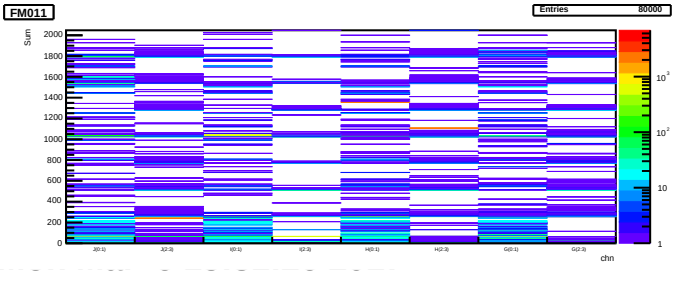

FM006
Entries 80000
Sum
chn

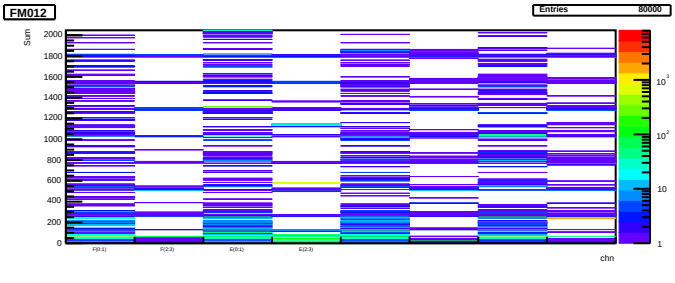

FM007
Entries 80000
Sum
chn

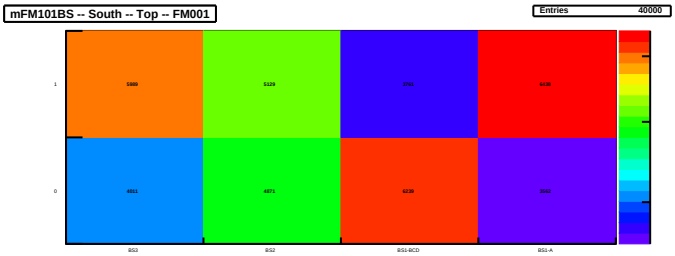

FM008
Entries 80000
Sum
chn

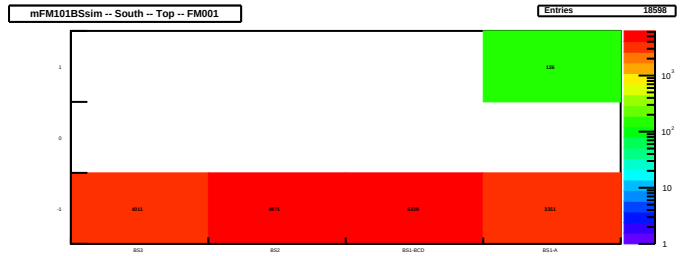

FM009
Entries 80000
Sum
chn

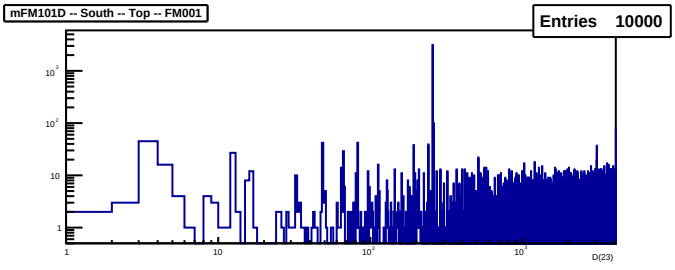

FM010
Entries 80000
Sum
chn

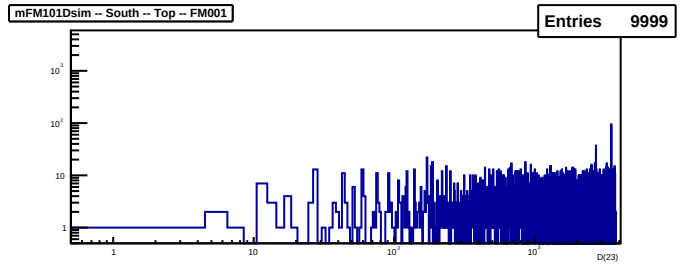

FM011
Entries 80000
Sum
chn

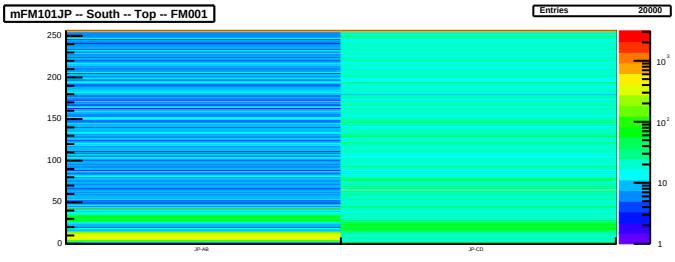

FM012
Entries 80000
Sum
chn

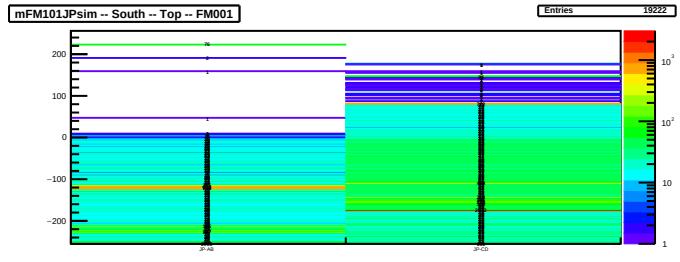

mFM101BS -- South -- Top -- FM001
Entries 40000

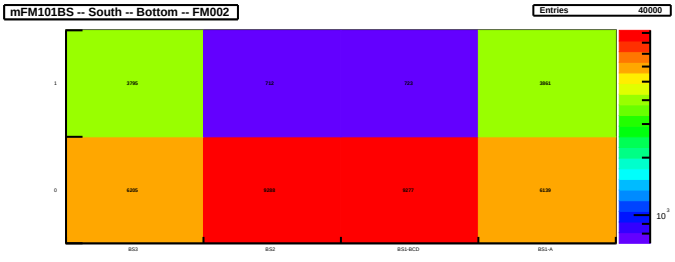

mFM101BSsim -- South -- Top -- FM001
Entries 18598

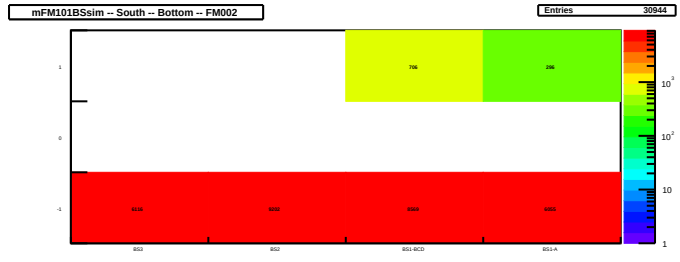

mFM101D -- South -- Top -- FM001
Entries 10000
D(23)

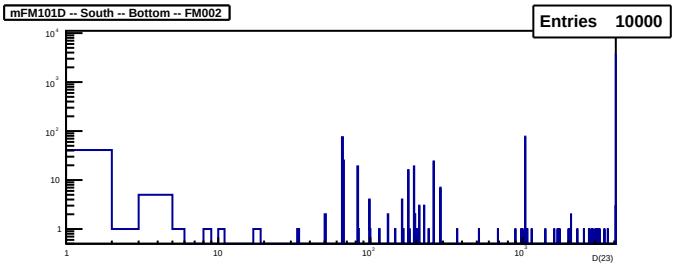

mFM101Dsim -- South -- Top -- FM001
Entries 9999
D(23)


mFM101JP -- South -- Top -- FM001
Entries 20000


mFM101JPsim -- South -- Top -- FM001
Entries 19222


mFM101BS -- South -- Bottom -- FM002
Entries 40000


mFM101BSsim -- South -- Bottom -- FM002
Entries 30944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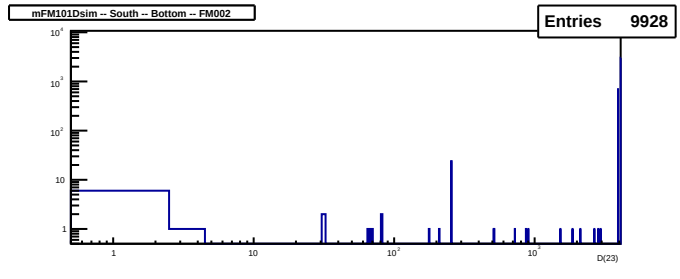
Entries 10000
D(23)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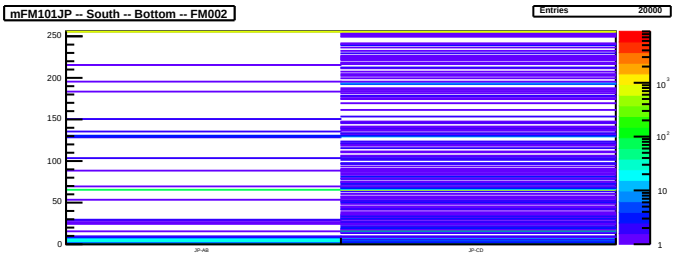
Entries 9928
D(23)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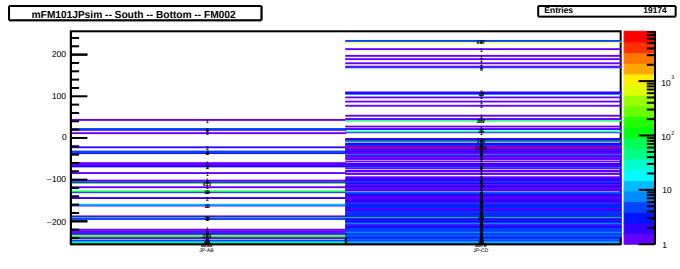
Entries 20000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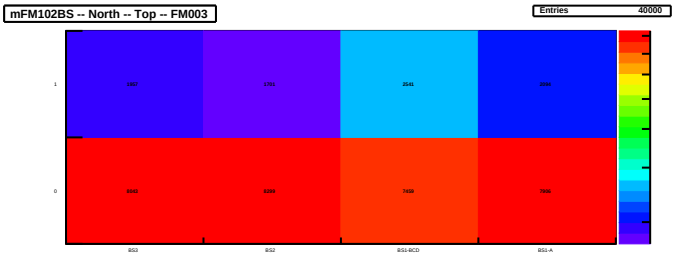
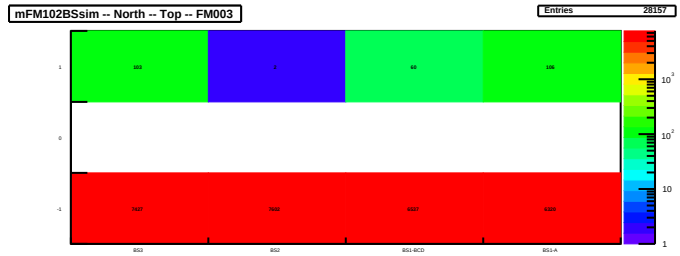

mFM102BS -- North -- Top -- FM003
Entries 40000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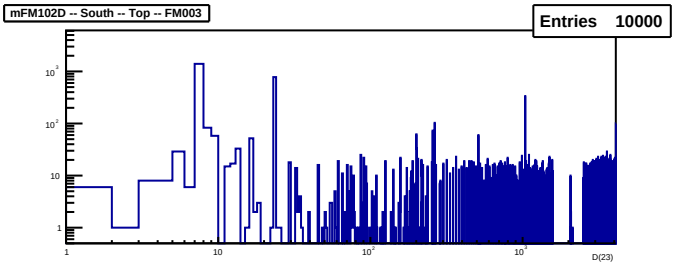
Entries 28157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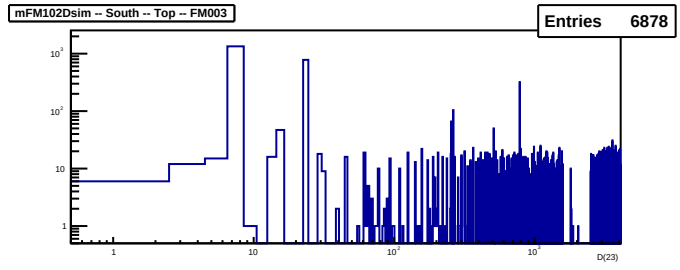
Entries 10000
D(23)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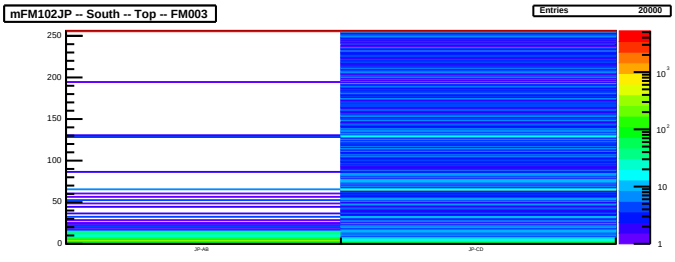
D(23)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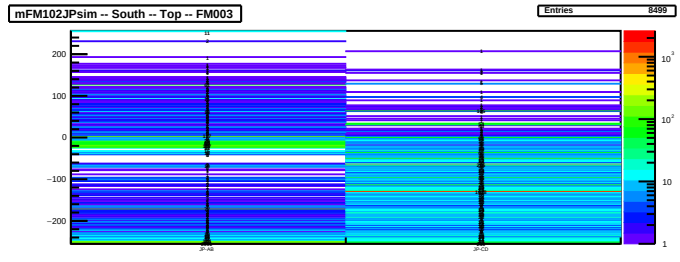
Entries 20000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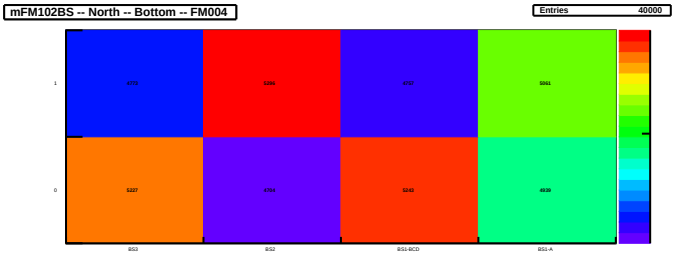
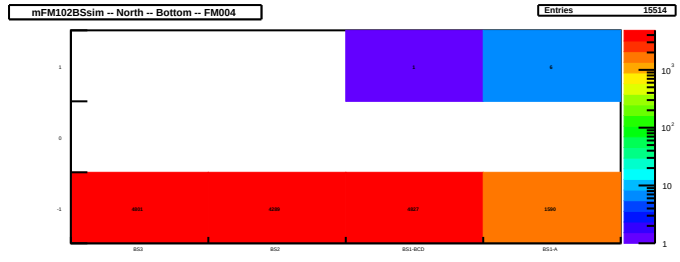

mFM102BS -- North -- Bottom -- FM004
Entries 40000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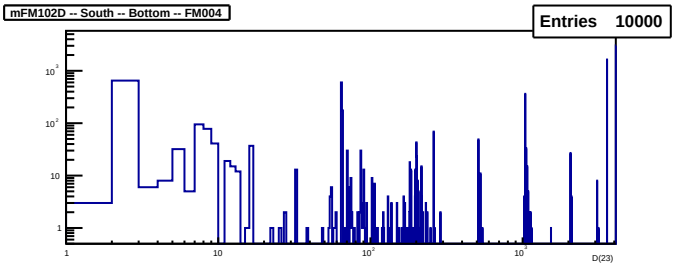
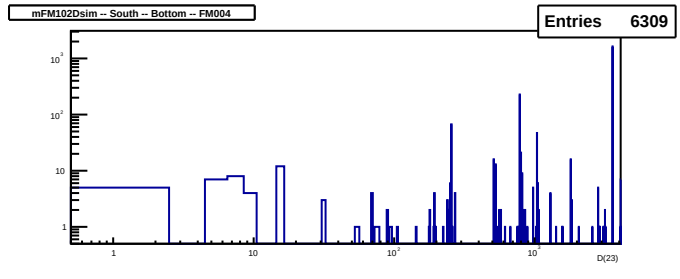

mFM102D -- South -- Bottom -- FM004
Entries 10000
D(23)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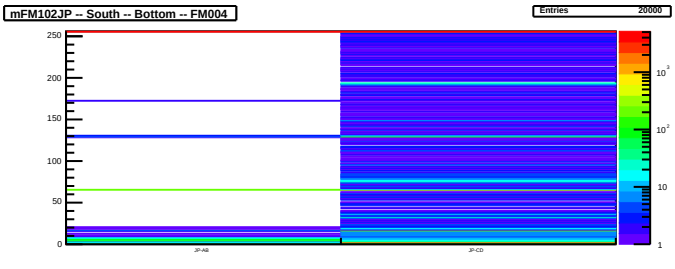
D(23)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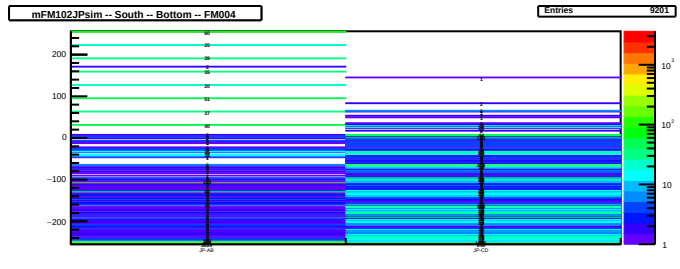
Entries 20000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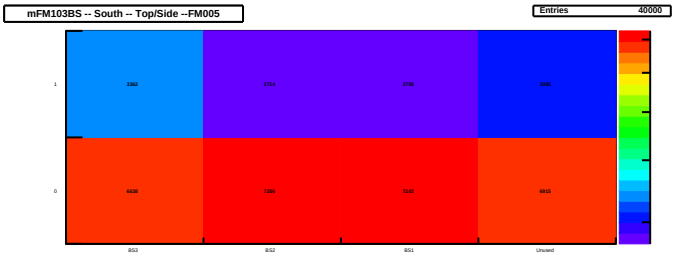
Entries 9201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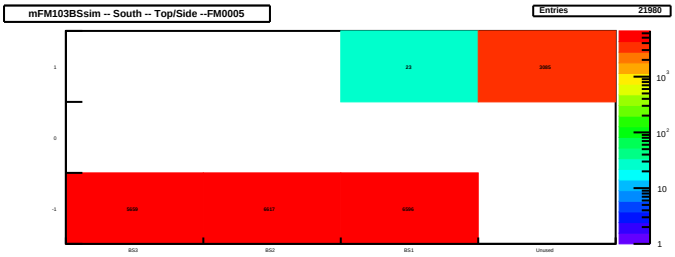
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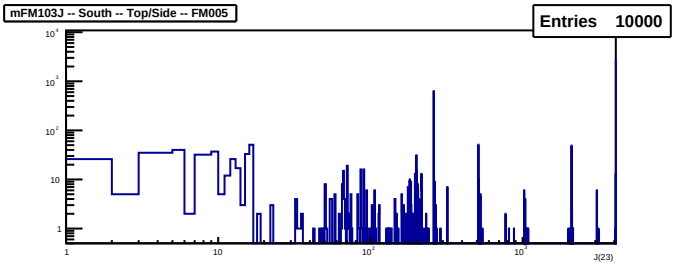
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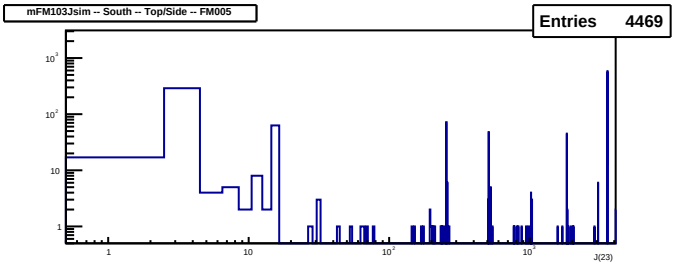
Entries 10000
J(23)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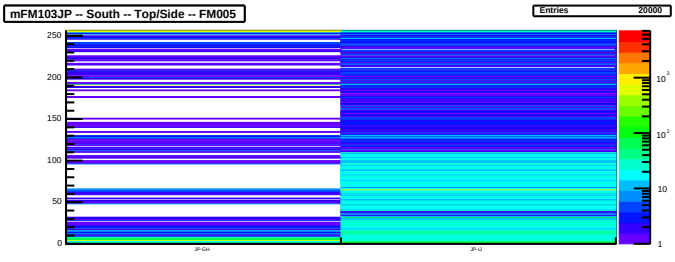
Entries 4469
J(23)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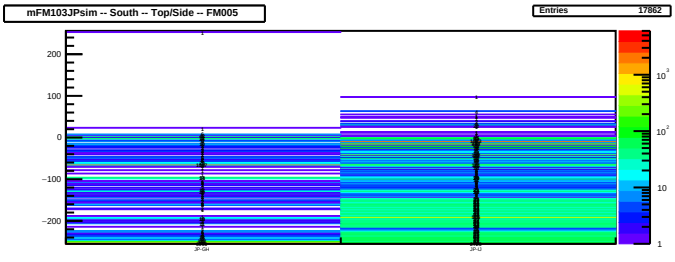
Entries 20000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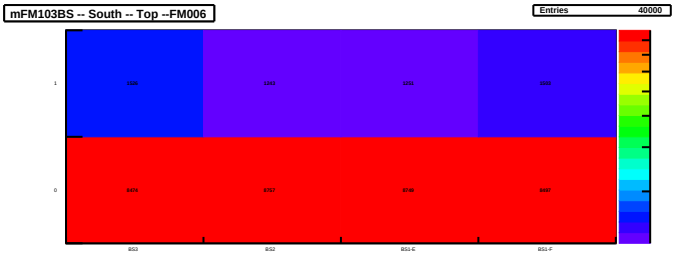
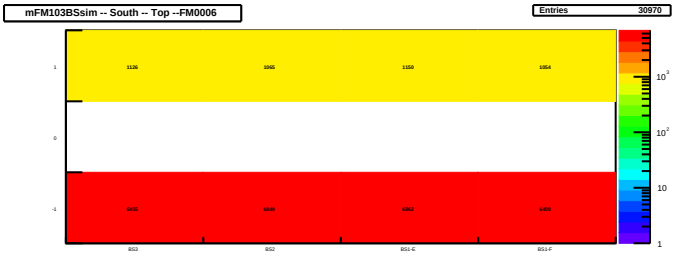

mFM103BS -- South -- Top --FM006
Entries 40000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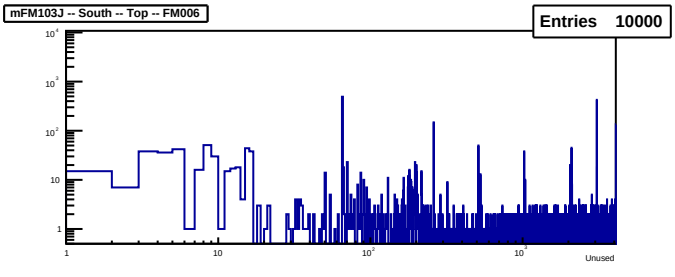
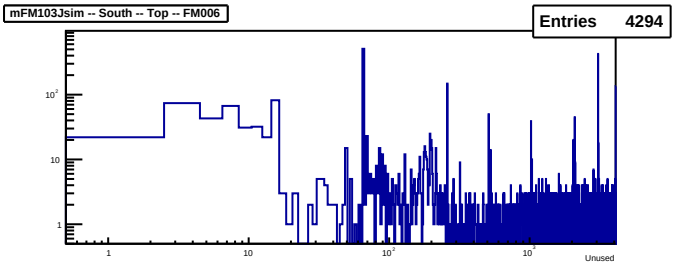
Entries 30970


mFM103J -- South -- Top -- FM006
Entries 10000
Unused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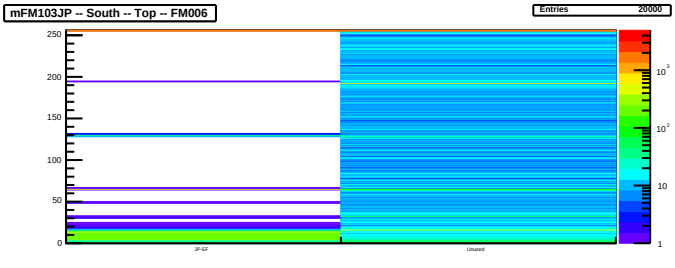
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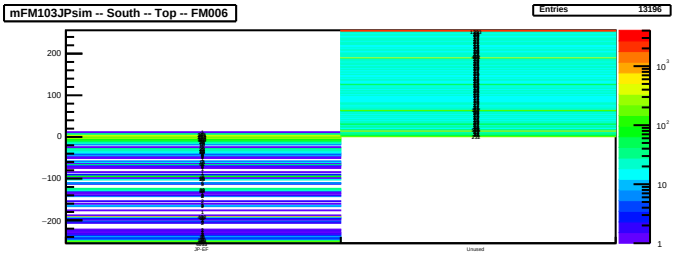
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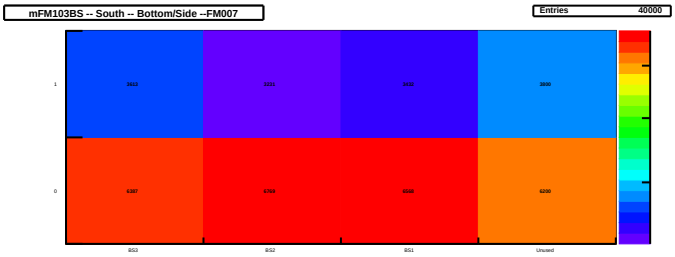
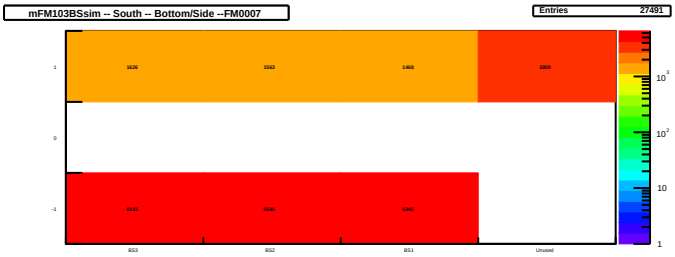
Entries 13196


mFM103BS -- South -- Bottom/Side --FM007
Entries 40000
Unused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Entries 27491
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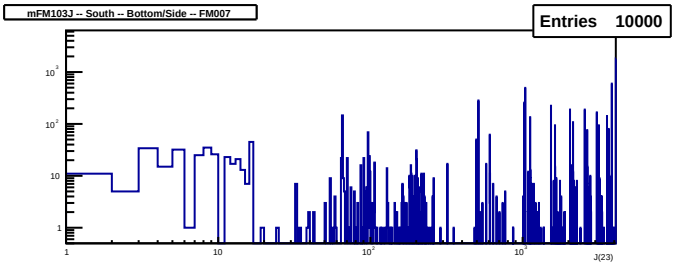
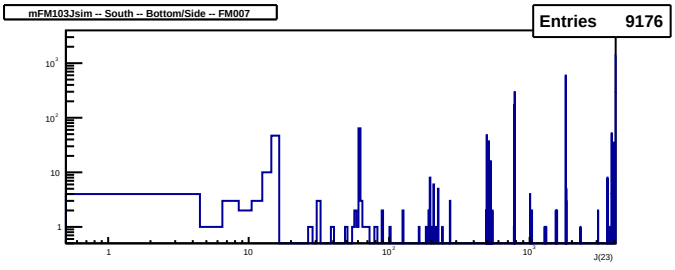

mFM103J -- South -- Bottom/Side -- FM007
Entries 10000
J(23)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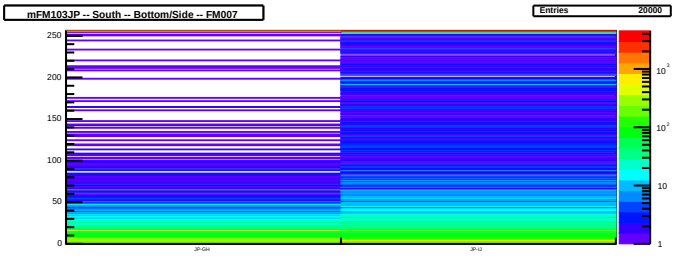
Entries 9176
J(23)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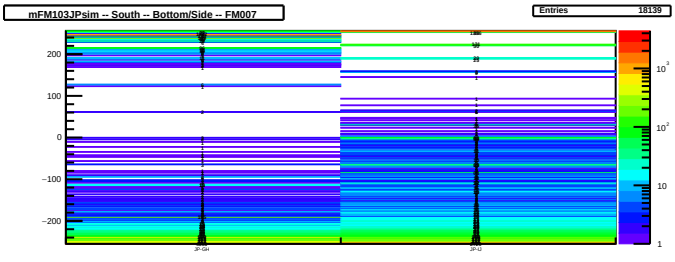
Entries 20000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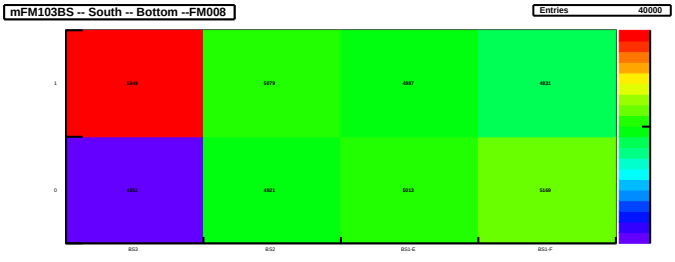
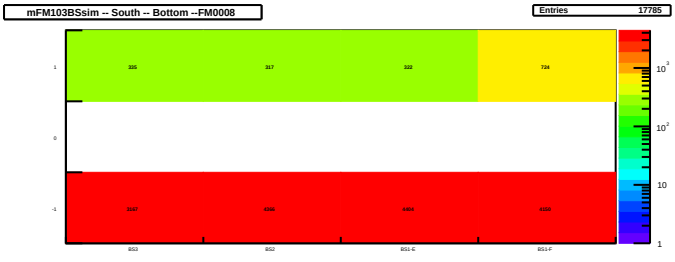
Entries 18139


mFM103BS -- South -- Bottom --FM008
Entries 40000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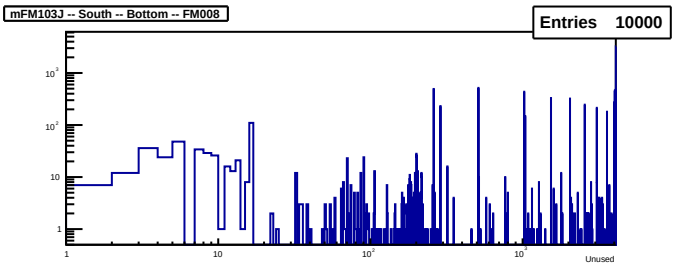
Entries 17785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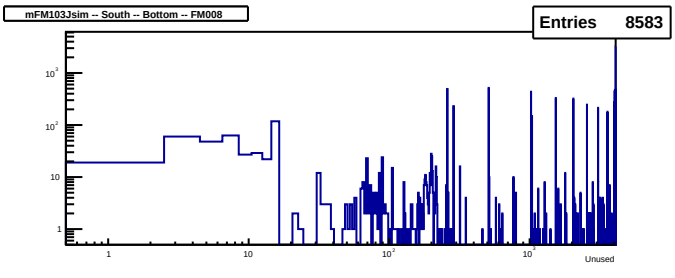
Entries 10000
Unused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Entries 8583
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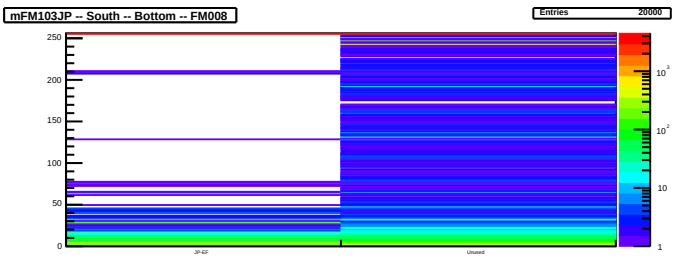
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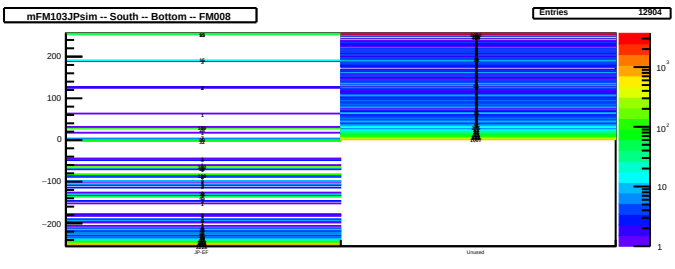
Entries 20000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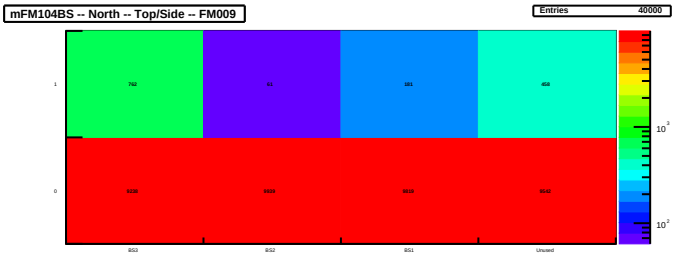
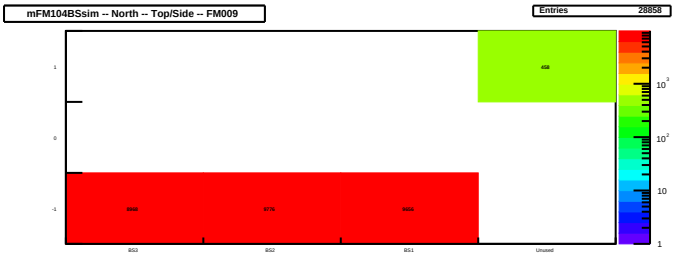

mFM104BS -- North -- Top/Side -- FM009
Entries 40000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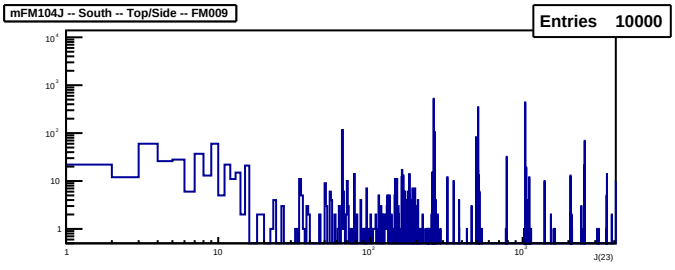
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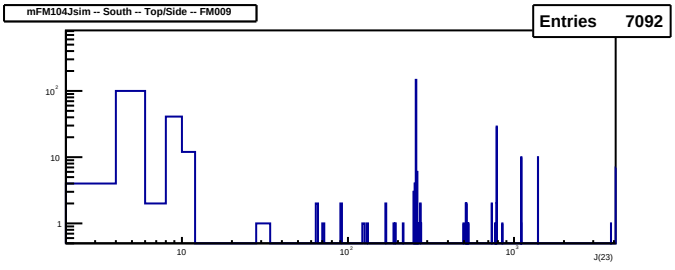
J(23)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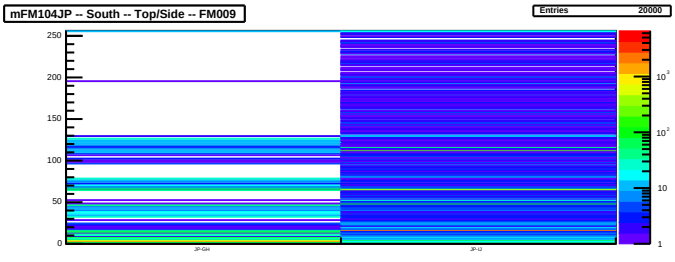
Entries 7092
J(23)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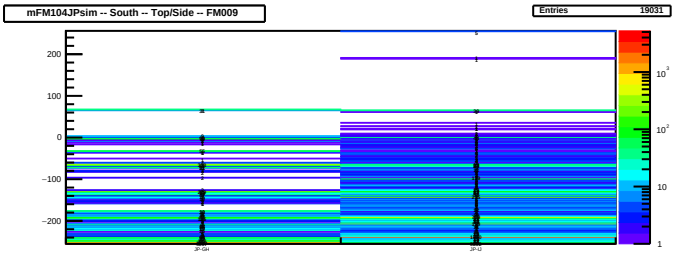
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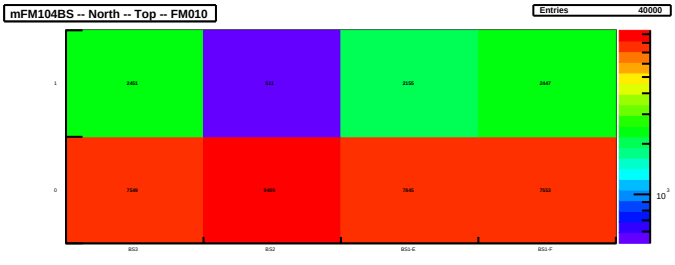
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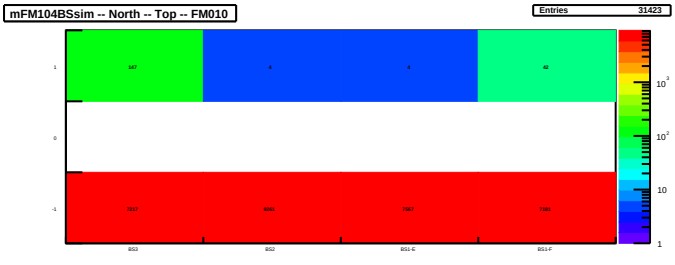
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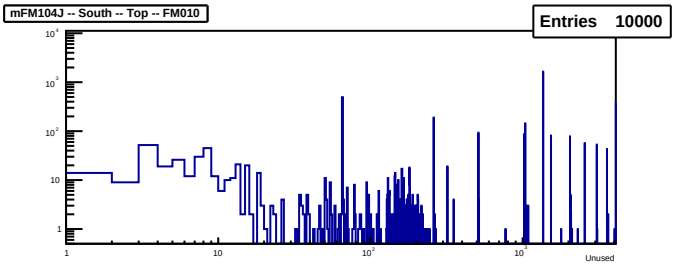
Entries 31423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Entries 10000
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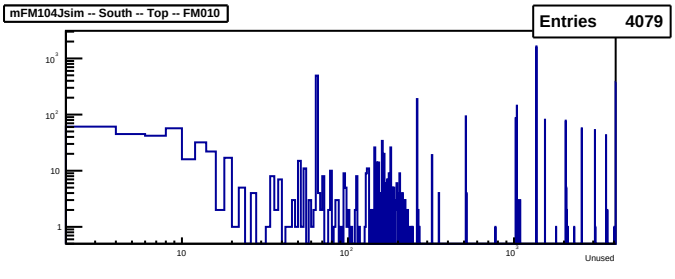
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Entries 4079
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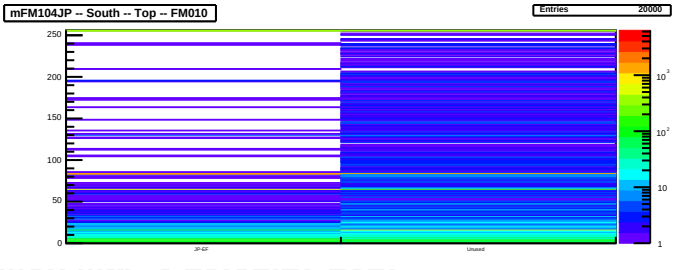
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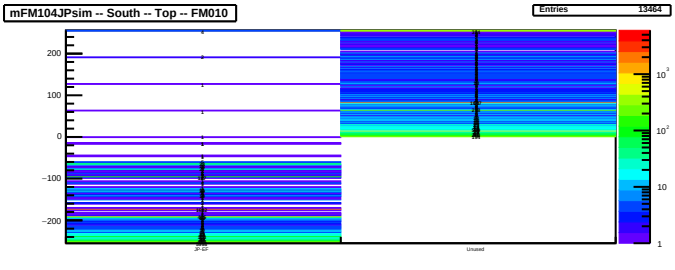
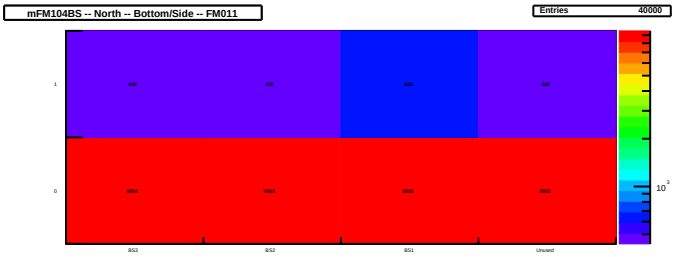
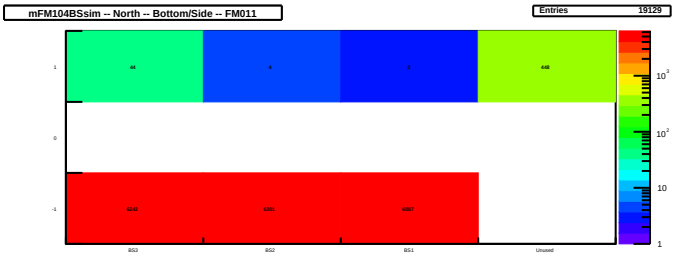
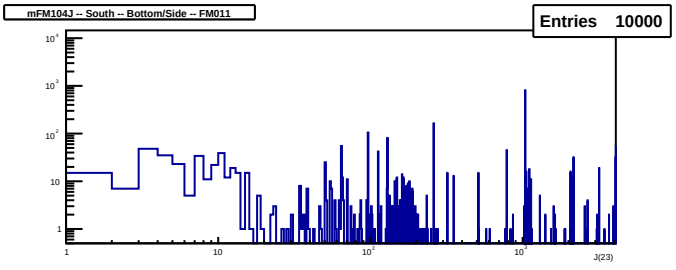
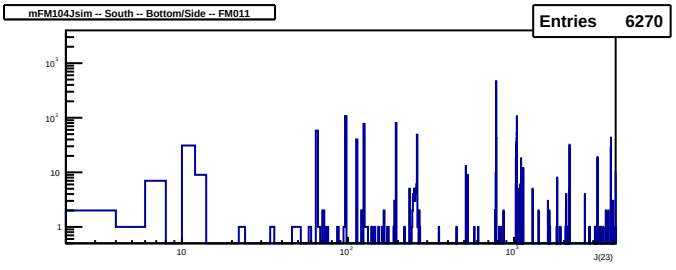
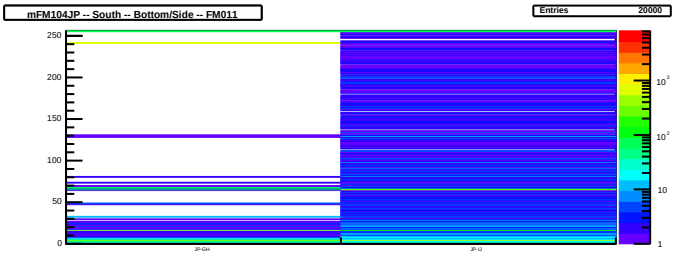

mFM104JPsim -- South -- Top -- FM010
Entries 13464

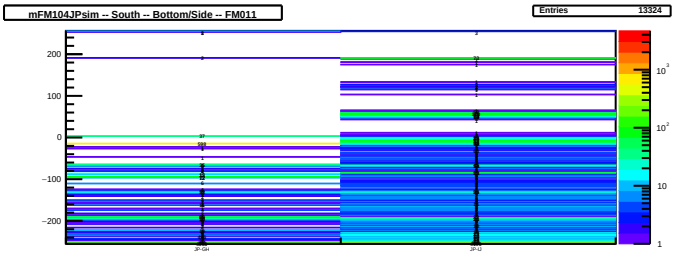

mFM104BS -- North -- Bottom/Side -- FM011
Entries 40000

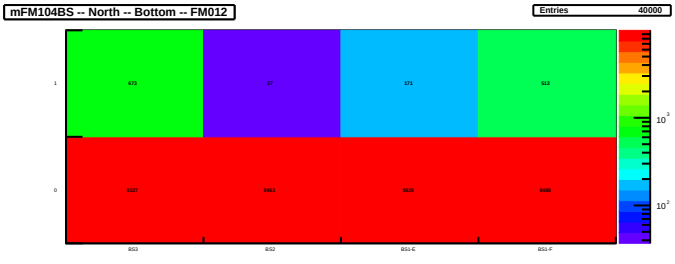

mFM104BSsim -- North -- Bottom/Side -- FM011
Entries 19129

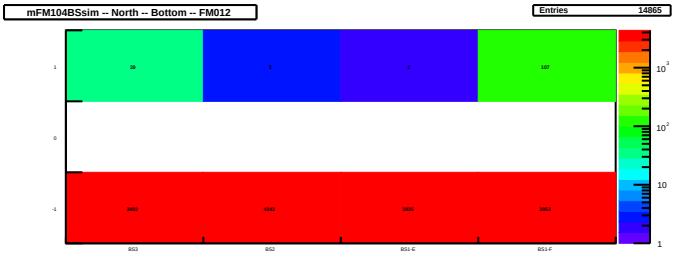

mFM104J -- South -- Bottom/Side -- FM011
Entries 10000
J(23)

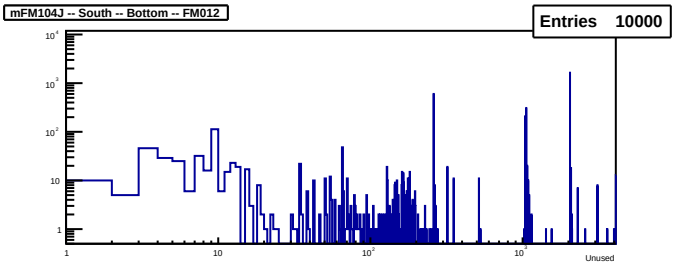

mFM104Jsim -- South -- Bottom/Side -- FM011
Entries 6270
J(23)

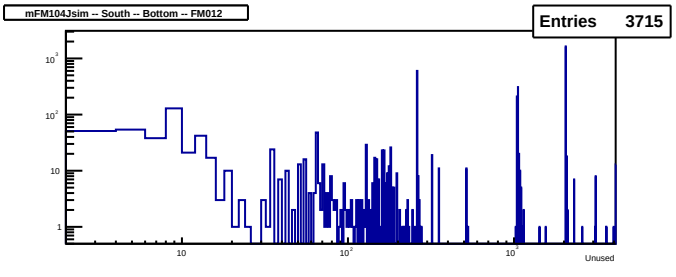

mFM104JP -- South -- Bottom/Side -- FM011
Entries 20000

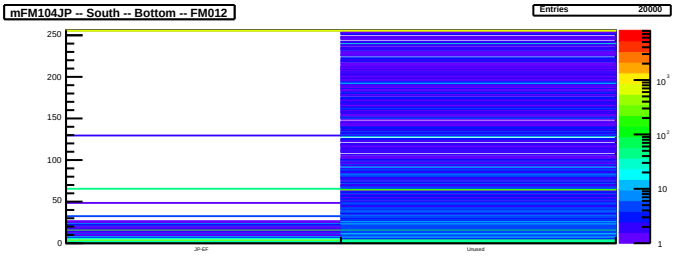

mFM104JPsim -- South -- Bottom/Side -- FM011
Entries 13324

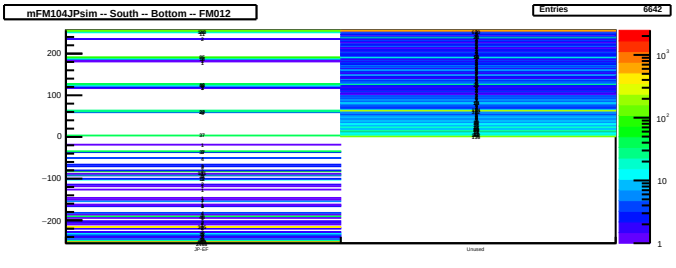

mFM104BS -- North -- Bottom -- FM012
Entries 40000

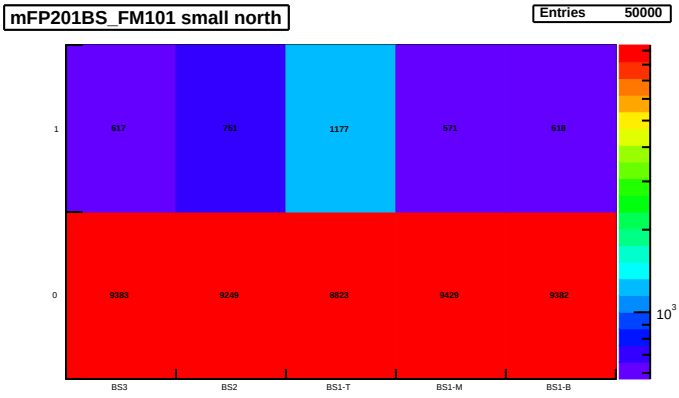

mFM104BSsim -- North -- Bottom -- FM012
Entries 14865

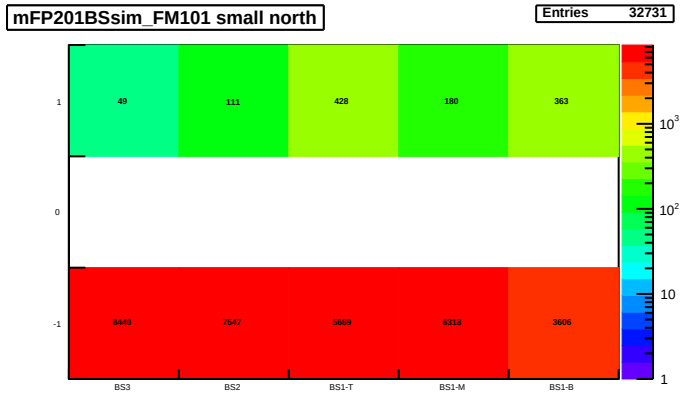

mFM104J -- South -- Bottom -- FM012
Entries 10000
Unused

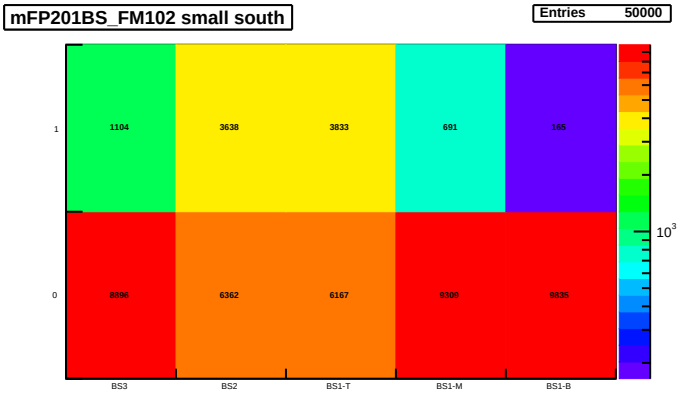

mFM104Jsim -- South -- Bottom -- FM012
Entries 3715
Unused

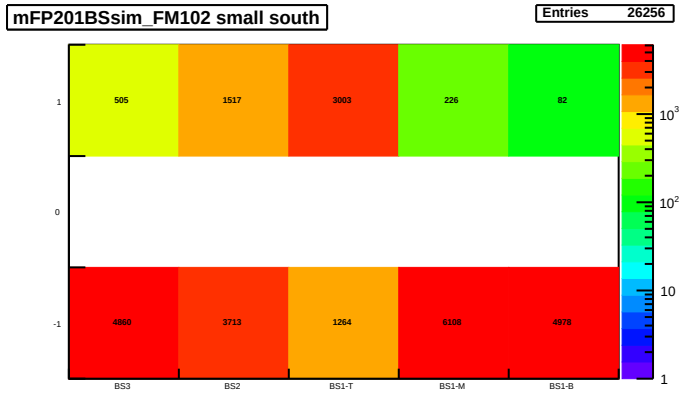

mFM104JP -- South -- Bottom -- FM012
Entries 20000

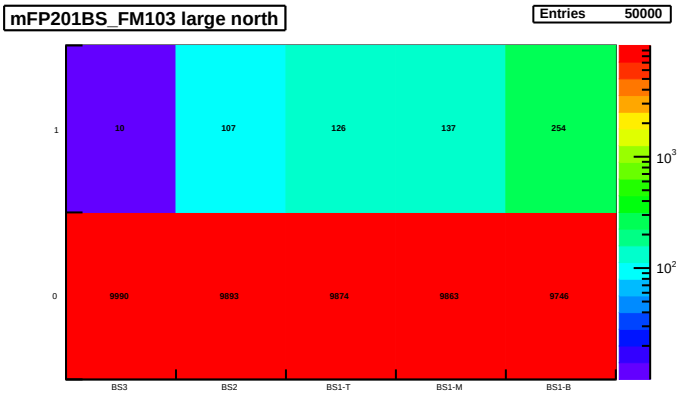

mFM104JPsim -- South -- Bottom -- FM012
Entries 6642

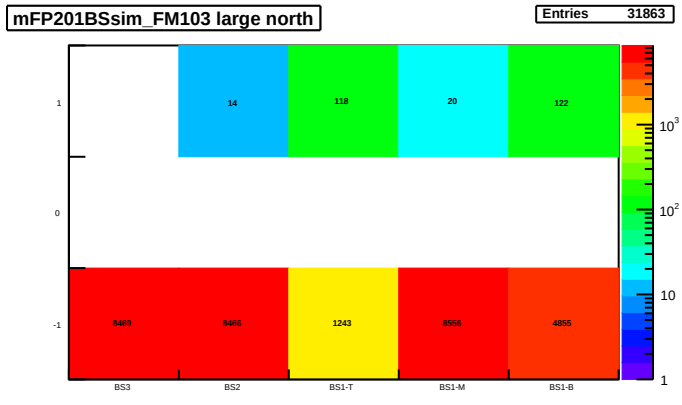

mFP201BS_FM101 small north
Entries 50000
1
0
617
751
1177
571
618
9383
9249
8823
9429
9382
BS3
BS2
BS1-T
BS1-M
BS1-B
10^3

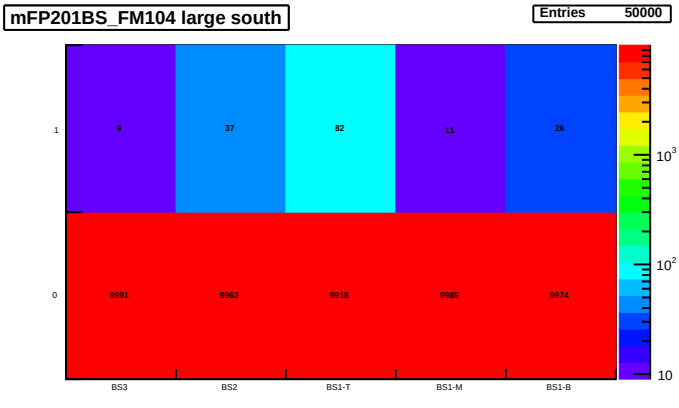

mFP201BSsim_FM101 small north
Entries 32731
1
0
-1
49
111
428
180
363
8440
7547
5689
6318
3606
BS3
BS2
BS1-T
BS1-M
BS1-B
10^3
10^2
10
1

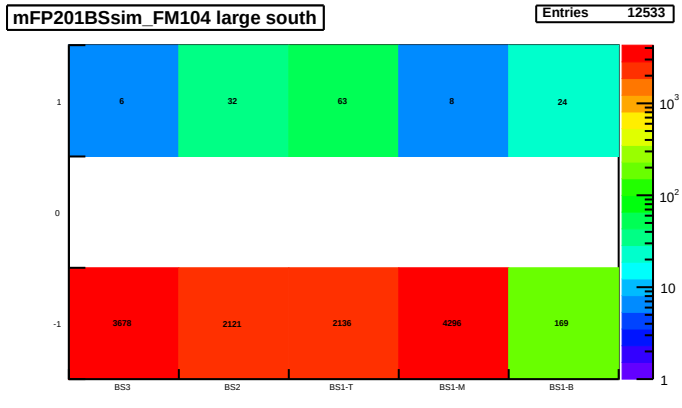

mFP201BS_FM102 small south
Entries 50000
1
0
1104
3638
3833
691
165
8896
6362
6167
9309
9835
BS3
BS2
BS1-T
BS1-M
BS1-B
10^3

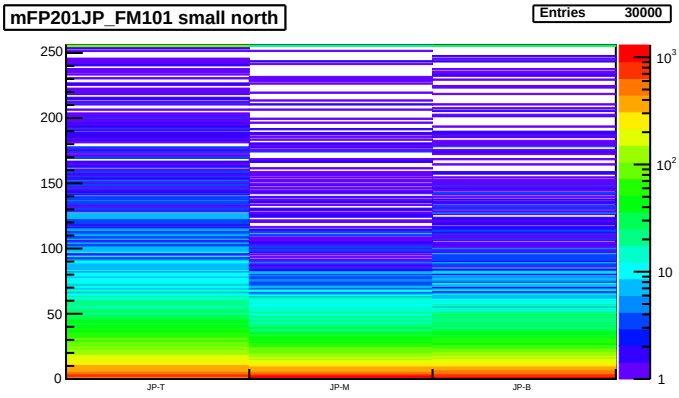

mFP201BSsim_FM102 small south
Entries 26256
1
0
-1
505
1517
3003
226
82
4860
3713
1264
6108
4978
BS3
BS2
BS1-T
BS1-M
BS1-B
10^3
10^2
10
1

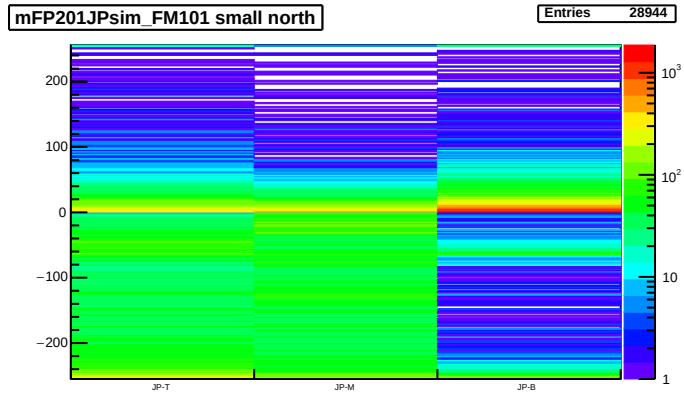

mFP201BS_FM103 large north
Entries 50000
1
0
10
107
126
137
254
9990
9893
9874
9863
9746
BS3
BS2
BS1-T
BS1-M
BS1-B
10^3
10^2

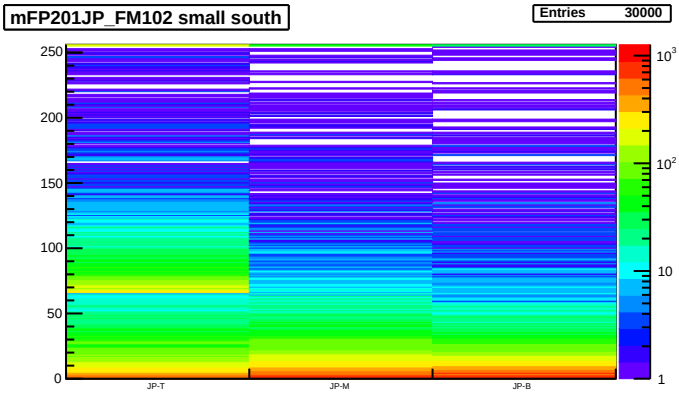

mFP201BSsim_FM103 large north
Entries 31863
1
0
-1
14
118
20
122
8469
8466
1243
8556
4855
BS3
BS2
BS1-T
BS1-M
BS1-B
10^3
10^2
10
1

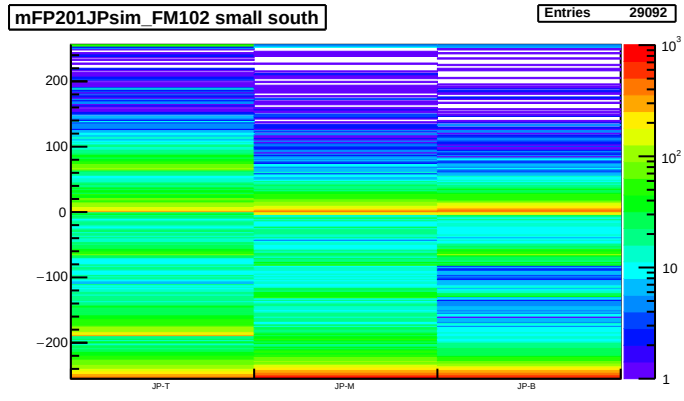

mFP201BS_FM104 large south
Entries 50000
1
0
9
37
82
11
26
9991
9963
9918
9989
9974
BS3
BS2
BS1-T
BS1-M
BS1-B
10^3
10^2
10

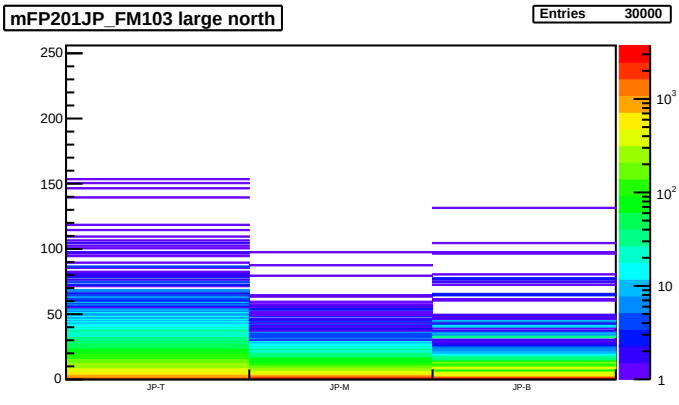

mFP201BSsim_FM104 large south
Entries 12533
1
0
-1
6
32
63
8
24
3678
2121
2136
4296
169
BS3
BS2
BS1-T
BS1-M
BS1-B
10^3
10^2
10
1

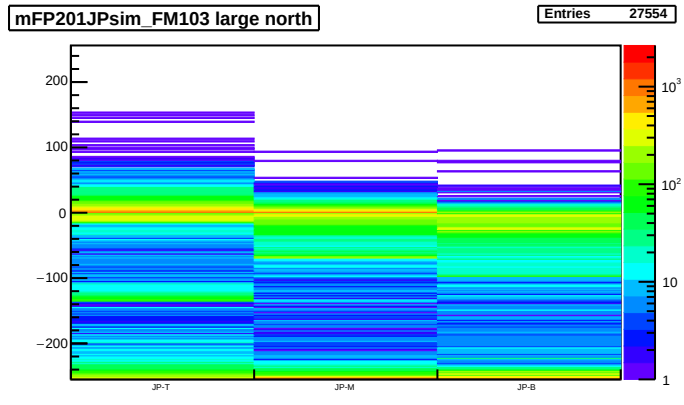

mFP201JP_FM101 small north
Entries 30000
JP-T
JP-M
JP-B

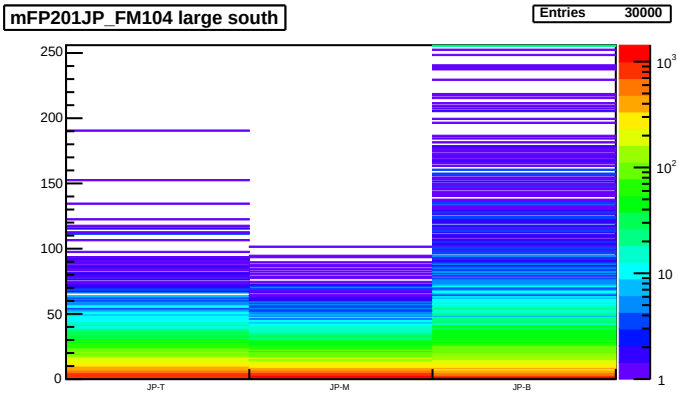

mFP201JPsim_FM101 small north
Entries 28944
JP-T
JP-M
JP-B

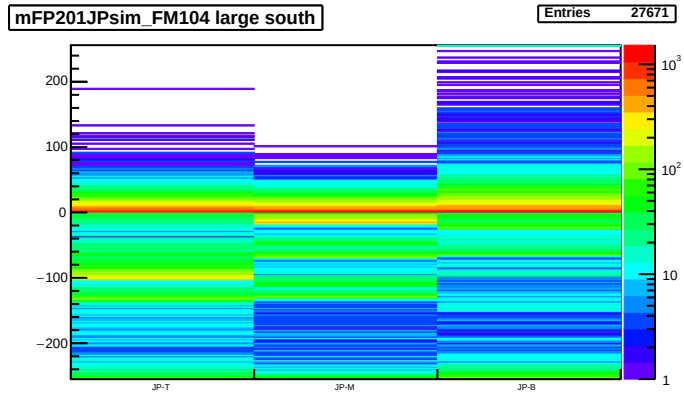

mFP201JP_FM102 small south
Entries 30000
JP-T
JP-M
JP-B

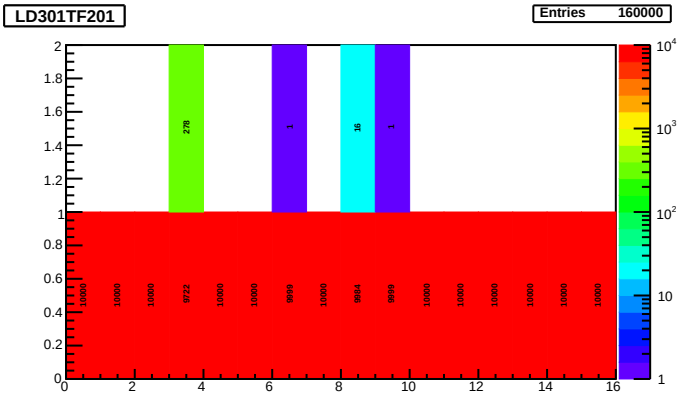

mFP201JPsim_FM102 small south
Entries 29092
JP-T
JP-M
JP-B

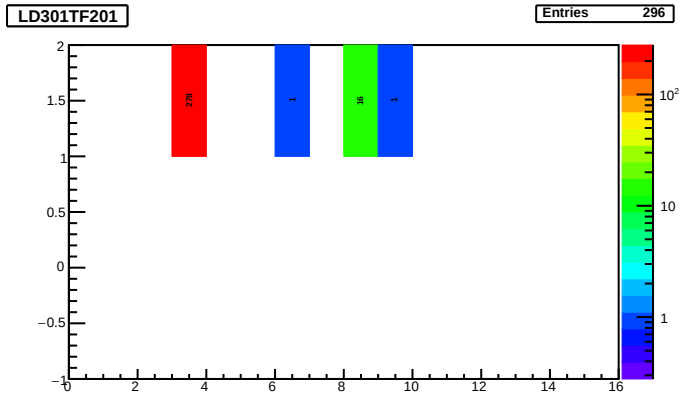

mFP201JP_FM103 large north
Entries 30000
JP-T
JP-M
JP-B

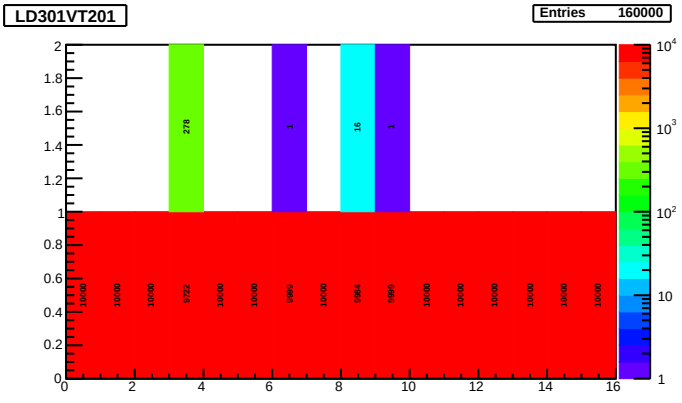

mFP201JPsim_FM103 large north
Entries 27554
JP-T
JP-M
JP-B

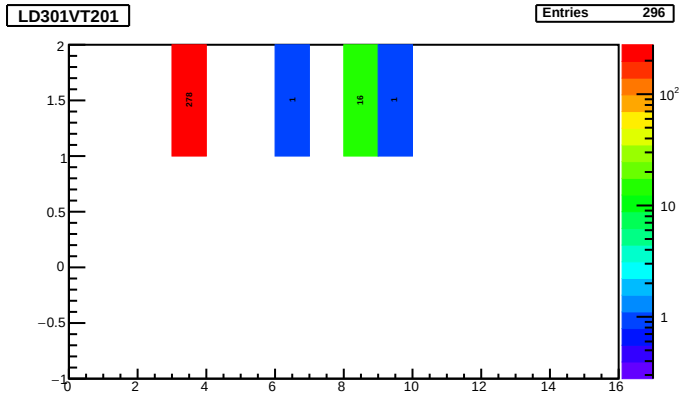

mFP201JP_FM104 large south
Entries 30000
JP-T
JP-M
JP-B

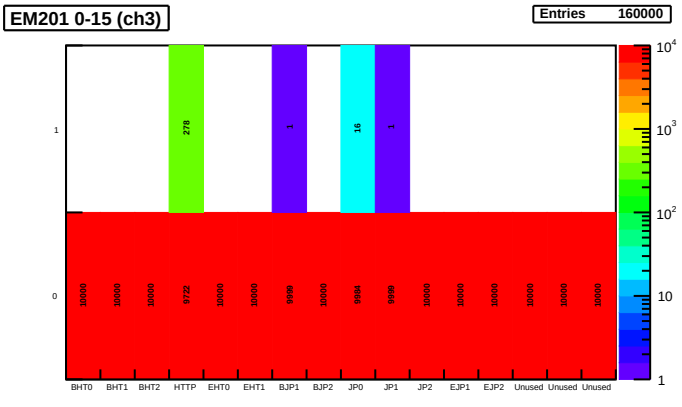

mFP201JPsim_FM104 large south
Entries 27671
JP-T
JP-M
JP-B

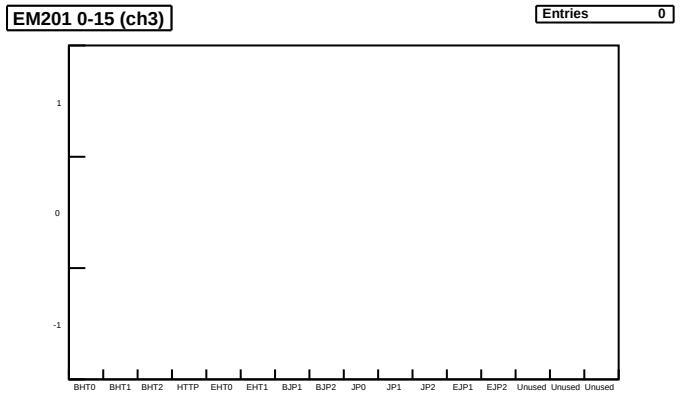

LD301TF201
Entries 160000

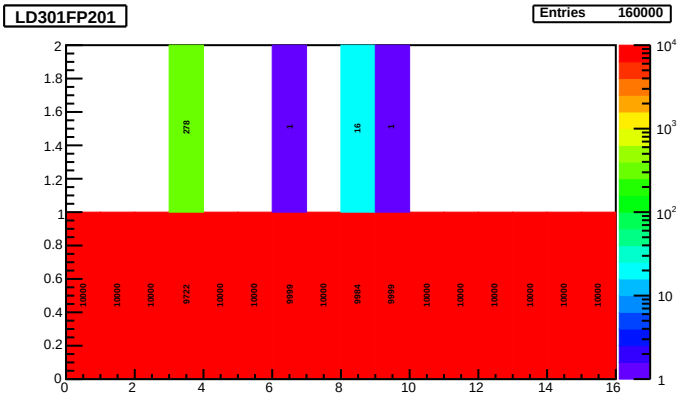

LD301TF201
Entries 296

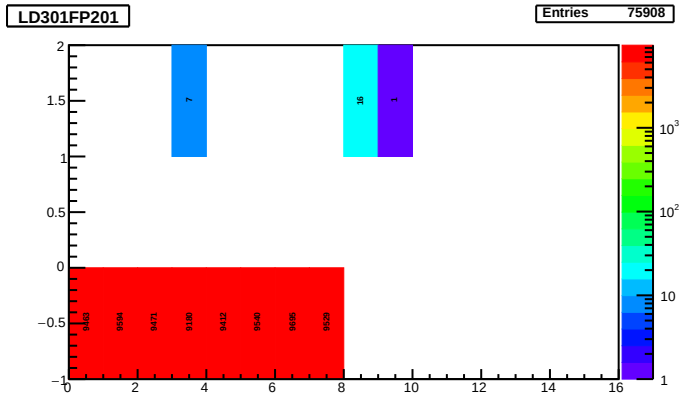

LD301VT201
Entries 160000


LD301VT201
Entries 296


EM201 0-15 (ch3)
Entries 160000
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


LD301FP201
Entries 160000


LD301FP201
Entries 75908