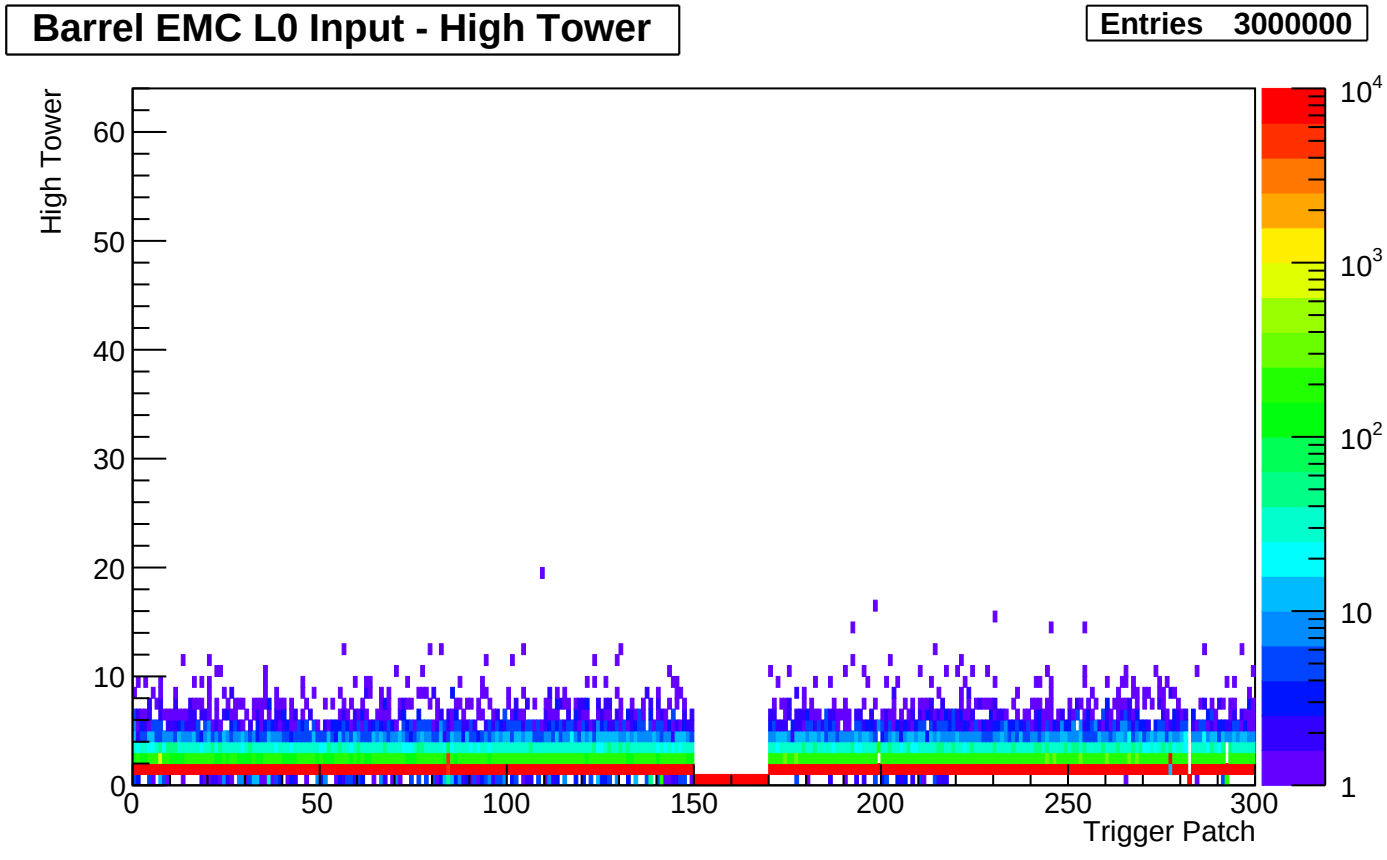
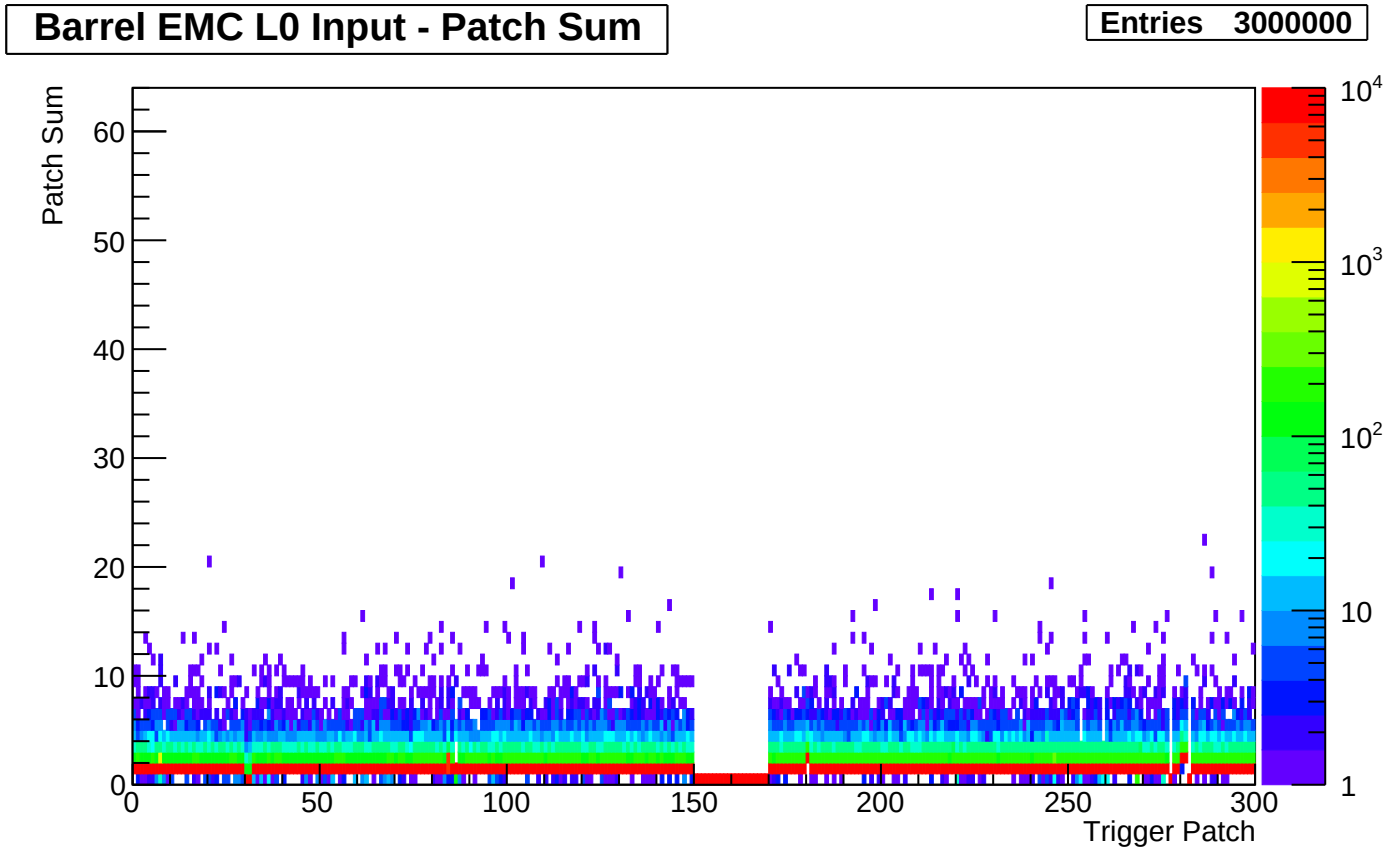
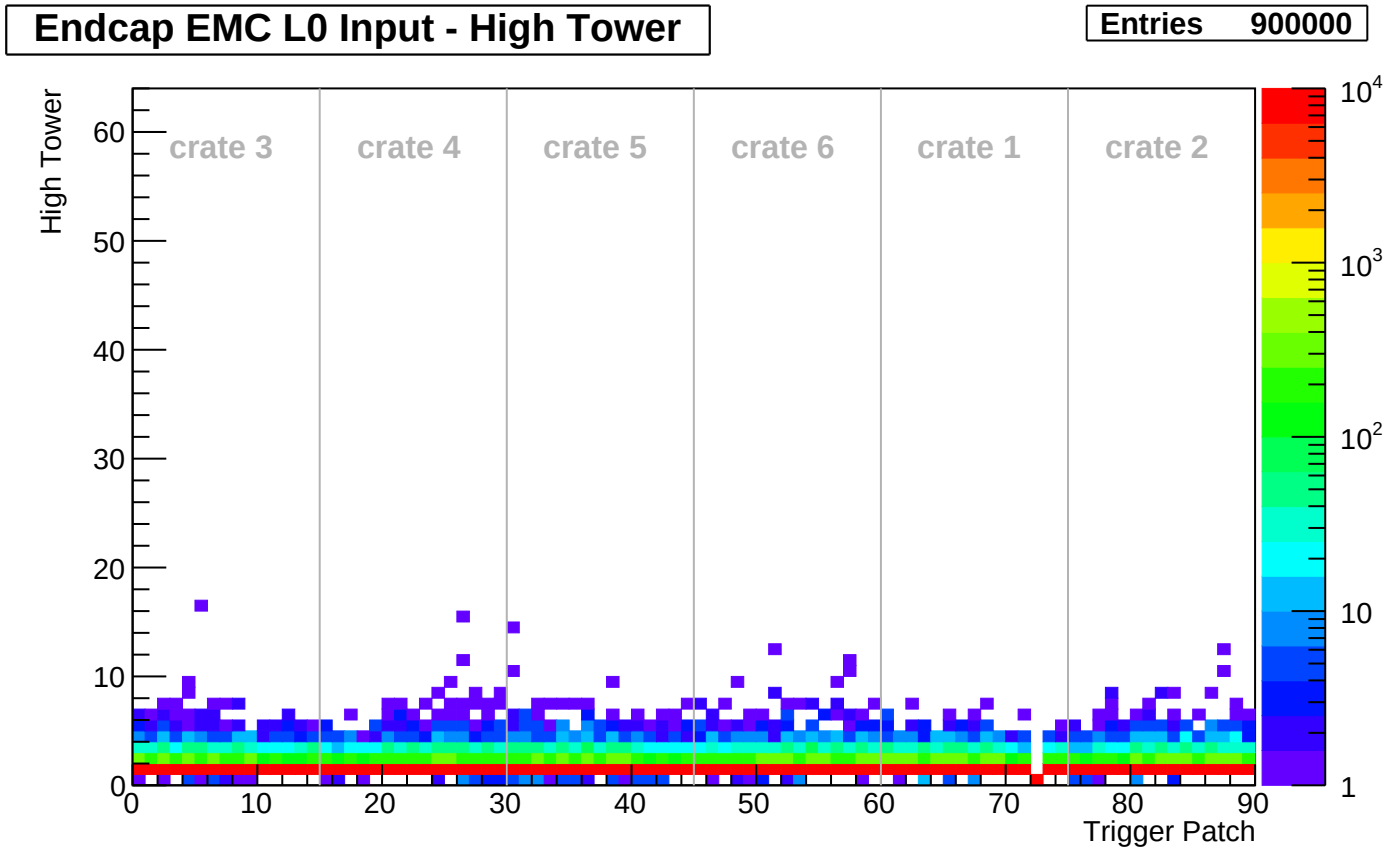
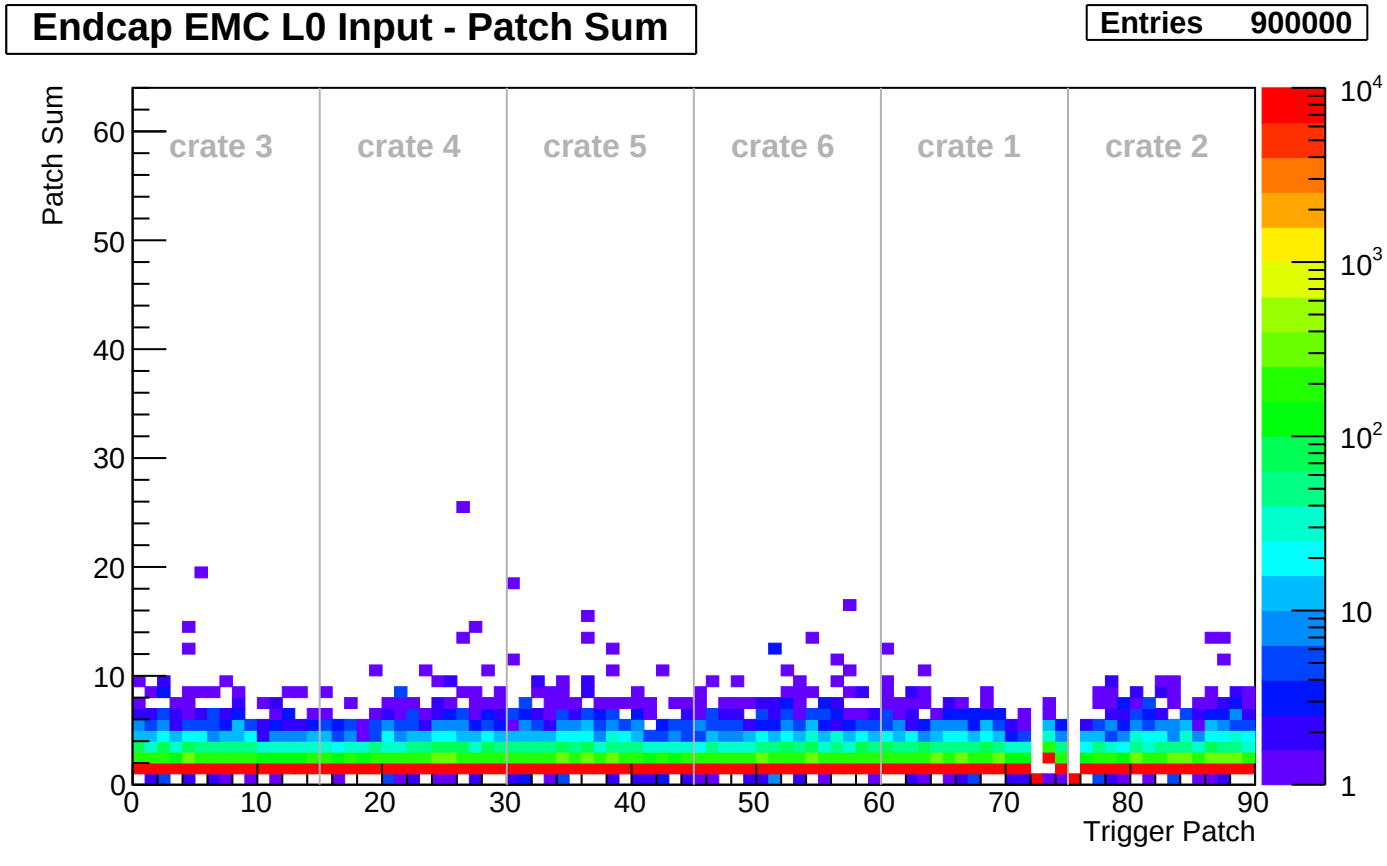
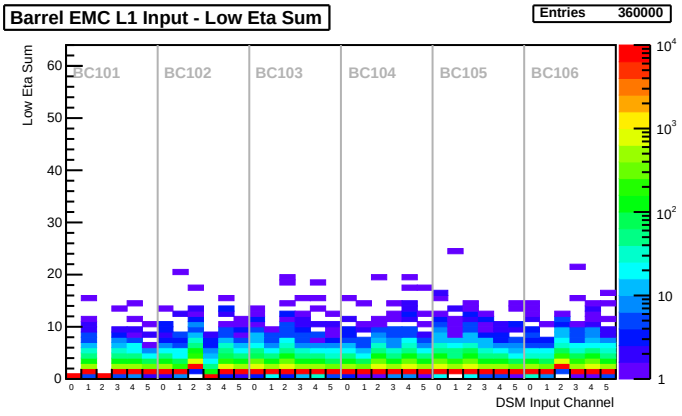
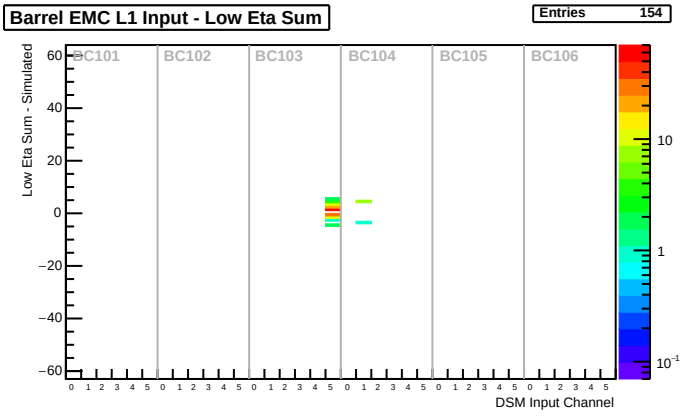

Barrel EMC L0 Input - High Tower
Entries 3000000
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

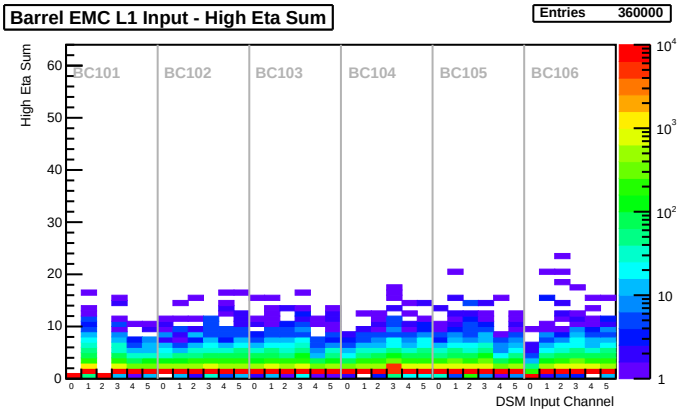

Barrel EMC L0 Input - Patch Sum
Entries 3000000
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

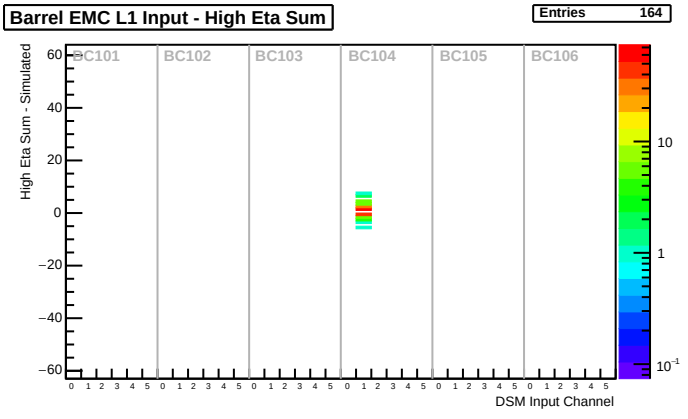

Endcap EMC L0 Input - High Tower
Entries 900000
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

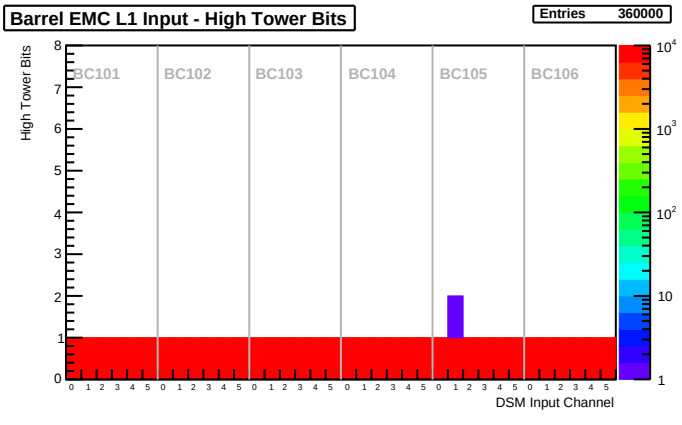

Endcap EMC L0 Input - Patch Sum
Entries 900000
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

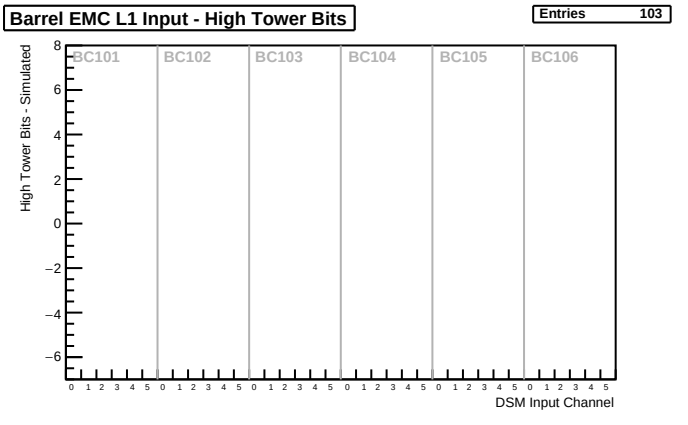

Barrel EMC L1 Input - Low Eta Sum
Entries 360000
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

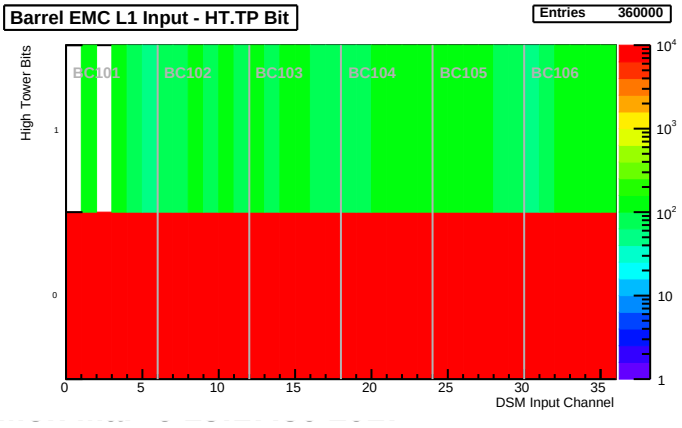

Barrel EMC L1 Input - Low Eta Sum
Entries 154
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

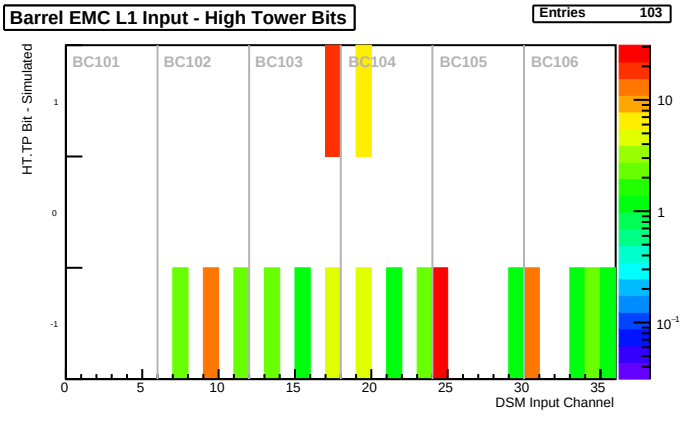

Barrel EMC L1 Input - High Eta Sum
Entries 360000
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

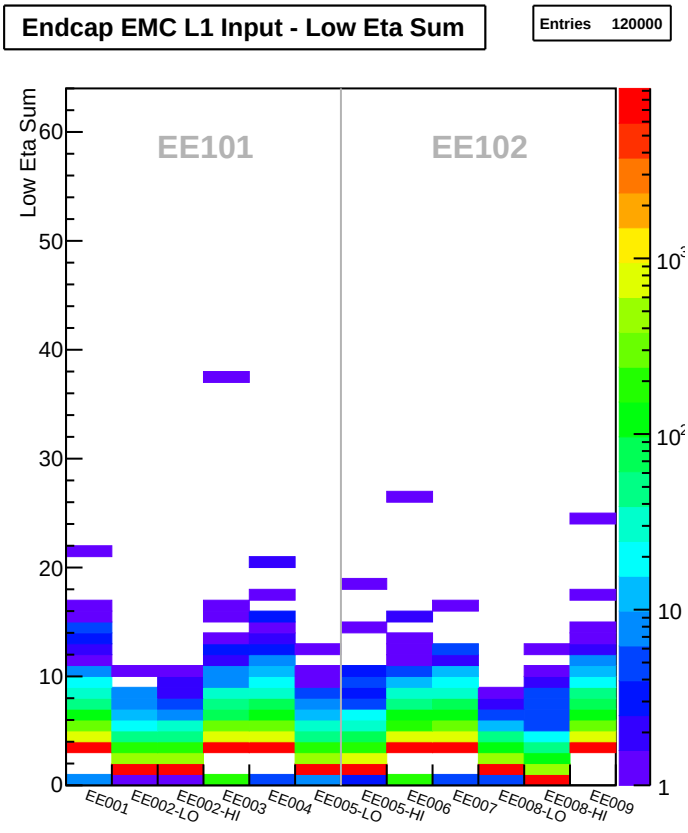

Barrel EMC L1 Input - High Eta Sum
Entries 164
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

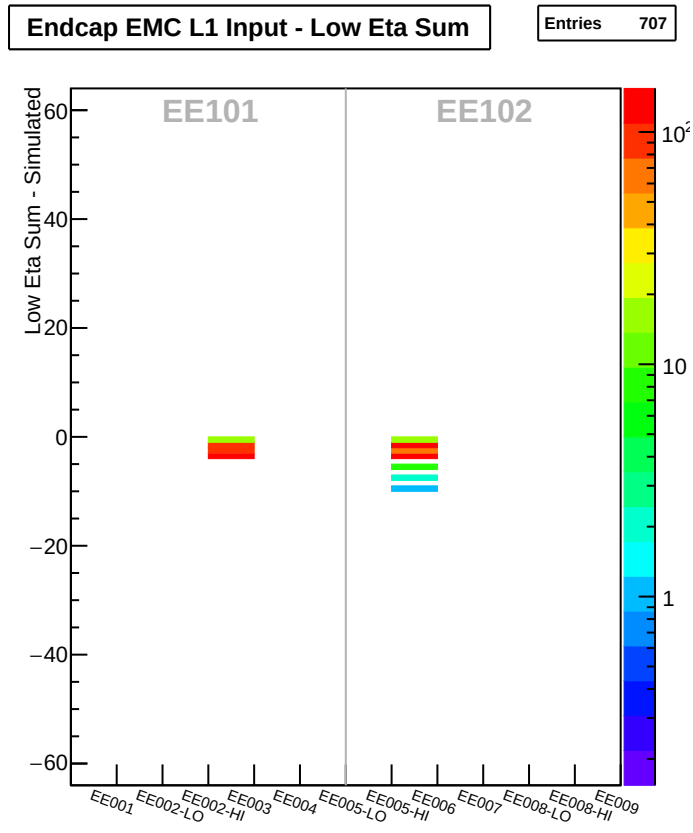

Barrel EMC L1 Input - High Tower Bits
Entries 360000
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

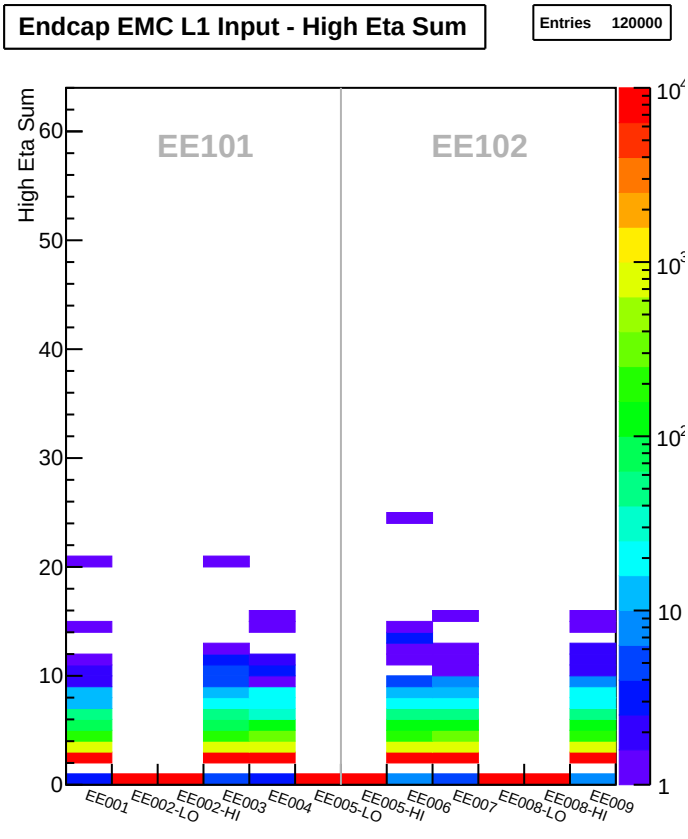

Barrel EMC L1 Input - High Tower Bits
Entries 103
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

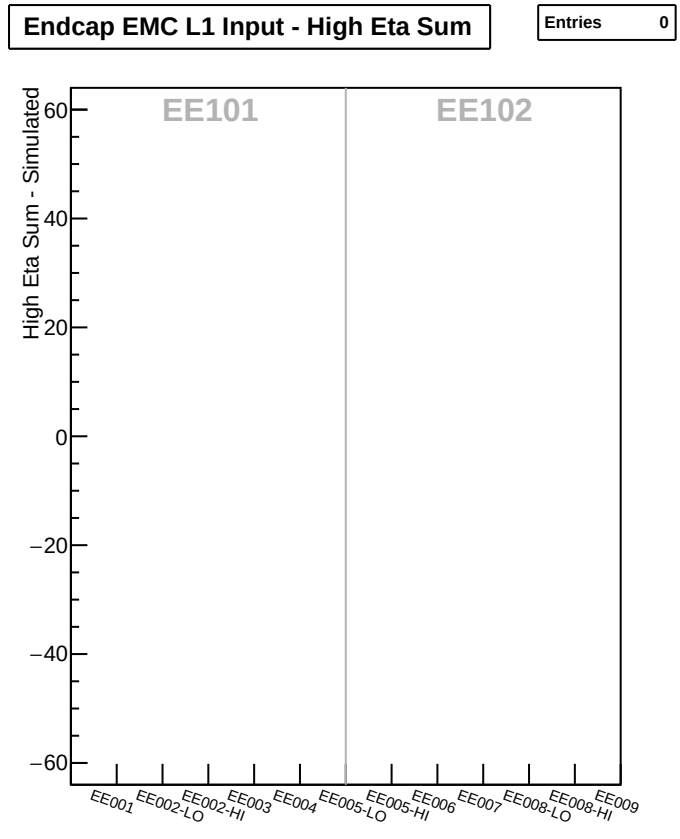

Barrel EMC L1 Input - HT.TP Bit
Entries 360000
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

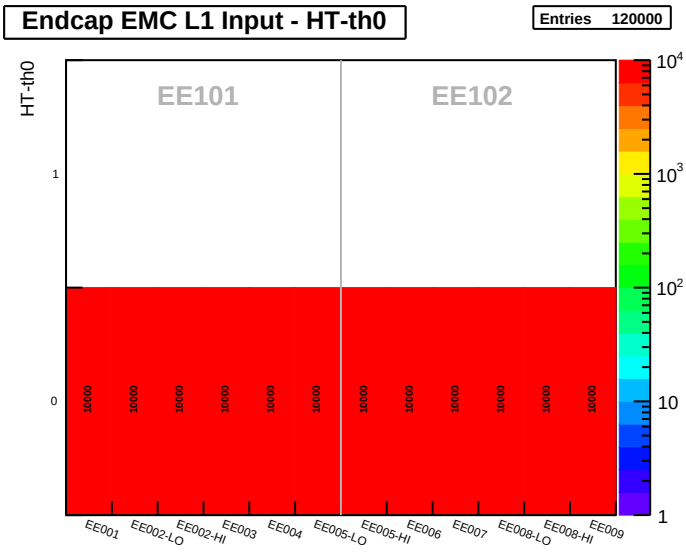

Barrel EMC L1 Input - High Tower Bits
Entries 103
HT.TP Bit - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

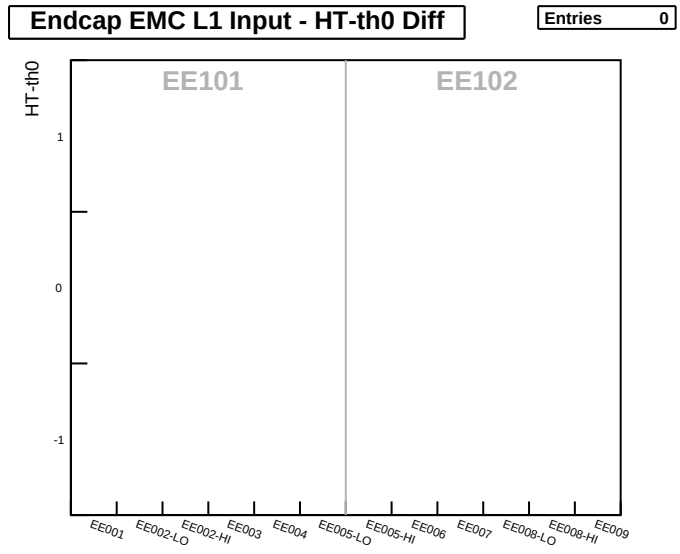

Endcap EMC L1 Input - Low Eta Sum
Entries 120000
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

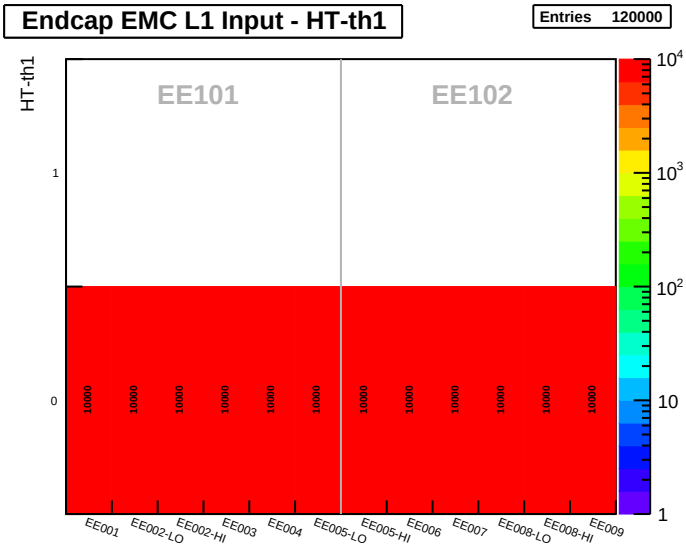

Endcap EMC L1 Input - Low Eta Sum
Entries 707
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

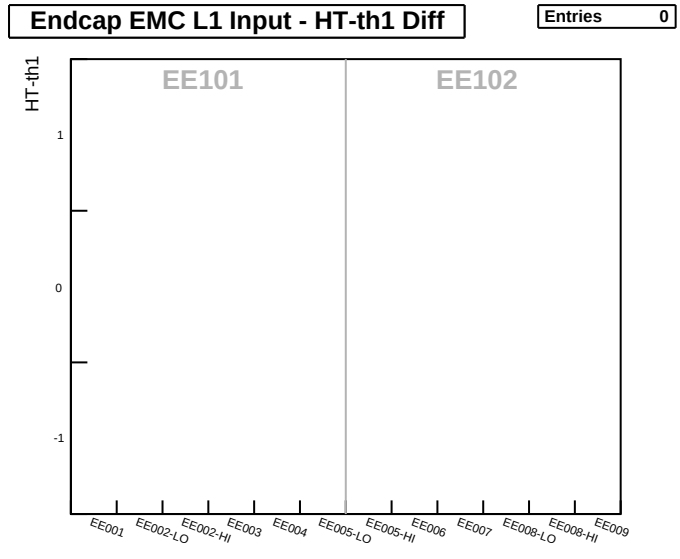

Endcap EMC L1 Input - High Eta Sum
Entries 120000
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

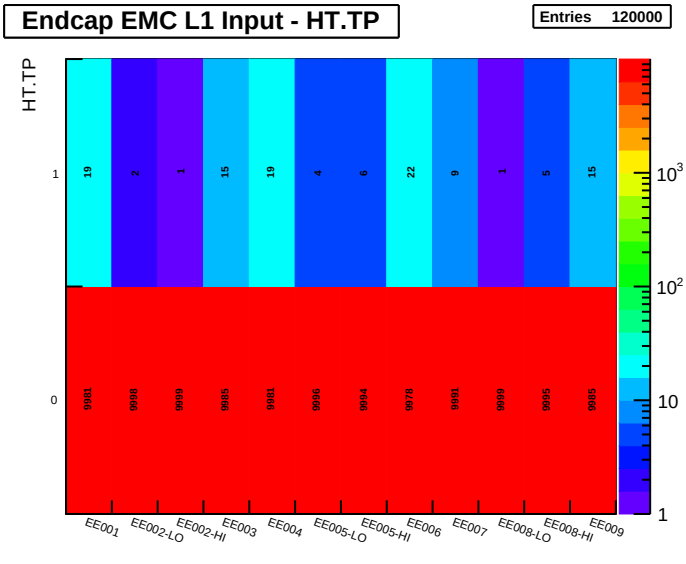

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

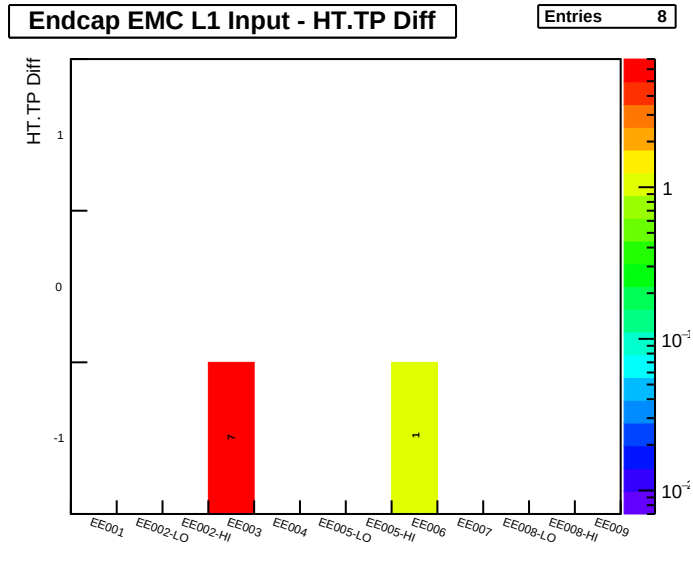

Endcap EMC L1 Input - HT-th0
Entries 120000
HT-th0
EE101
EE102
1
0
10000
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009
10^4
10^3
10^2
10
1

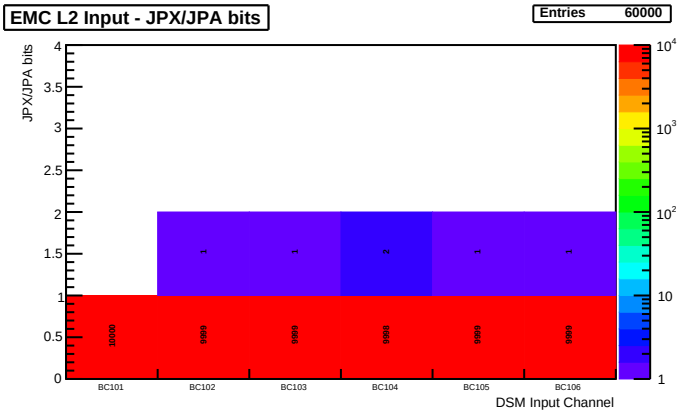

Endcap EMC L1 Input - HT-th0 Diff
Entries 0
HT-th0
EE101
EE102
1
0
-1
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

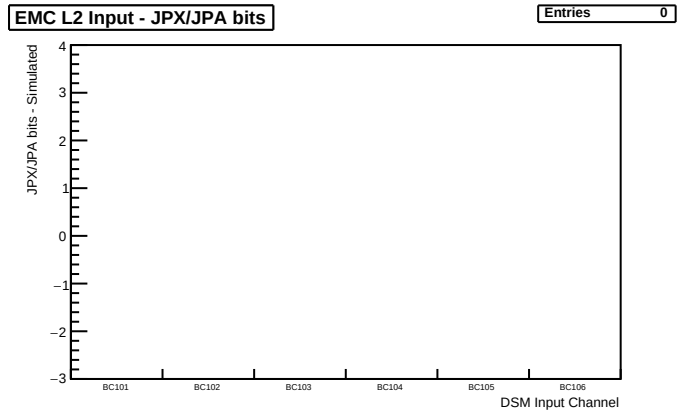

Endcap EMC L1 Input - HT-th1
Entries 120000
HT-th1
EE101
EE102
1
0
10000
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009
10^4
10^3
10^2
10
1

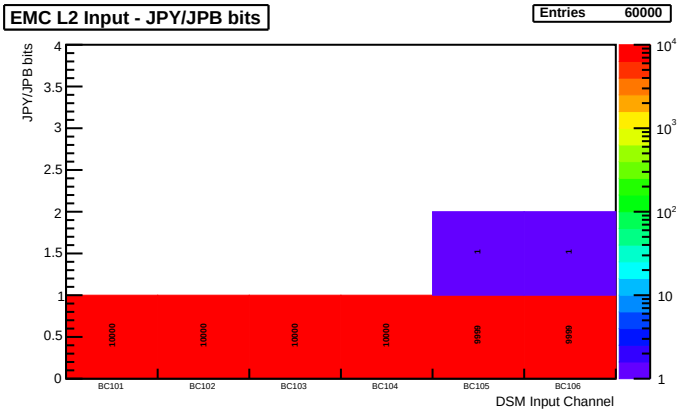

Endcap EMC L1 Input - HT-th1 Diff
Entries 0
HT-th1
EE101
EE102
1
0
-1
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

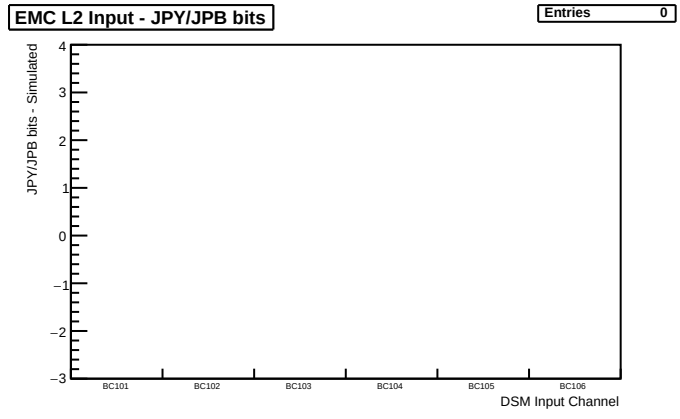

Endcap EMC L1 Input - HT.TP
Entries 120000
HT.TP
1
0
19 2 1 15 19 4 6 22 9 1 5 15
9981 9998 9999 9985 9981 9996 9994 9978 9991 9999 9995 9985
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009
10^3
10^2
10
1

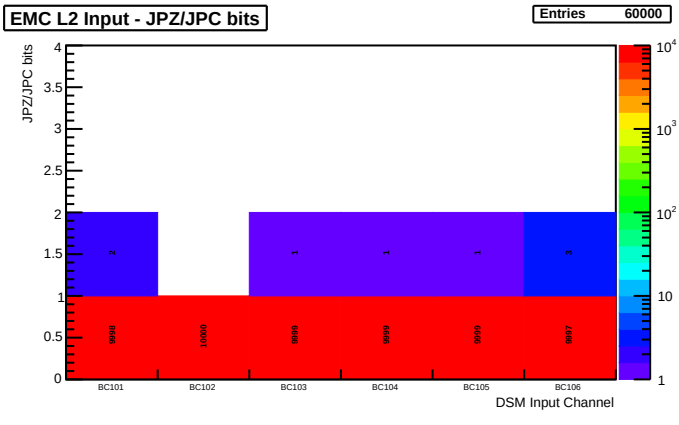

Endcap EMC L1 Input - HT.TP Diff
Entries 8
HT.TP Diff
1
0
-1
7
1
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009
1

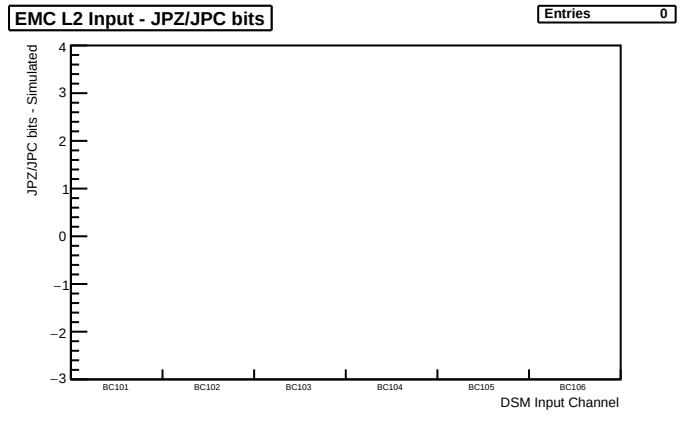

EMC L2 Input - JPX/JPA bits
Entries 60000
JPX/JPA bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

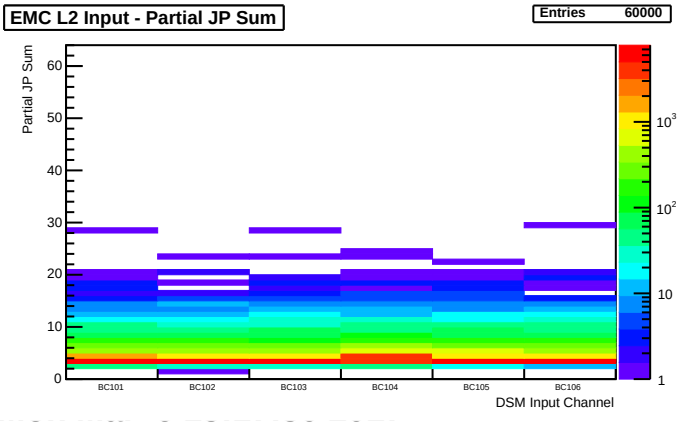

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

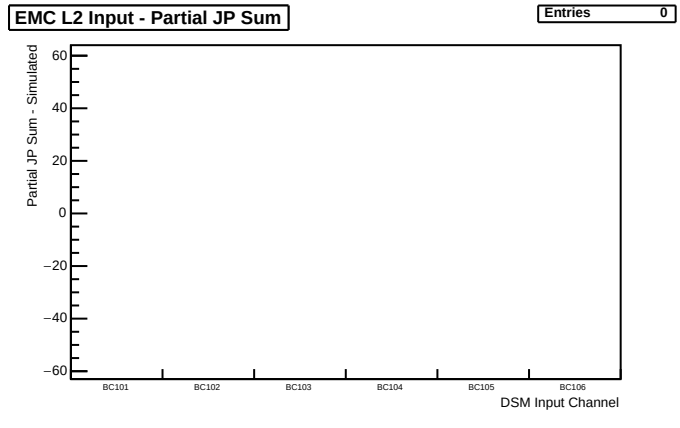

EMC L2 Input - JPY/JPB bits
Entries 60000
JPY/JPB bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

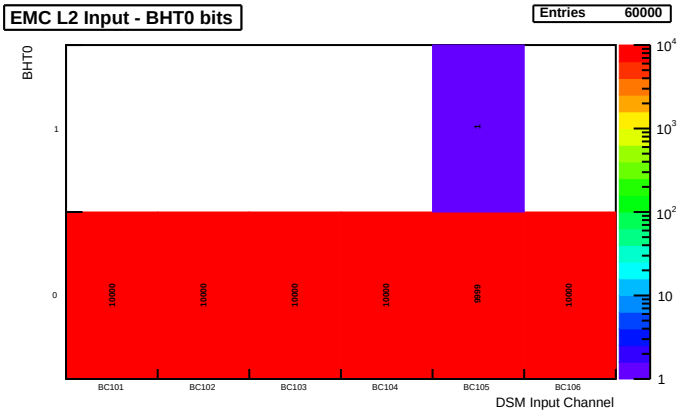

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

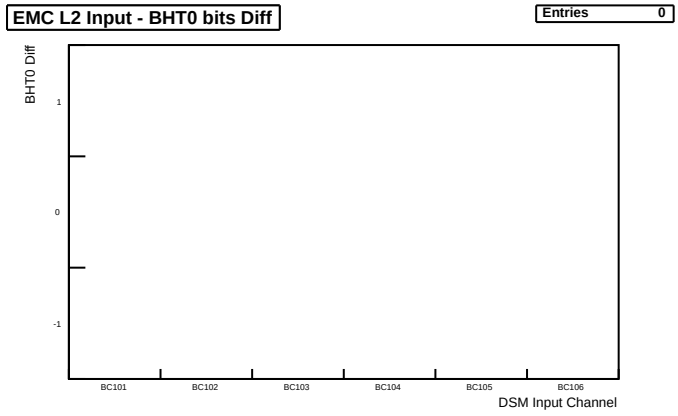

EMC L2 Input - JPZ/JPC bits
Entries 60000
JPZ/JPC bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

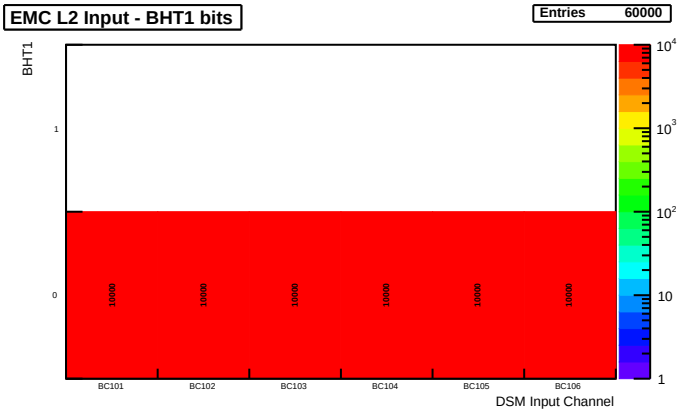

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

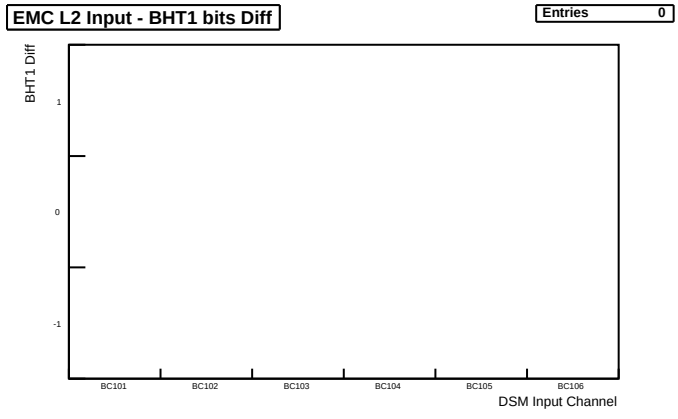

EMC L2 Input - Partial JP Sum
Entries 60000
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

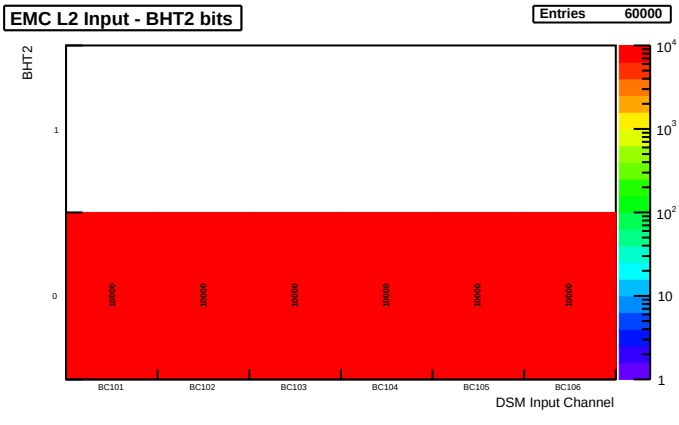

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

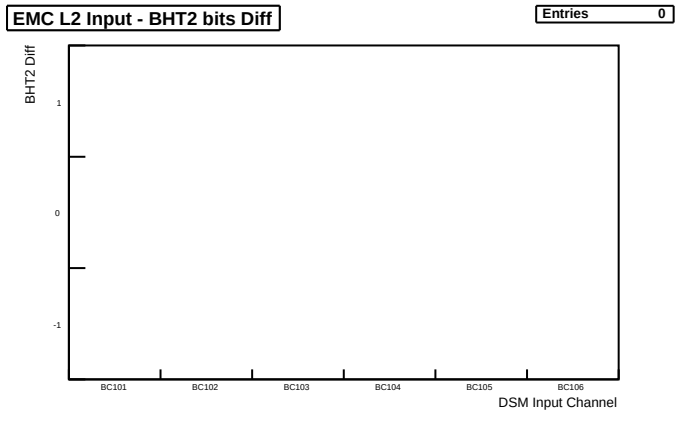

EMC L2 Input - BHT0 bits
Entries 60000
BHT0
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

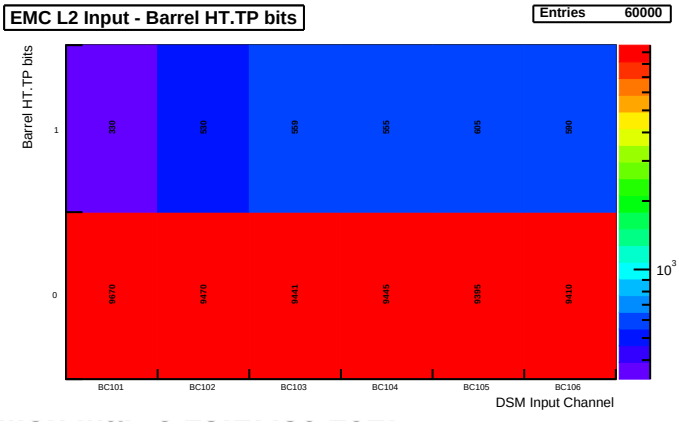

EMC L2 Input - BHT0 bits Diff
Entries 0
BHT0 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

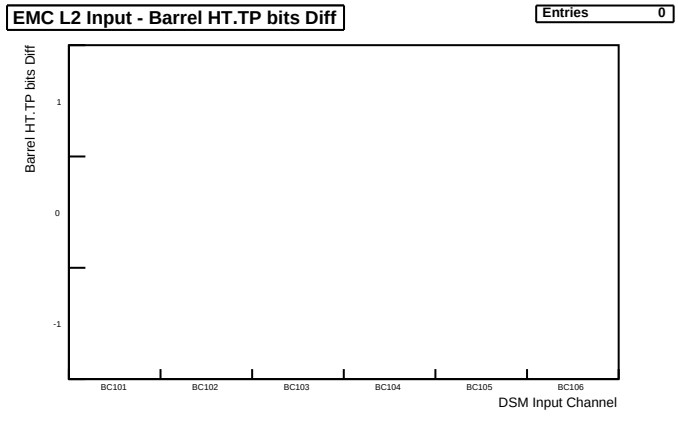

EMC L2 Input - BHT1 bits
Entries 60000
BHT1
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

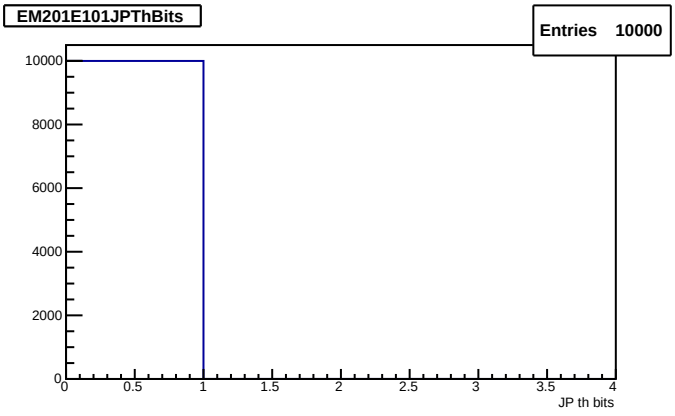

EMC L2 Input - BHT1 bits Diff
Entries 0
BHT1 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

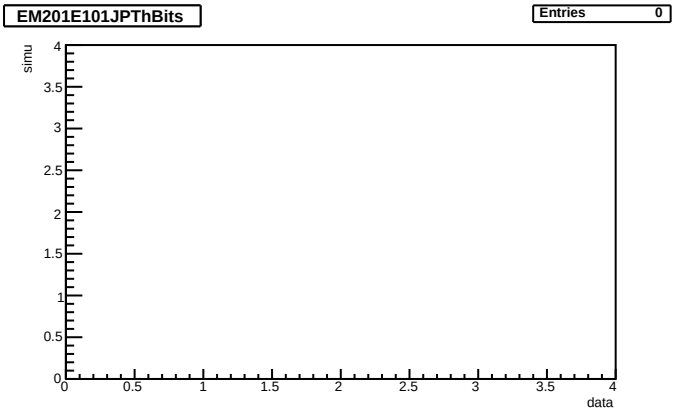

EMC L2 Input - BHT2 bits
Entries 60000
BHT2
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

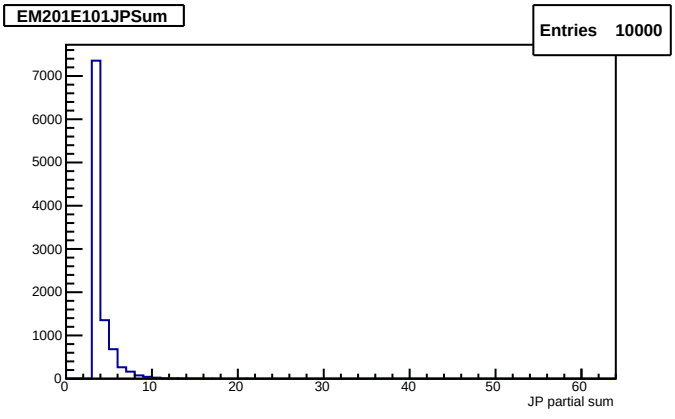

EMC L2 Input - BHT2 bits Diff
Entries 0
BHT2 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

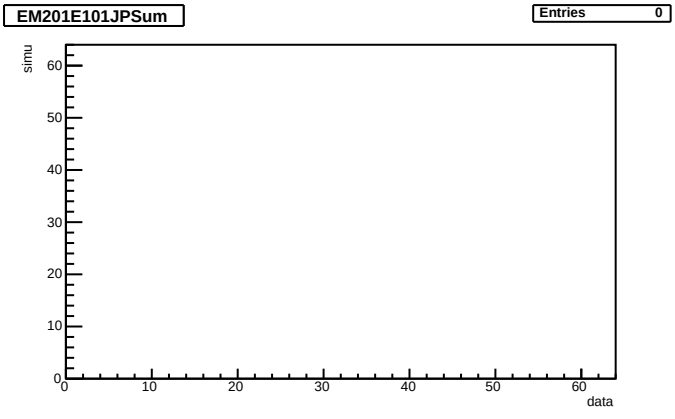

EMC L2 Input - Barrel HT.TP bits
Entries 60000
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

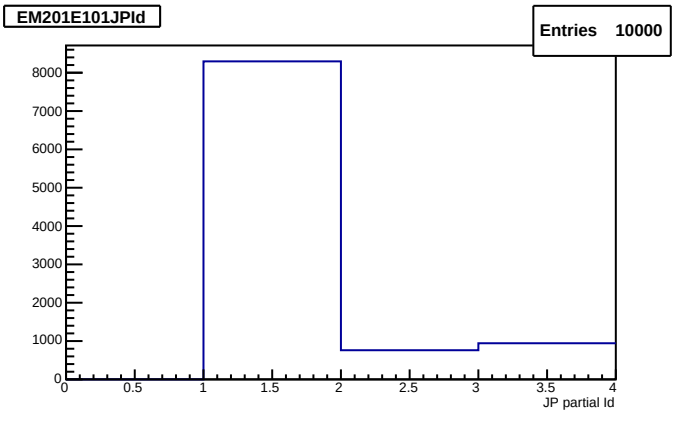

EMC L2 Input - Barrel HT.TP bits Diff
Entries 0
Barrel HT.TP bits Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

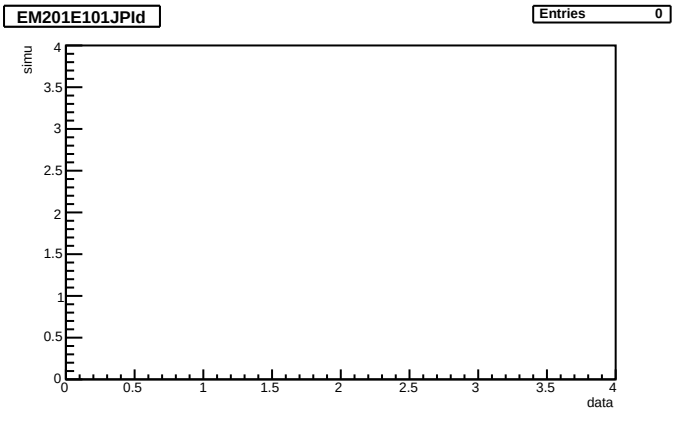

EM201E101JPThBits
Entries 10000
JP th bits

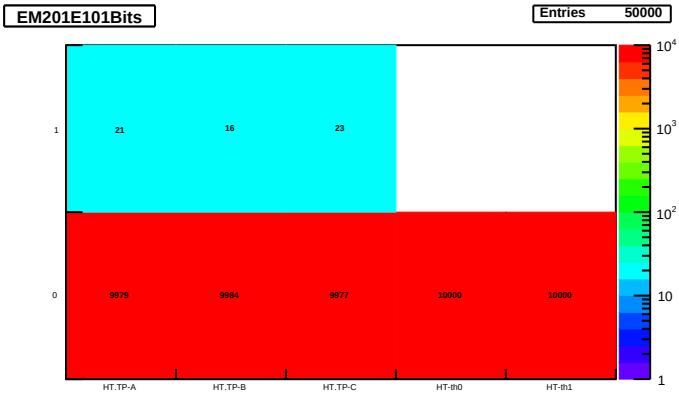

EM201E101JPThBits
Entries 0
simu
data

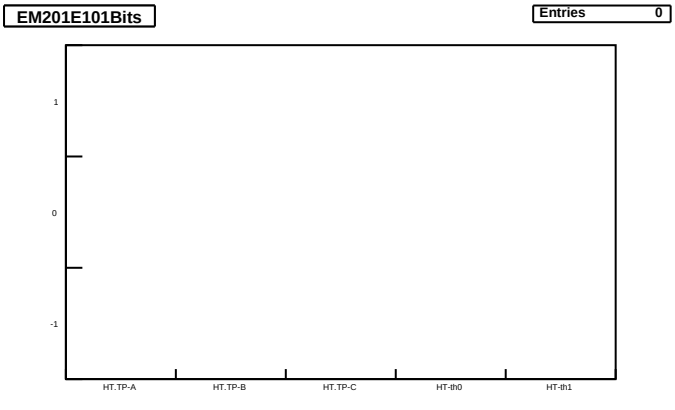

EM201E101JPSum
Entries 10000
JP partial sum

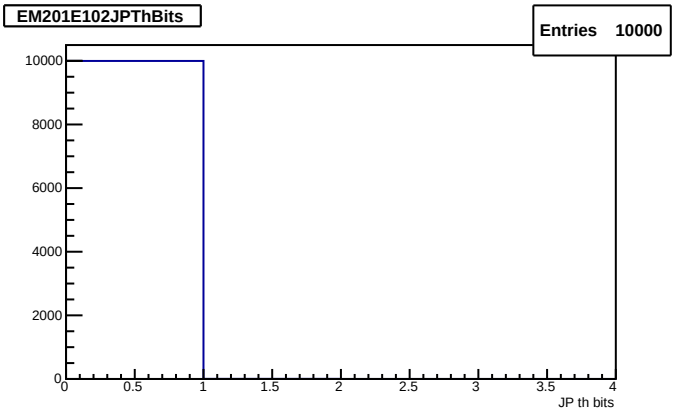

EM201E101JPSum
Entries 0
simu
data

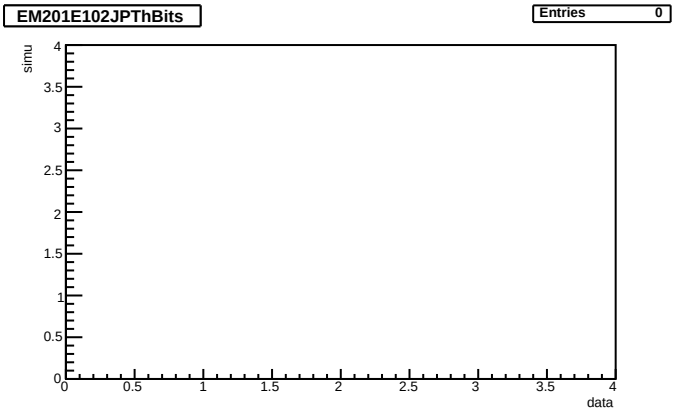

EM201E101JPId
Entries 10000
JP partial Id

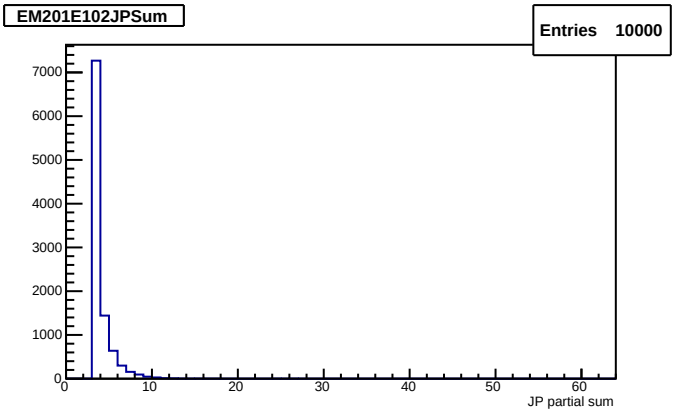

EM201E101JPId
Entries 0
simu
data

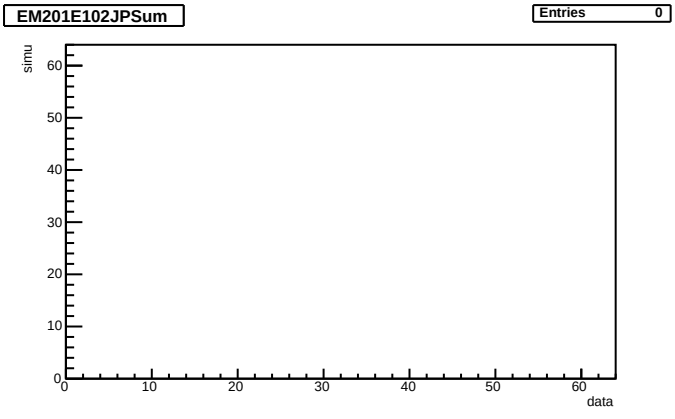

EM201E101Bits
Entries 50000
21
16
23
9979
9984
9977
10000
10000
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

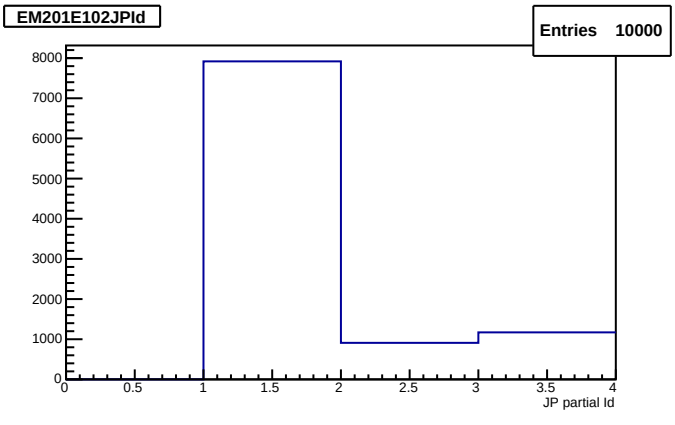

EM201E101Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

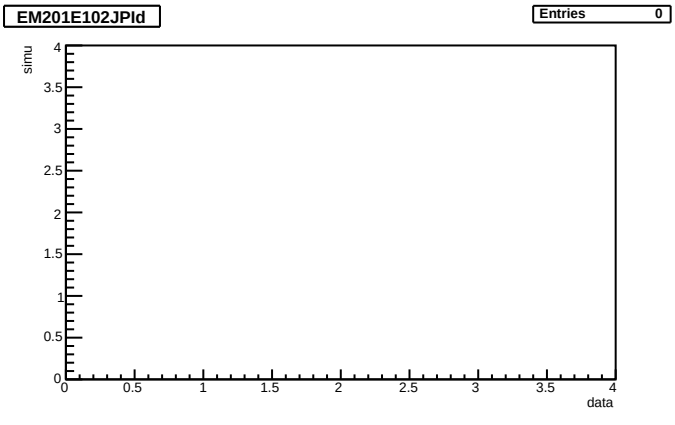

EM201E102JPThBits
Entries 10000
JP th bits

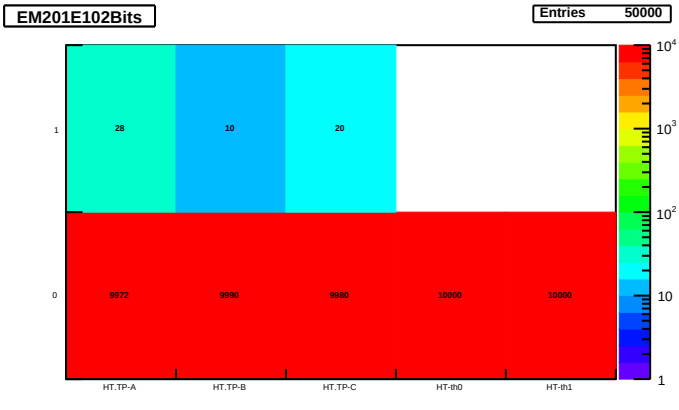

EM201E102JPThBits
Entries 0
simu
data

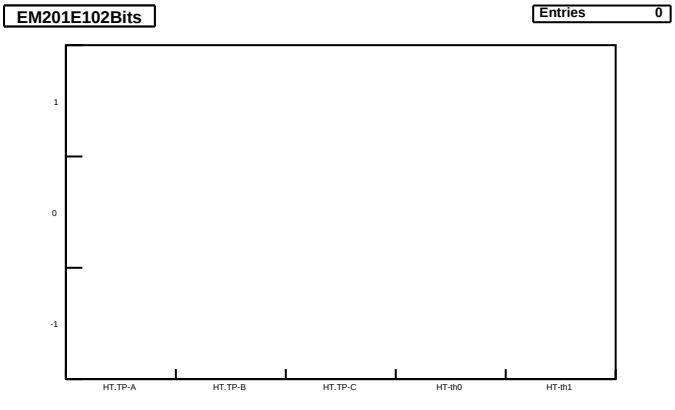

EM201E102JPSum
Entries 10000
JP partial sum

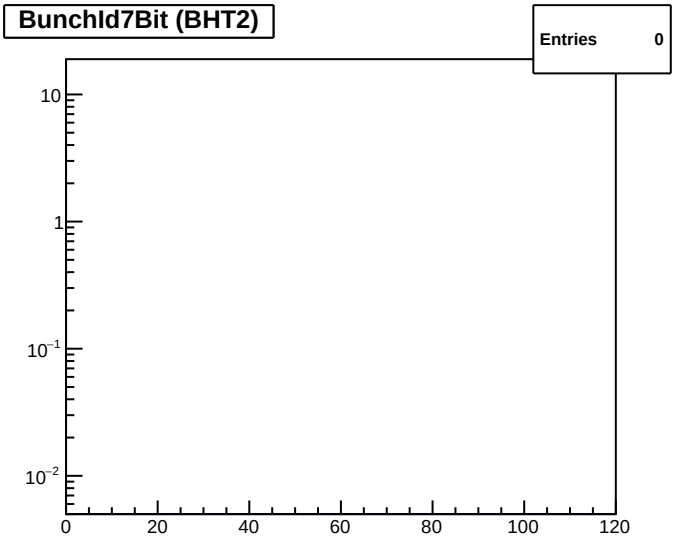

EM201E102JPSum
Entries 0
simu
data

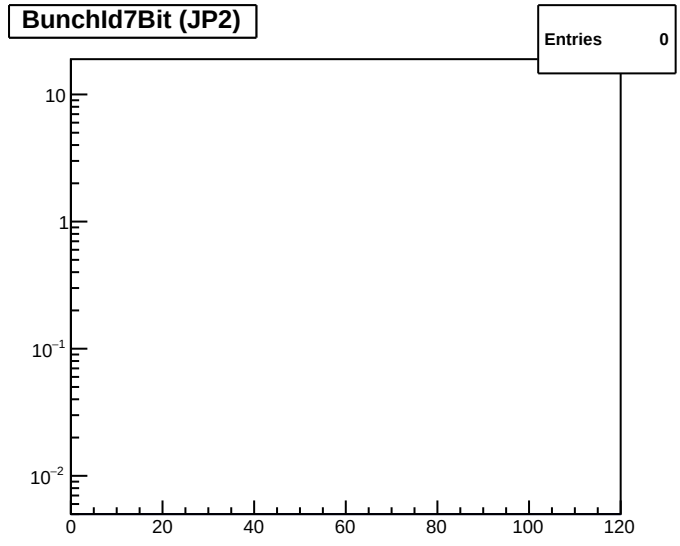

EM201E102JPId
Entries 10000
JP partial Id

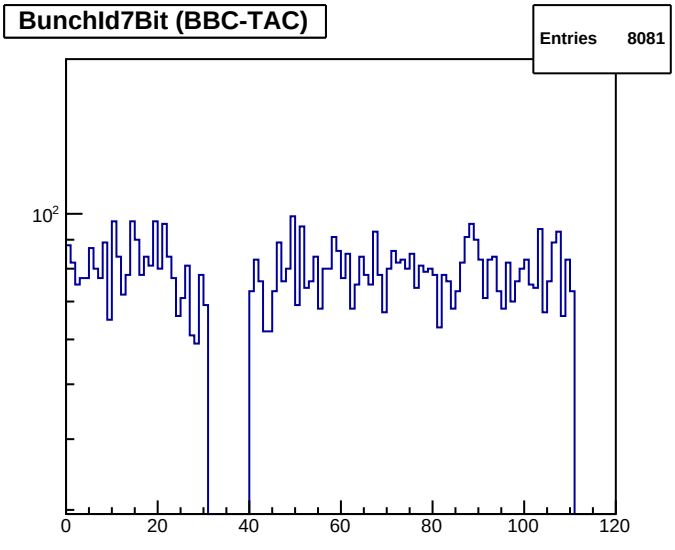

EM201E102JPId
Entries 0
simu
data

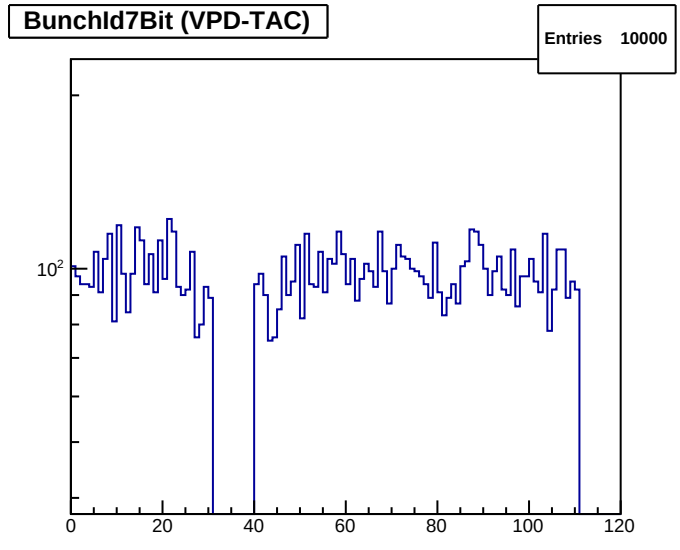

EM201E102Bits
Entries 50000
28
10
20
9972
9990
9980
10000
10000
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

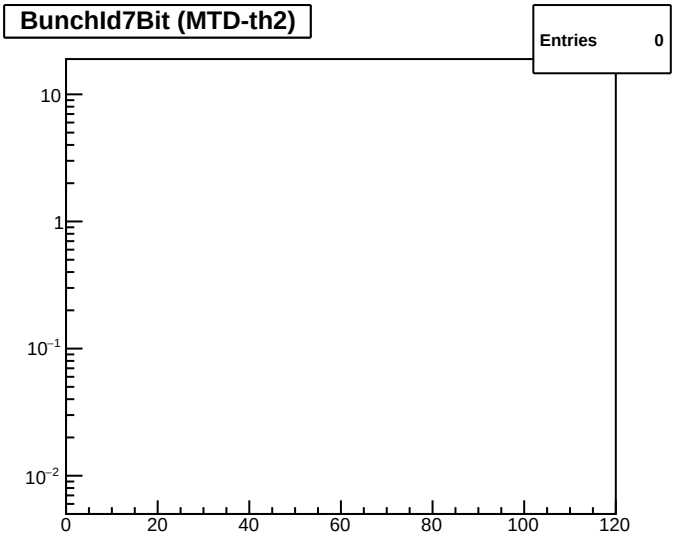

EM201E102Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

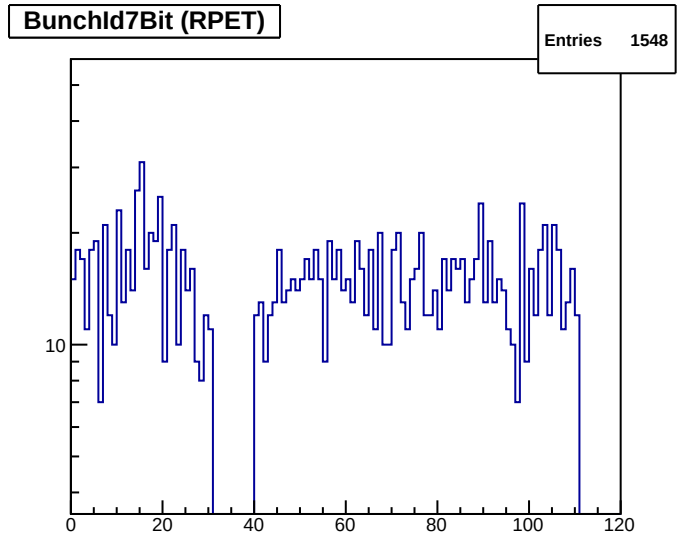

BunchId7Bit (BHT2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

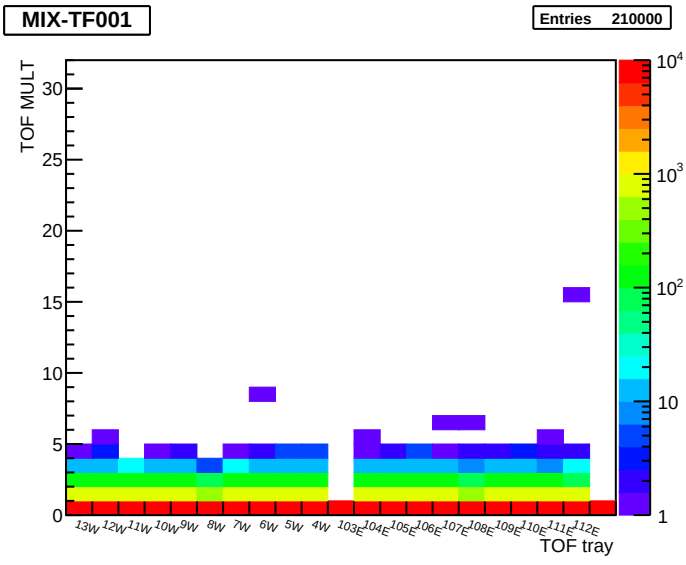

BunchId7Bit (JP2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

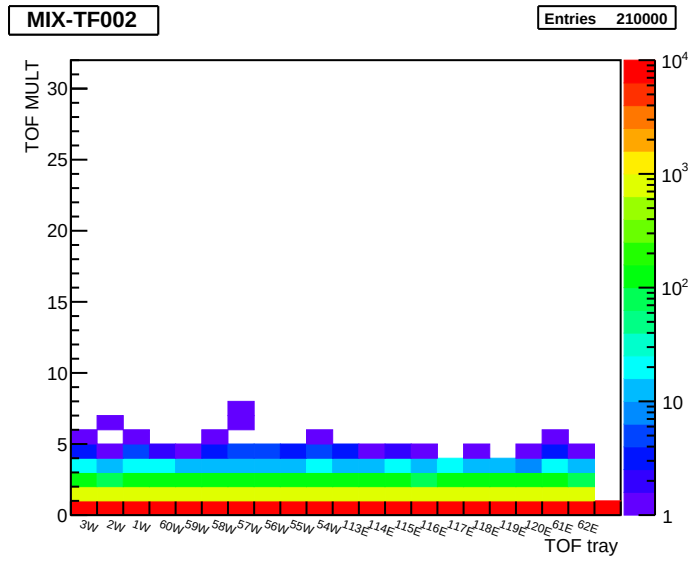

BunchId7Bit (BBC-TAC)
Entries 8081
10^{2}
0 20 40 60 80 100 120

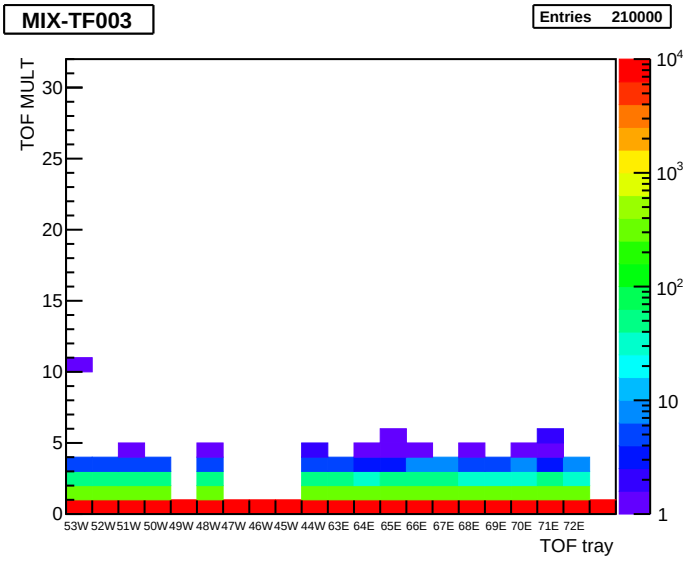

BunchId7Bit (VPD-TAC)
Entries 10000
10^{2}
0 20 40 60 80 100 120

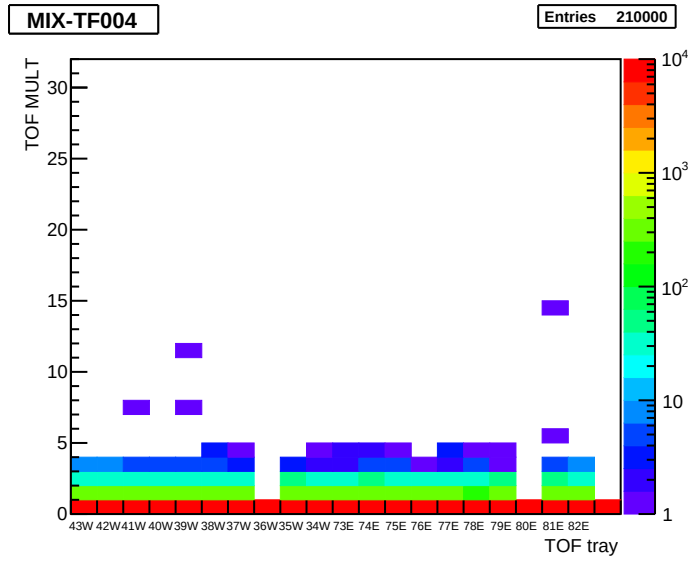

BunchId7Bit (MTD-th2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

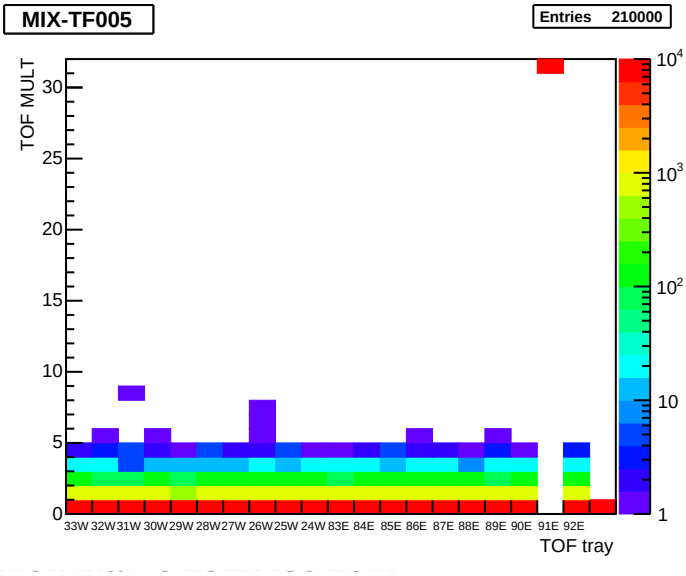

BunchId7Bit (RPET)
Entries 1548
10
0 20 40 60 80 100 120

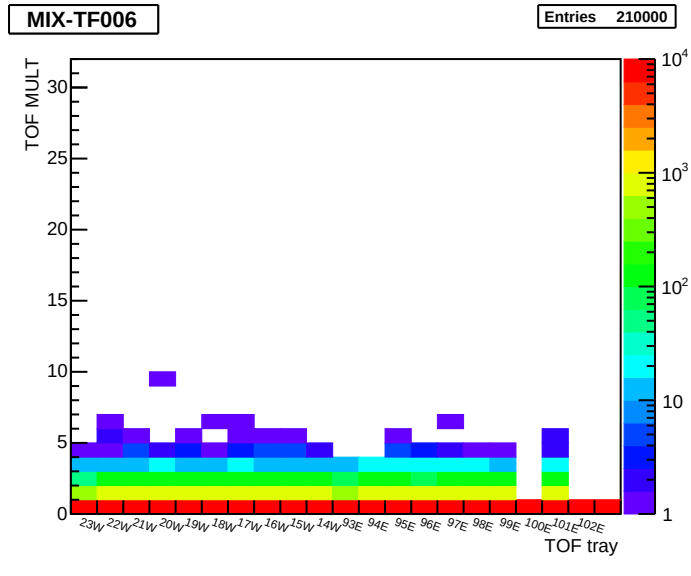

MIX-TF001
Entries 210000
TOF MULT
TOF tray

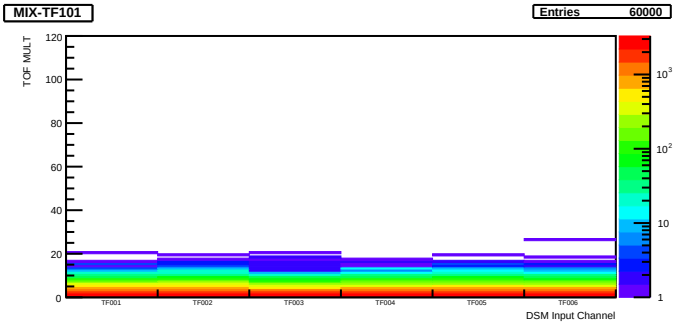

MIX-TF002
Entries 210000
TOF MULT
TOF tray

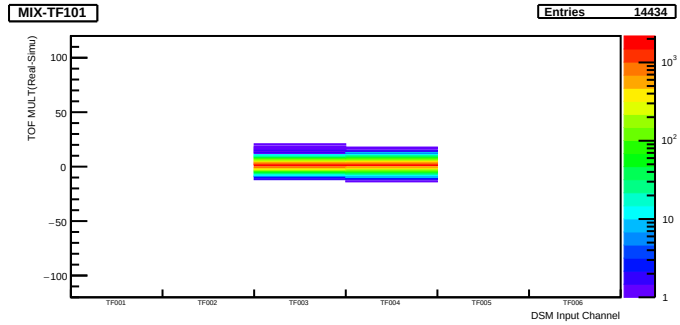

MIX-TF003
Entries 210000
TOF MULT
TOF tray

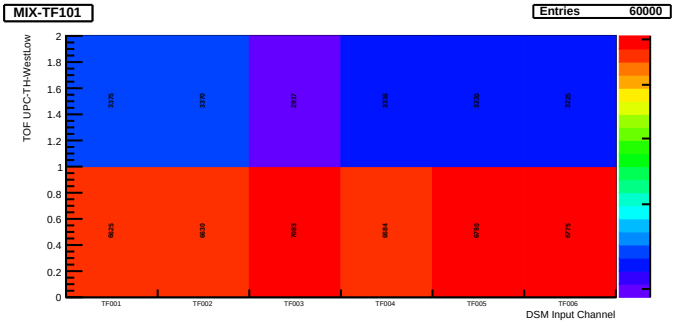

MIX-TF004
Entries 210000
TOF MULT
TOF tray

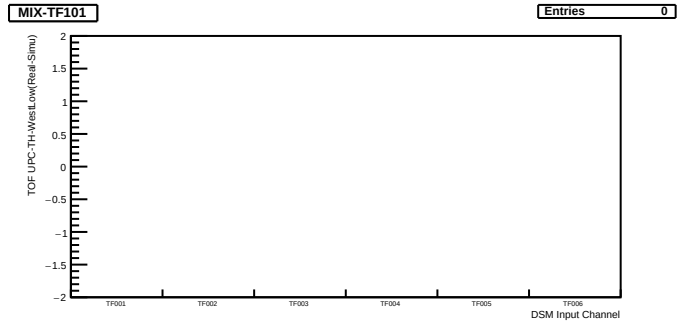

MIX-TF005
Entries 210000
TOF MULT
TOF tray

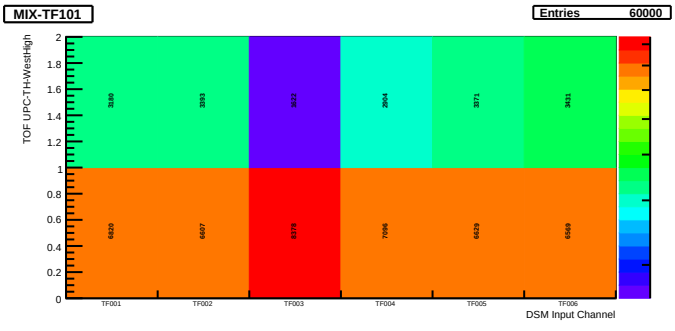

MIX-TF006
Entries 210000
TOF MULT
TOF tray

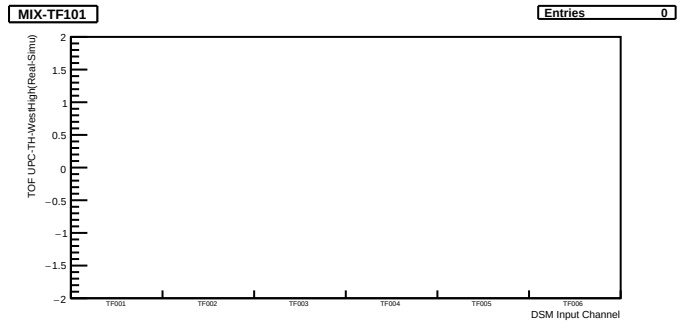

MIX-TF101
Entries 60000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

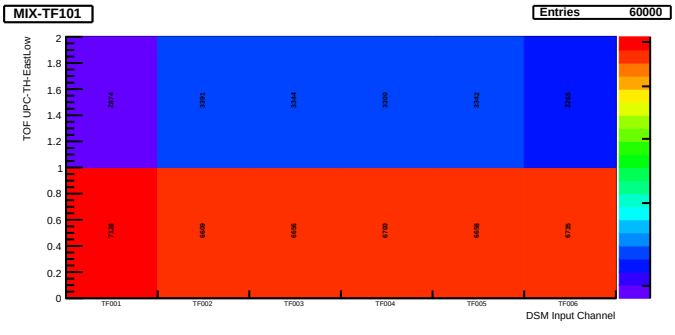

MIX-TF101
Entries 14434
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

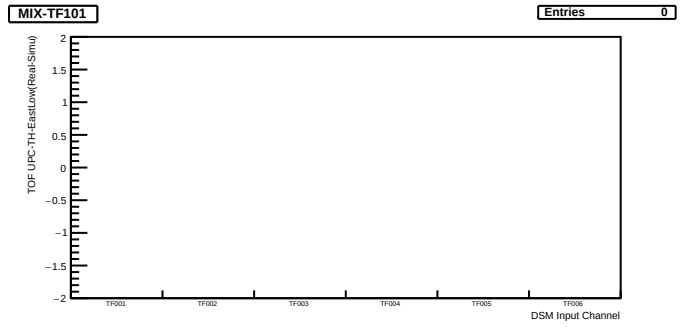

MIX-TF101
Entries 60000
TOF UPC-TH-WestLow
3375
3370
2917
3336
3220
3225
6825
6830
7083
6684
6780
6775
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

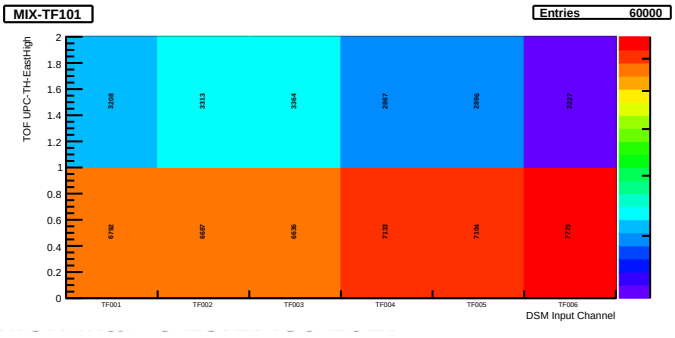

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

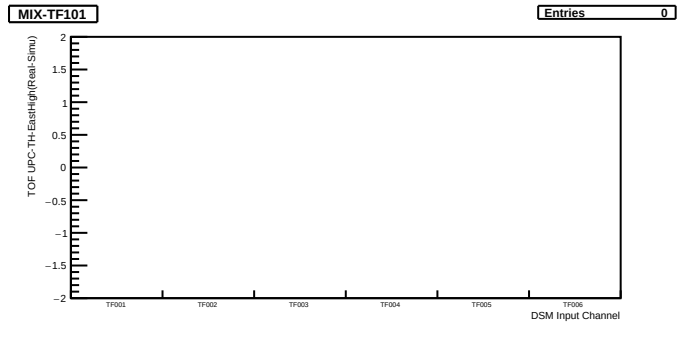

MIX-TF101
Entries 60000
TOF UPC-TH-WestHigh
3180
3393
3522
2904
3371
3411
6820
6607
6978
7096
6629
6649
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

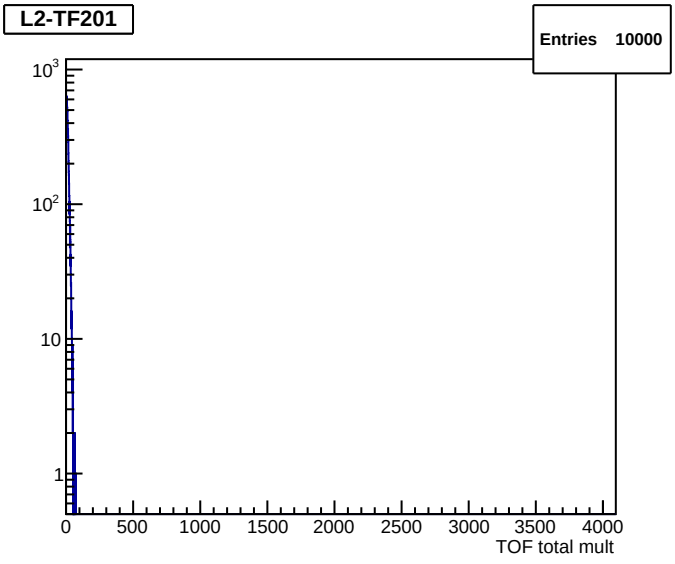

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

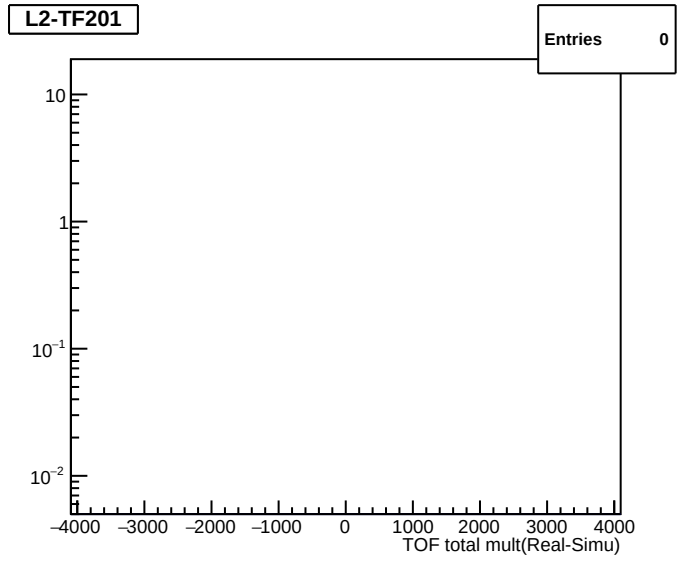

MIX-TF101
Entries 60000
TOF UPC-TH-EastLow
2274
3391
3344
3300
3342
3265
7126
6609
6636
6700
6658
6726
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

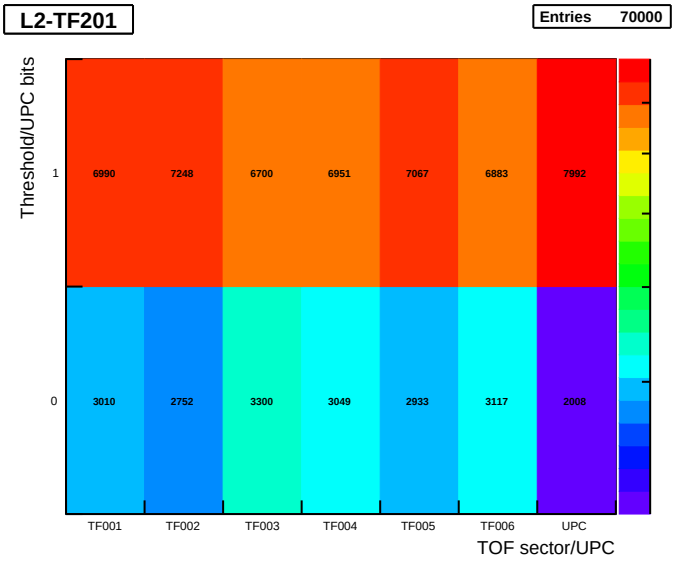

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

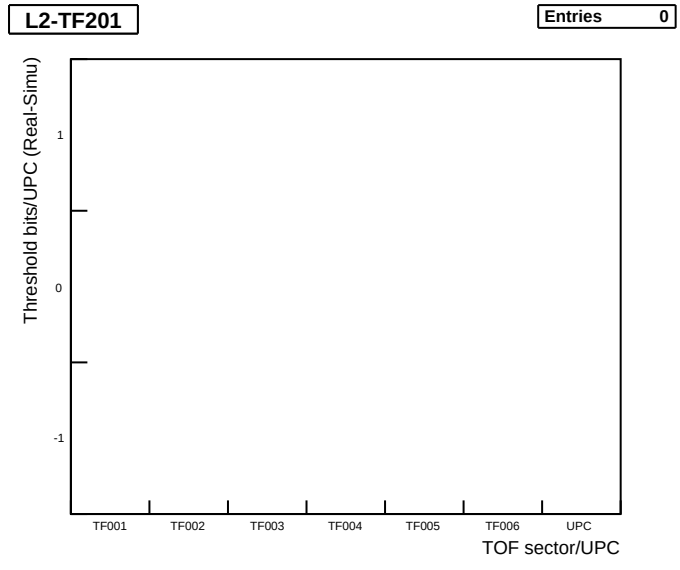

MIX-TF101
Entries 60000
TOF UPC-TH-EastHigh
3208
3313
3364
2867
2896
3227
6782
6687
6636
7133
7104
7773
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

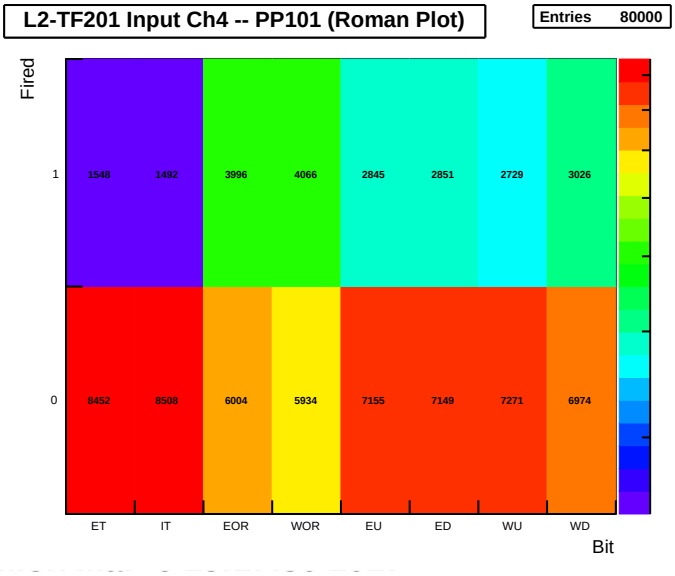

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

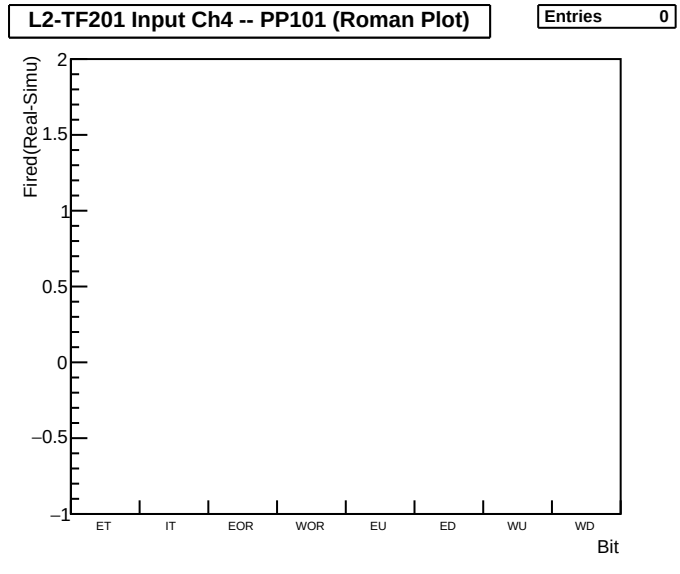

L2-TF201
Entries 10000
TOF total mult

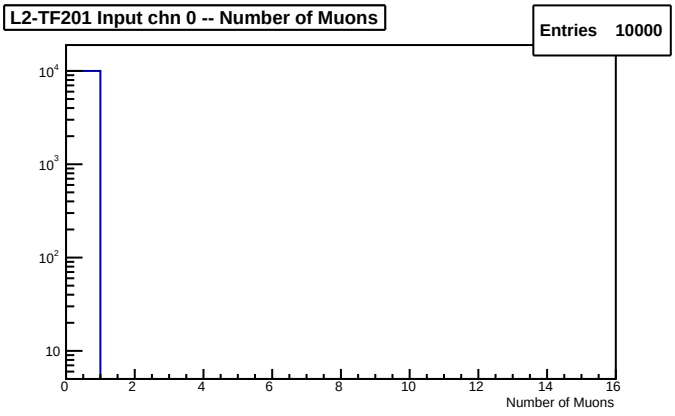

L2-TF201
Entries 0
TOF total mult(Real-Simu)

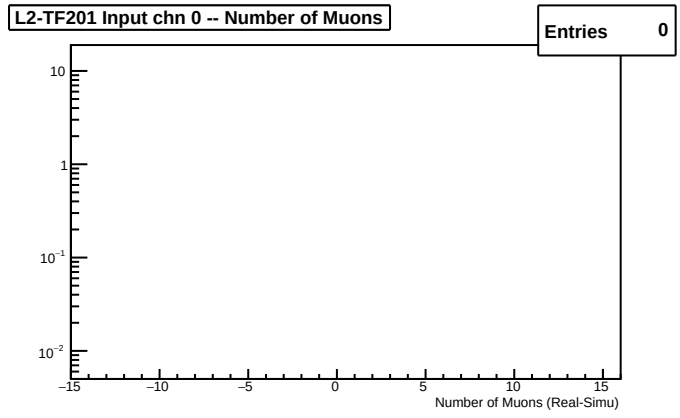

L2-TF201
Entries 70000
Threshold/UPC bits
6990 7248 6700 6951 7067 6883 7992
3010 2752 3300 3049 2933 3117 2008
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

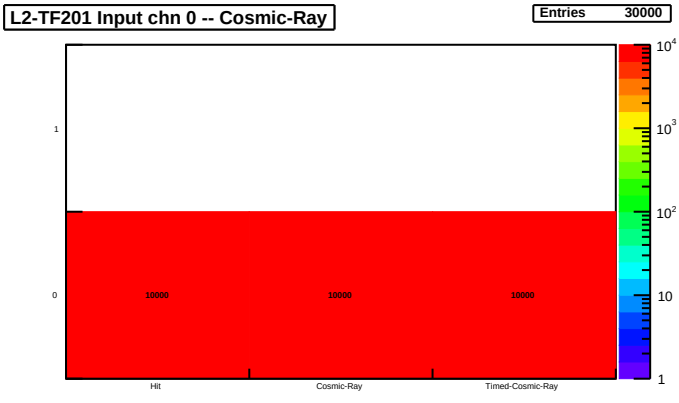

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

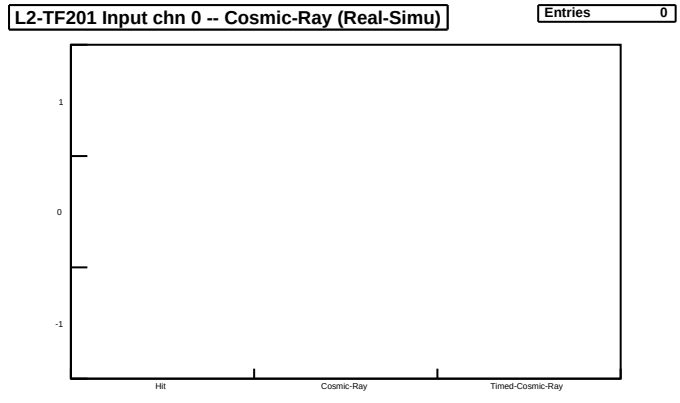

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 80000
Fired
1548 1492 3996 4066 2845 2851 2729 3026
8452 8508 6004 5934 7155 7149 7271 6974
ET IT EOR WOR EU ED WU WD
Bit

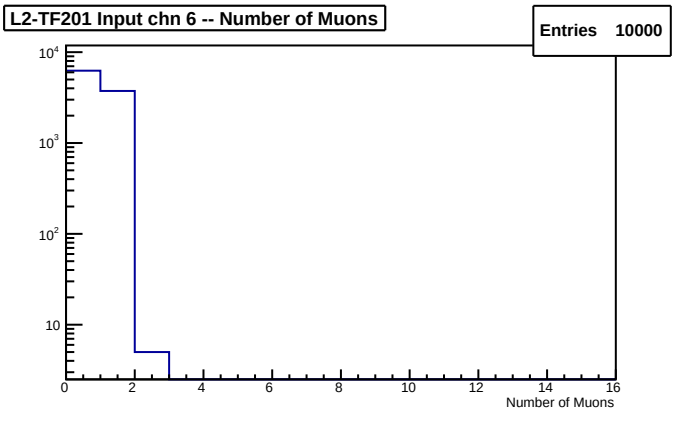

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 0
Fired(Real-Simu)
ET IT EOR WOR EU ED WU WD
Bit

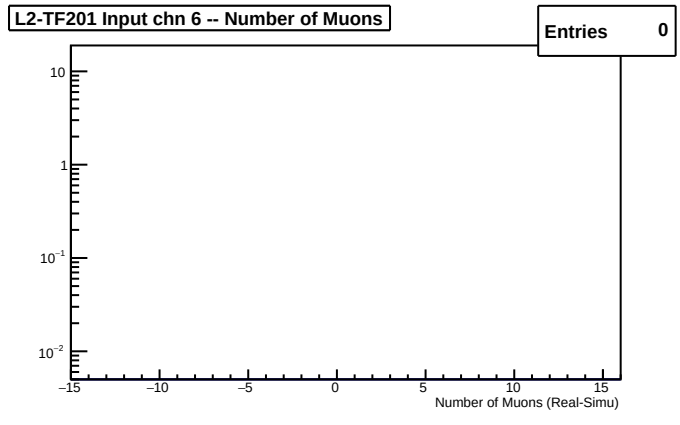

L2-TF201 Input chn 0 -- Number of Muons
Entries 10000
Number of Muons

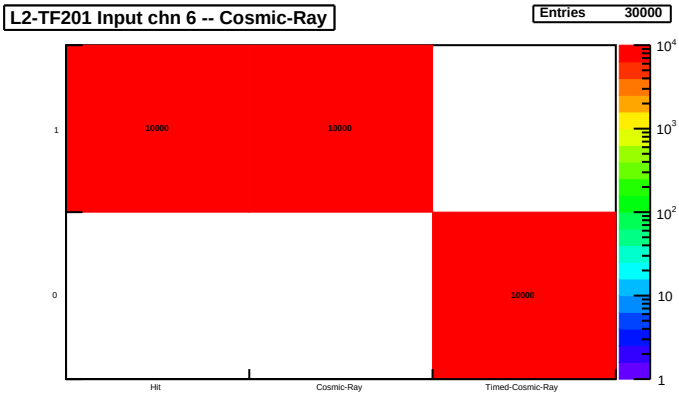

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

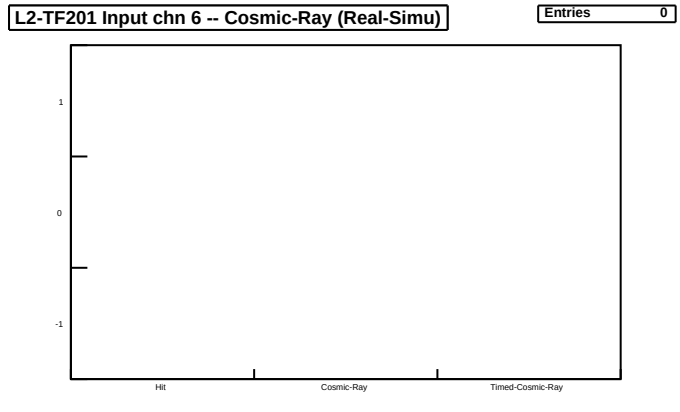

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 30000
10000
10000
10000
Hit
Cosmic-Ray
Timed-Cosmic-Ray

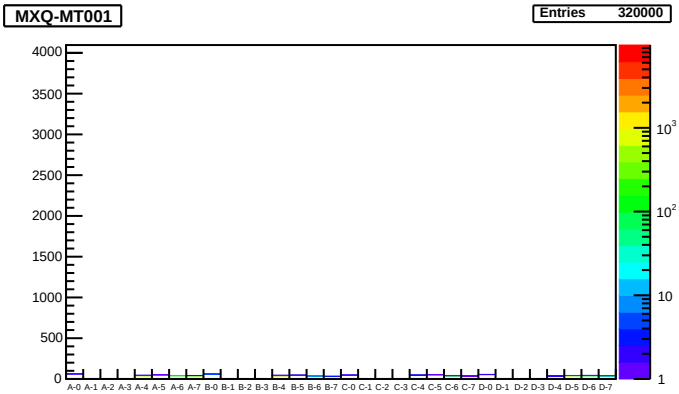

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

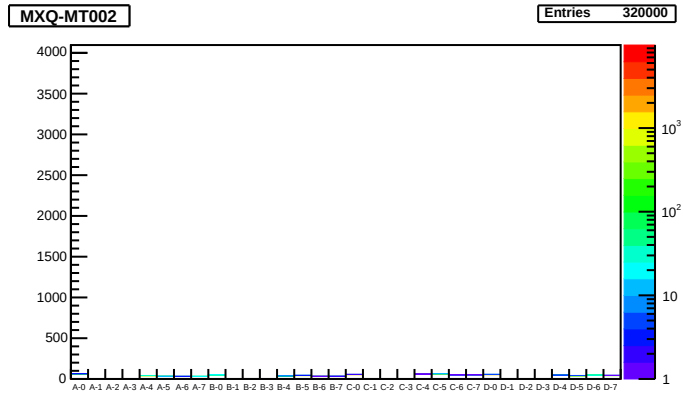

L2-TF201 Input chn 6 -- Number of Muons
Entries 10000
Number of Muons

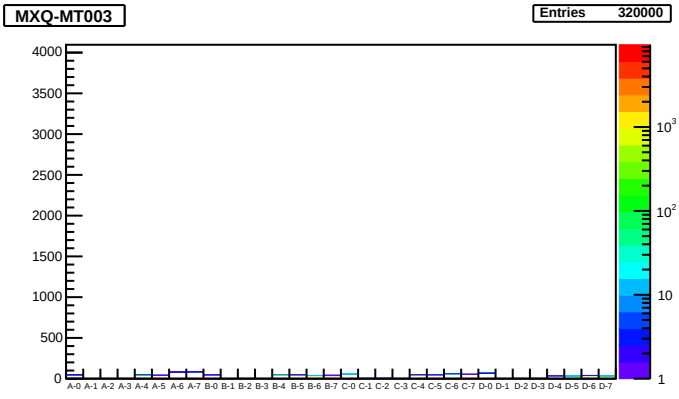

L2-TF201 Input chn 6 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

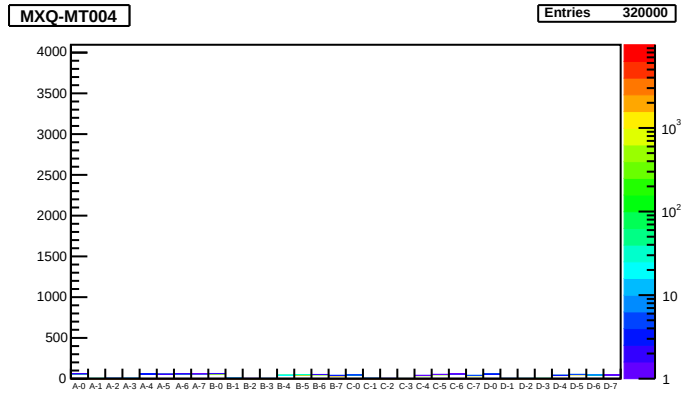

L2-TF201 Input chn 6 -- Cosmic-Ray
Entries 30000
10000
10000
10000
Hit
Cosmic-Ray
Timed-Cosmic-Ray

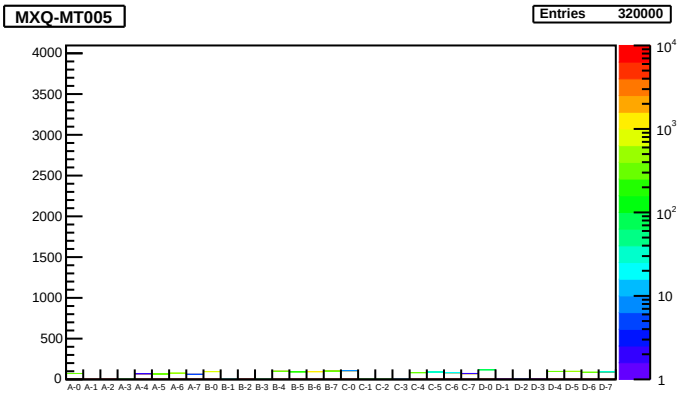

L2-TF201 Input chn 6 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

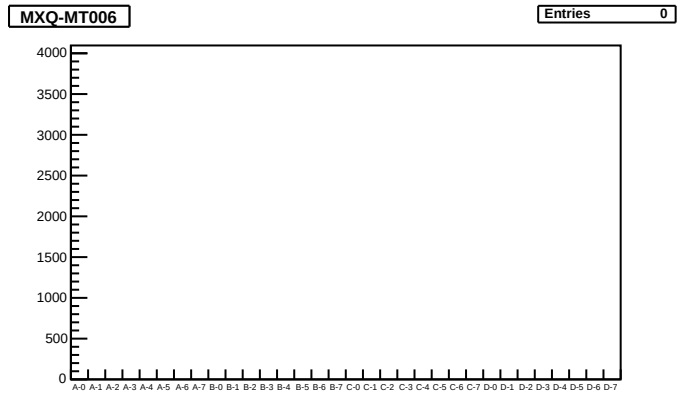

MXQ-MT001
Entries 320000

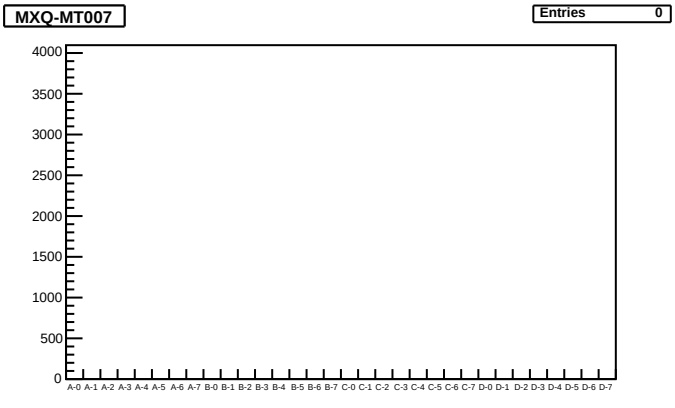

MXQ-MT002
Entries 320000

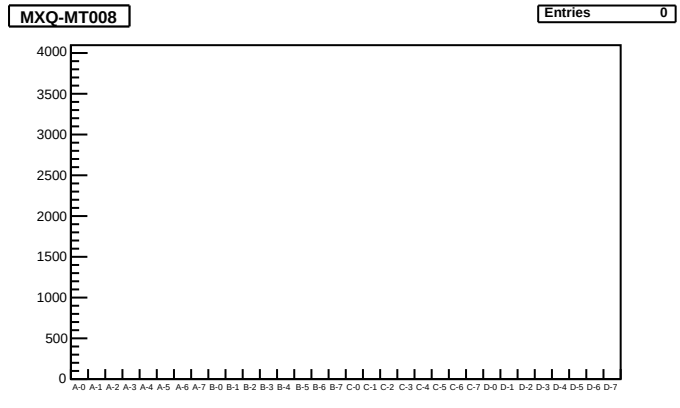

MXQ-MT003
Entries 320000

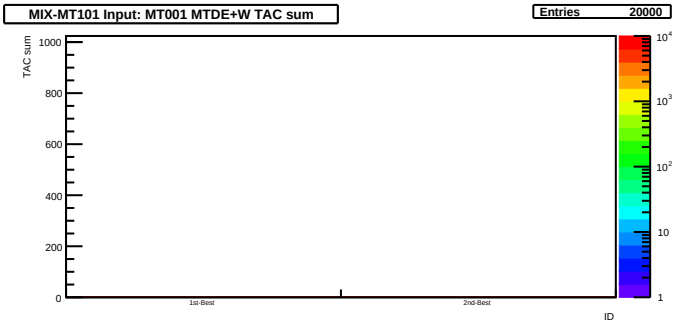

MXQ-MT004
Entries 320000

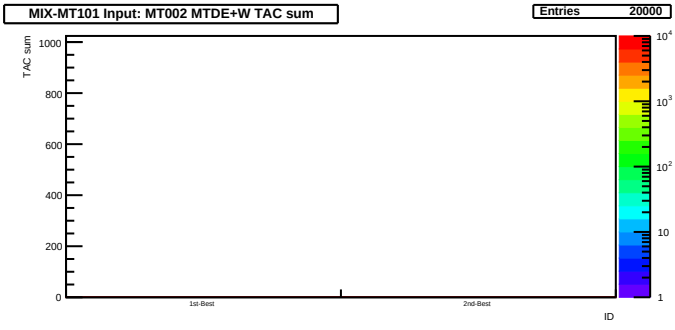

MXQ-MT005
Entries 320000

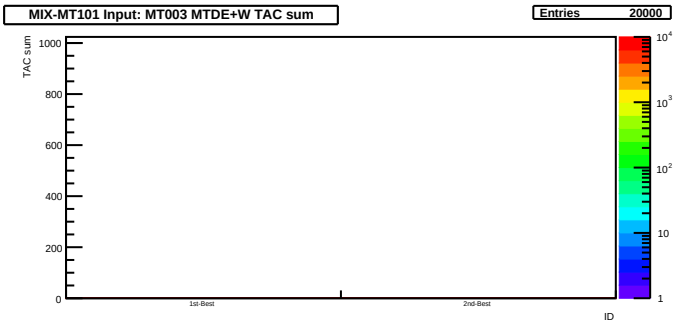

MXQ-MT006
Entries 0

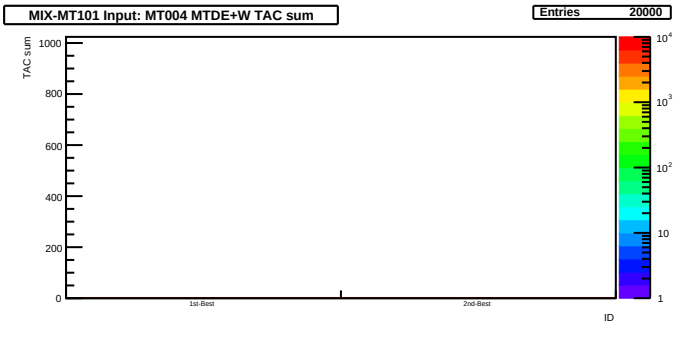

MXQ-MT007
Entries 0

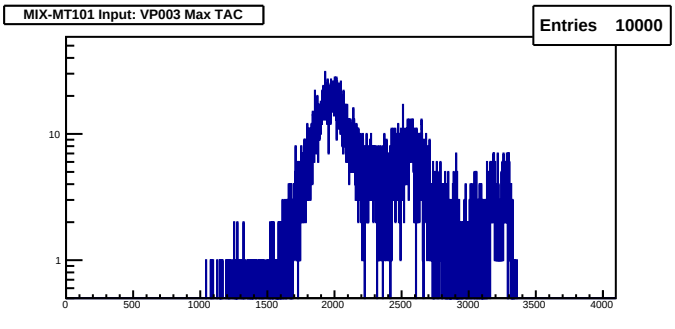

MXQ-MT008
Entries 0

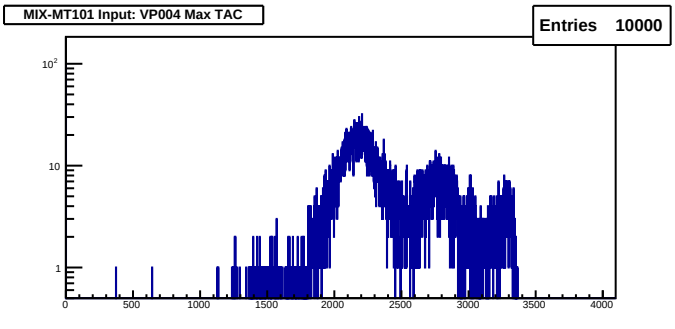

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

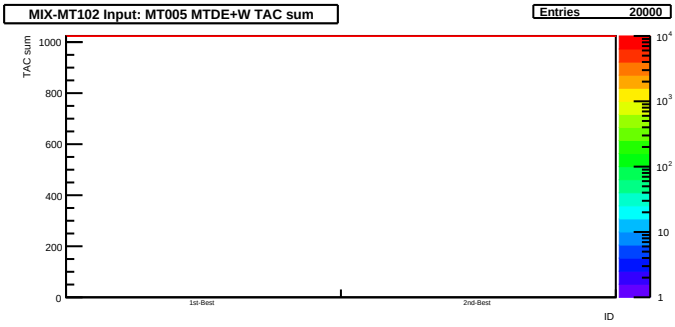

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

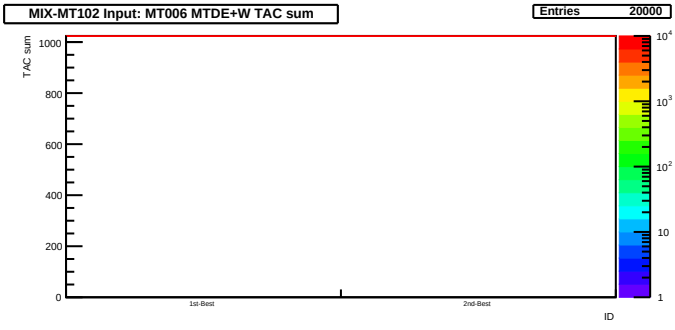

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

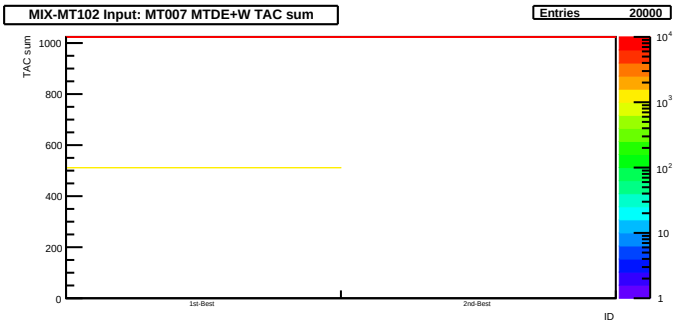

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

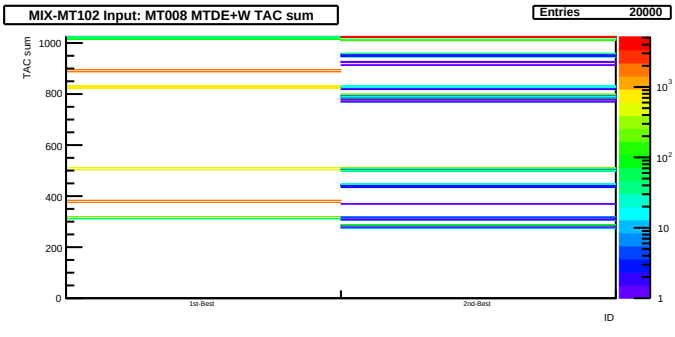

MIX-MT101 Input: VP003 Max TAC
Entries 10000

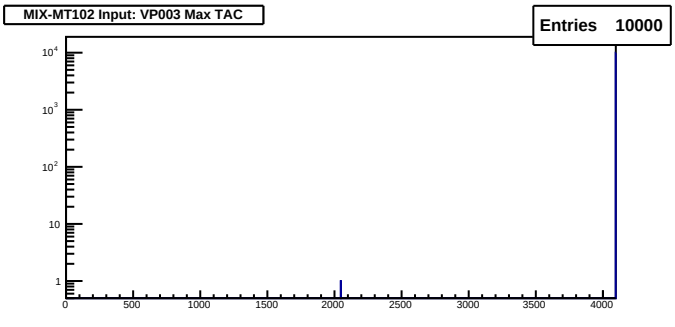

MIX-MT101 Input: VP004 Max TAC
Entries 10000

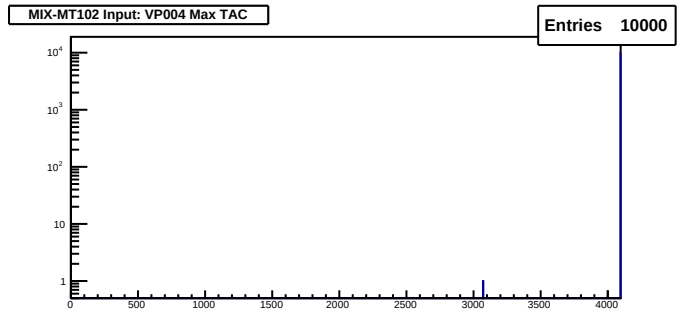

MIX-MT102 Input: MT005 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

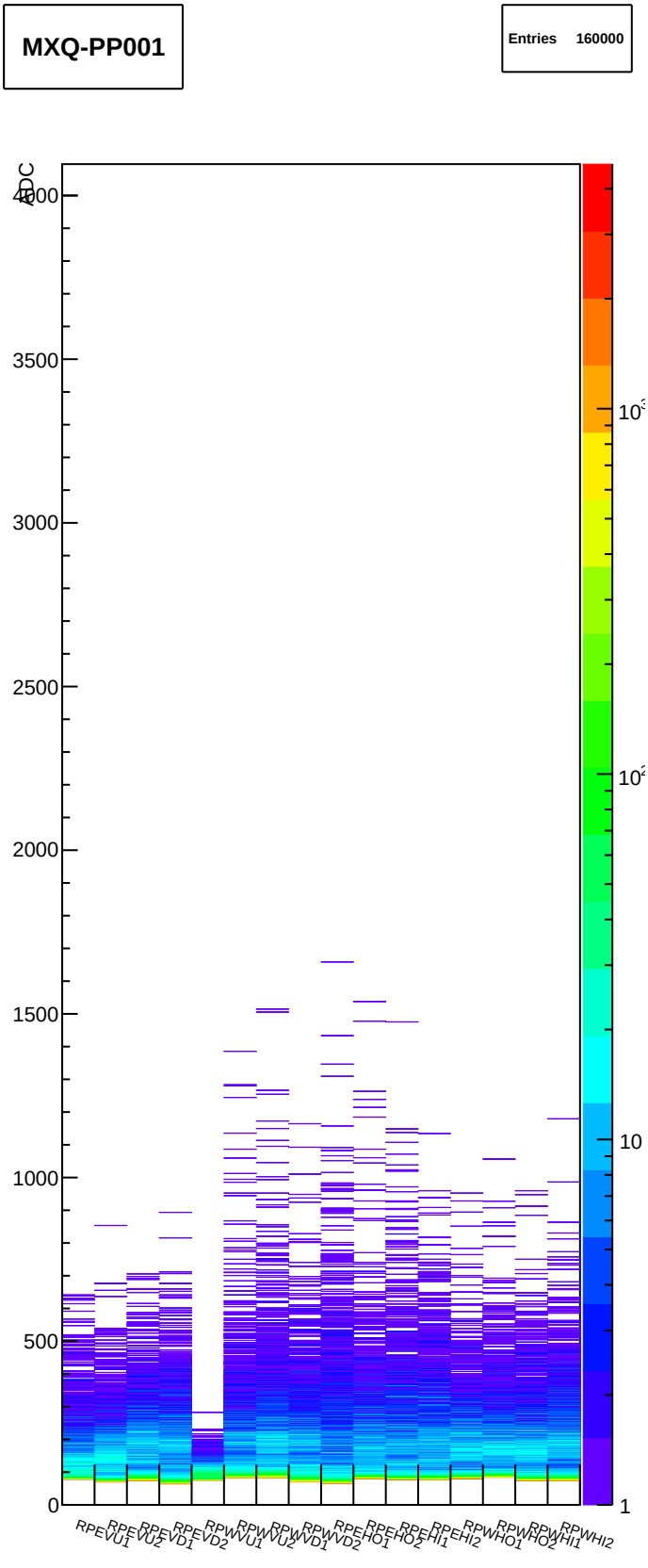

MIX-MT102 Input: MT006 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

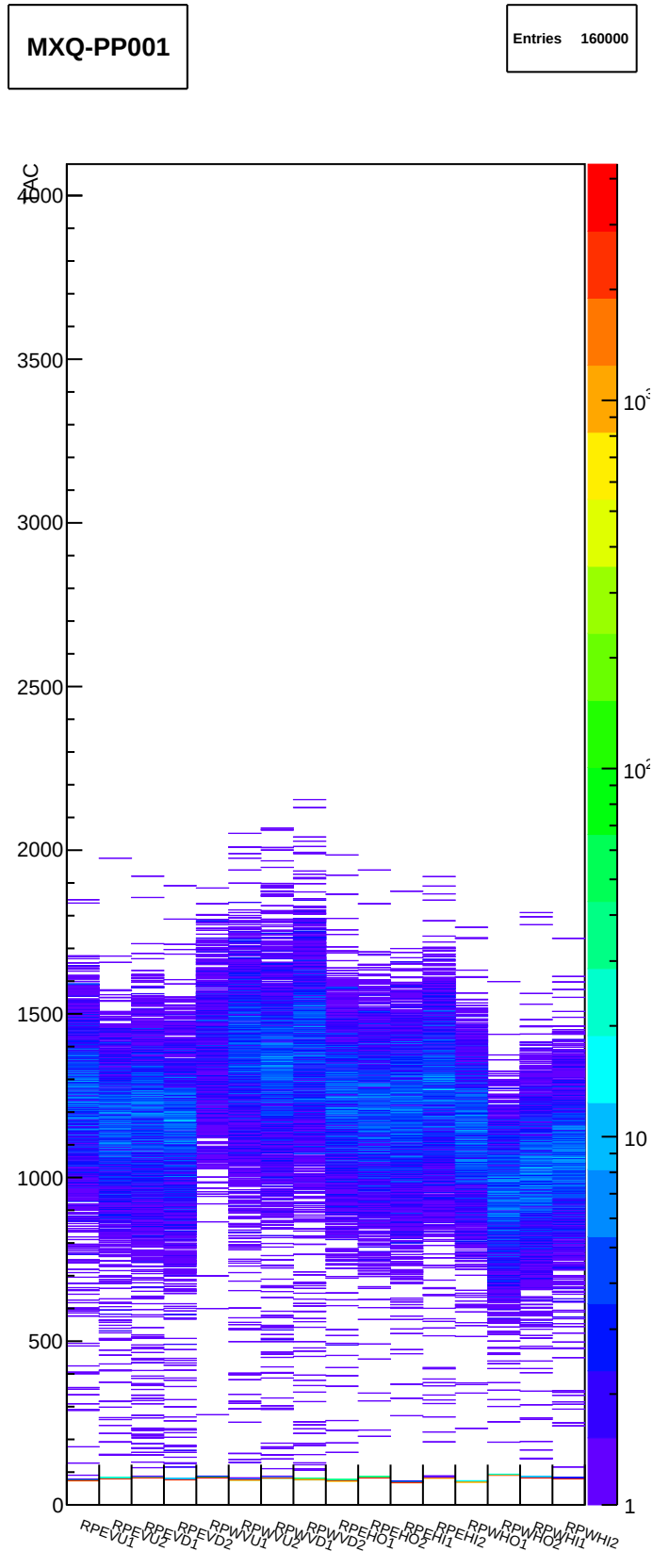

MIX-MT102 Input: MT007 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

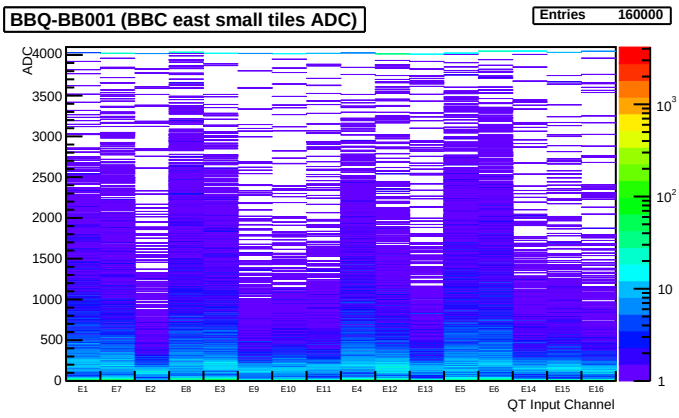

MIX-MT102 Input: MT008 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

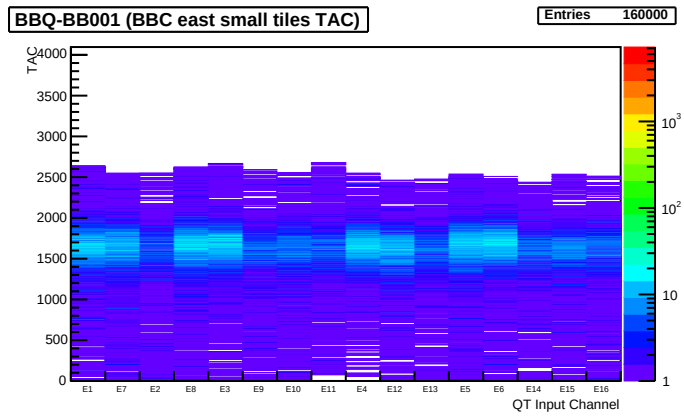

MIX-MT102 Input: VP003 Max TAC
Entries 10000

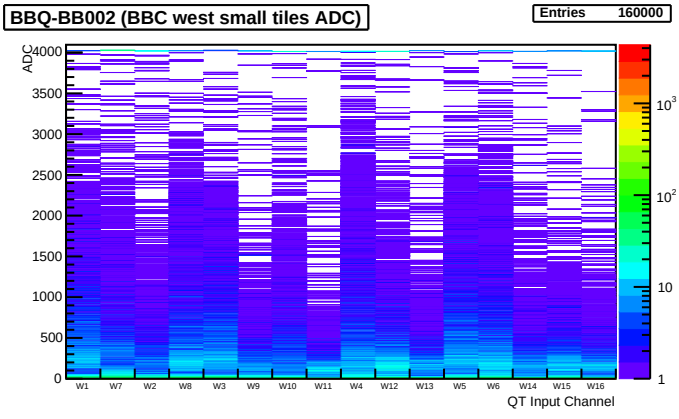

MIX-MT102 Input: VP004 Max TAC
Entries 10000

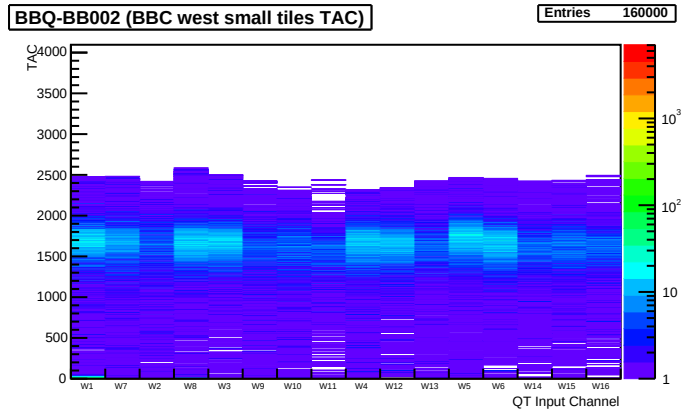

MXQ-PP001
Entries 160000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

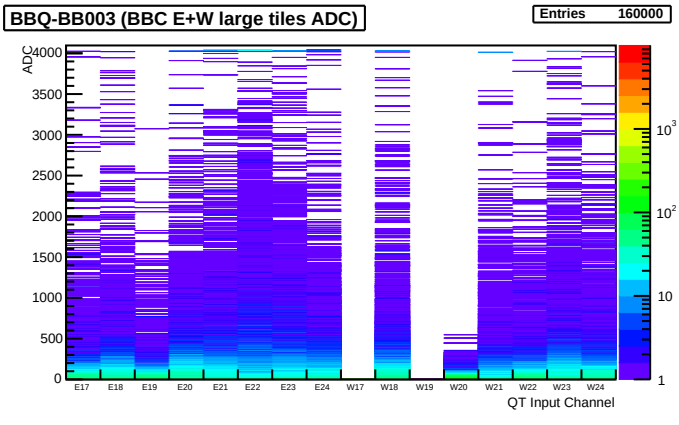

MXQ-PP001
Entries 160000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

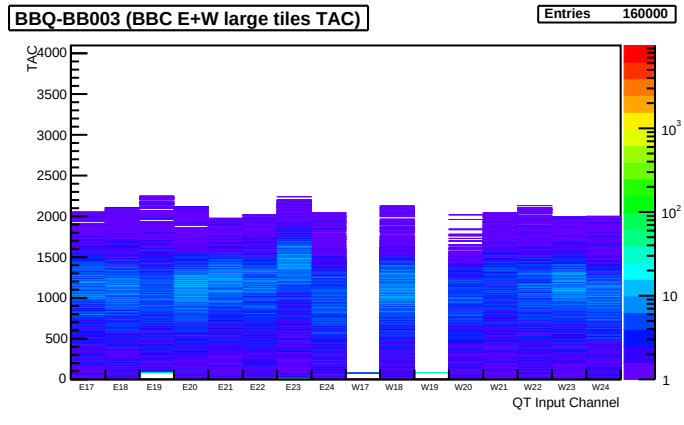

BBQ-BB001 (BBC east small tiles ADC)
Entries 160000
ADC
QT Input Channel

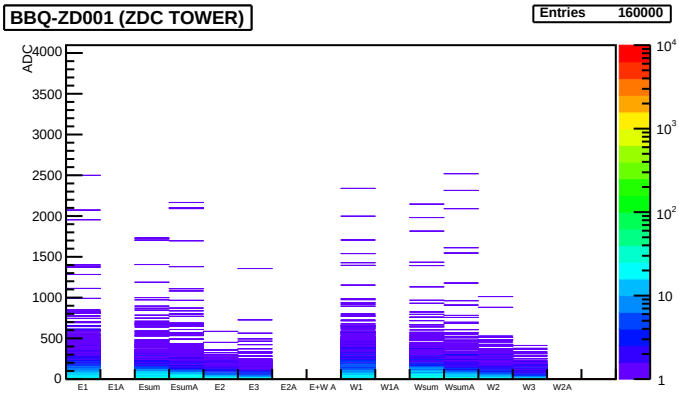

BBQ-BB001 (BBC east small tiles TAC)
Entries 160000
TAC
QT Input Channel

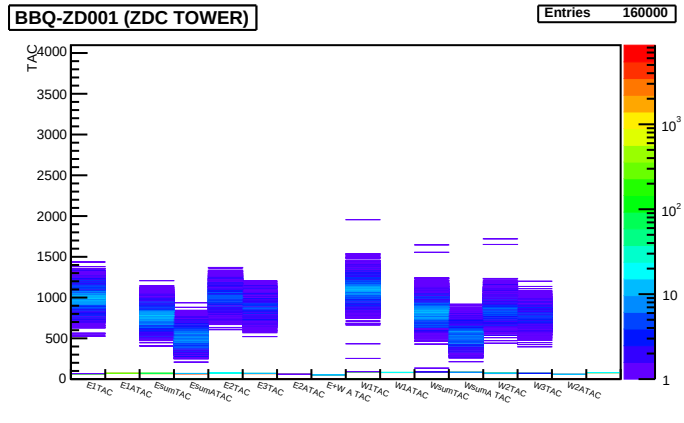

BBQ-BB002 (BBC west small tiles ADC)
Entries 160000
ADC
QT Input Channel

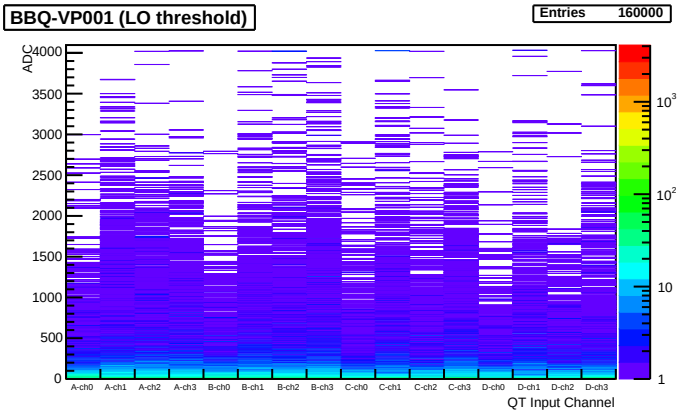

BBQ-BB002 (BBC west small tiles TAC)
Entries 160000
TAC
QT Input Channel

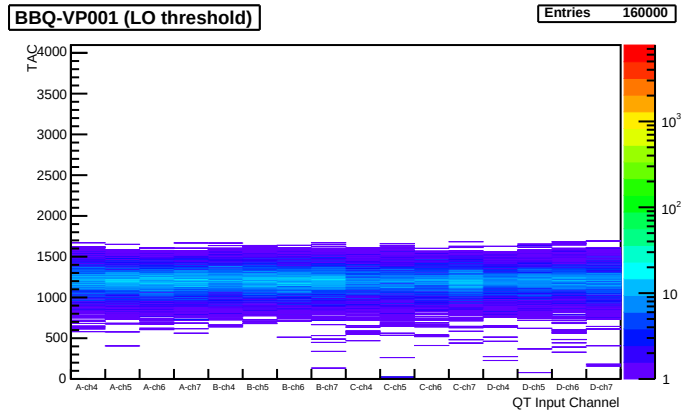

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 160000
ADC
QT Input Channel

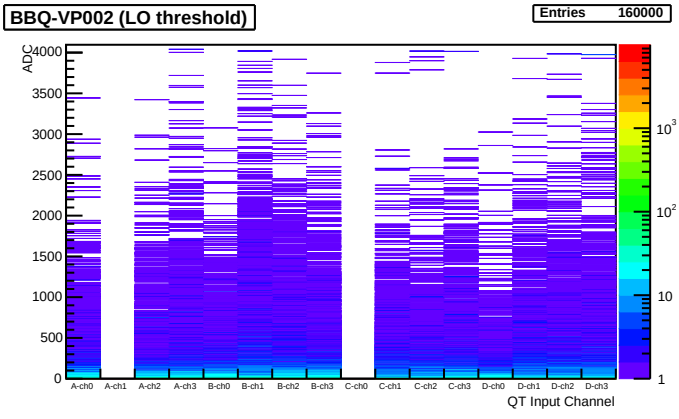

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 160000
TAC
QT Input Channel

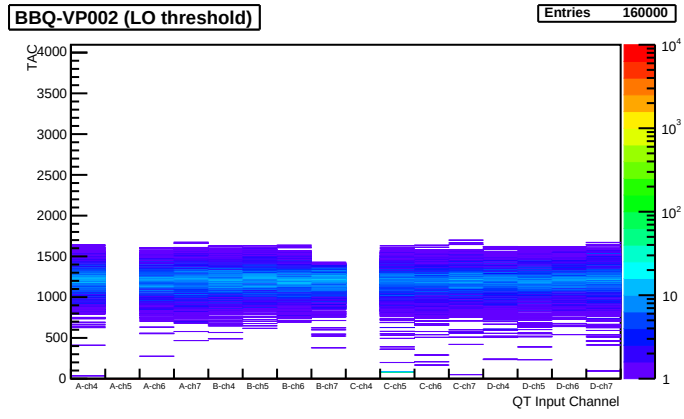

BBQ-ZD001 (ZDC TOWER)
Entries 160000
ADC

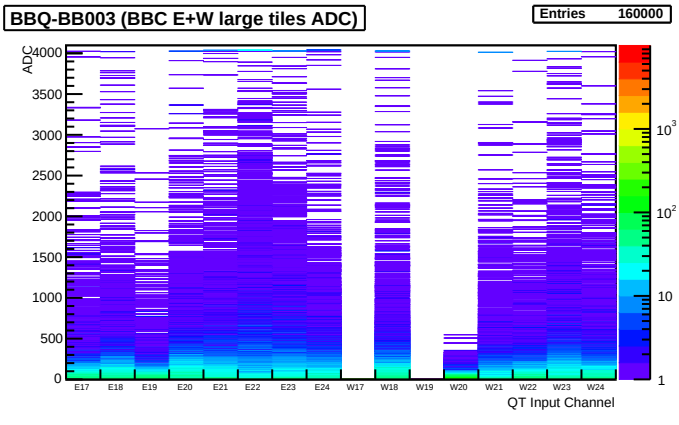

BBQ-ZD001 (ZDC TOWER)
Entries 160000
TAC

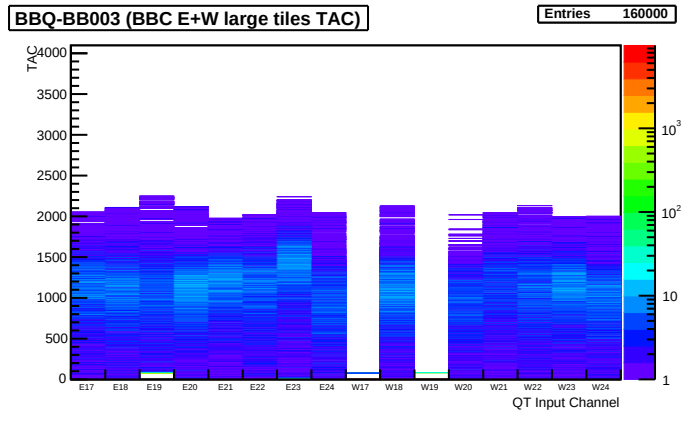

BBQ-VP001 (LO threshold)
Entries 160000
ADC
QT Input Channel

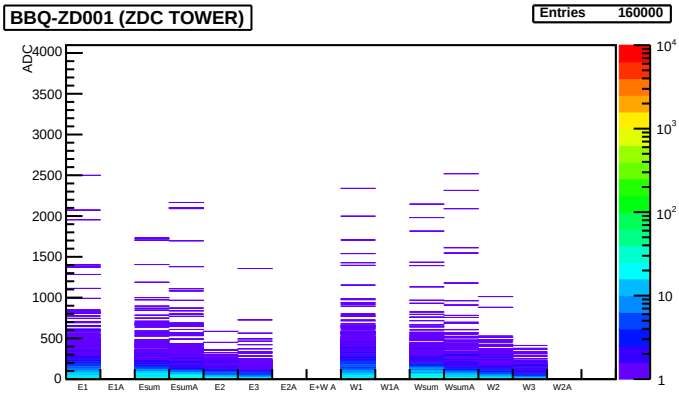

BBQ-VP001 (LO threshold)
Entries 160000
TAC
QT Input Channel

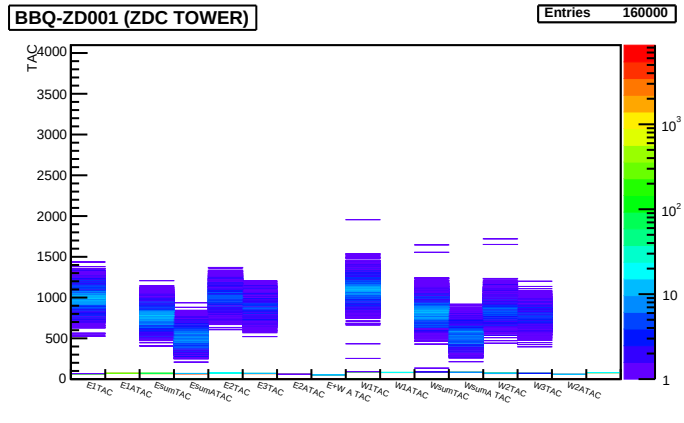

BBQ-VP002 (LO threshold)
Entries 160000
ADC
QT Input Channel

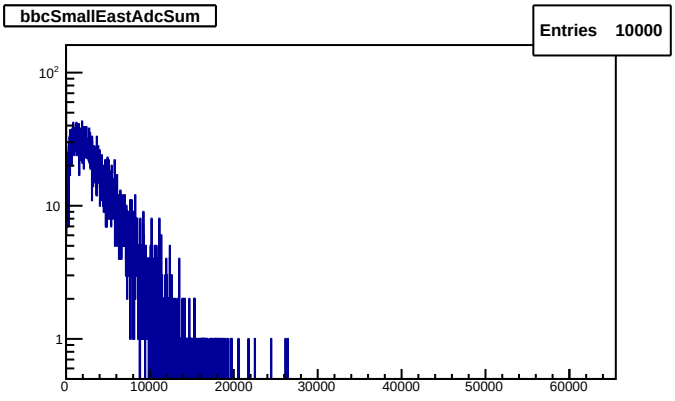

BBQ-VP002 (LO threshold)
Entries 160000
TAC
QT Input Channel

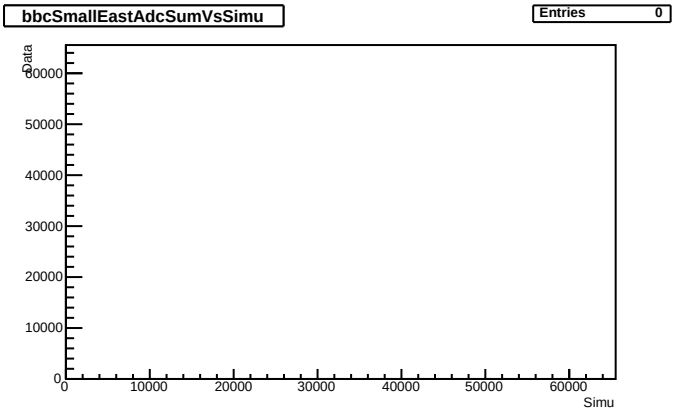

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 160000
ADC
QT Input Channel

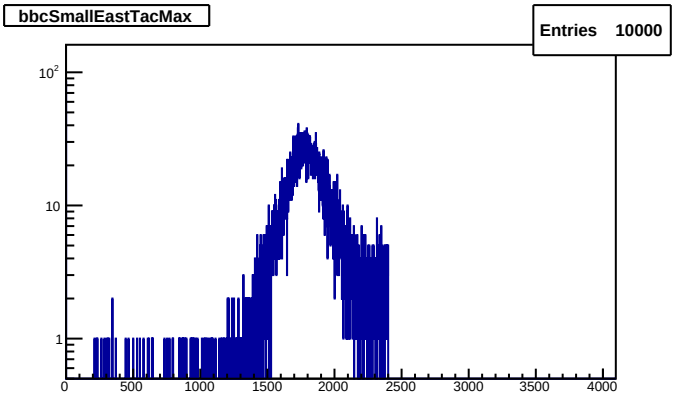

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 160000
TAC
QT Input Channel

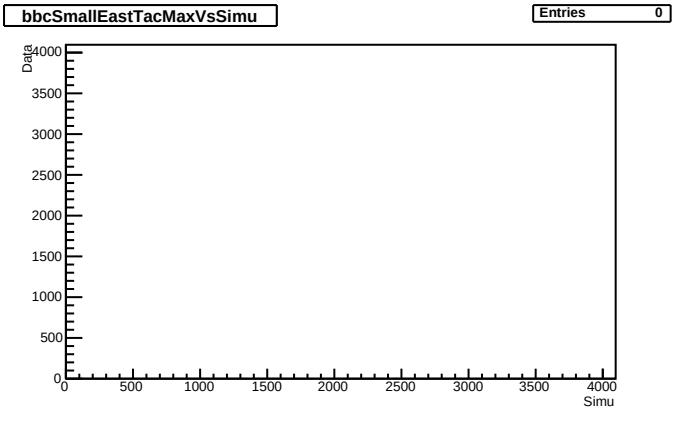

BBQ-ZD001 (ZDC TOWER)
Entries 160000
ADC

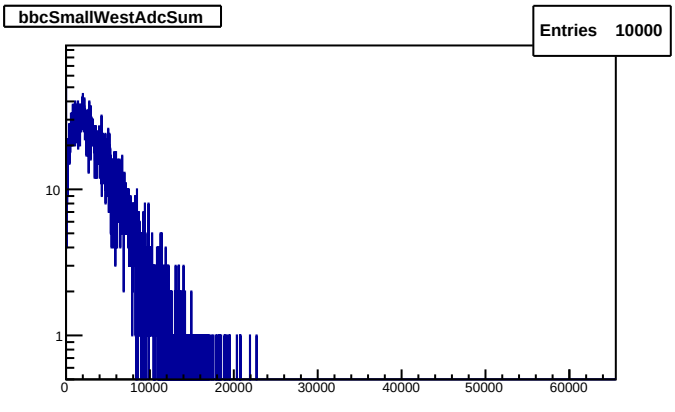

BBQ-ZD001 (ZDC TOWER)
Entries 160000
TAC

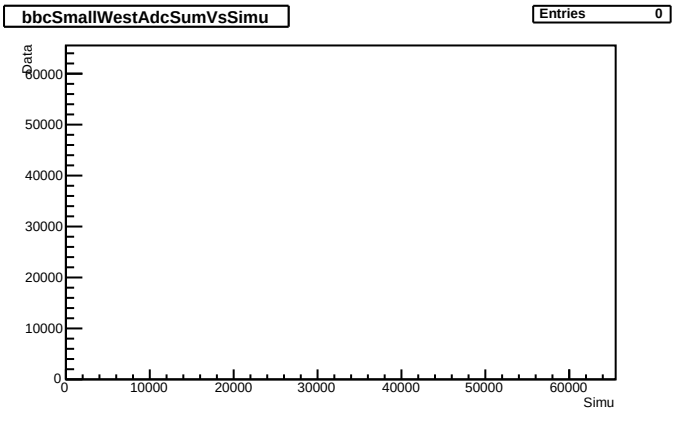

bbcSmallEastAdcSum
Entries 10000

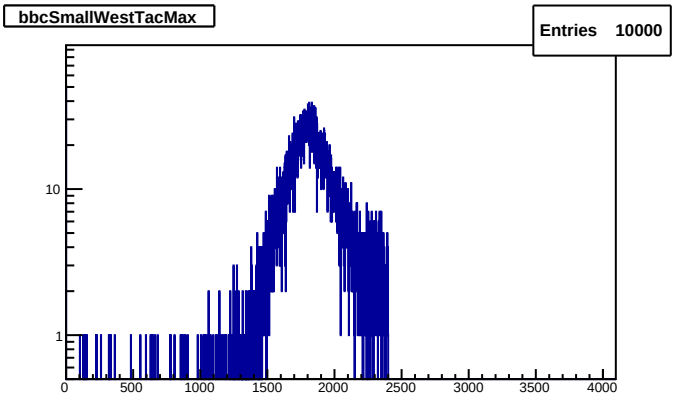

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

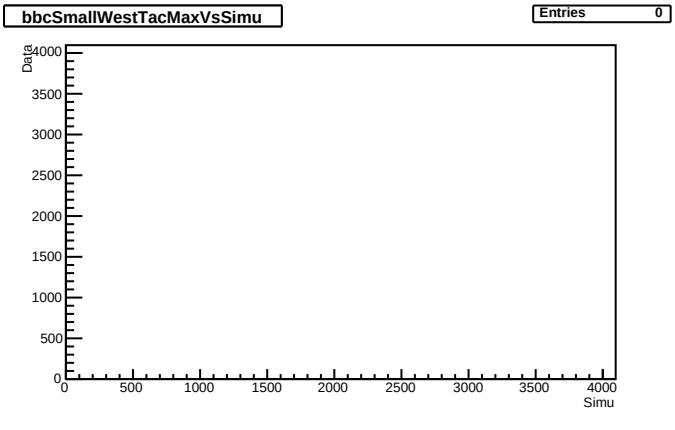

bbcSmallEastTacMax
Entries 10000

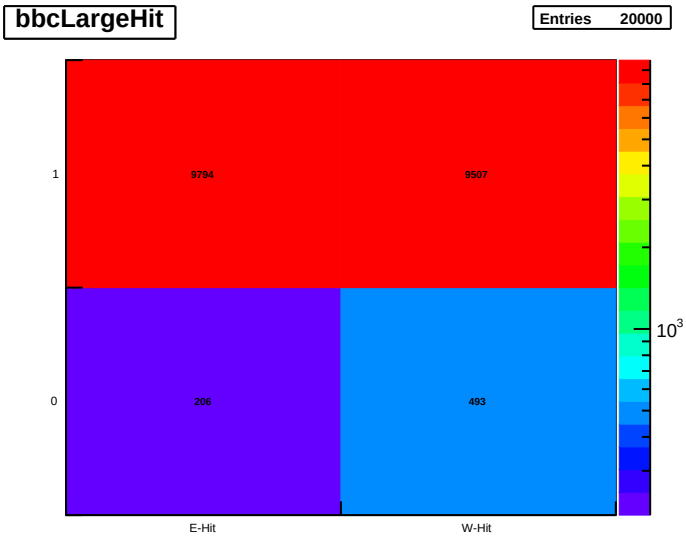

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

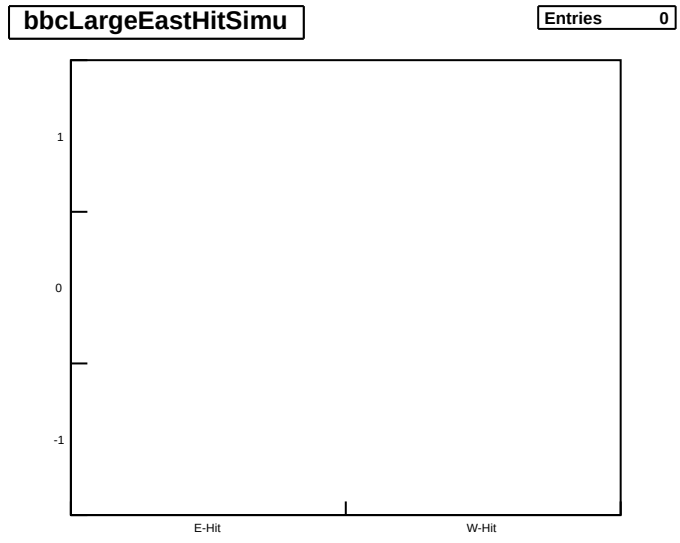

bbcSmallWestAdcSum
Entries 10000

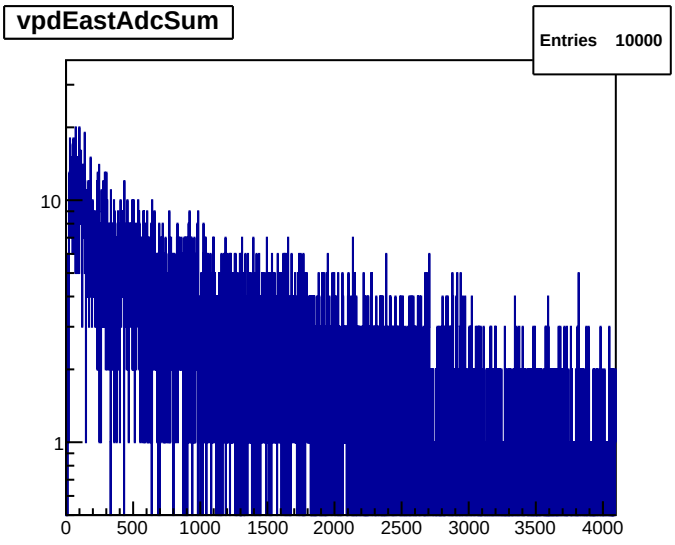

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

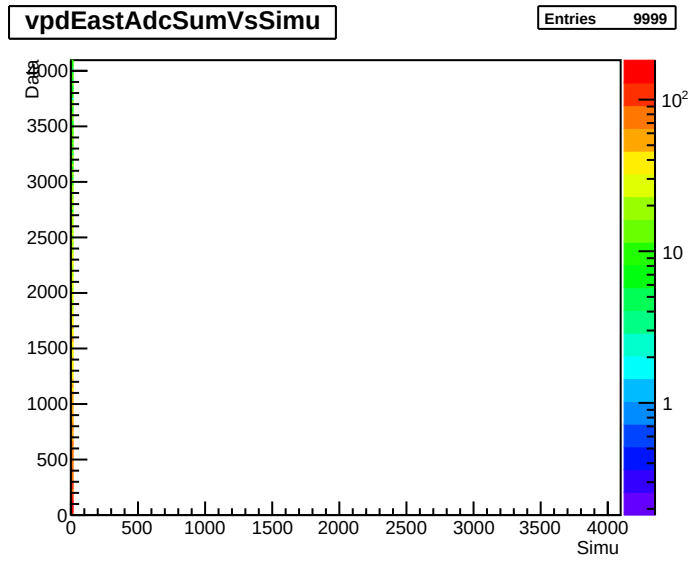

bbcSmallWestTacMax
Entries 10000

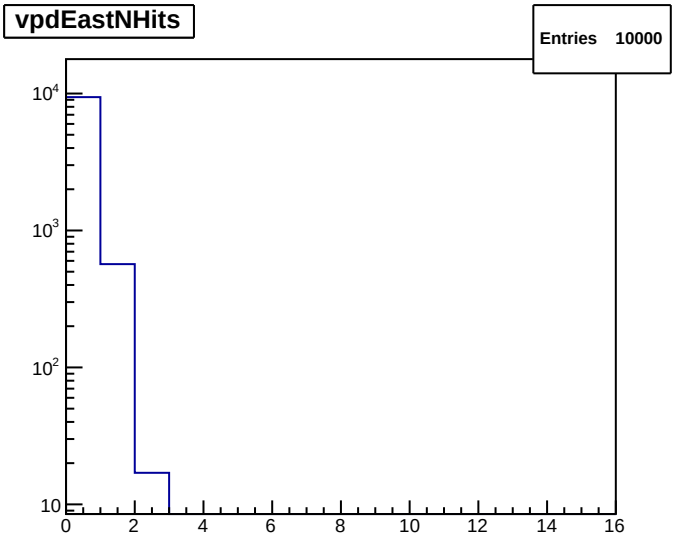

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

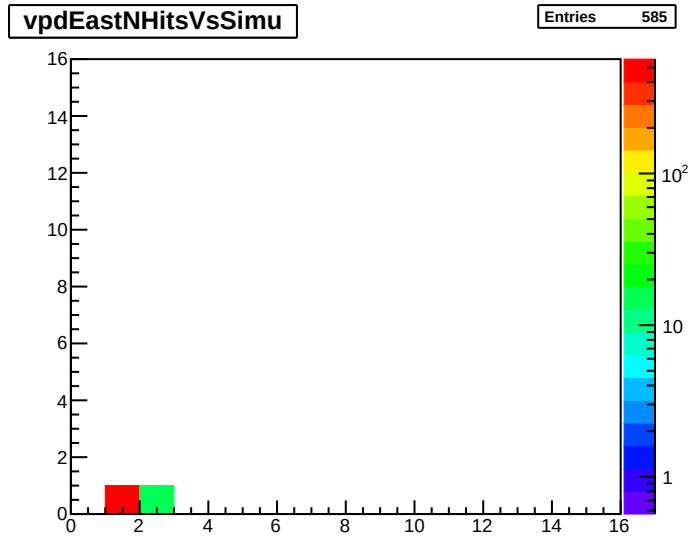

bbcLargeHit
Entries 20000
1
0
9794
9507
206
493
E-Hit
W-Hit
10^3

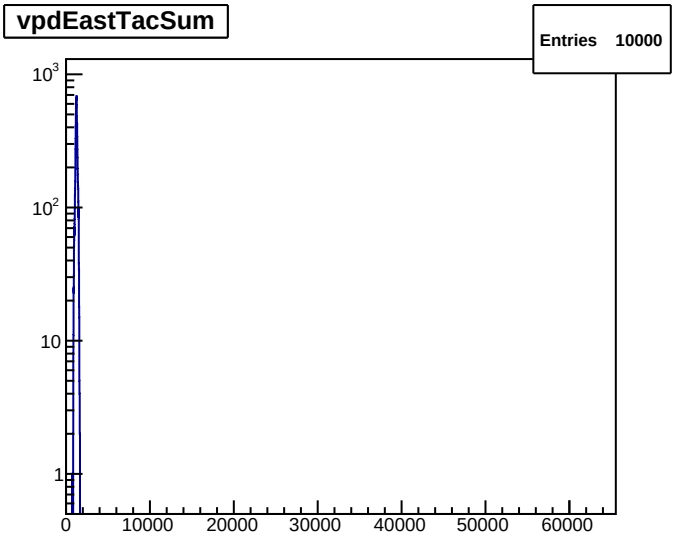

bbcLargeEastHitSimu
Entries 0
1
0
-1
E-Hit
W-Hit

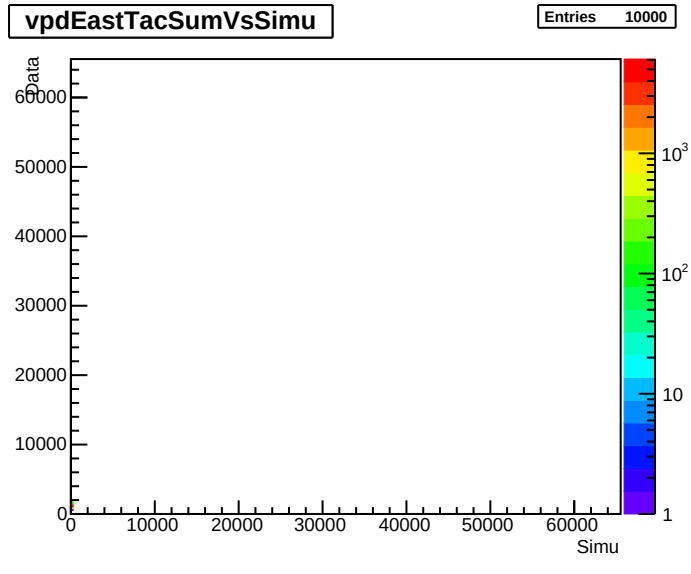

vpdEastAdcSum
Entries 10000

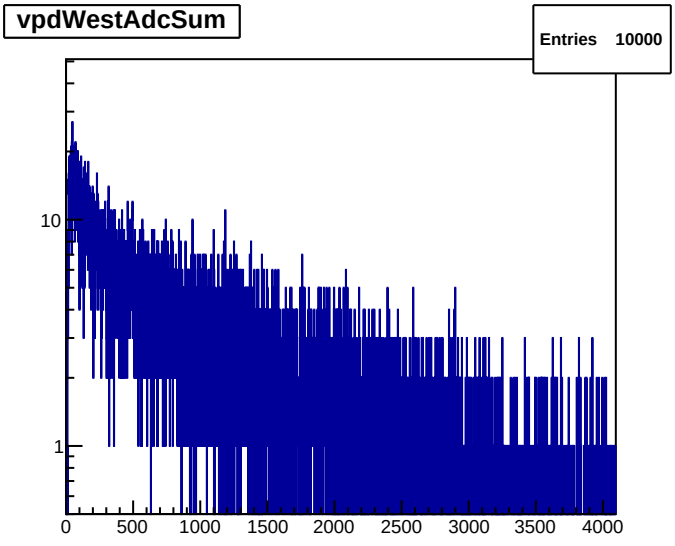

vpdEastAdcSumVsSimu
Entries 9999
Data
Simu

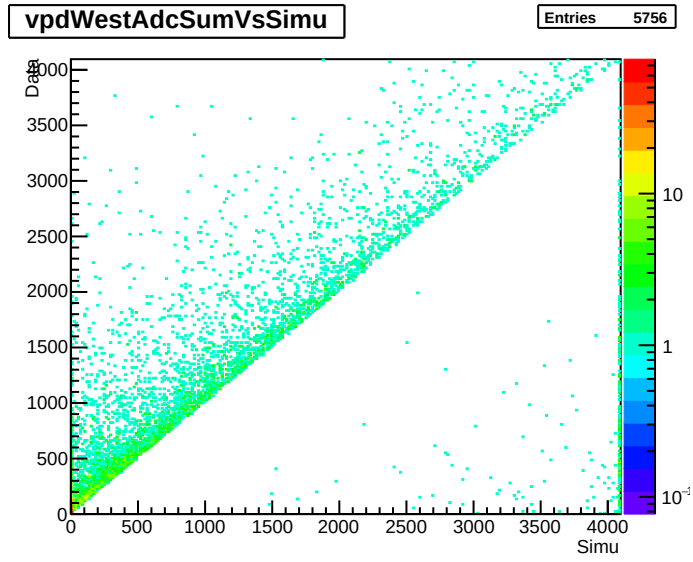

vpdEastNHits
Entries 10000

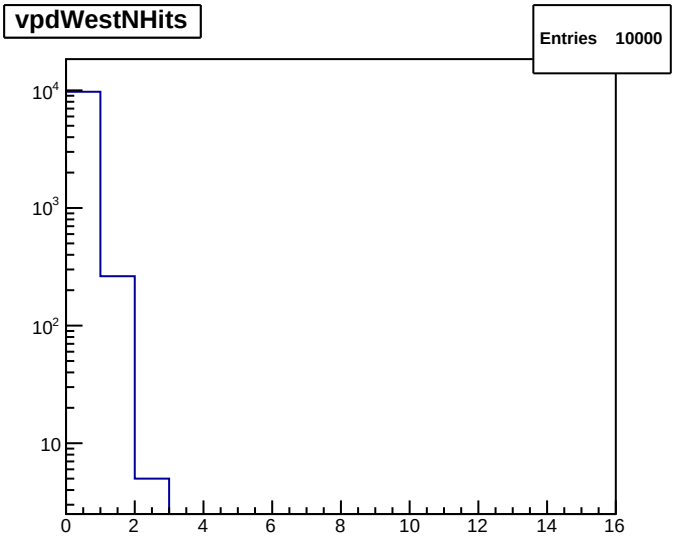

vpdEastNHitsVsSimu
Entries 585

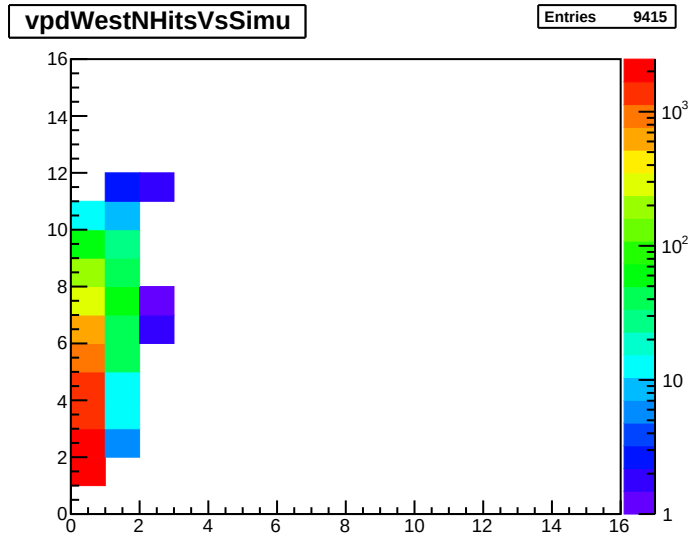

vpdEastTacSum
Entries 10000

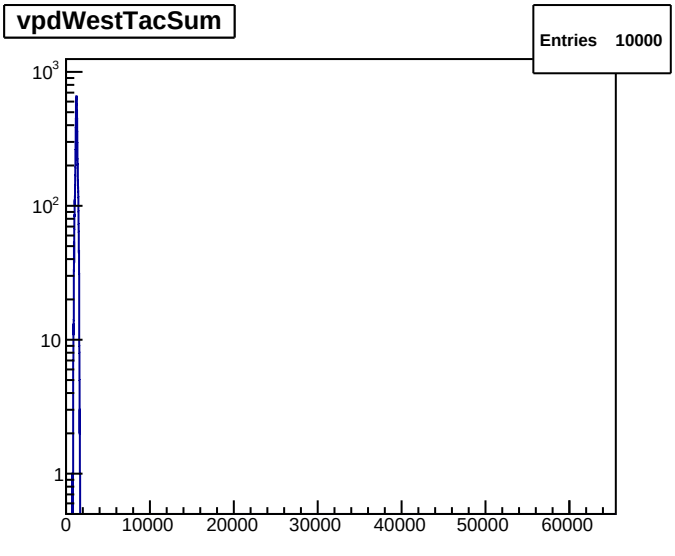

vpdEastTacSumVsSimu
Entries 10000
Data
Simu

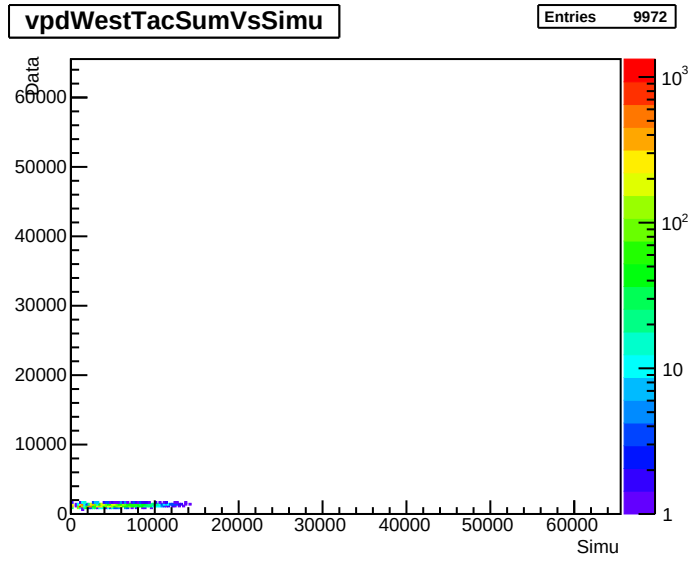

vpdWestAdcSum
Entries 10000

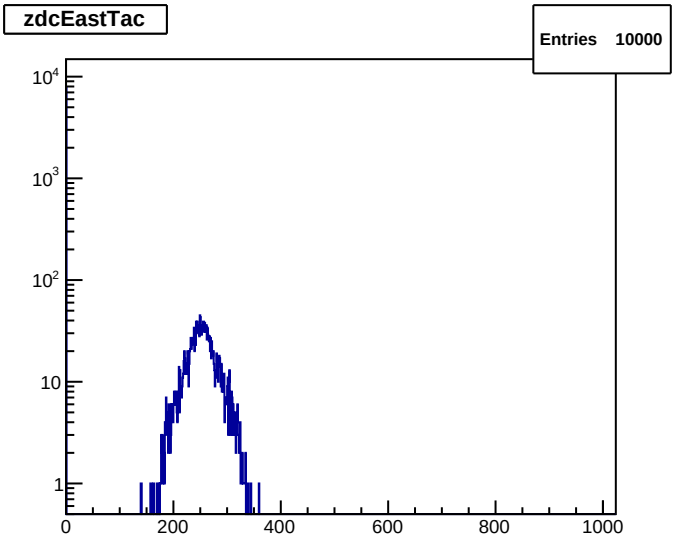

vpdWestAdcSumVsSimu
Entries 5756
Data
Simu

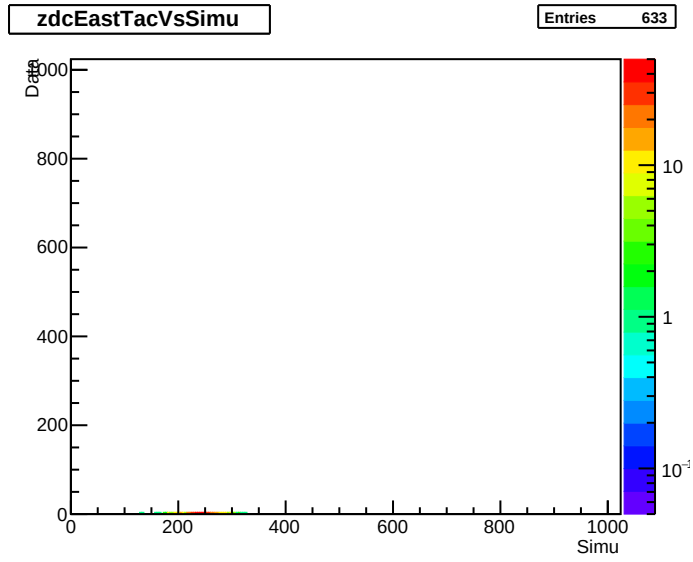

vpdWestNHits
Entries 10000

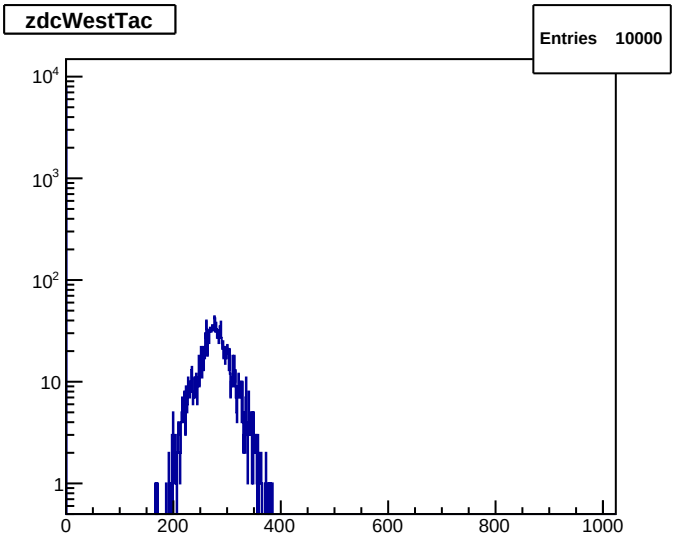

vpdWestNHitsVsSimu
Entries 9415

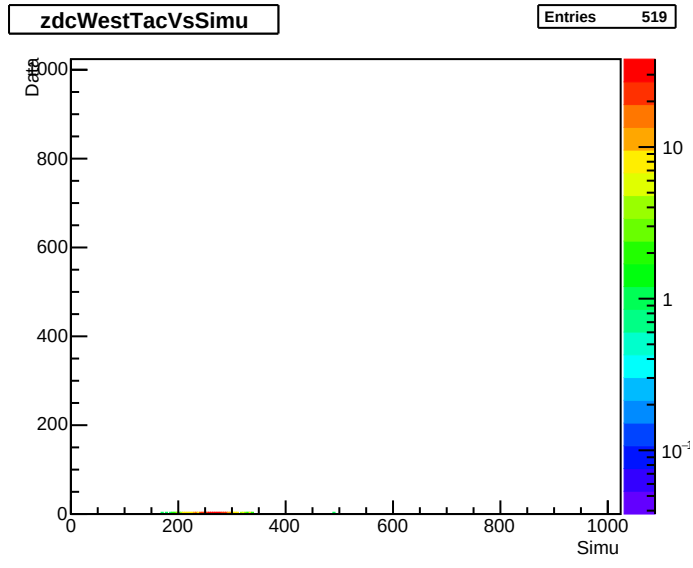

vpdWestTacSum
Entries 10000

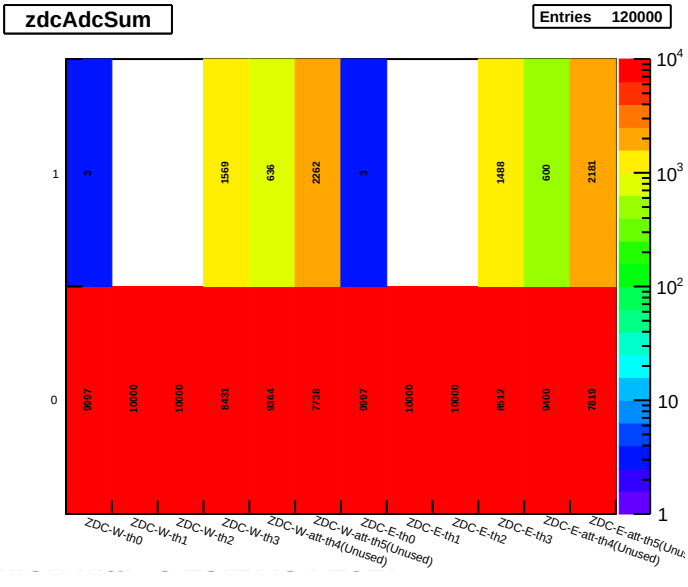

vpdWestTacSumVsSimu
Entries 9972
Data
Simu

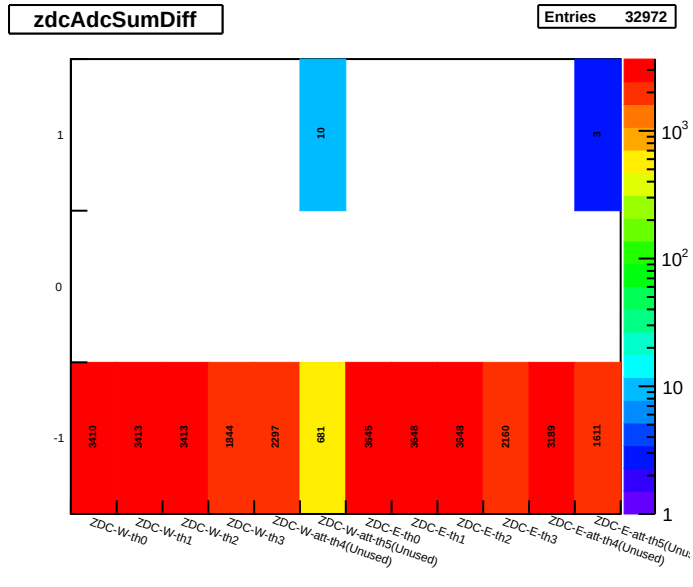

zdcEastTac
Entries 10000

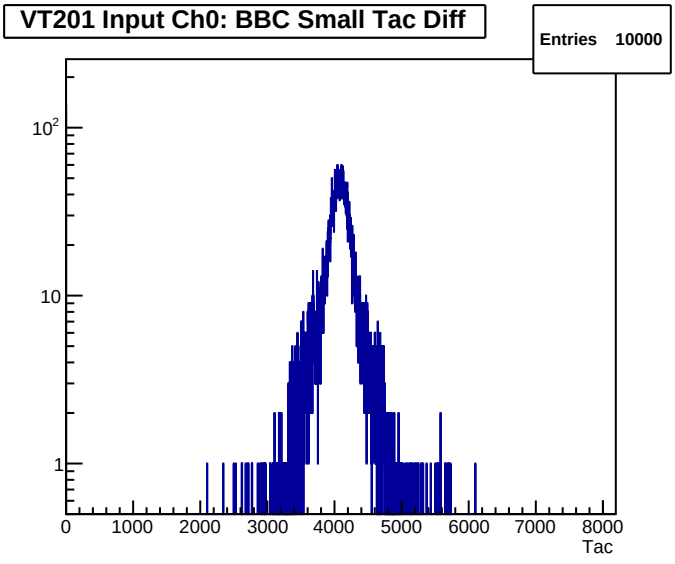

zdcEastTacVsSimu
Entries 633
Data
Simu

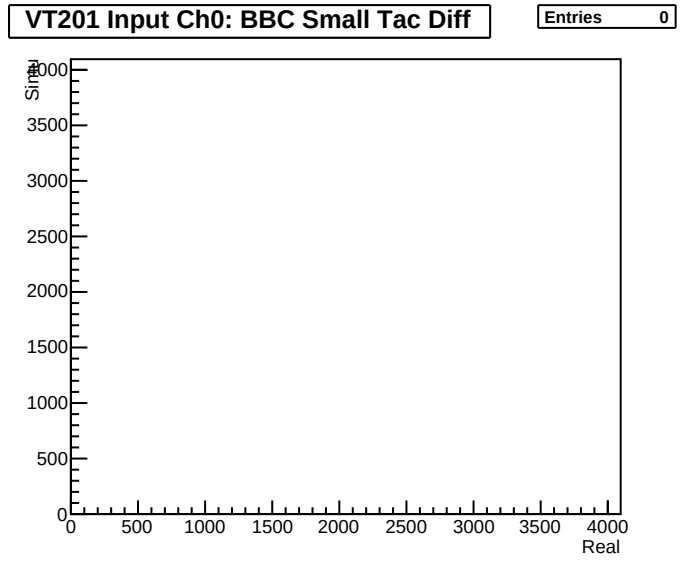

zdcWestTac
Entries 10000

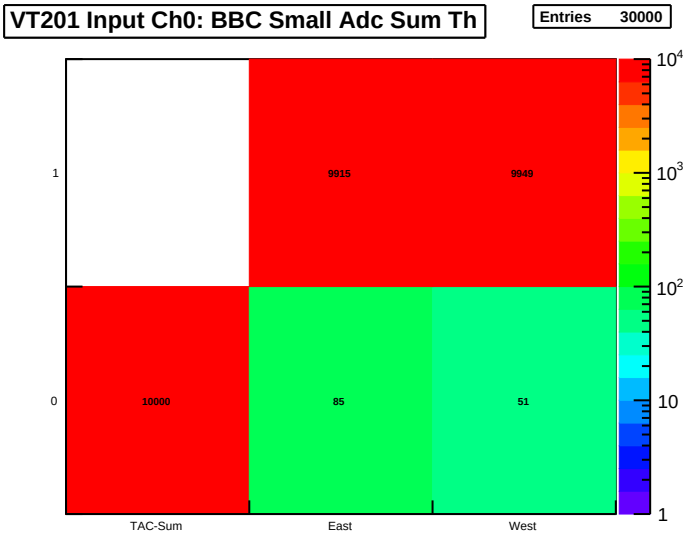

zdcWestTacVsSimu
Entries 519
Data
Simu

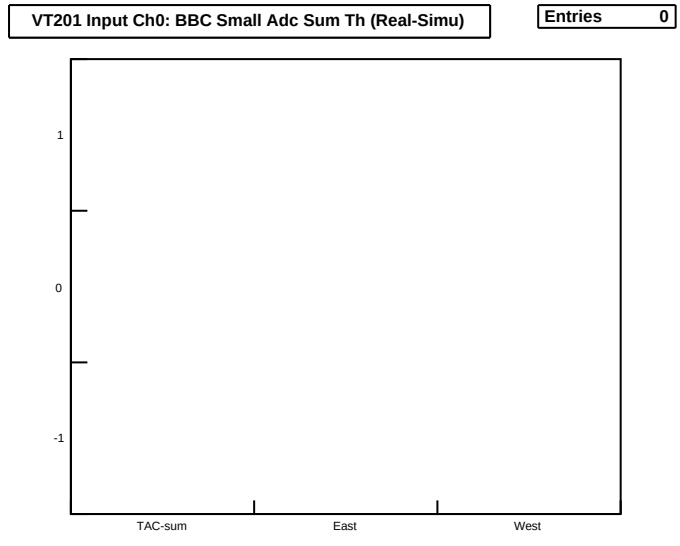

zdcAdcSum
Entries 120000
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

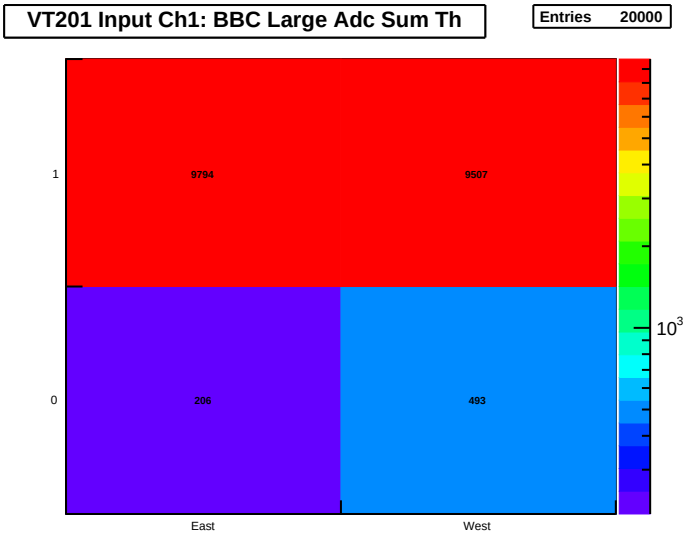

zdcAdcSumDiff
Entries 32972
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

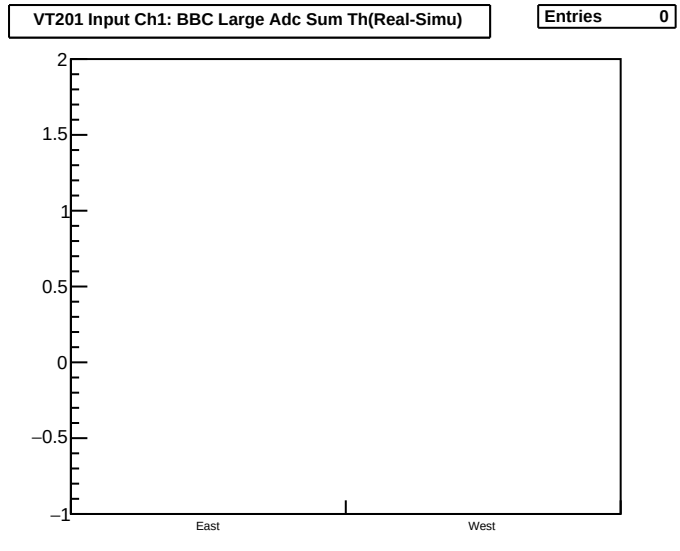

VT201 Input Ch0: BBC Small Tac Diff
Entries 10000
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

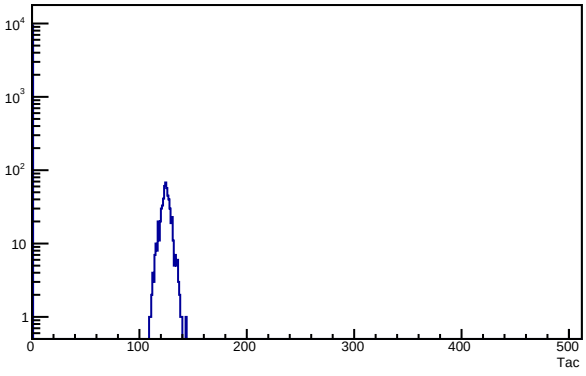

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 30000
9915
9949
10000
85
51
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

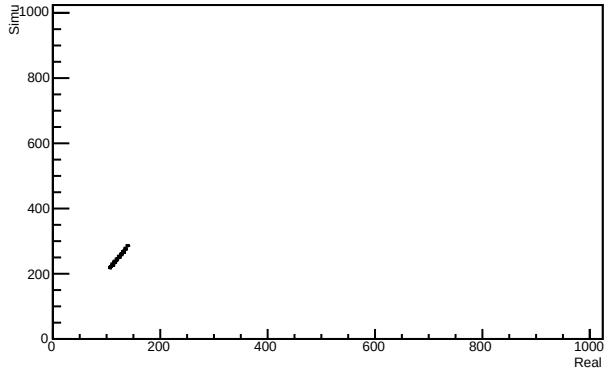

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 20000
9794
9507
206
493
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

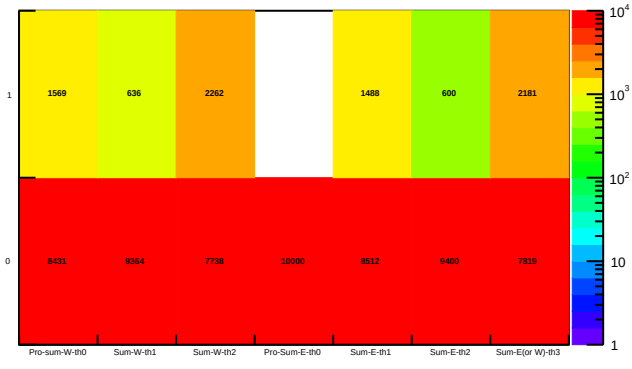
VT201 Input Ch2: ZDC Tac Diff



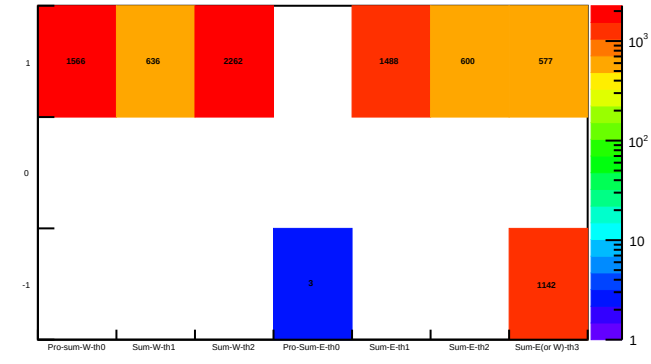
VT201 Input Ch2: ZDC Tac Diff



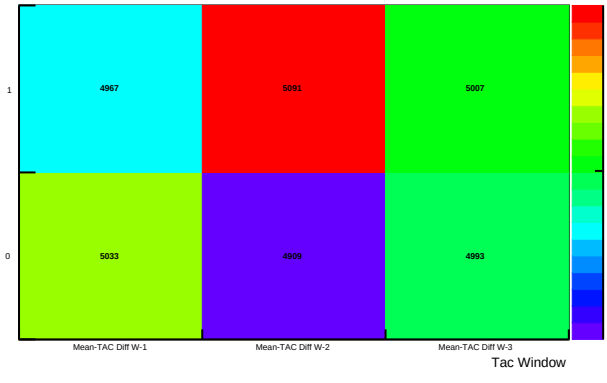
VT201 Input Ch2: ZDC Adc Sum Th



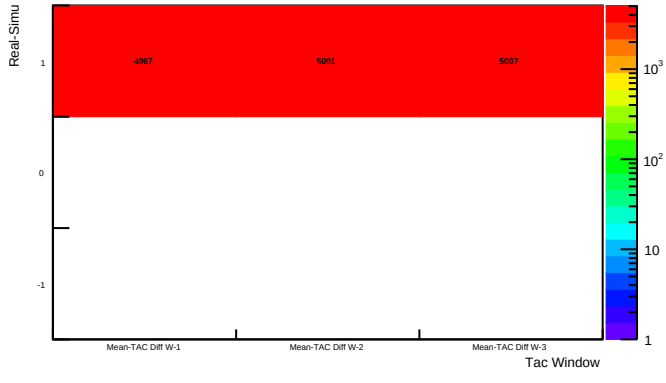
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



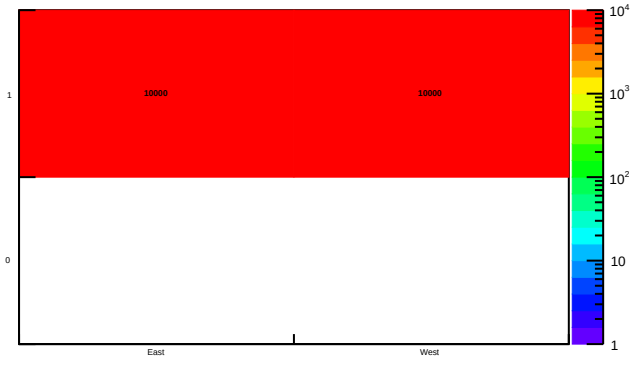
VT201 Input Ch4: VPD Tac Diff



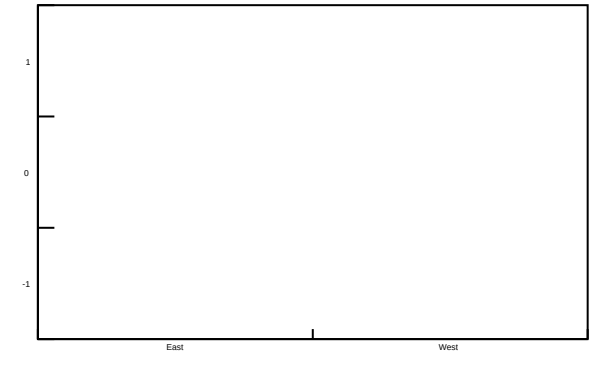
VT201 Input Ch4: VPD Tac Diff

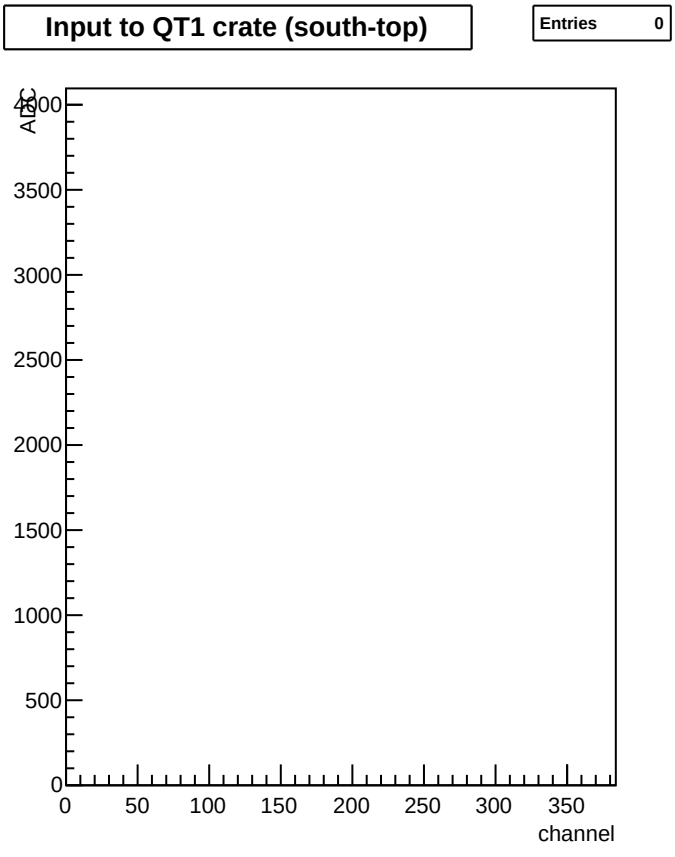


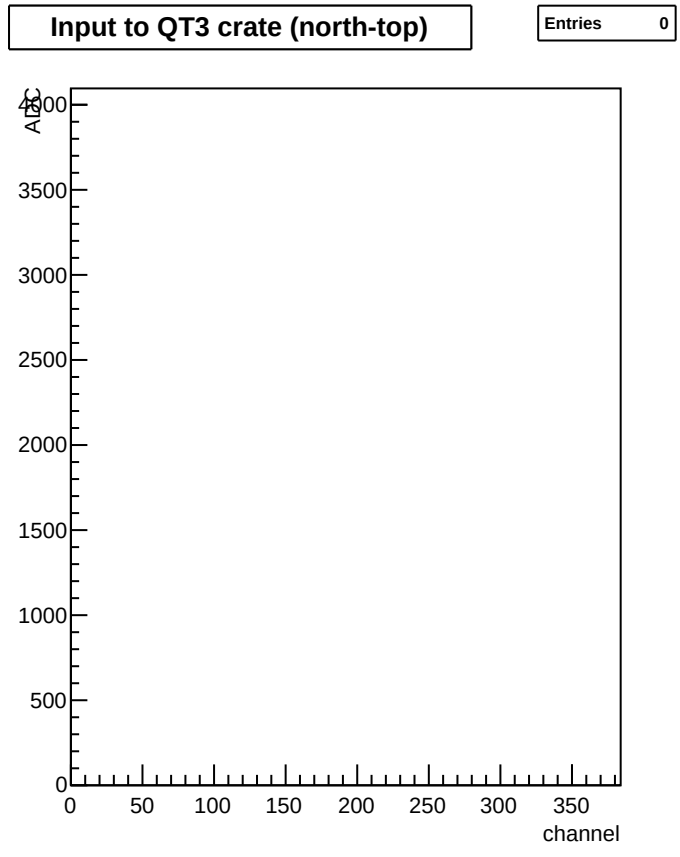
VT201 Input Ch4: VPD Adc Sum Th

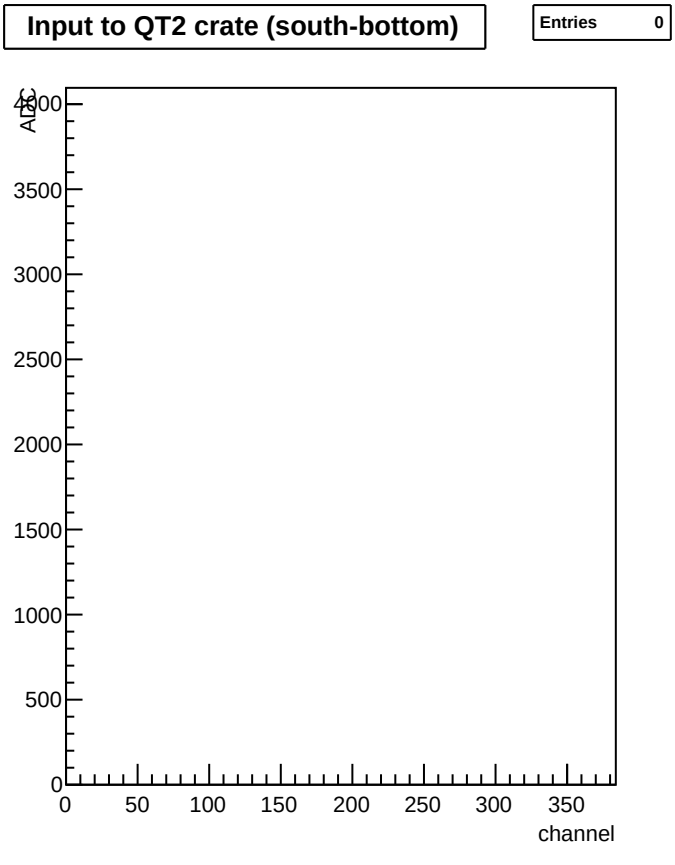


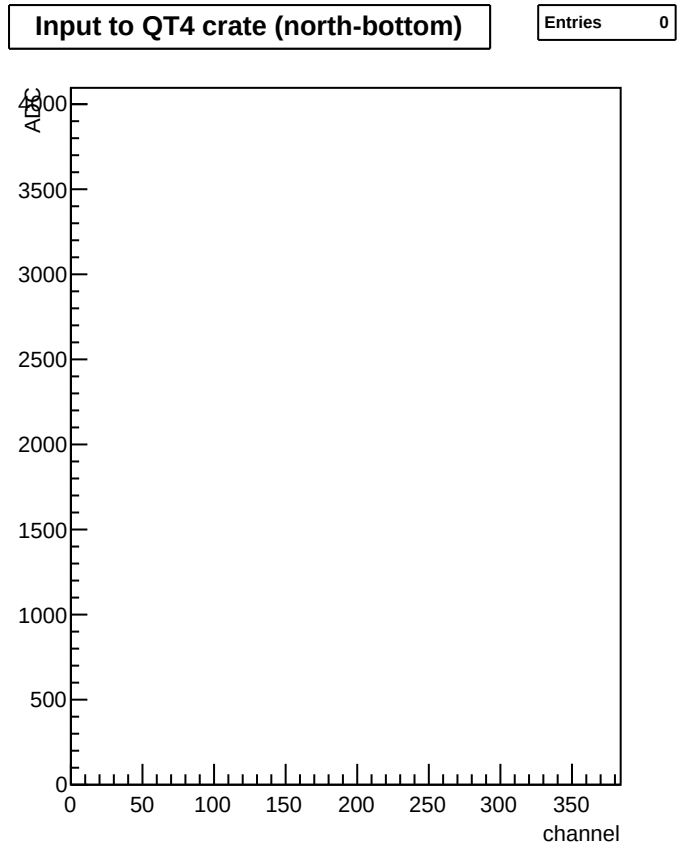
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

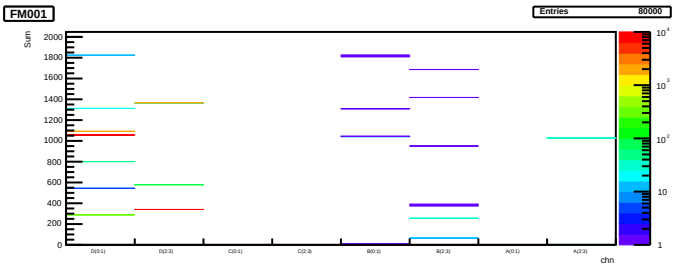


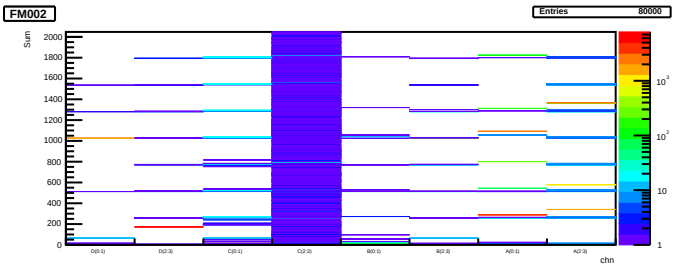

Input to QT1 crate (south-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

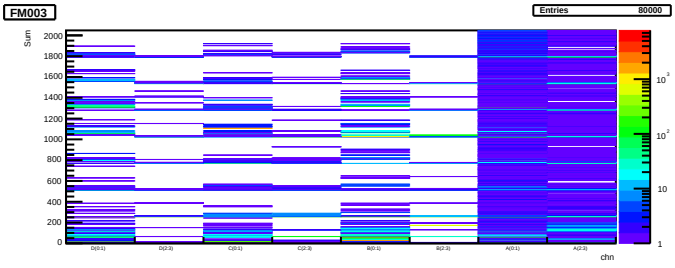

Input to QT3 crate (north-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

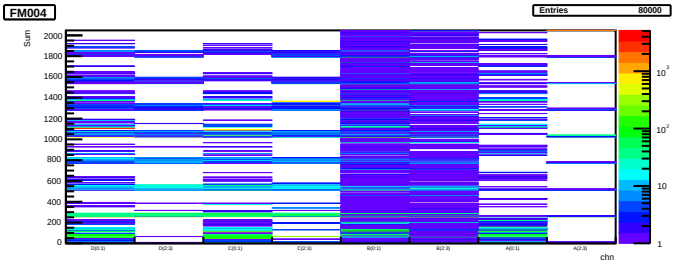

Input to QT2 crate (south-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

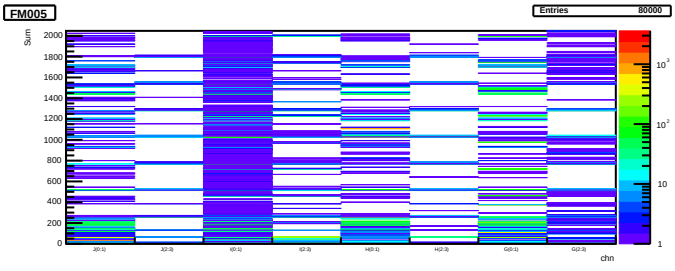

Input to QT4 crate (north-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

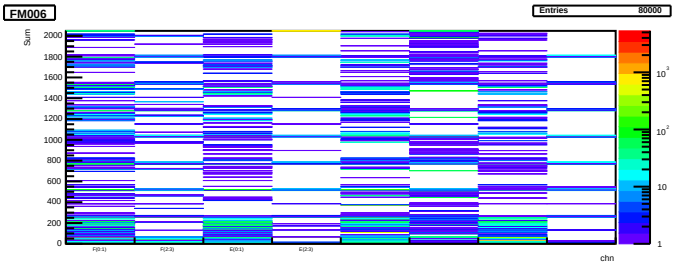

FM001
Entries 80000
Sum
chn

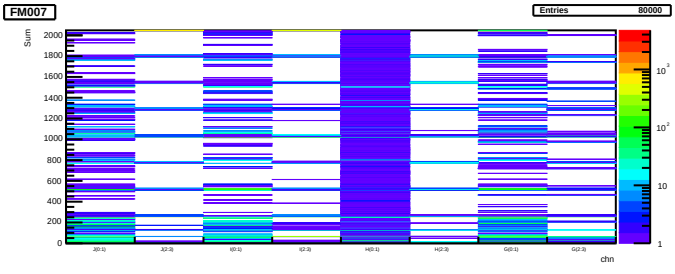

FM002
Entries 80000
Sum
chn

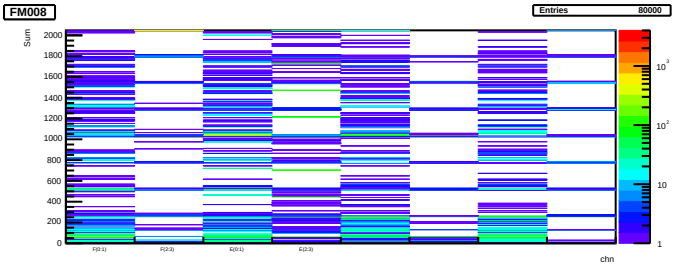

FM003
Entries 80000
Sum
chn

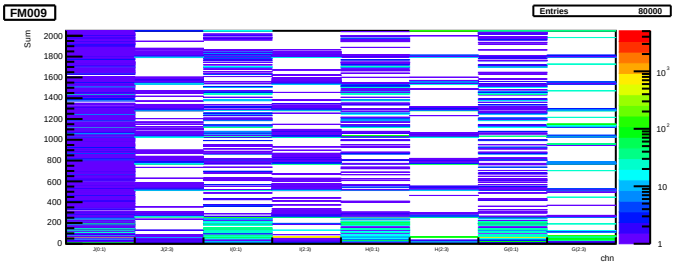

FM004
Entries 80000
Sum
chn

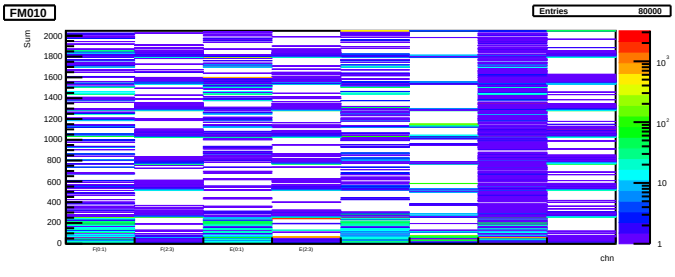

FM005
Entries 80000
Sum
chn

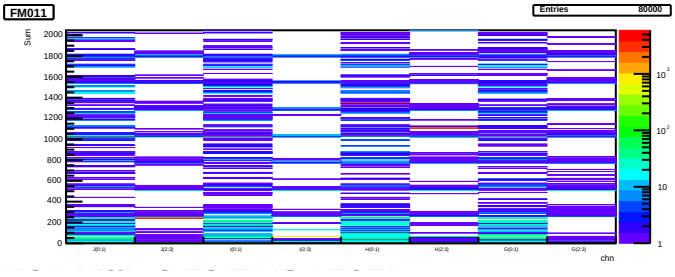

FM006
Entries 80000
Sum
chn

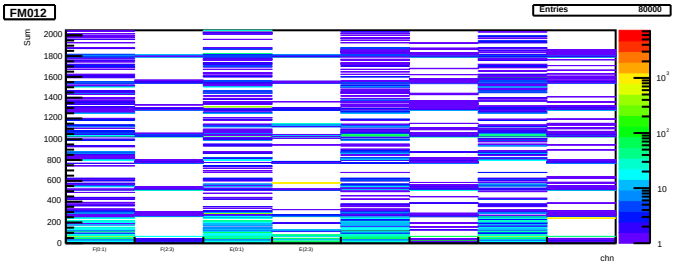

FM007
Entries 80000
Sum
chn

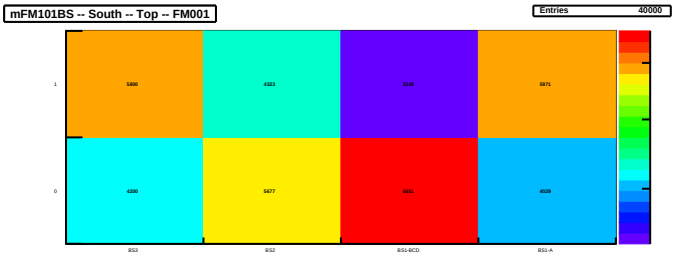

FM008
Entries 80000
Sum
chn

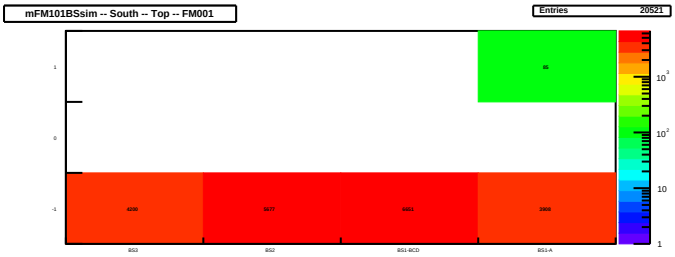

FM009
Entries 80000
Sum
chn

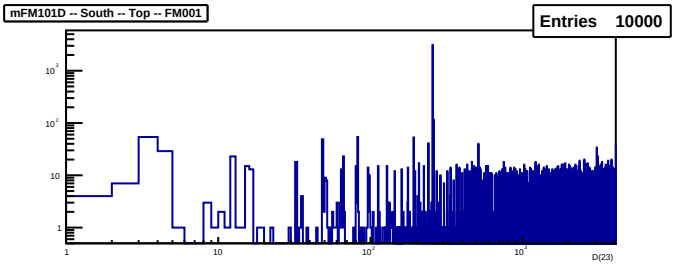

FM010
Entries 80000
Sum
chn

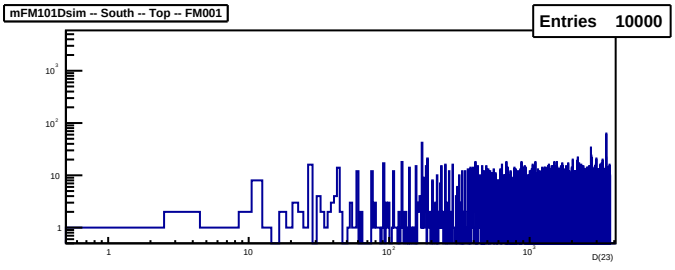

FM011
Entries 80000
Sum
chn

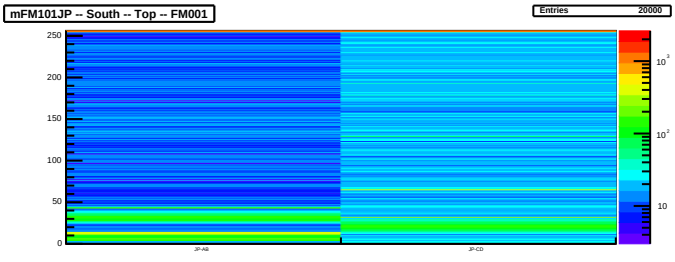

FM012
Entries 80000
Sum
chn

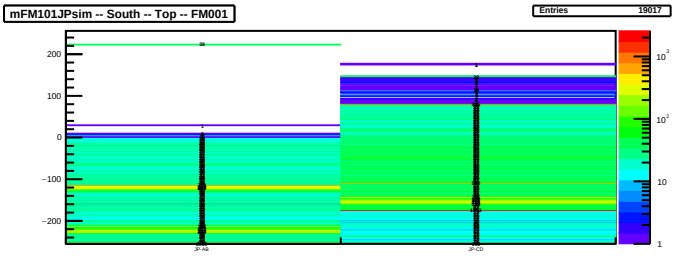

mFM101BS -- South -- Top -- FM001
Entries 40000
5800
4323
12410
5971
4200
5477
6651
4129
BS3
BS2
BS1-BCD
BS1-A

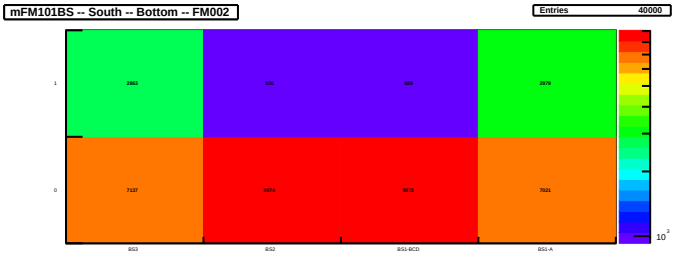

mFM101BSsim -- South -- Top -- FM001
Entries 20521
65
4200
5477
6651
2908
BS3
BS2
BS1-BCD
BS1-A

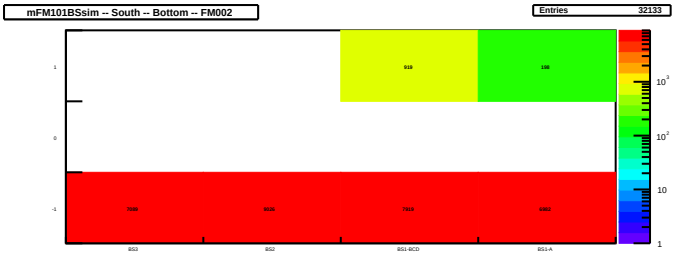

mFM101D -- South -- Top -- FM001
Entries 10000
D(23)

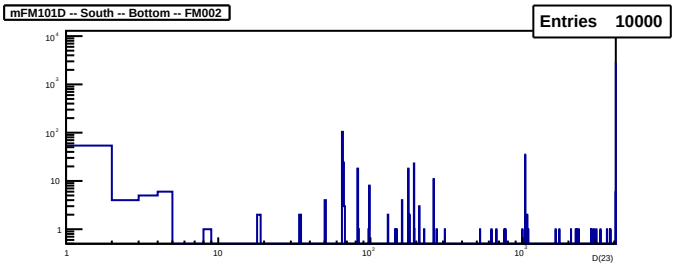

mFM101Dsim -- South -- Top -- FM001
Entries 10000
D(23)

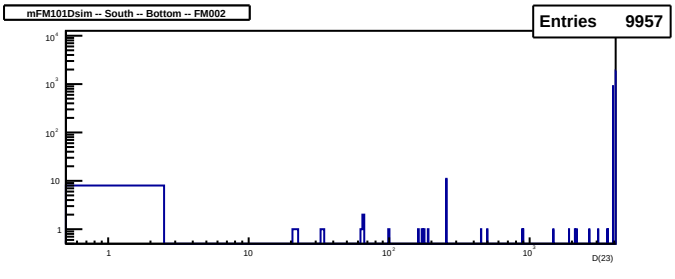

mFM101JP -- South -- Top -- FM001
Entries 20000
JP-AB
JP-CD

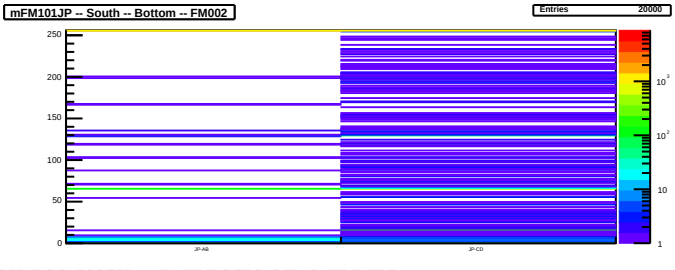

mFM101JPsim -- South -- Top -- FM001
Entries 19017
JP-AB
JP-CD

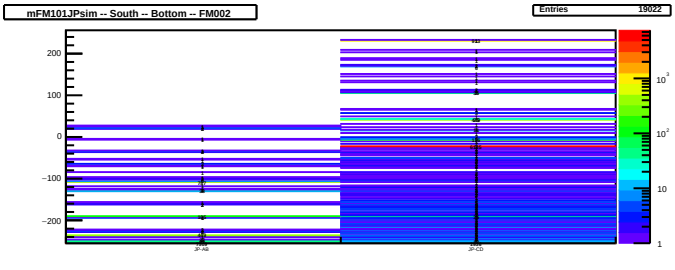

mFM101BS -- South -- Bottom -- FM002
Entries 40000
2863
926
920
2978
7137
9074
9072
7021
BS3
BS2
BS1-BCD
BS1-A

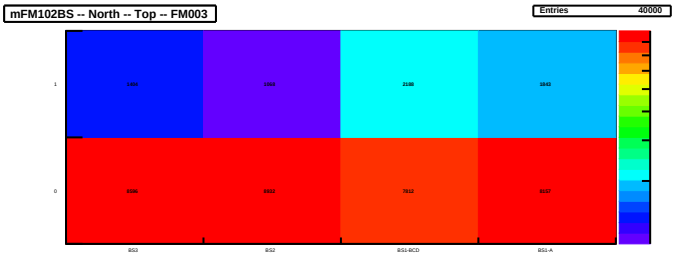

mFM101BSsim -- South -- Bottom -- FM002
Entries 32133
919
198
7099
9035
7919
6862
BS3
BS2
BS1-BCD
BS1-A

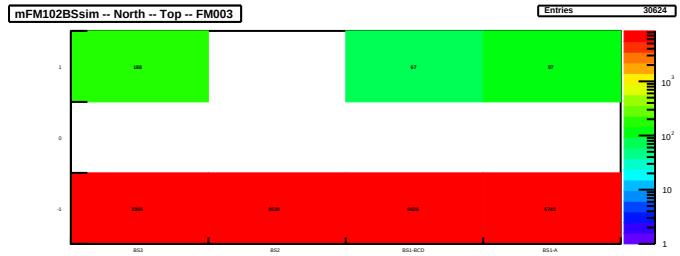

mFM101D -- South -- Bottom -- FM002
Entries 10000
D(23)

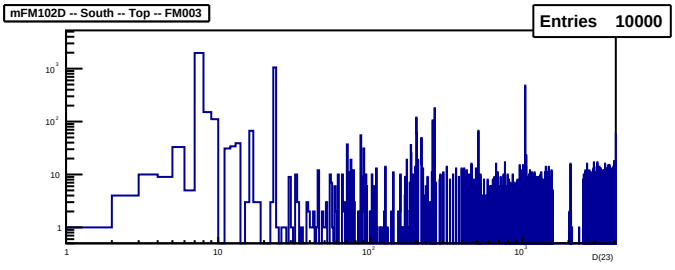

mFM101Dsim -- South -- Bottom -- FM002
Entries 9957
D(23)

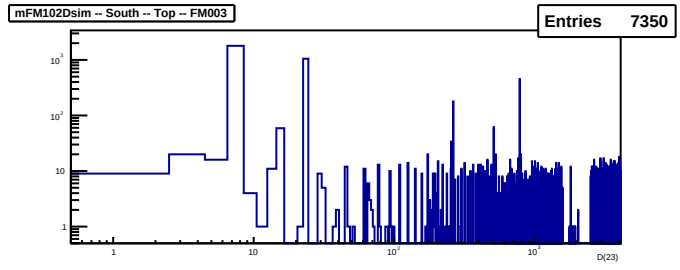

mFM101JP -- South -- Bottom -- FM002
Entries 20000
JP-AB
JP-CD

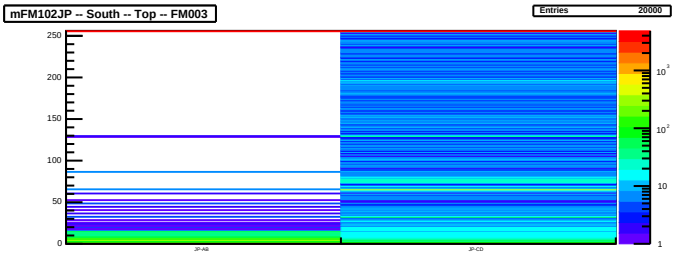

mFM101JPsim -- South -- Bottom -- FM002
Entries 19022
JP-AB
JP-CD

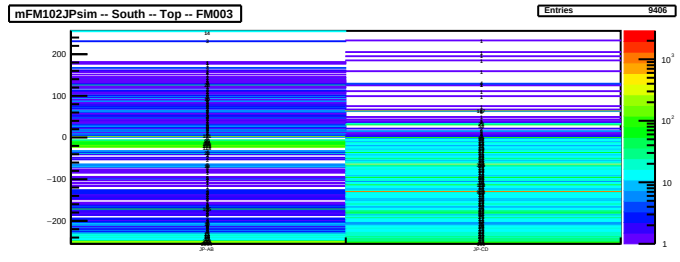

mFM102BS -- North -- Top -- FM003
Entries 40000

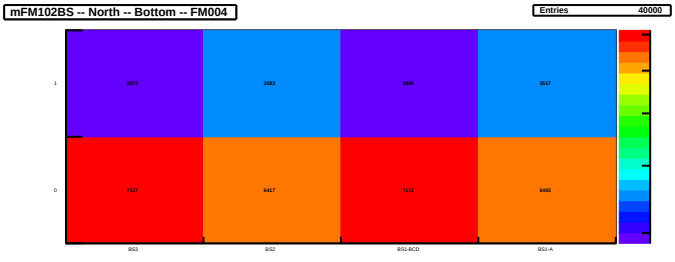

mFM102BSsim -- North -- Top -- FM003
Entries 30624

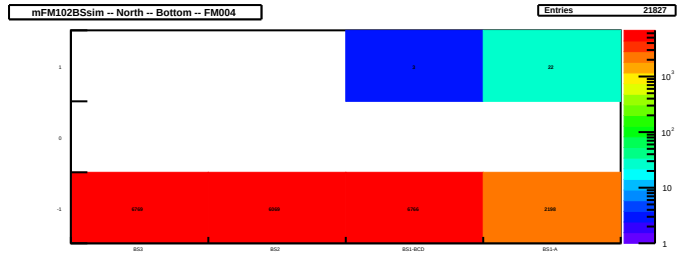

mFM102D -- South -- Top -- FM003
Entries 10000
D(23)

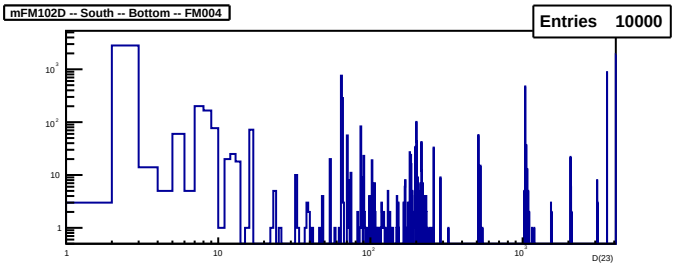

mFM102Dsim -- South -- Top -- FM003
Entries 7350
D(23)

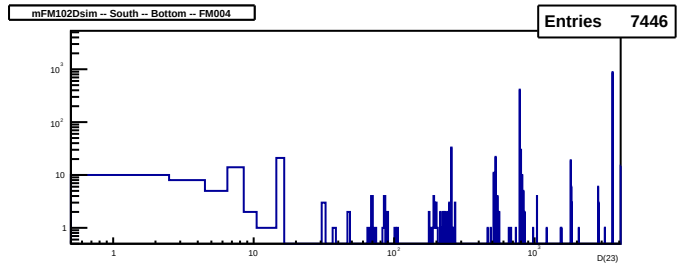

mFM102JP -- South -- Top -- FM003
Entries 20000

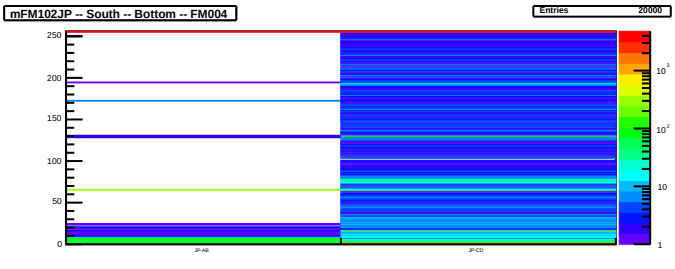

mFM102JPsim -- South -- Top -- FM003
Entries 9406

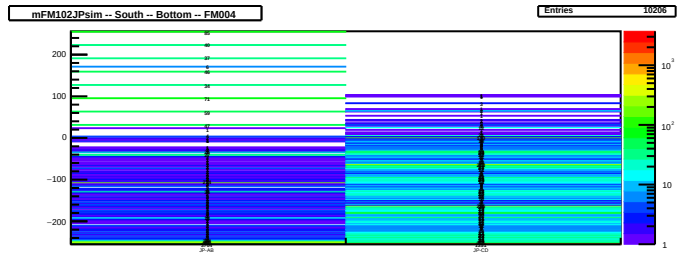

mFM102BS -- North -- Bottom -- FM004
Entries 40000

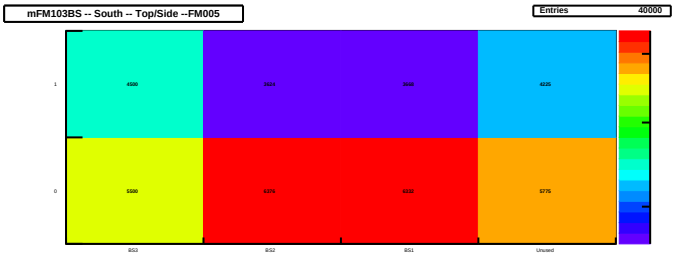

mFM102BSsim -- North -- Bottom -- FM004
Entries 21827

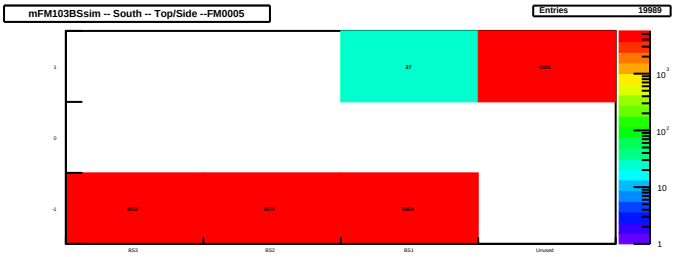

mFM102D -- South -- Bottom -- FM004
Entries 10000
D(23)

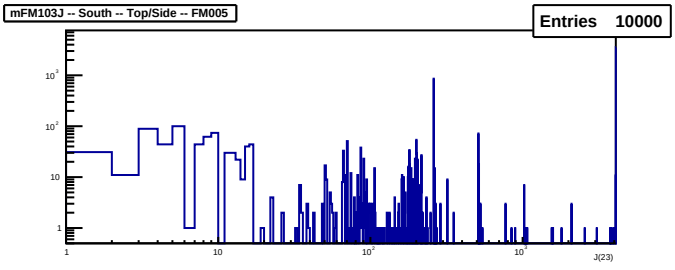

mFM102Dsim -- South -- Bottom -- FM004
Entries 7446
D(23)

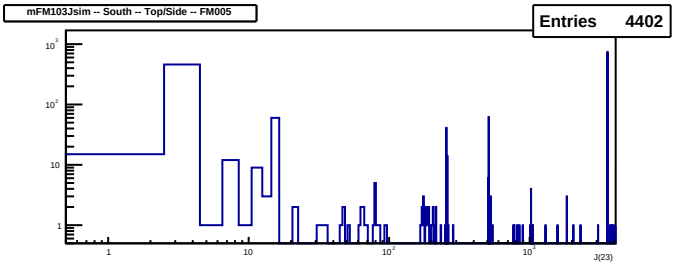

mFM102JP -- South -- Bottom -- FM004
Entries 20000

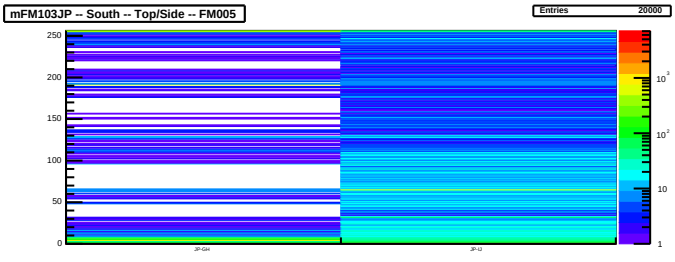

mFM102JPsim -- South -- Bottom -- FM004
Entries 10206

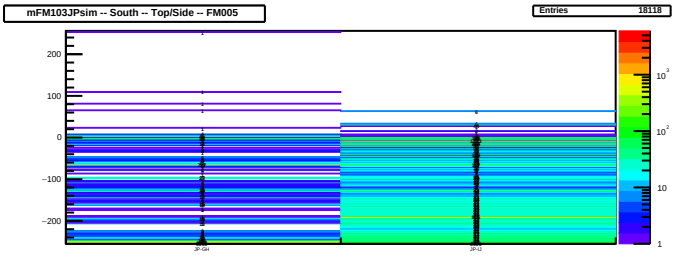

mFM103BS -- South -- Top/Side --FM005
Entries 40000

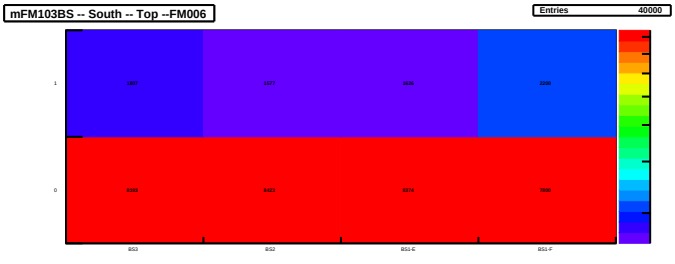

mFM103BSsim -- South -- Top/Side --FM0005
Entries 19989

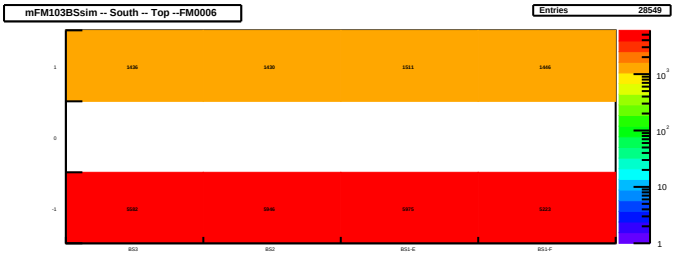

mFM103J -- South -- Top/Side -- FM005
Entries 10000
J(23)

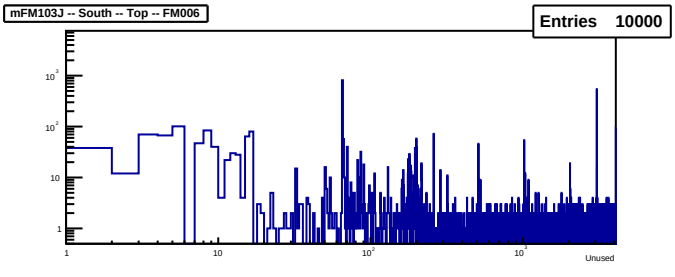

mFM103Jsim -- South -- Top/Side -- FM005
Entries 4402
J(23)

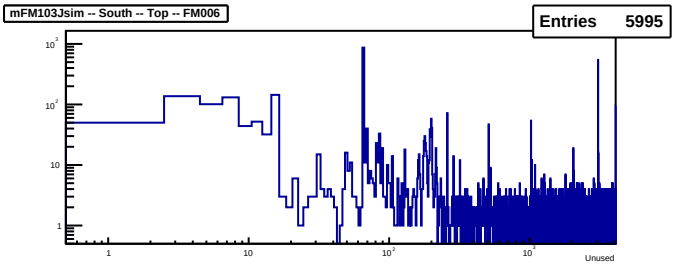

mFM103JP -- South -- Top/Side -- FM005
Entries 20000

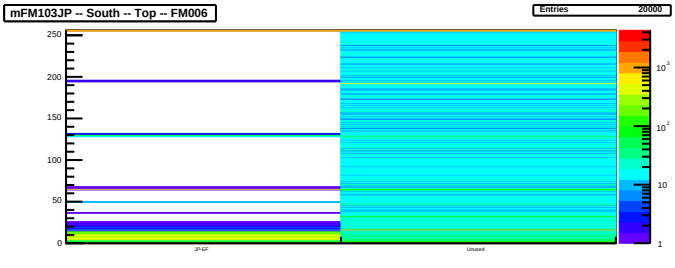

mFM103JPsim -- South -- Top/Side -- FM005
Entries 18118

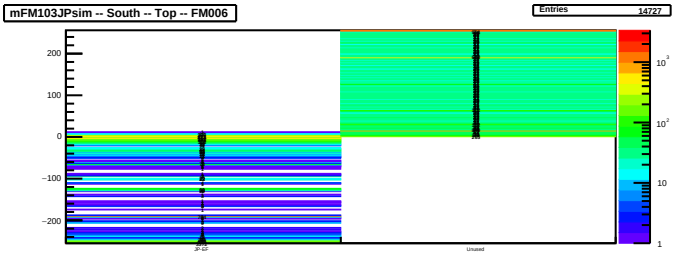

mFM103BS -- South -- Top --FM006
Entries 40000

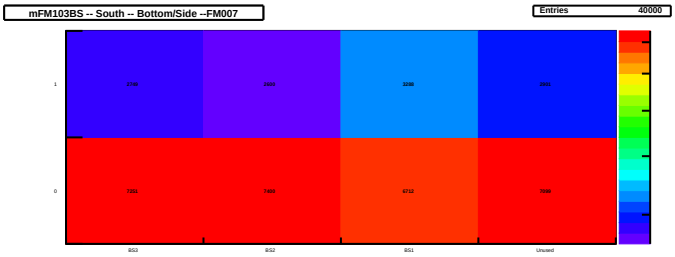

mFM103BSsim -- South -- Top --FM0006
Entries 28549

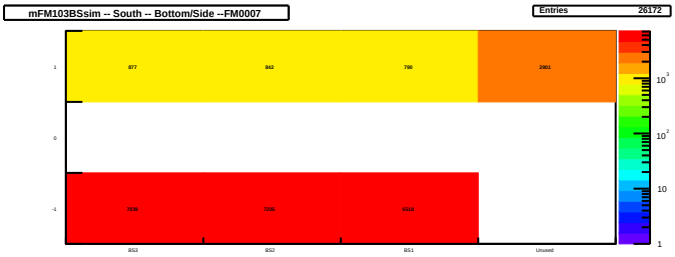

mFM103J -- South -- Top -- FM006
Entries 10000
Unused

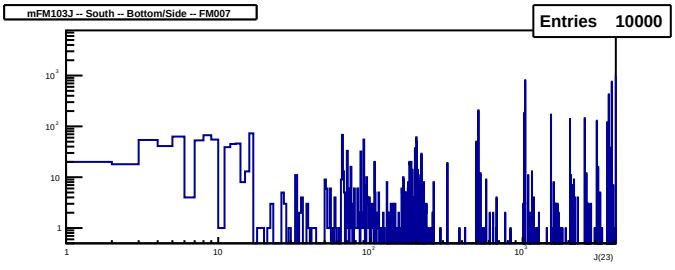

mFM103Jsim -- South -- Top -- FM006
Entries 5995
Unused

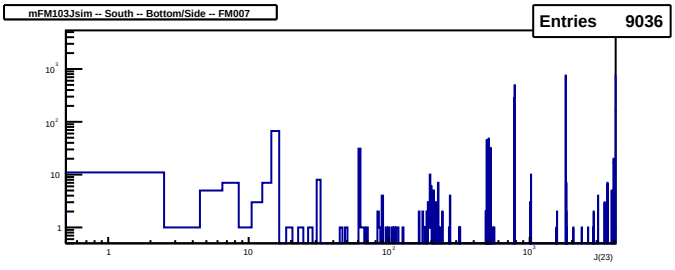

mFM103JP -- South -- Top -- FM006
Entries 20000

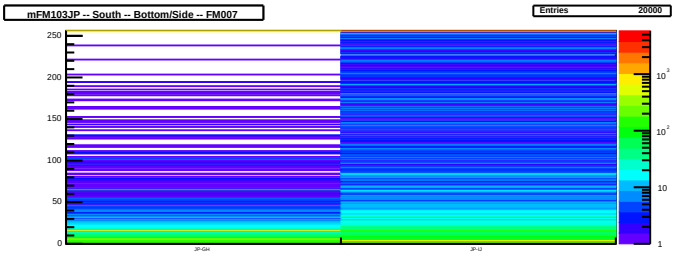

mFM103JPsim -- South -- Top -- FM006
Entries 14727

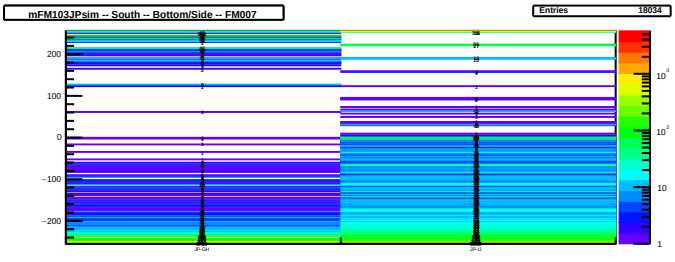

mFM103BS -- South -- Bottom/Side --FM007
Entries 40000

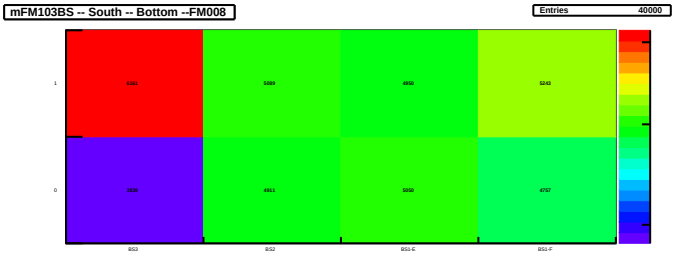

mFM103BSsim -- South -- Bottom/Side --FM0007
Entries 26172

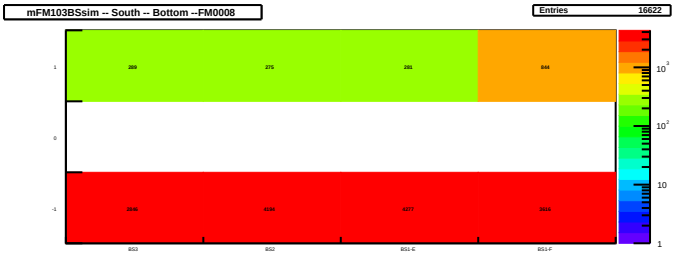

mFM103J -- South -- Bottom/Side -- FM007
Entries 10000
J(23)

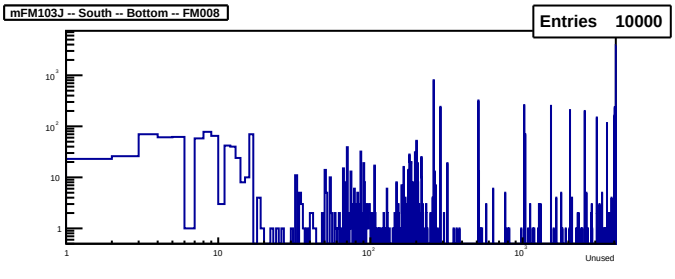

mFM103Jsim -- South -- Bottom/Side -- FM007
Entries 9036
J(23)

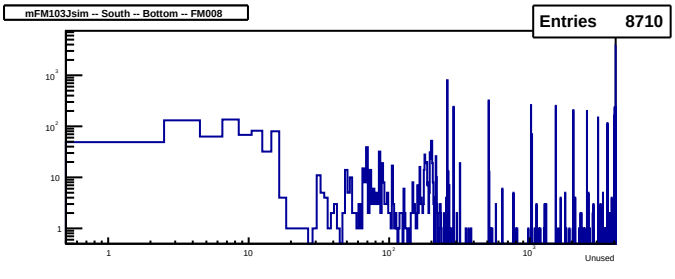

mFM103JP -- South -- Bottom/Side -- FM007
Entries 20000

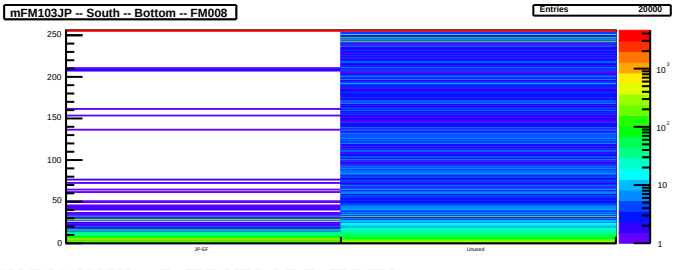

mFM103JPsim -- South -- Bottom/Side -- FM007
Entries 18034

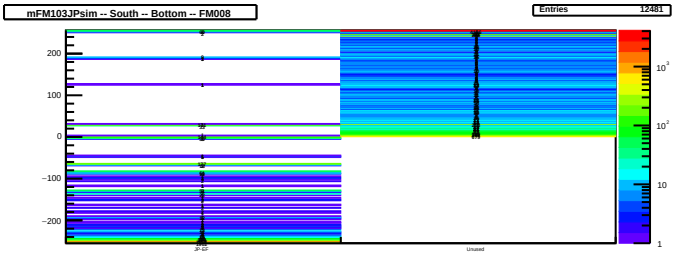

mFM103BS -- South -- Bottom --FM008
Entries 40000

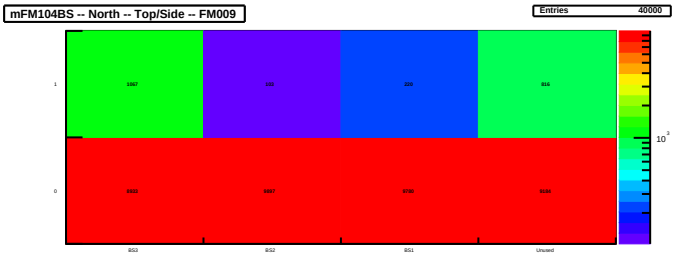

mFM103BSsim -- South -- Bottom --FM0008
Entries 16622

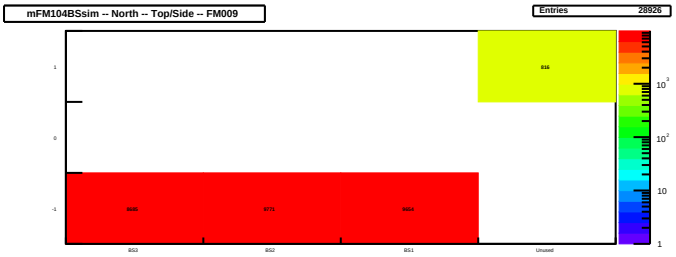

mFM103J -- South -- Bottom -- FM008
Entries 10000
Unused

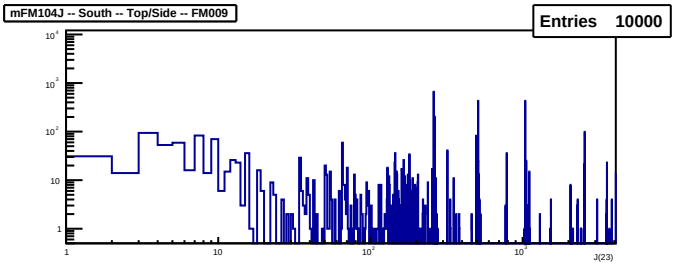

mFM103Jsim -- South -- Bottom -- FM008
Entries 8710
Unused

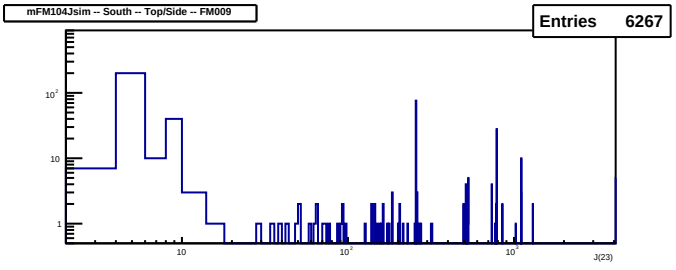

mFM103JP -- South -- Bottom -- FM008
Entries 20000

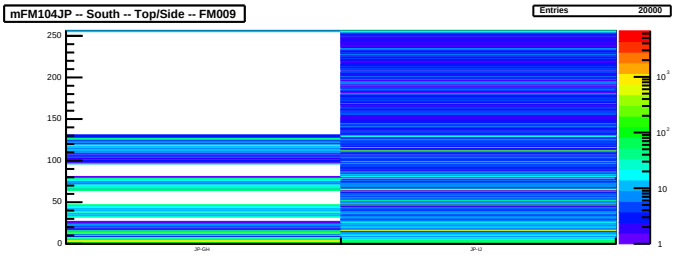

mFM103JPsim -- South -- Bottom -- FM008
Entries 12481

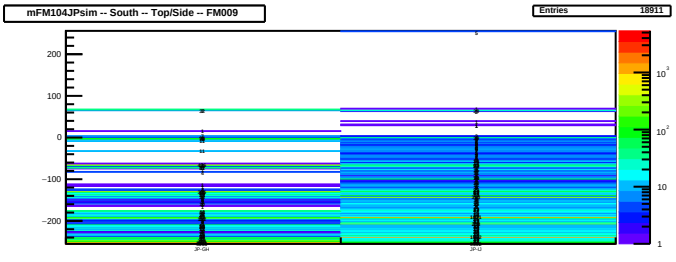

mFM104BS -- North -- Top/Side -- FM009
Entries 40000

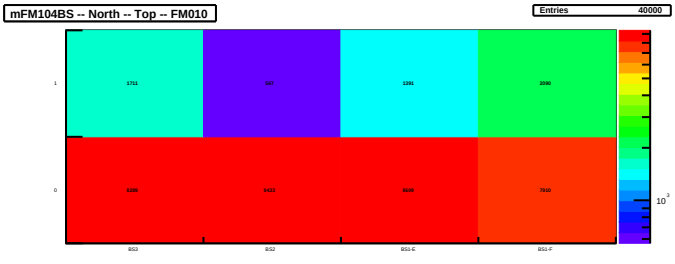

mFM104BSsim -- North -- Top/Side -- FM009
Entries 28926

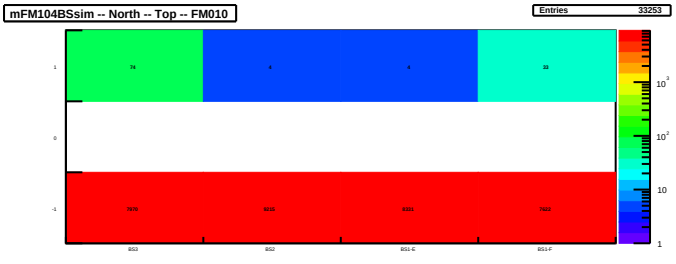

mFM104J -- South -- Top/Side -- FM009
Entries 10000
J(23)

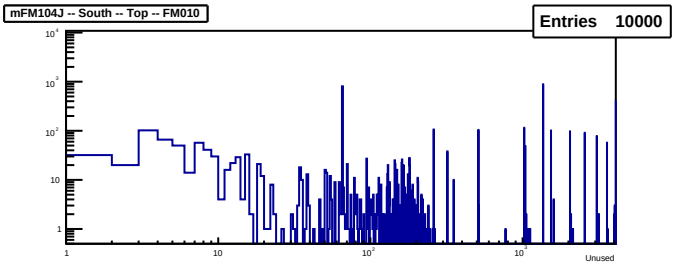

mFM104Jsim -- South -- Top/Side -- FM009
Entries 6267
J(23)

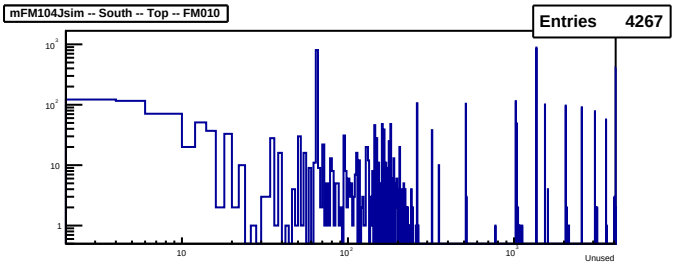

mFM104JP -- South -- Top/Side -- FM009
Entries 20000

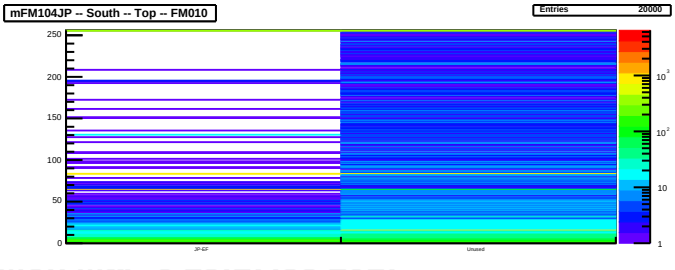

mFM104JPsim -- South -- Top/Side -- FM009
Entries 18911

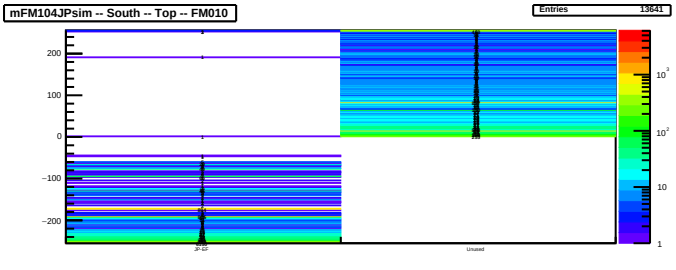

mFM104BS -- North -- Top -- FM010
Entries 40000

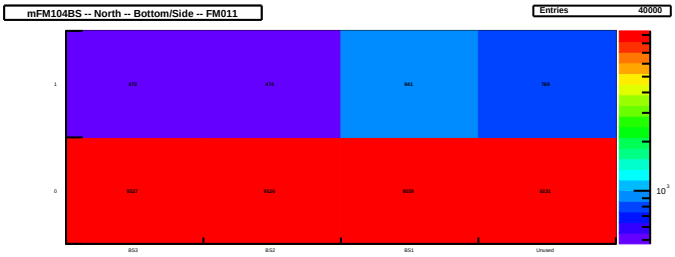

mFM104BSsim -- North -- Top -- FM010
Entries 33253

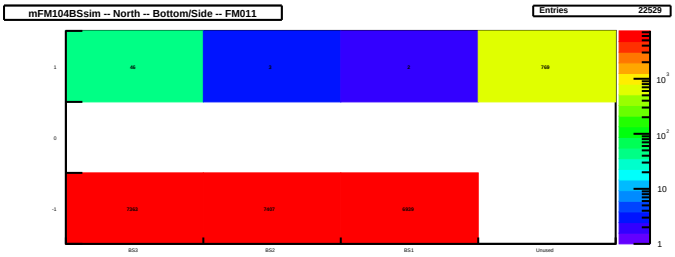

mFM104J -- South -- Top -- FM010
Entries 10000
Unused

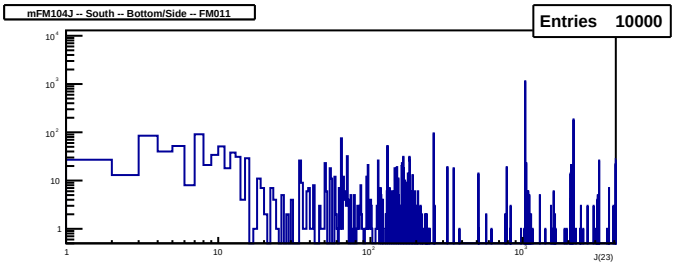

mFM104Jsim -- South -- Top -- FM010
Entries 4267
Unused

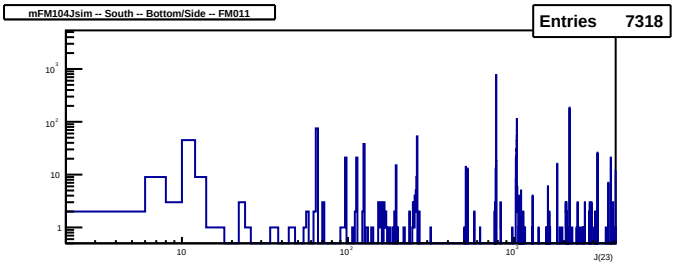

mFM104JP -- South -- Top -- FM010
Entries 20000

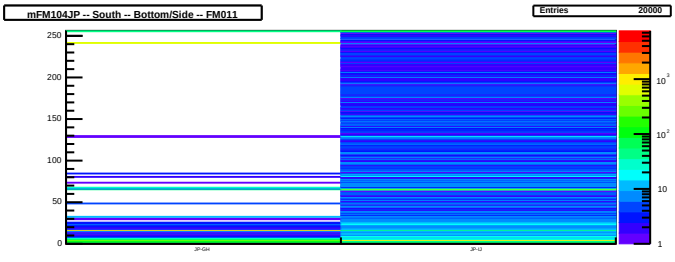

mFM104JPsim -- South -- Top -- FM010
Entries 13641

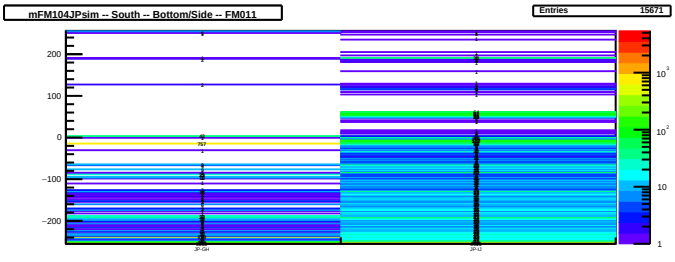

mFM104BS -- North -- Bottom/Side -- FM011
Entries 40000

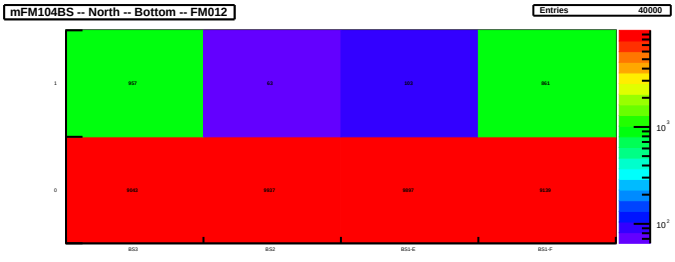

mFM104BSsim -- North -- Bottom/Side -- FM011
Entries 22529

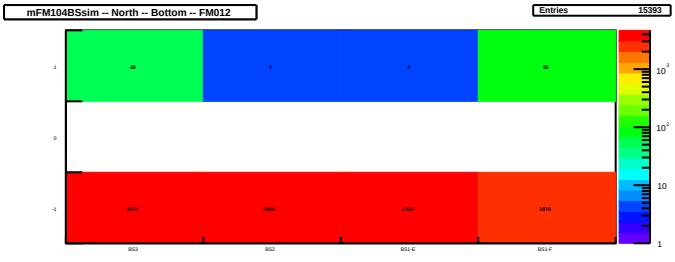

mFM104J -- South -- Bottom/Side -- FM011
Entries 10000
J(23)

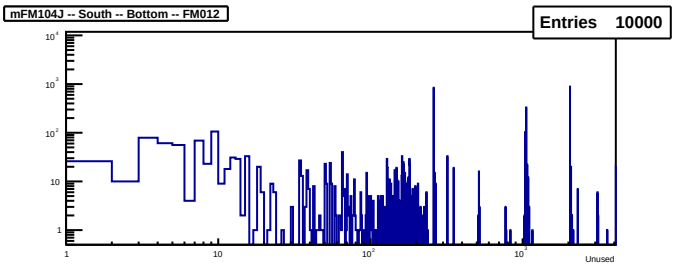

mFM104Jsim -- South -- Bottom/Side -- FM011
Entries 7318
J(23)

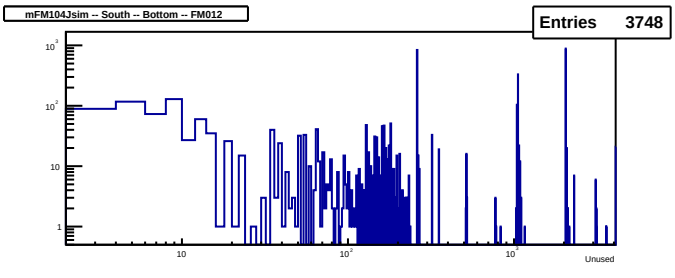

mFM104JP -- South -- Bottom/Side -- FM011
Entries 20000

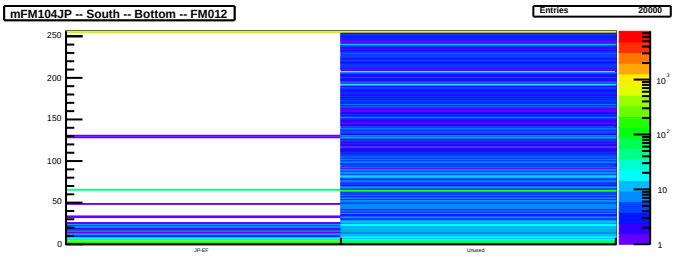

mFM104JPsim -- South -- Bottom/Side -- FM011
Entries 15671

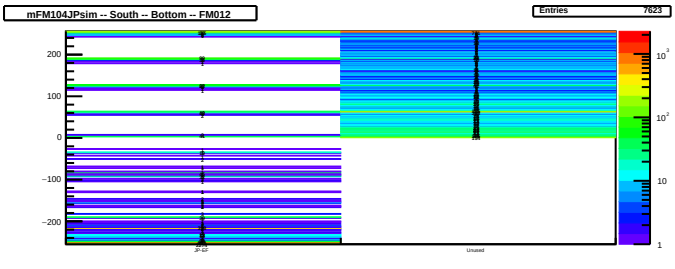

mFM104BS -- North -- Bottom -- FM012
Entries 40000

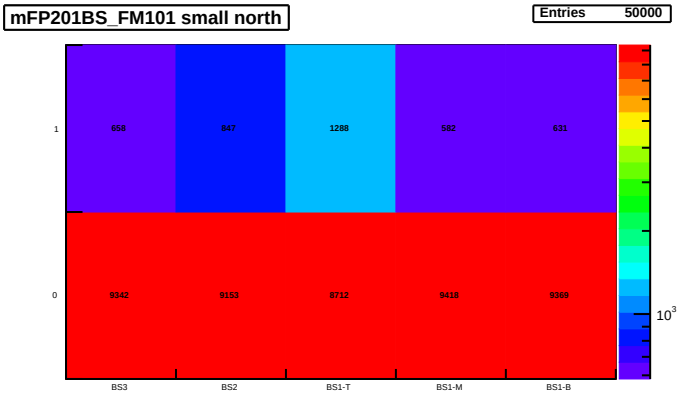

mFM104BSsim -- North -- Bottom -- FM012
Entries 15393

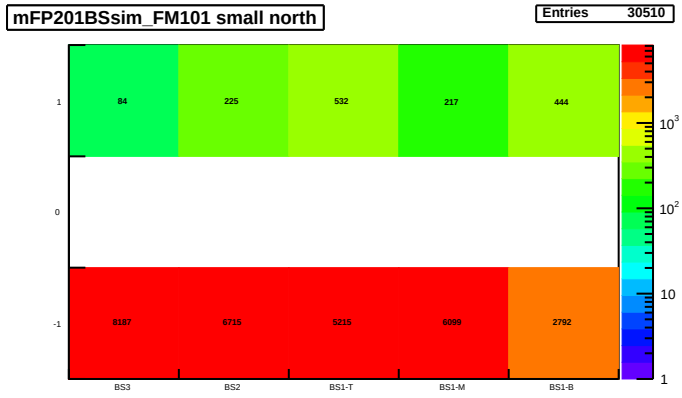

mFM104J -- South -- Bottom -- FM012
Entries 10000
Unused

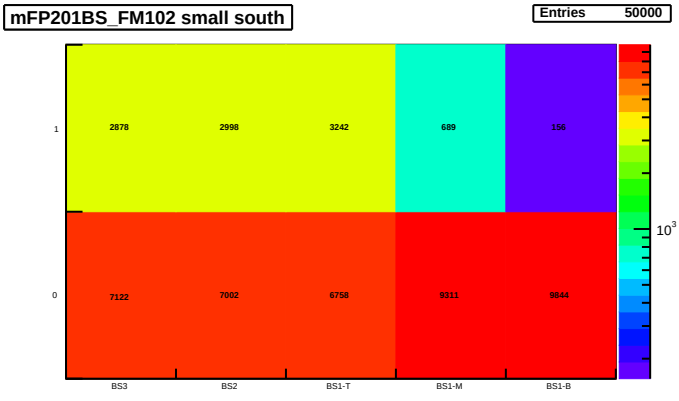

mFM104Jsim -- South -- Bottom -- FM012
Entries 3748
Unused

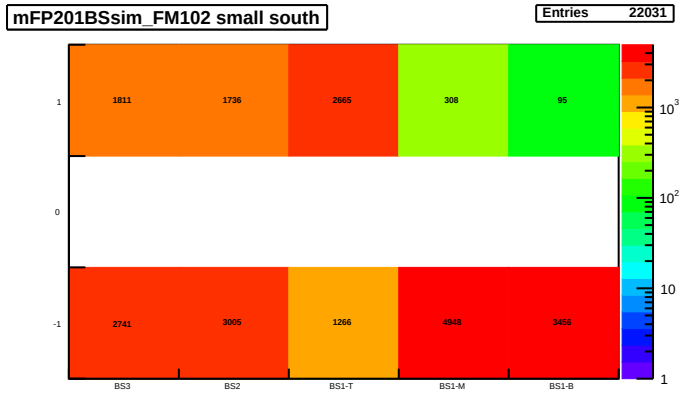

mFM104JP -- South -- Bottom -- FM012
Entries 20000

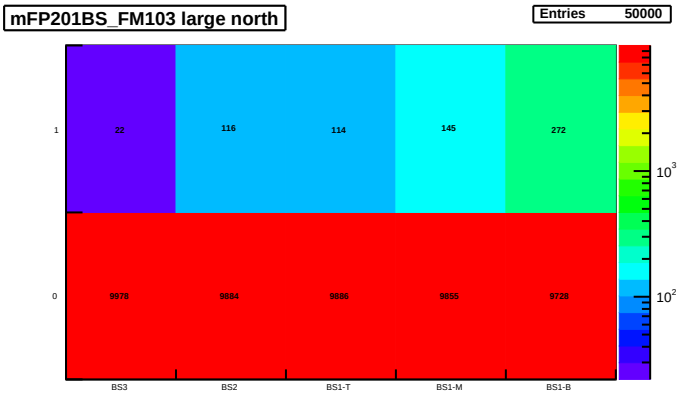

mFM104JPsim -- South -- Bottom -- FM012
Entries 7623

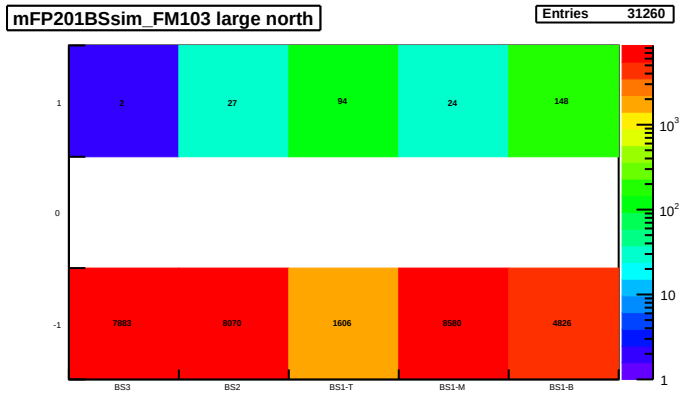

mFP201BS_FM101 small north
Entries 50000
658 847 1288 582 631
9342 9153 8712 9418 9369
BS3 BS2 BS1-T BS1-M BS1-B

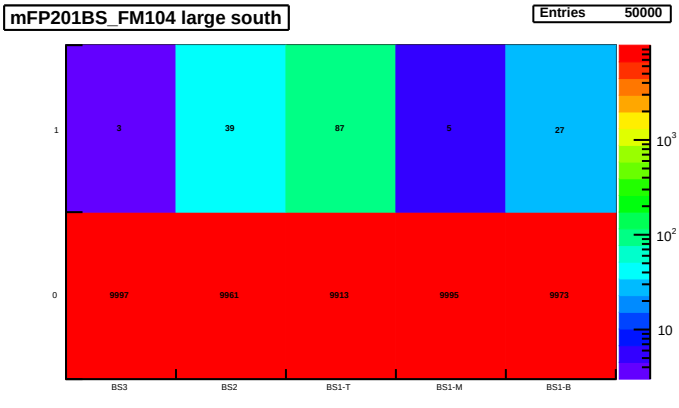

mFP201BSsim_FM101 small north
Entries 30510
84 225 532 217 444
8187 6715 5215 6099 2792
BS3 BS2 BS1-T BS1-M BS1-B

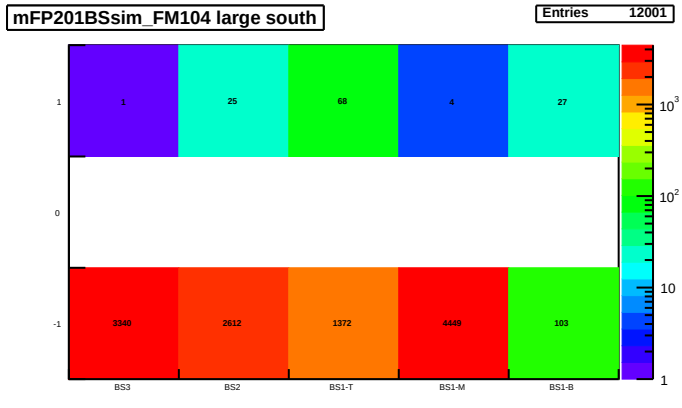

mFP201BS_FM102 small south
Entries 50000
2878 2998 3242 689 156
7122 7002 6758 9311 9844
BS3 BS2 BS1-T BS1-M BS1-B

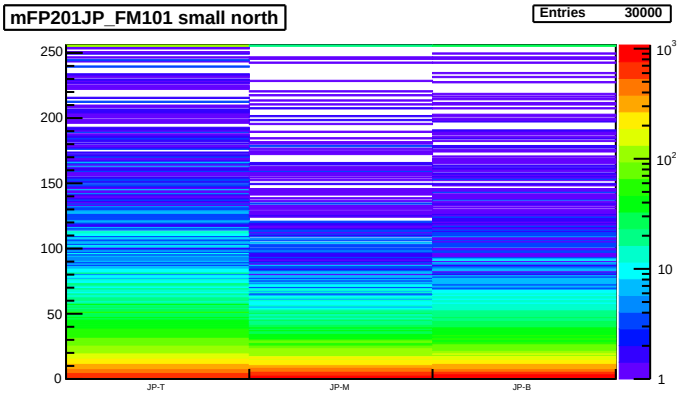

mFP201BSsim_FM102 small south
Entries 22031
1811 1736 2665 308 95
2741 3005 1266 4948 3456
BS3 BS2 BS1-T BS1-M BS1-B

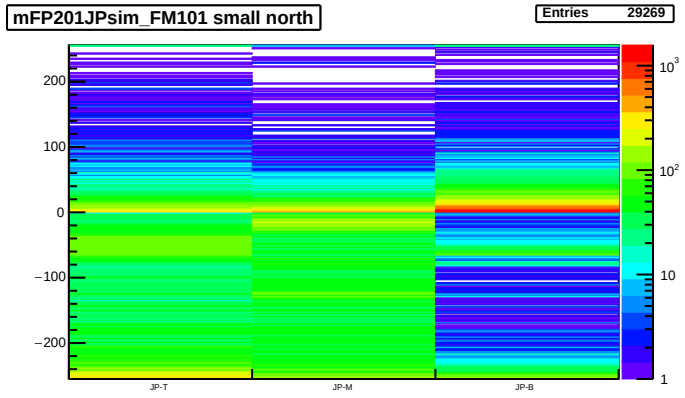

mFP201BS_FM103 large north
Entries 50000
22 116 114 145 272
9978 9884 9886 9855 9728
BS3 BS2 BS1-T BS1-M BS1-B

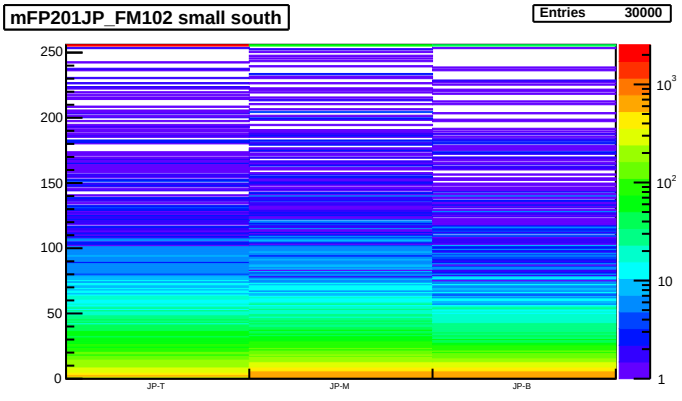

mFP201BSsim_FM103 large north
Entries 31260
2 27 94 24 148
7883 8070 1606 8580 4826
BS3 BS2 BS1-T BS1-M BS1-B

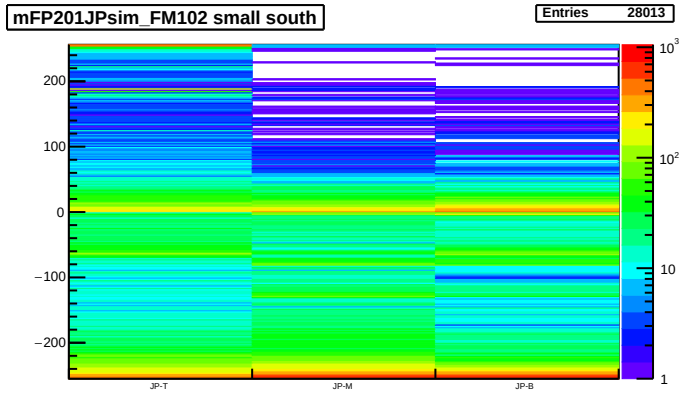

mFP201BS_FM104 large south
Entries 50000
3 39 87 5 27
9997 9961 9913 9995 9973
BS3 BS2 BS1-T BS1-M BS1-B

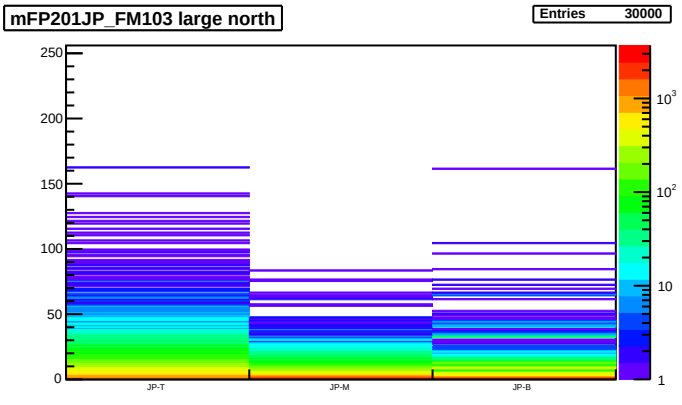

mFP201BSsim_FM104 large south
Entries 12001
1 25 68 4 27
3340 2612 1372 4449 103
BS3 BS2 BS1-T BS1-M BS1-B

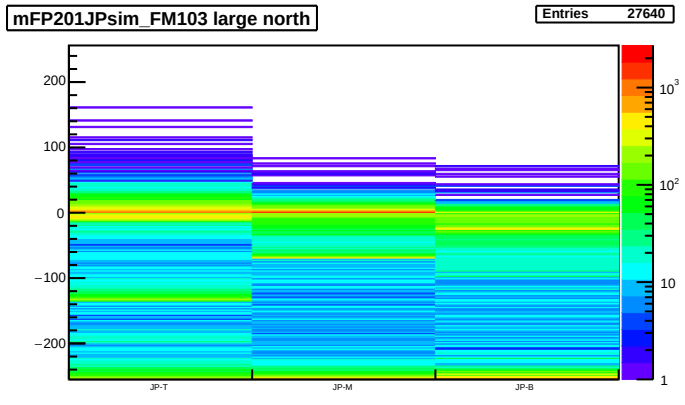

mFP201JP_FM101 small north
Entries 30000
JP-T
JP-M
JP-B

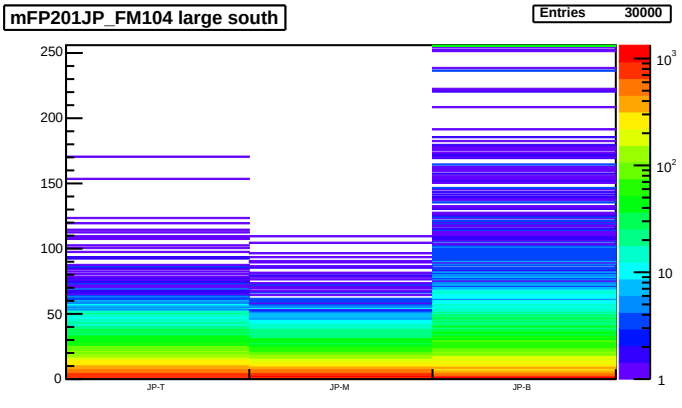

mFP201JPsim_FM101 small north
Entries 29269
JP-T
JP-M
JP-B

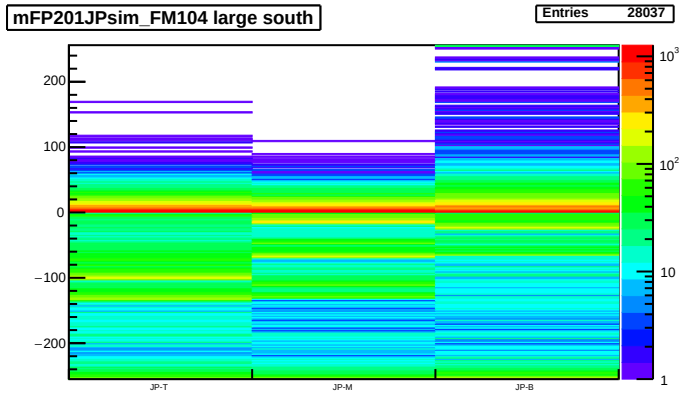

mFP201JP_FM102 small south
Entries 30000
JP-T
JP-M
JP-B

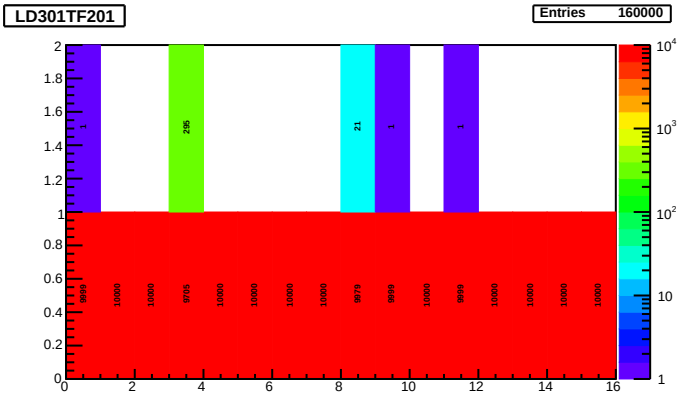

mFP201JPsim_FM102 small south
Entries 28013
JP-T
JP-M
JP-B

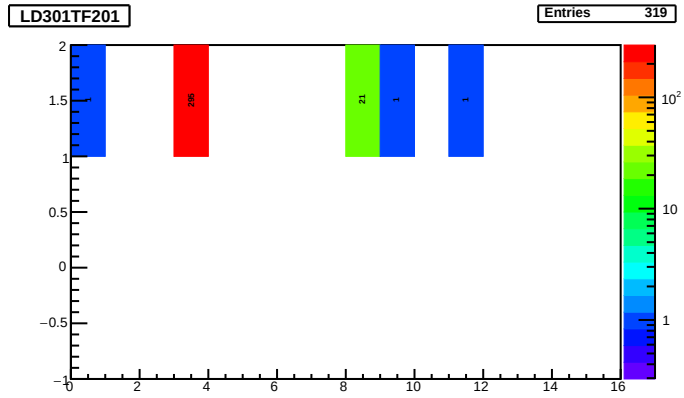

mFP201JP_FM103 large north
Entries 30000
JP-T
JP-M
JP-B

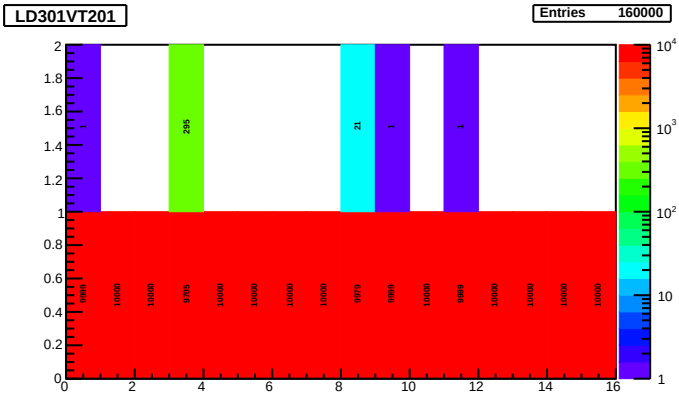

mFP201JPsim_FM103 large north
Entries 27640
JP-T
JP-M
JP-B

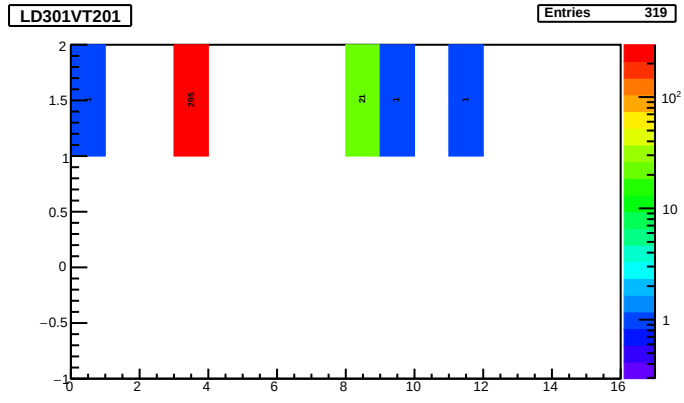

mFP201JP_FM104 large south
Entries 30000
JP-T
JP-M
JP-B

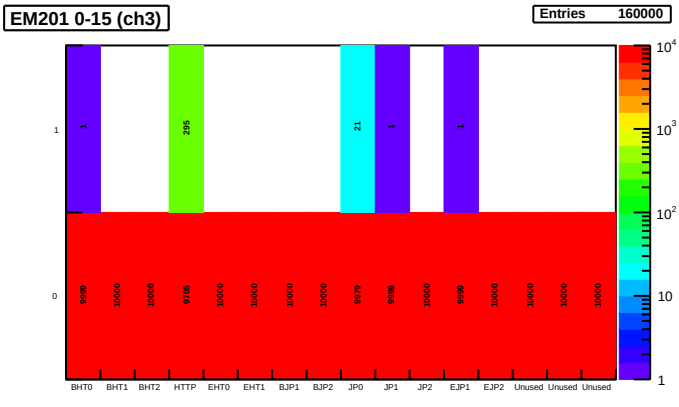

mFP201JPsim_FM104 large south
Entries 28037
JP-T
JP-M
JP-B

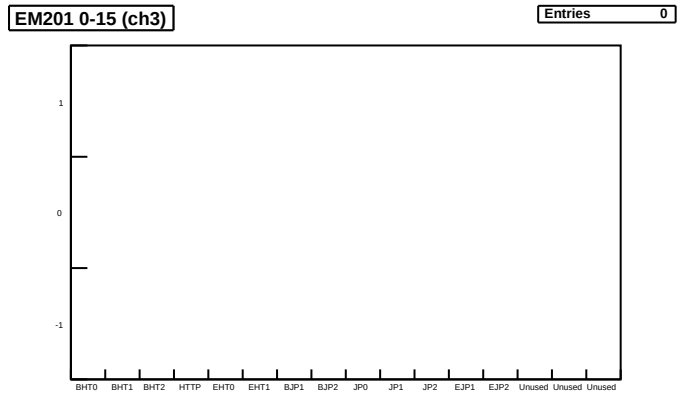

LD301TF201
Entries 160000

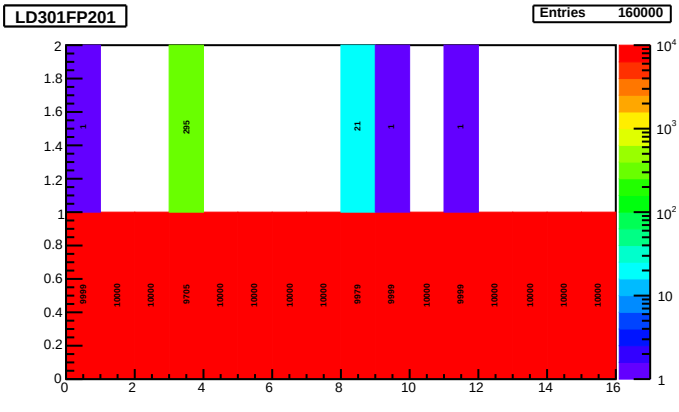

LD301TF201
Entries 319

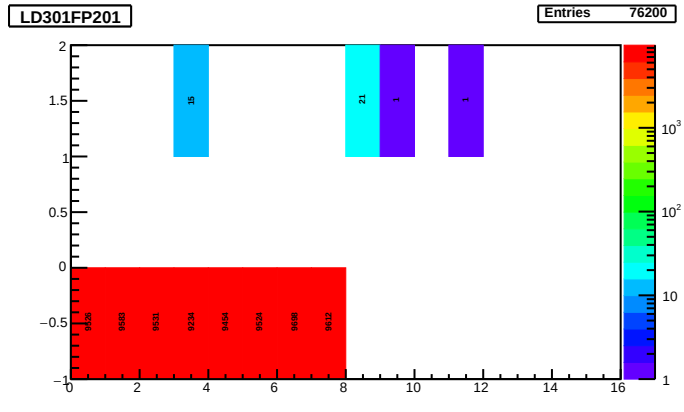

LD301VT201
Entries 160000


LD301VT201
Entries 319


EM201 0-15 (ch3)
Entries 160000
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


LD301FP201
Entries 160000


LD301FP201
Entries 76200