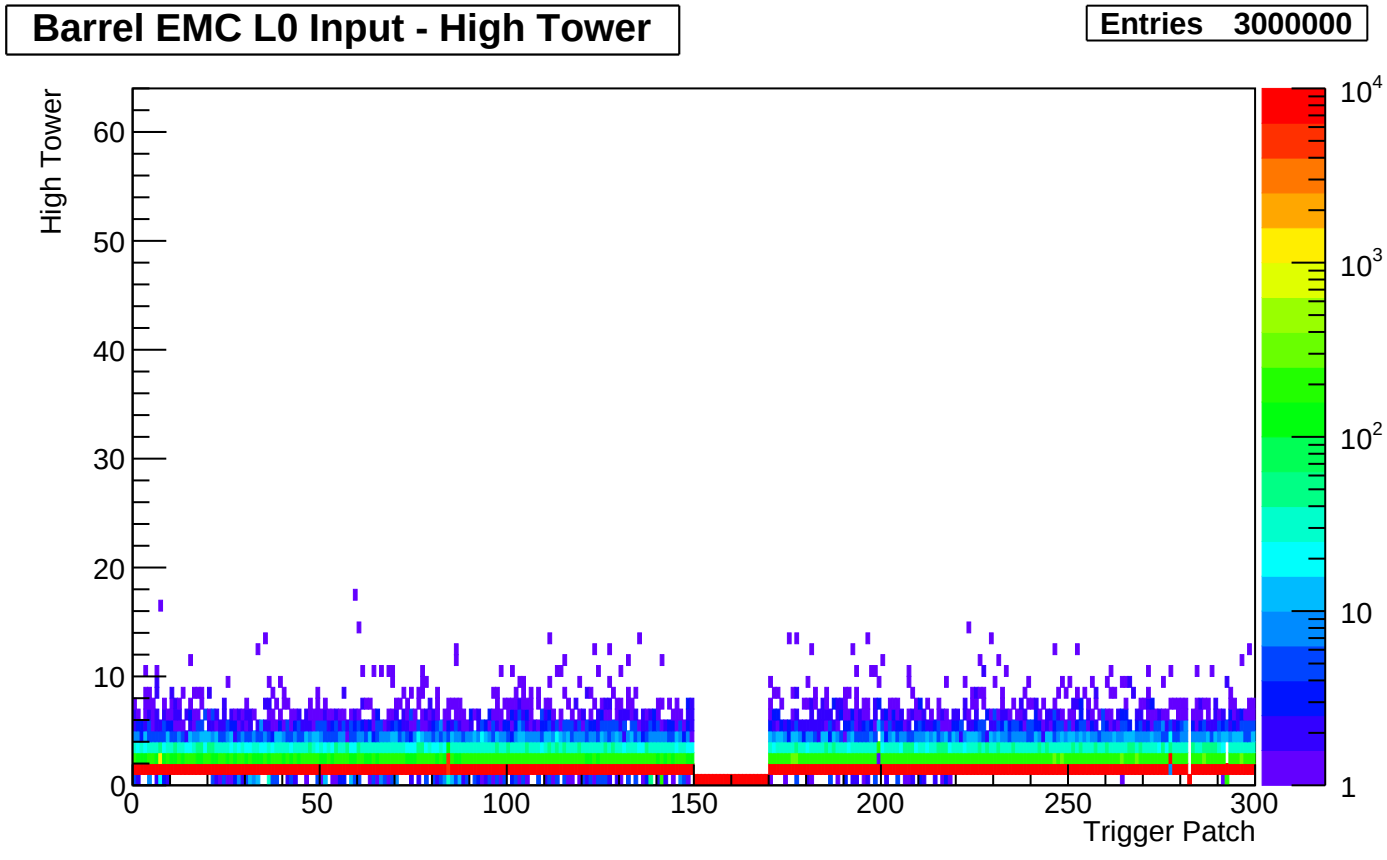
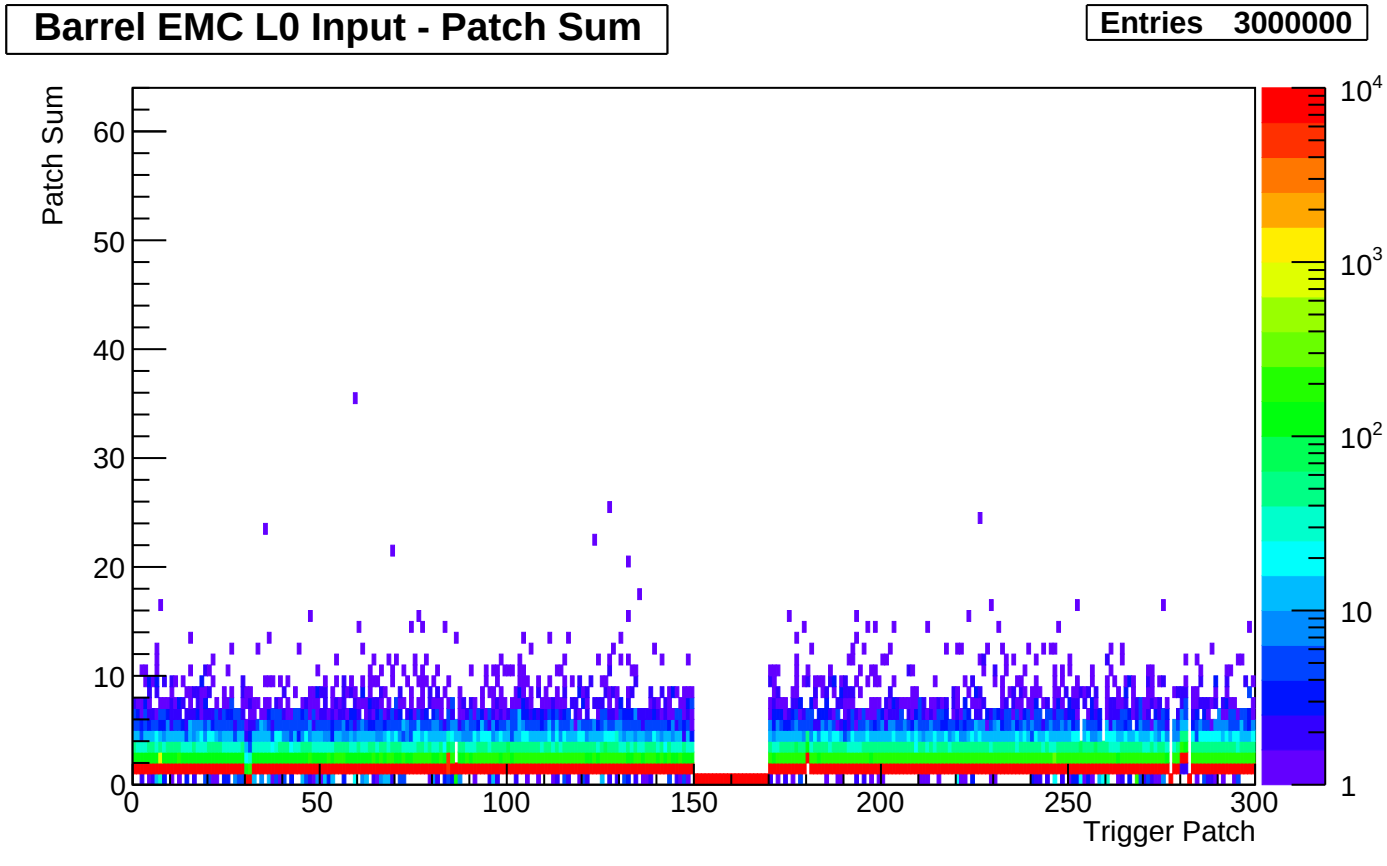
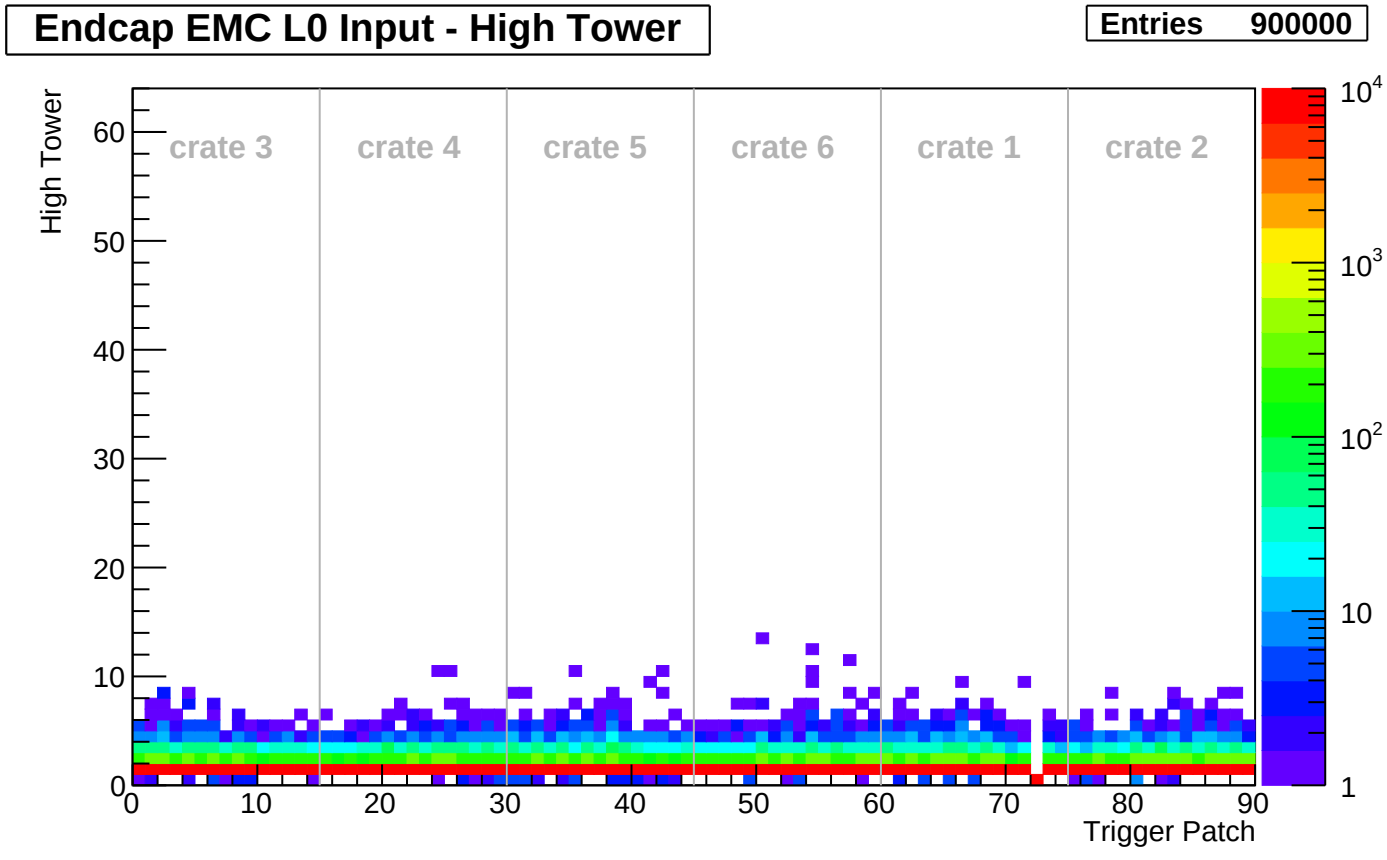
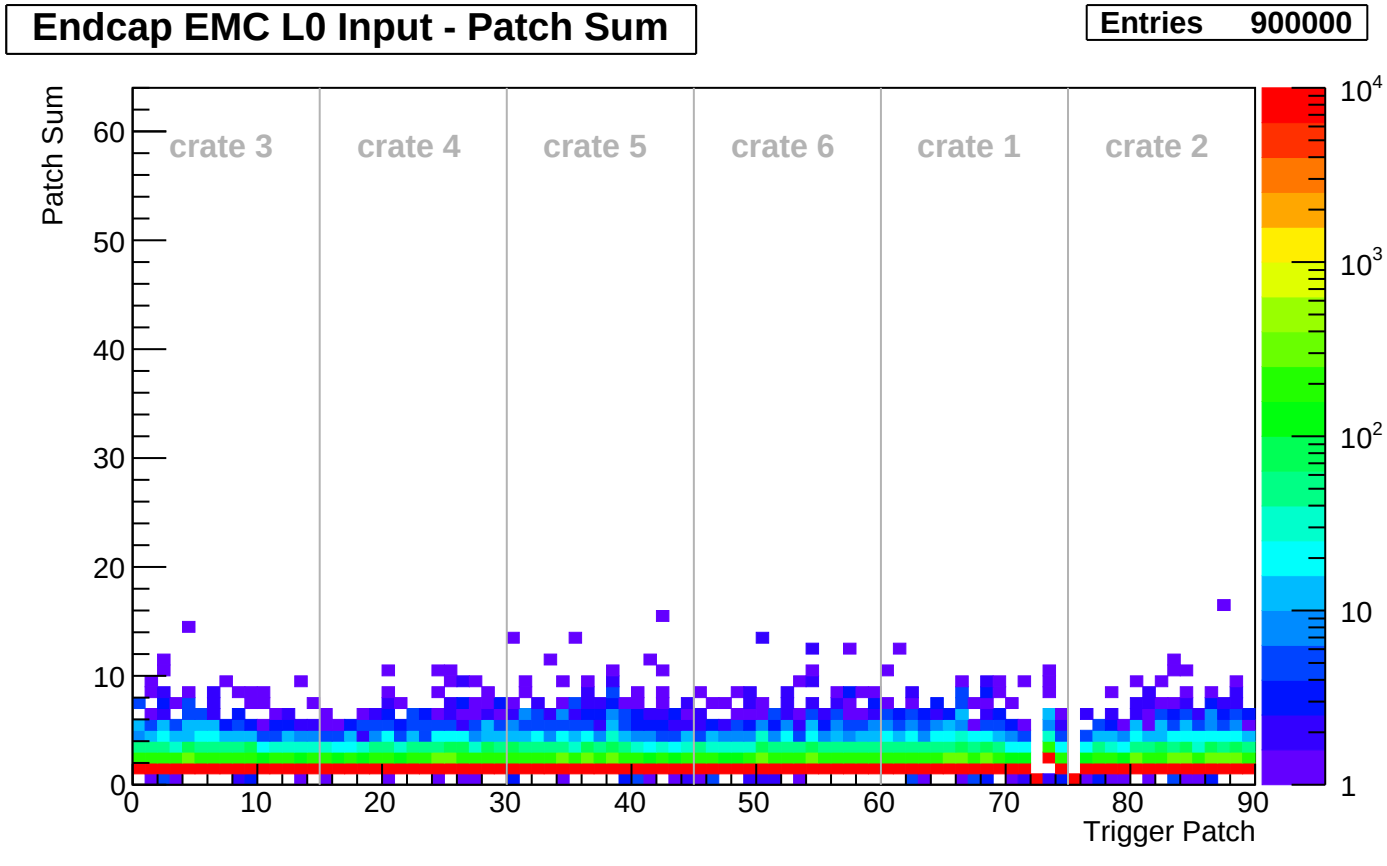
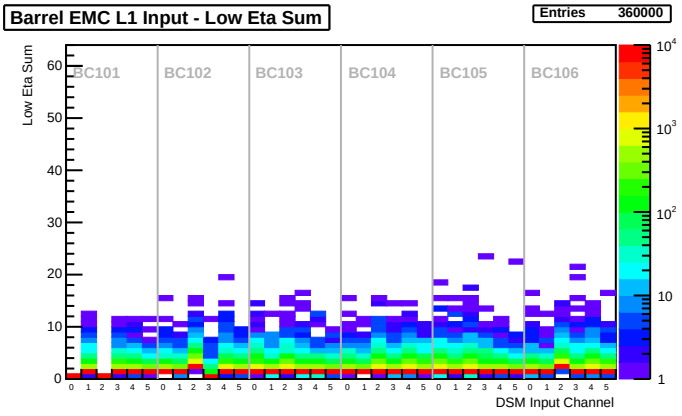
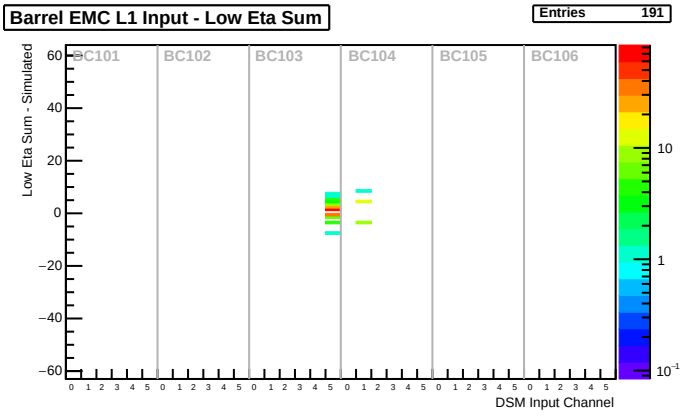

Barrel EMC L0 Input - High Tower
Entries 3000000
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

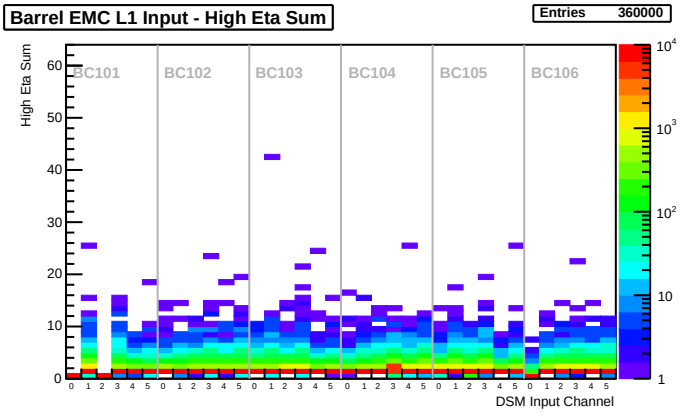

Barrel EMC L0 Input - Patch Sum
Entries 3000000
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

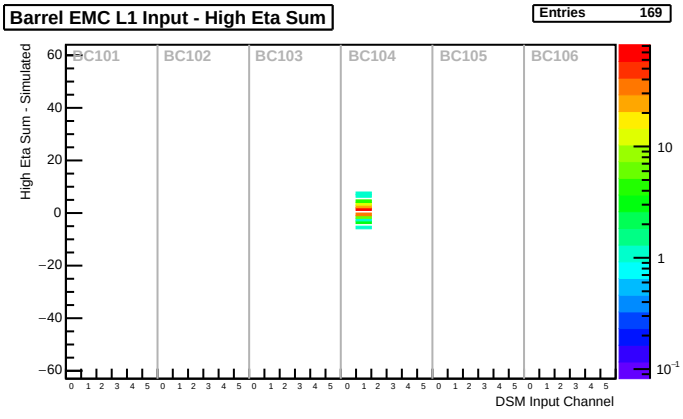

Endcap EMC L0 Input - High Tower
Entries 900000
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3
10^4

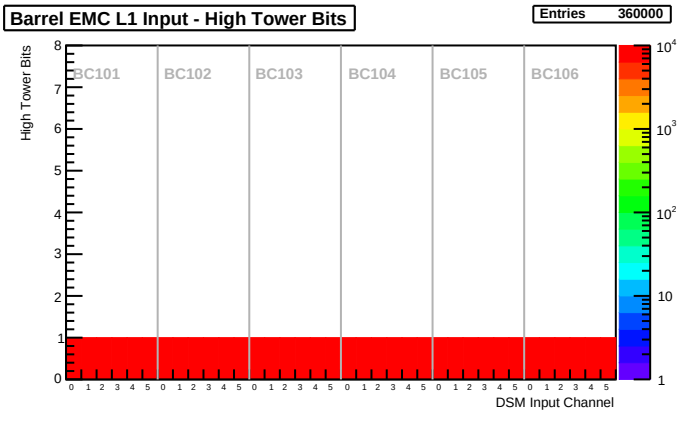

Endcap EMC L0 Input - Patch Sum
Entries 900000
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3
10^4

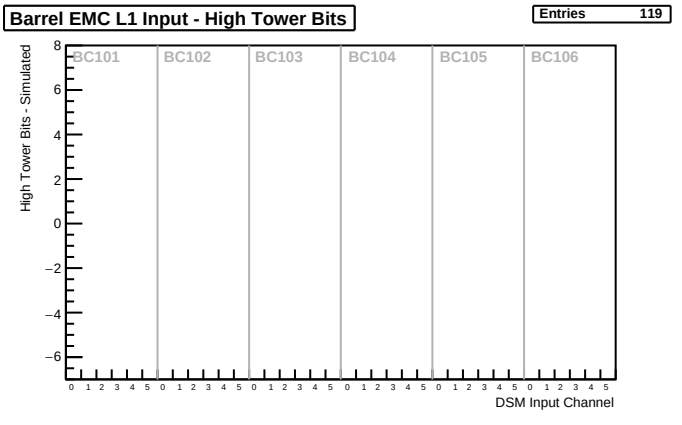

Barrel EMC L1 Input - Low Eta Sum
Entries 360000
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

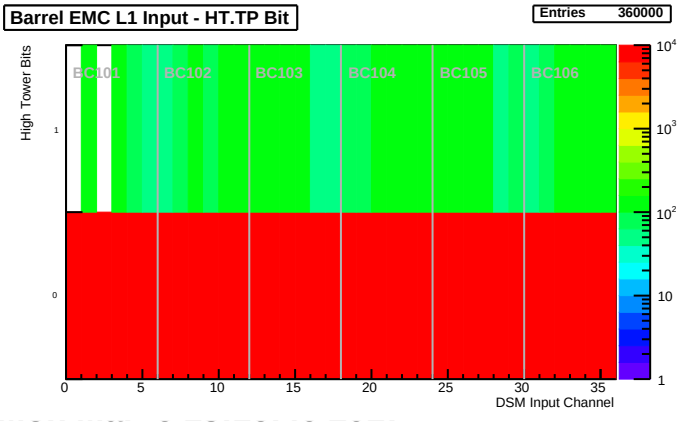

Barrel EMC L1 Input - Low Eta Sum
Entries 191
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

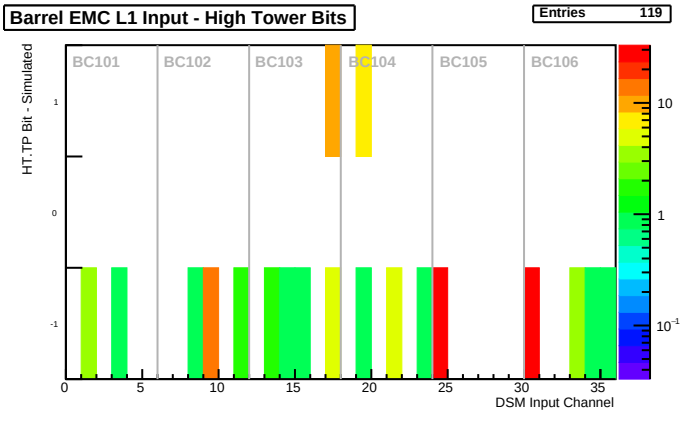

Barrel EMC L1 Input - High Eta Sum
Entries 360000
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

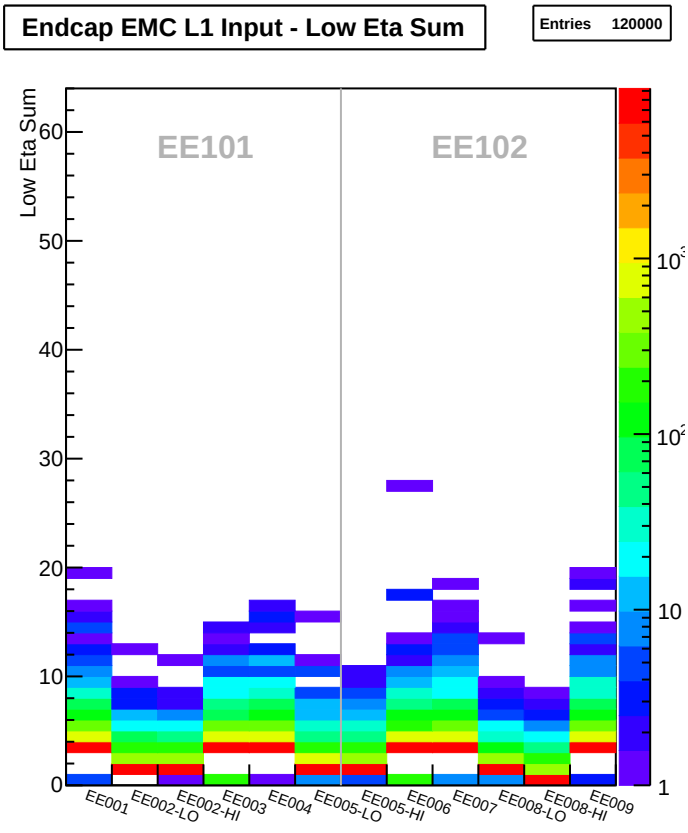

Barrel EMC L1 Input - High Eta Sum
Entries 169
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

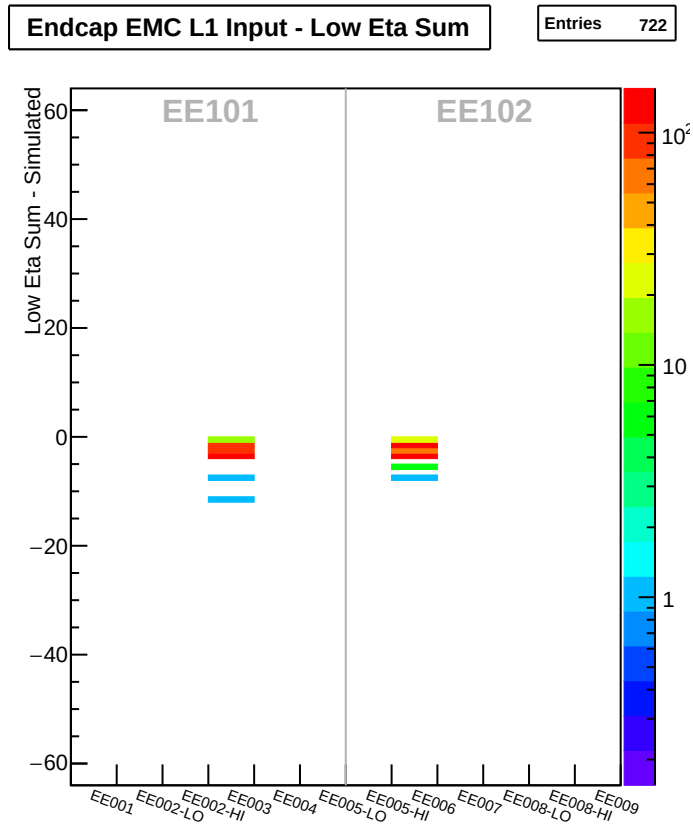

Barrel EMC L1 Input - High Tower Bits
Entries 360000
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

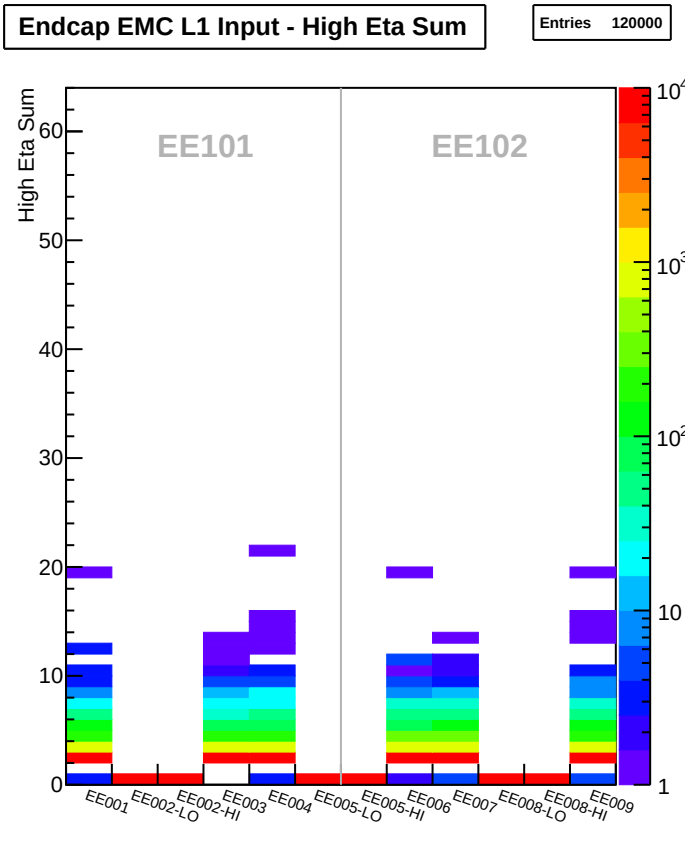

Barrel EMC L1 Input - High Tower Bits
Entries 119
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

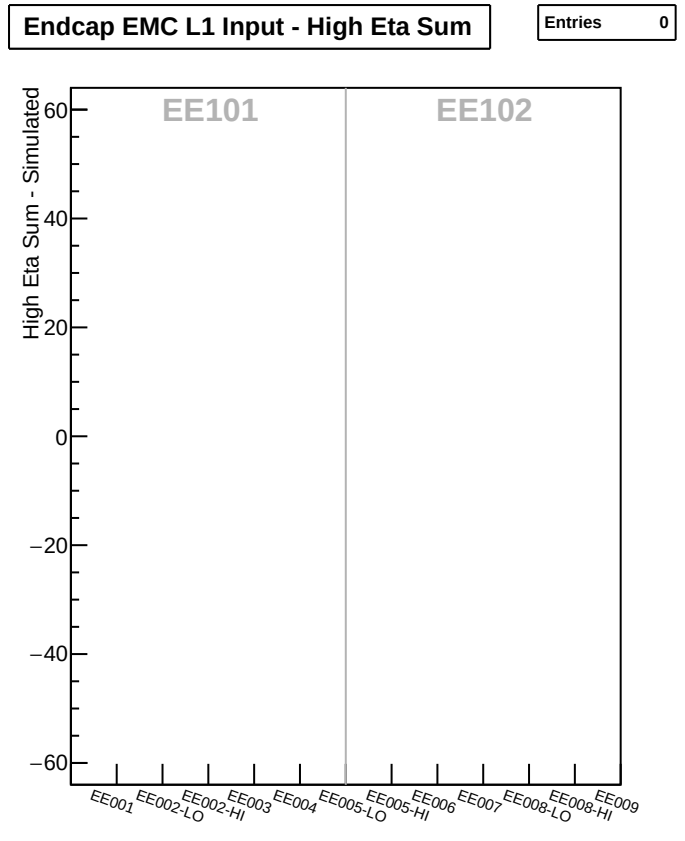

Barrel EMC L1 Input - HT.TP Bit
Entries 360000
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

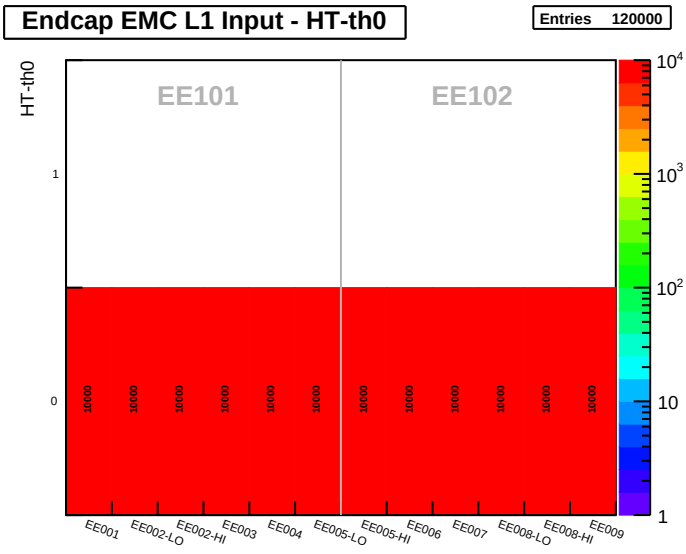

Barrel EMC L1 Input - High Tower Bits
Entries 119
HT.TP Bit - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

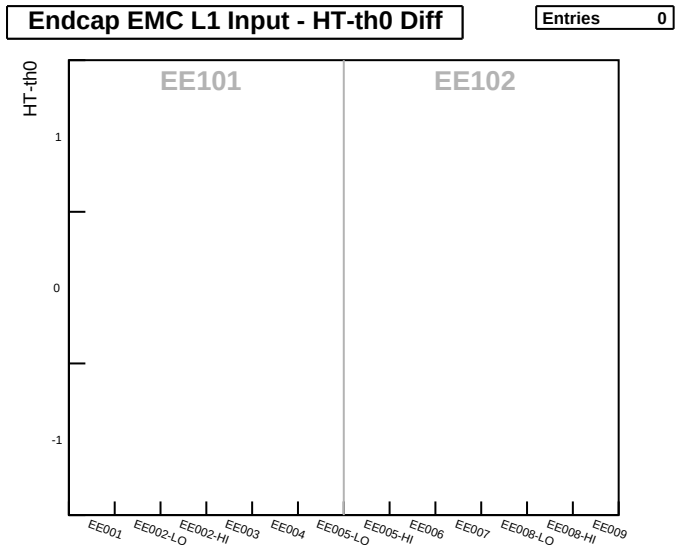

Endcap EMC L1 Input - Low Eta Sum
Entries 120000
Low Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

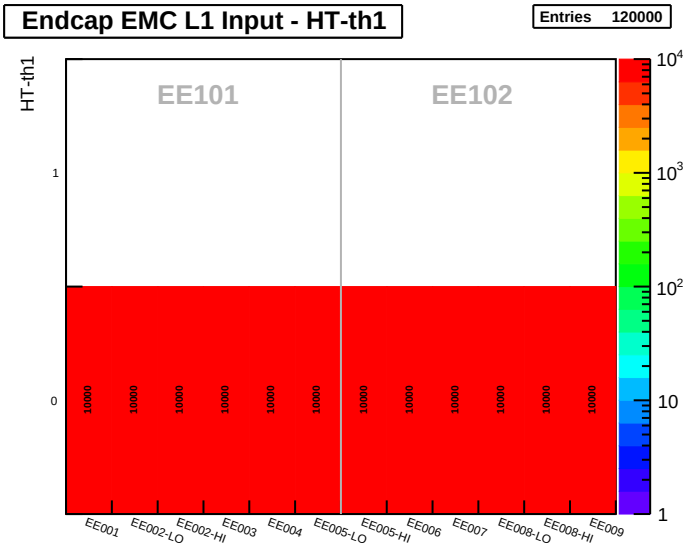

Endcap EMC L1 Input - Low Eta Sum
Entries 722
Low Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

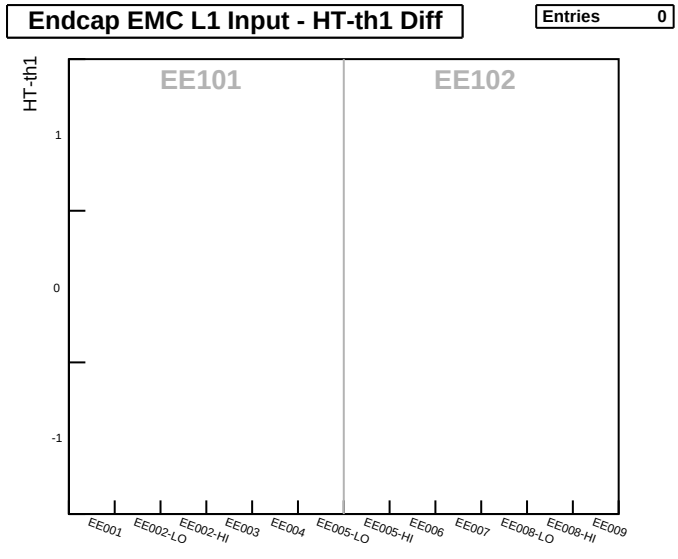

Endcap EMC L1 Input - High Eta Sum
Entries 120000
High Eta Sum
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

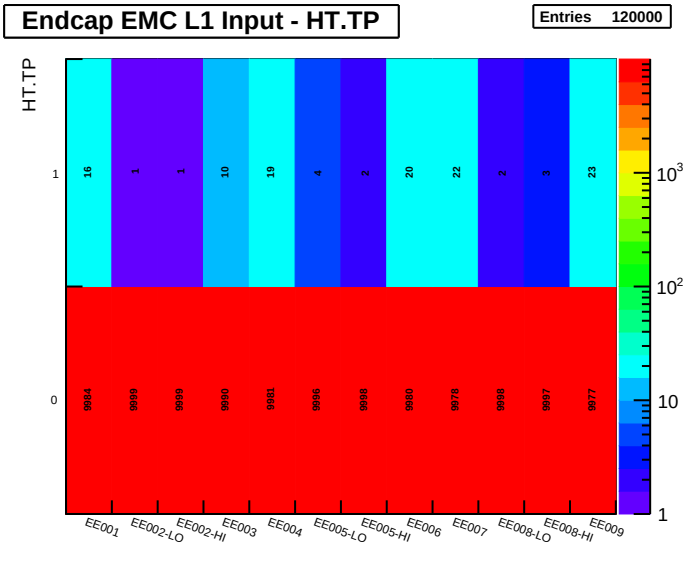

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

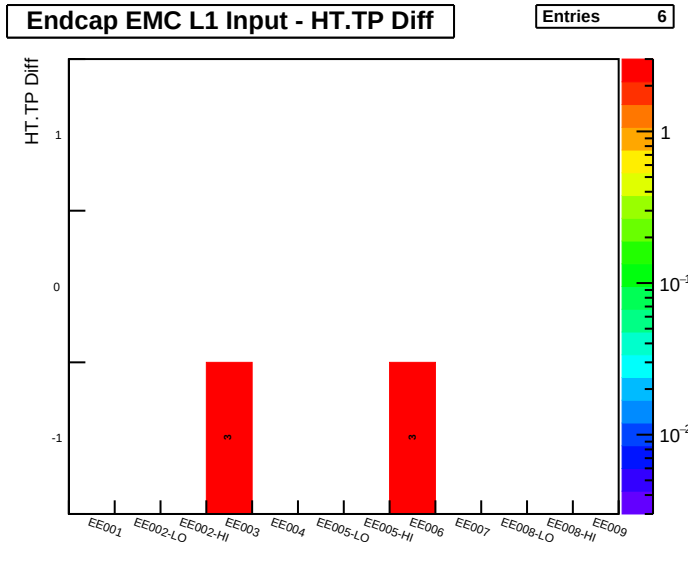

Endcap EMC L1 Input - HT-th0
Entries 120000
HT-th0
EE101
EE102
1
0
10000
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009
10^4
10^3
10^2
10
1

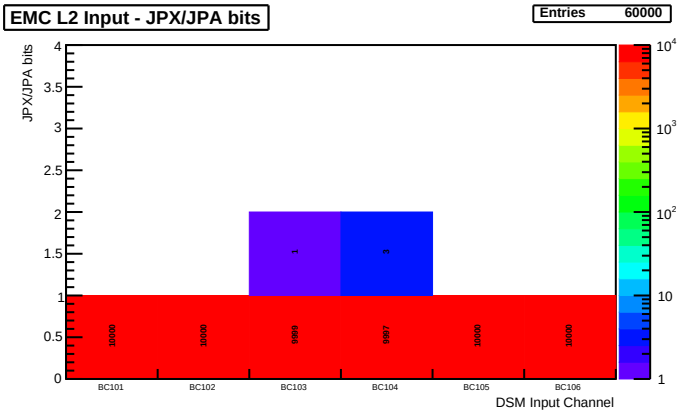

Endcap EMC L1 Input - HT-th0 Diff
Entries 0
HT-th0
EE101
EE102
1
0
-1
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

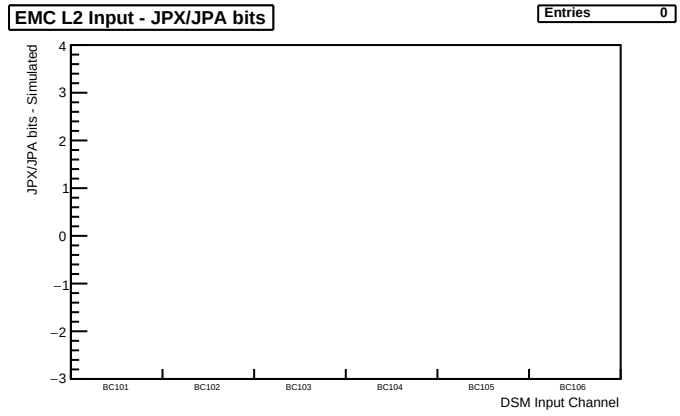

Endcap EMC L1 Input - HT-th1
Entries 120000
HT-th1
EE101
EE102
1
0
10000
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009
10^4
10^3
10^2
10
1

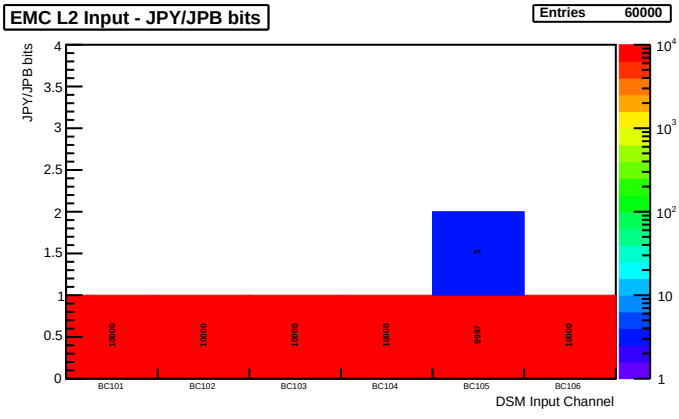

Endcap EMC L1 Input - HT-th1 Diff
Entries 0
HT-th1
EE101
EE102
1
0
-1
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009

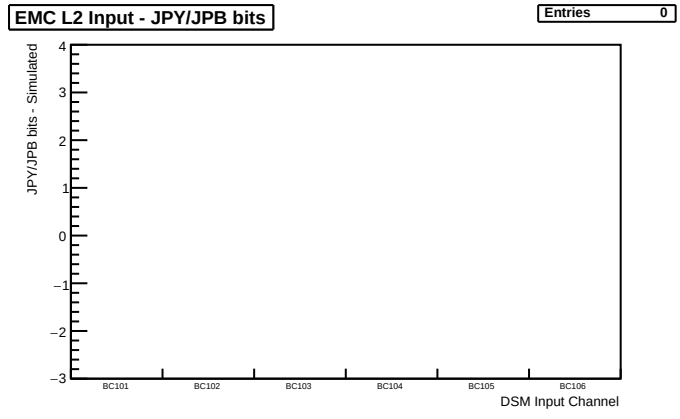

Endcap EMC L1 Input - HT.TP
Entries 120000
HT.TP
1
0
16
1
1
10
19
4
2
20
22
2
3
23
9984
9999
9999
9990
9981
9996
9998
9980
9978
9998
9997
9977
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009
10^3
10^2
10
1

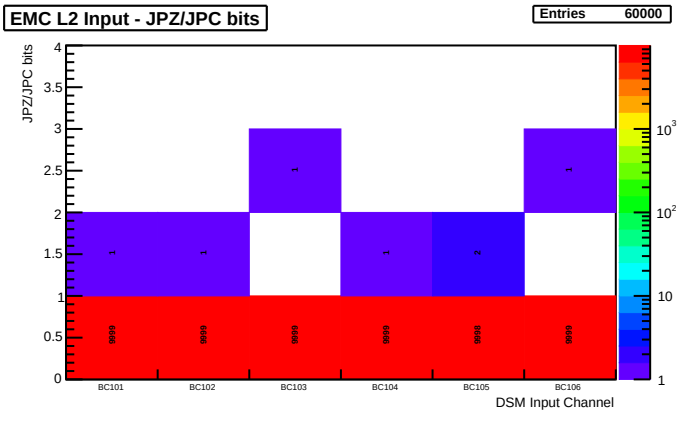

Endcap EMC L1 Input - HT.TP Diff
Entries 6
HT.TP Diff
1
0
-1
3
3
EE001
EE002-LO
EE002-HI
EE003
EE004
EE005-LO
EE005-HI
EE006
EE007
EE008-LO
EE008-HI
EE009
1

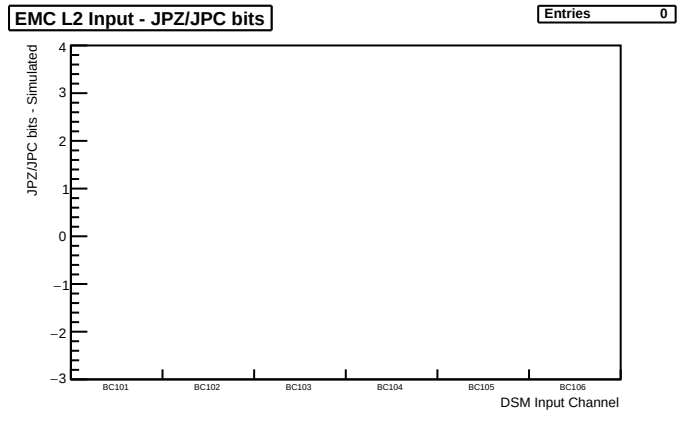

EMC L2 Input - JPX/JPA bits
Entries 60000
JPX/JPA bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

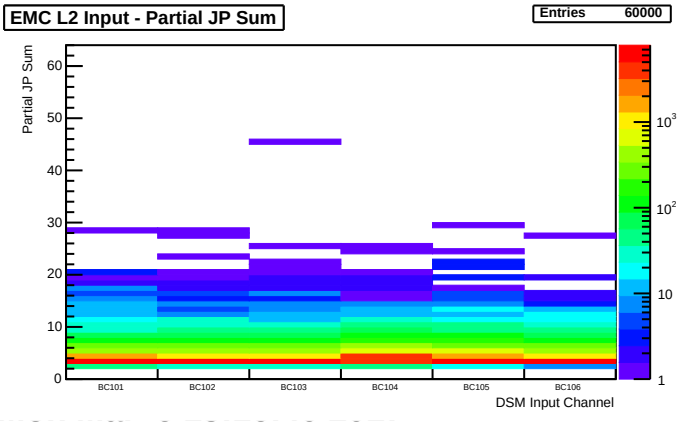

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

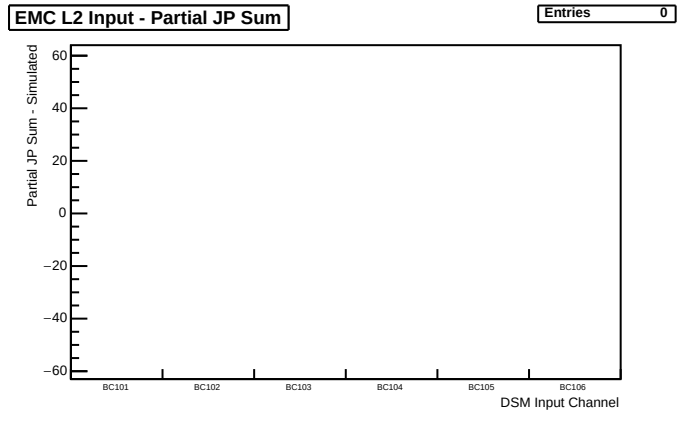

EMC L2 Input - JPY/JPB bits
Entries 60000
JPY/JPB bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

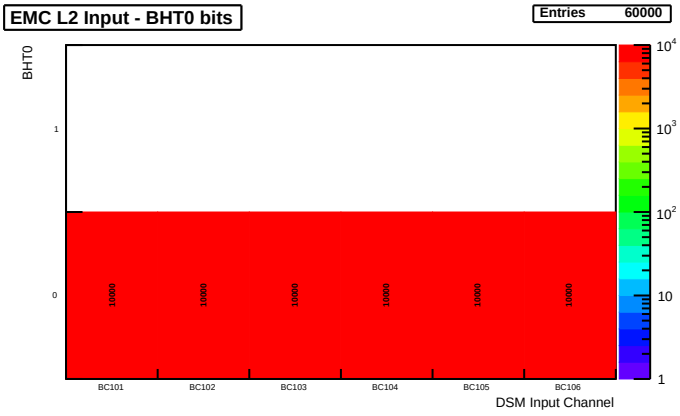

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

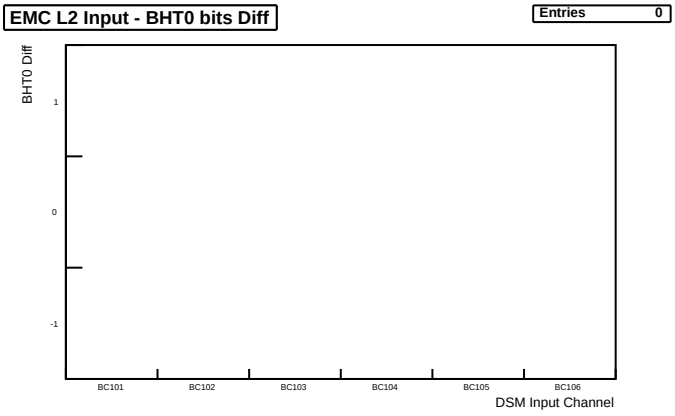

EMC L2 Input - JPZ/JPC bits
Entries 60000
JPZ/JPC bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

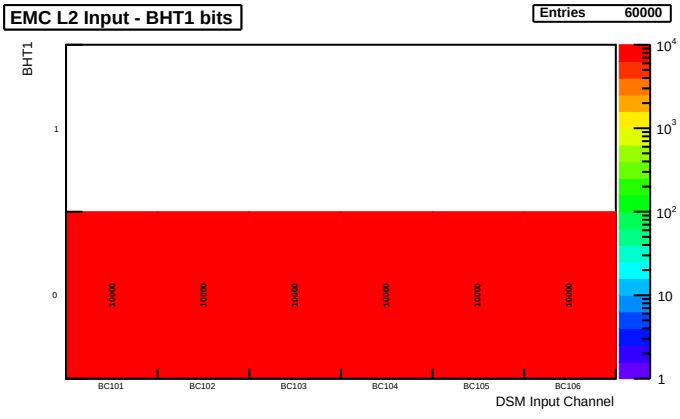

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

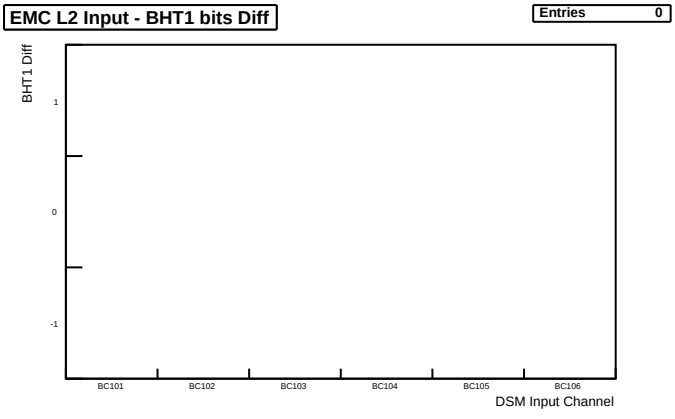

EMC L2 Input - Partial JP Sum
Entries 60000
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

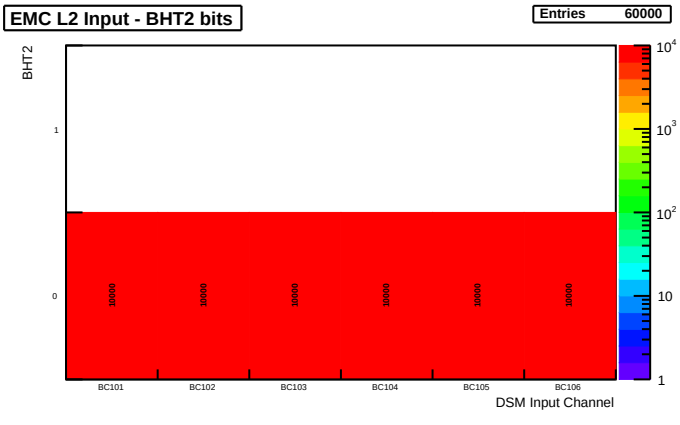

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

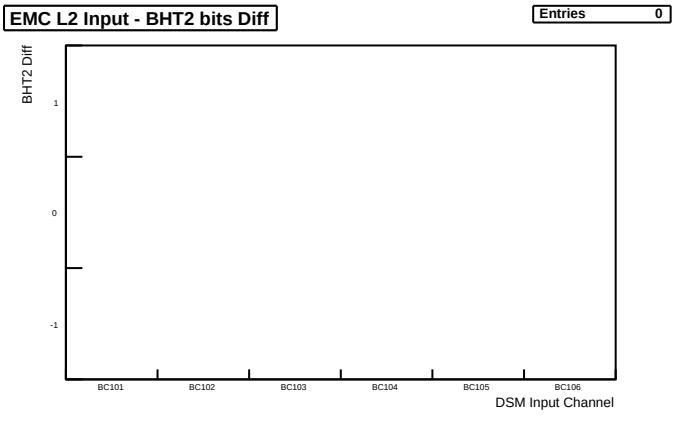

EMC L2 Input - BHT0 bits
Entries 60000
BHT0
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
10000

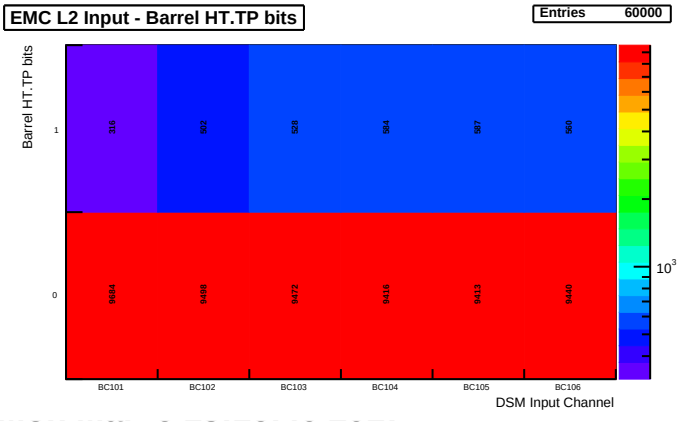

EMC L2 Input - BHT0 bits Diff
Entries 0
BHT0 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

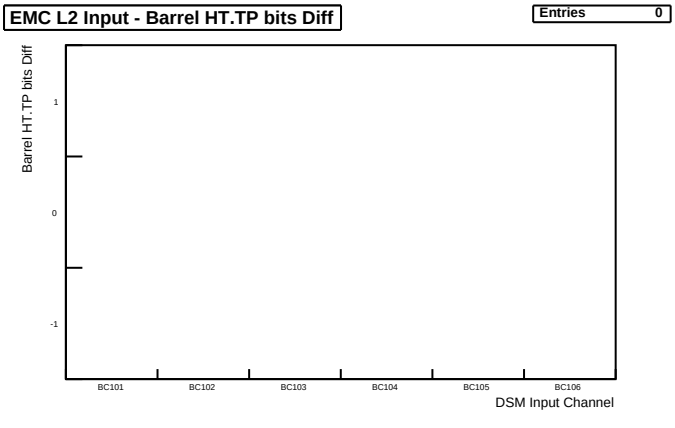

EMC L2 Input - BHT1 bits
Entries 60000
BHT1
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
10000

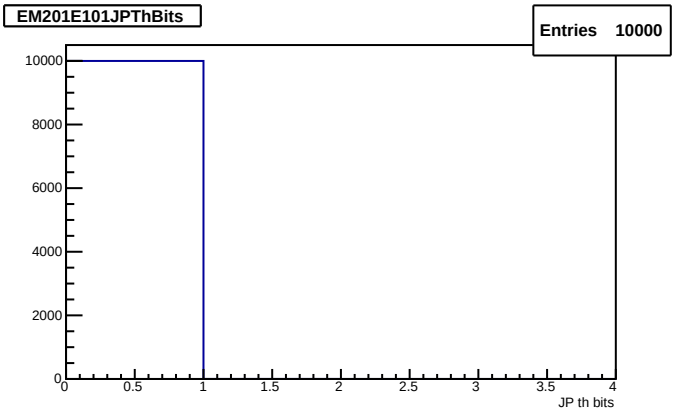

EMC L2 Input - BHT1 bits Diff
Entries 0
BHT1 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

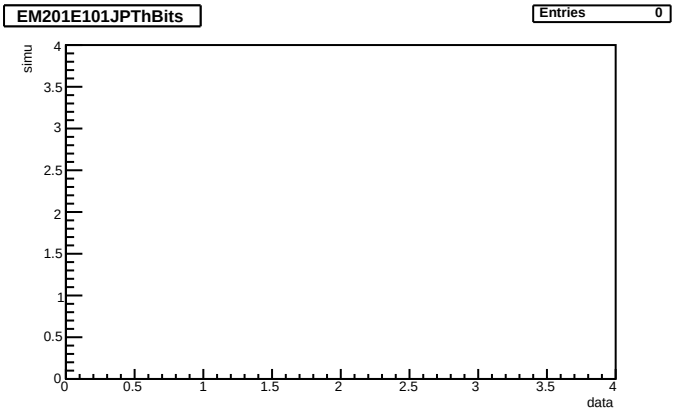

EMC L2 Input - BHT2 bits
Entries 60000
BHT2
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
10000

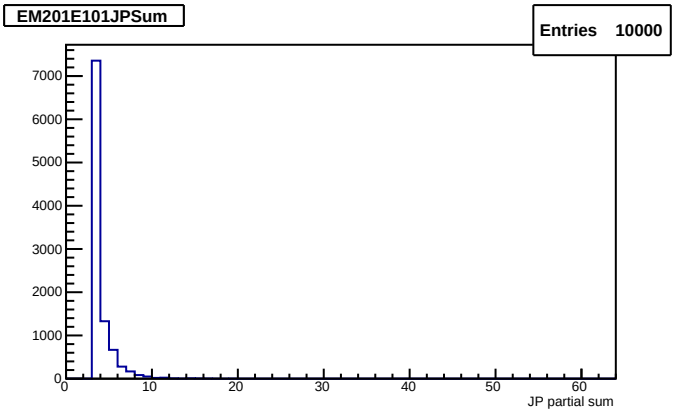

EMC L2 Input - BHT2 bits Diff
Entries 0
BHT2 Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

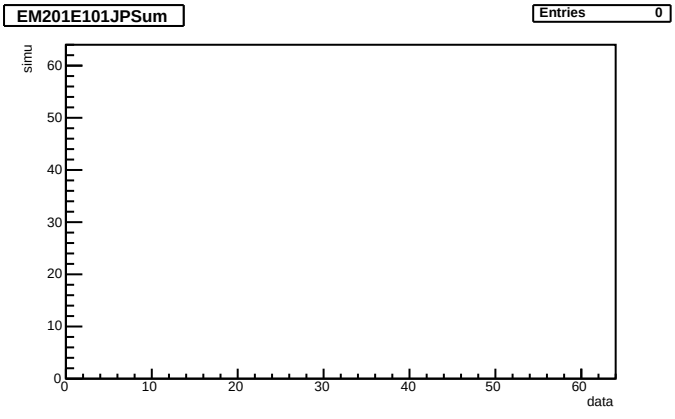

EMC L2 Input - Barrel HT.TP bits
Entries 60000
Barrel HT.TP bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
316
502
528
594
587
560
9484
9498
9472
9416
9413
9440

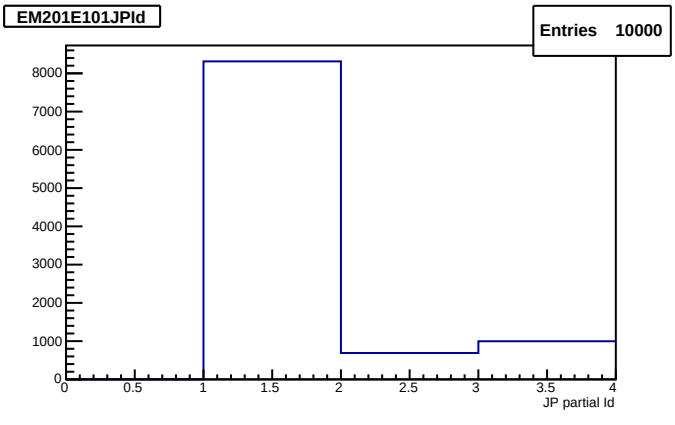

EMC L2 Input - Barrel HT.TP bits Diff
Entries 0
Barrel HT.TP bits Diff
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106

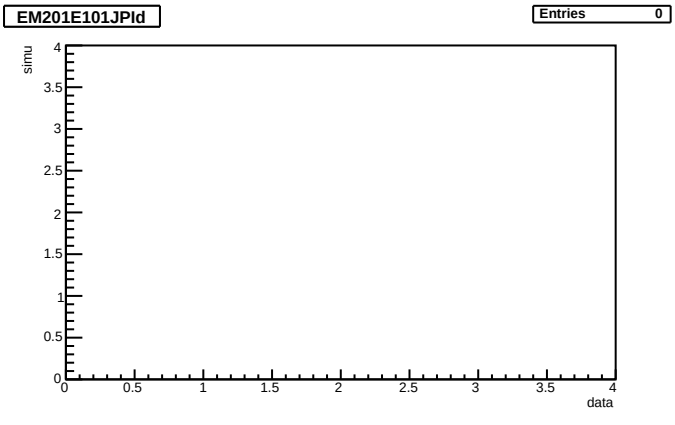

EM201E101JPThBits
Entries 10000
JP th bits

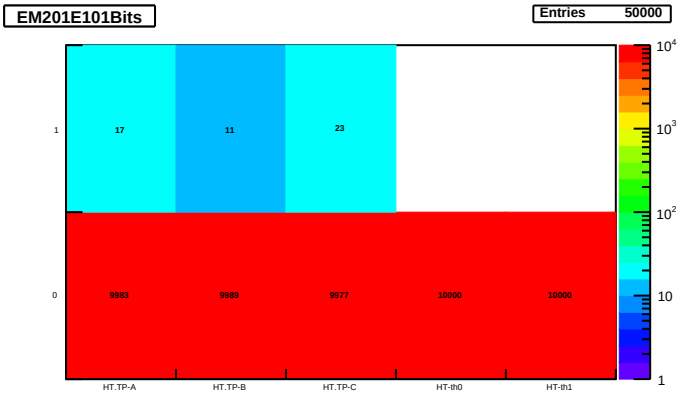

EM201E101JPThBits
Entries 0
simu
data

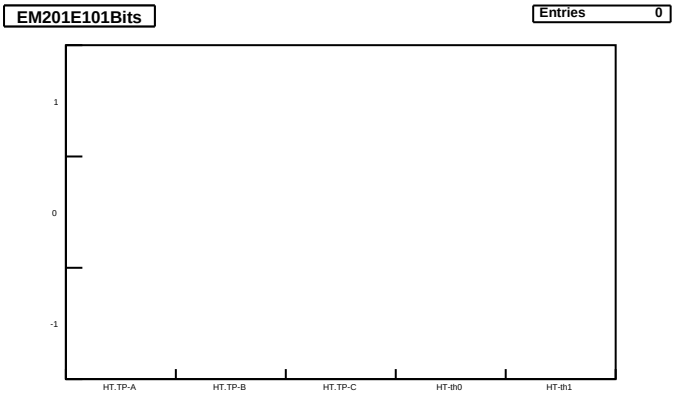

EM201E101JPSum
Entries 10000
JP partial sum

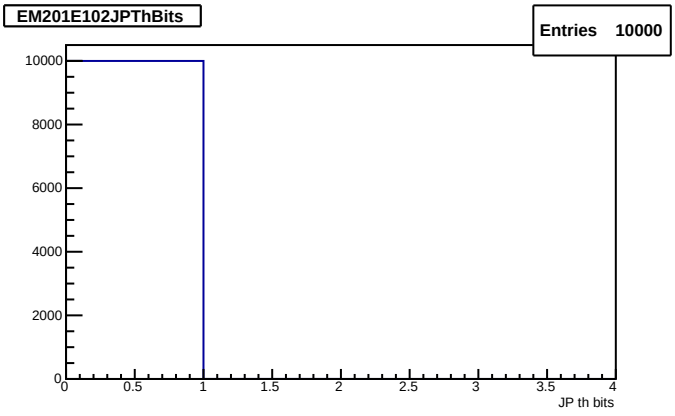

EM201E101JPSum
Entries 0
simu
data

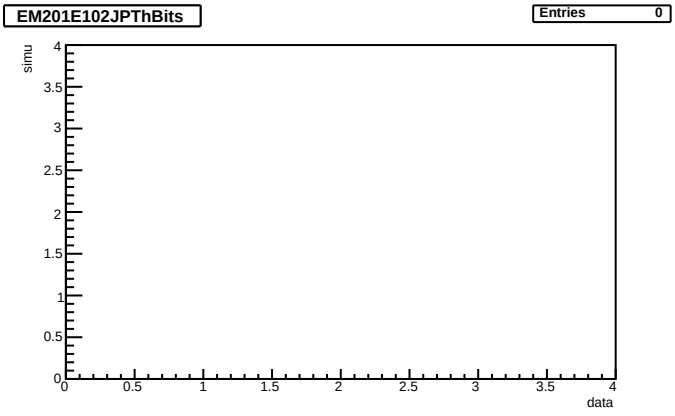

EM201E101JPId
Entries 10000
JP partial Id

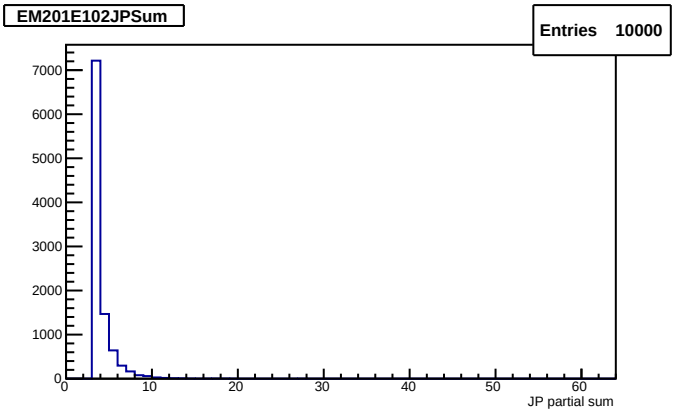

EM201E101JPId
Entries 0
simu
data

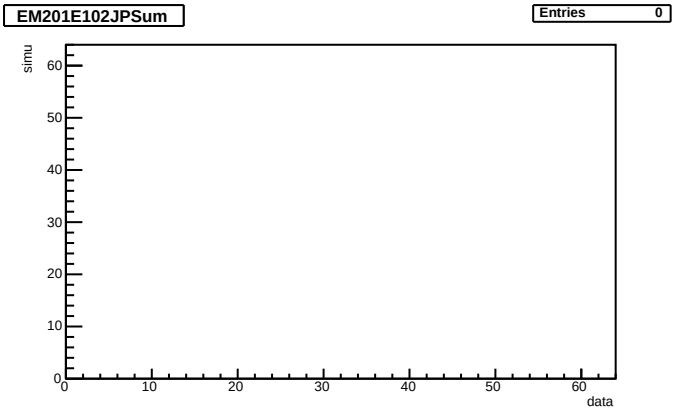

EM201E101Bits
Entries 50000
17
11
23
9983
9989
9977
10000
10000
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

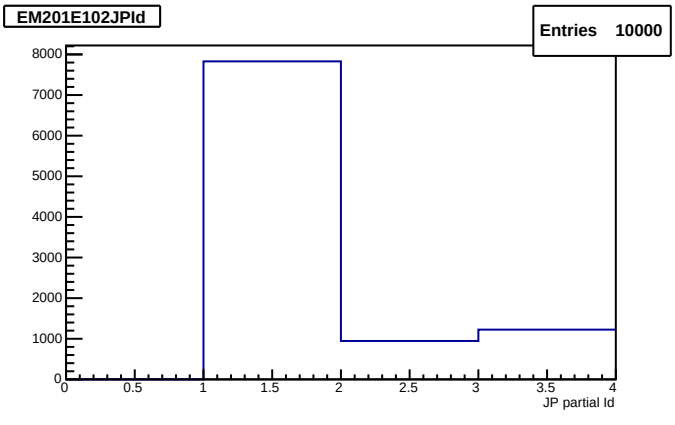

EM201E101Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

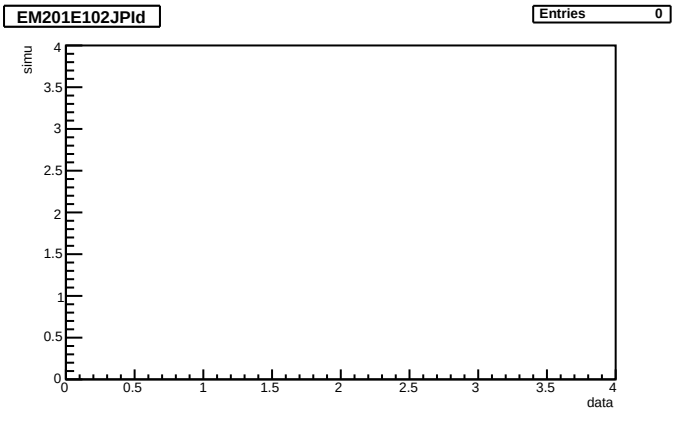

EM201E102JPThBits
Entries 10000
JP th bits

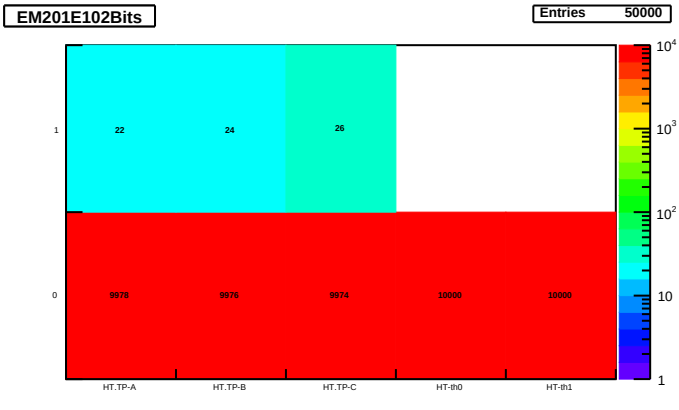

EM201E102JPThBits
Entries 0
simu
data

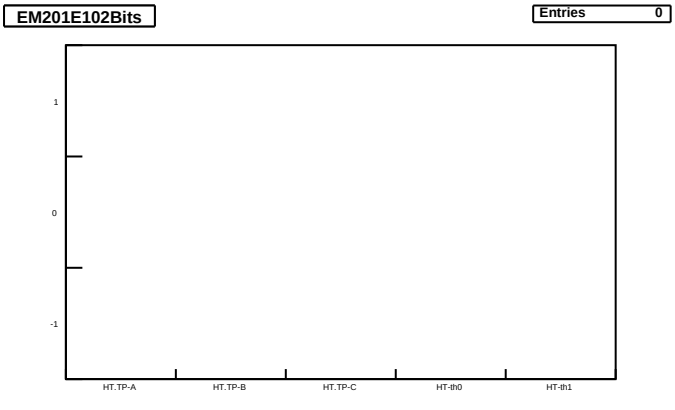

EM201E102JPSum
Entries 10000
JP partial sum

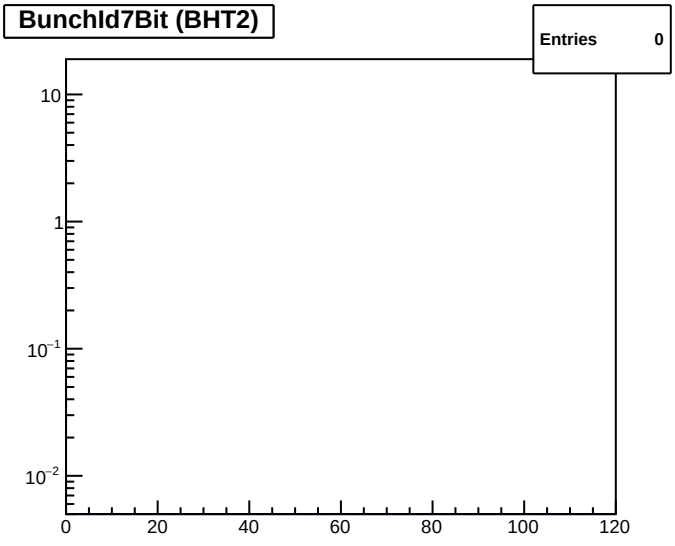

EM201E102JPSum
Entries 0
simu
data

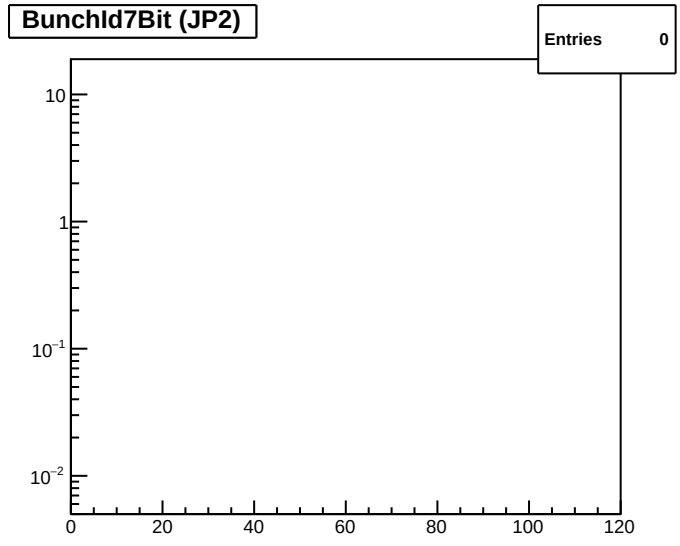

EM201E102JPId
Entries 10000
JP partial Id

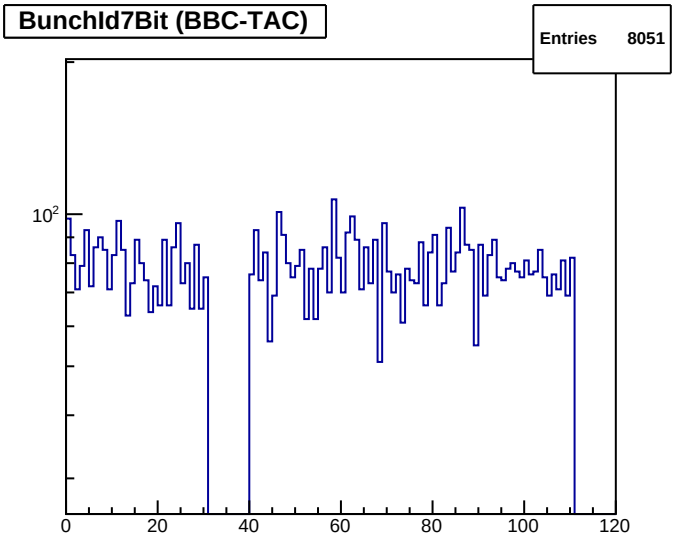

EM201E102JPId
Entries 0
simu
data

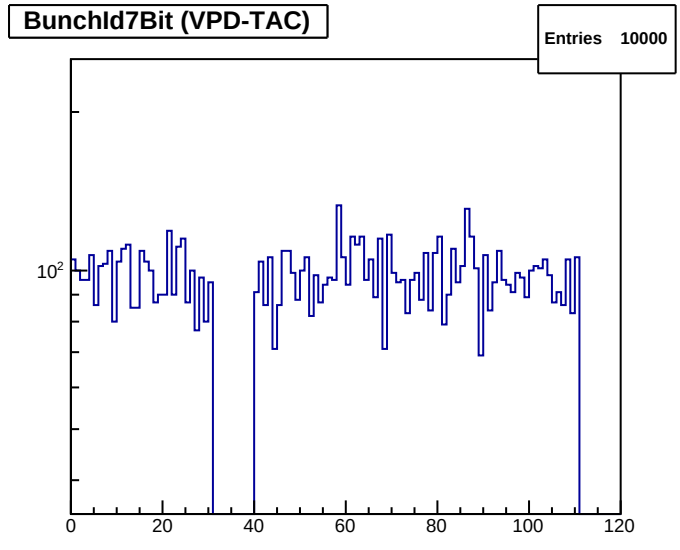

EM201E102Bits
Entries 50000
22
24
26
9978
9976
9974
10000
10000
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

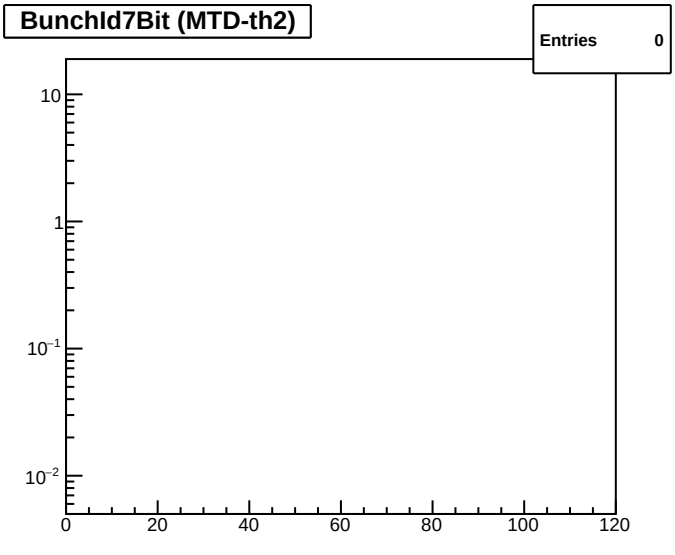

EM201E102Bits
Entries 0
HT.TP-A
HT.TP-B
HT.TP-C
HT-th0
HT-th1

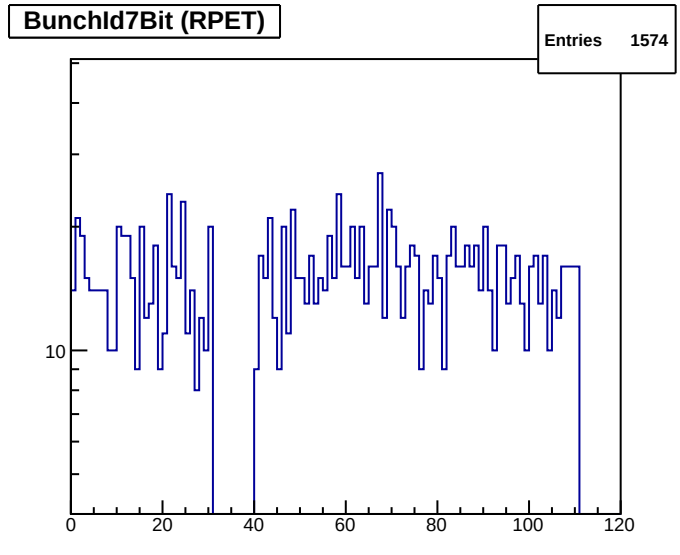

BunchId7Bit (BHT2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

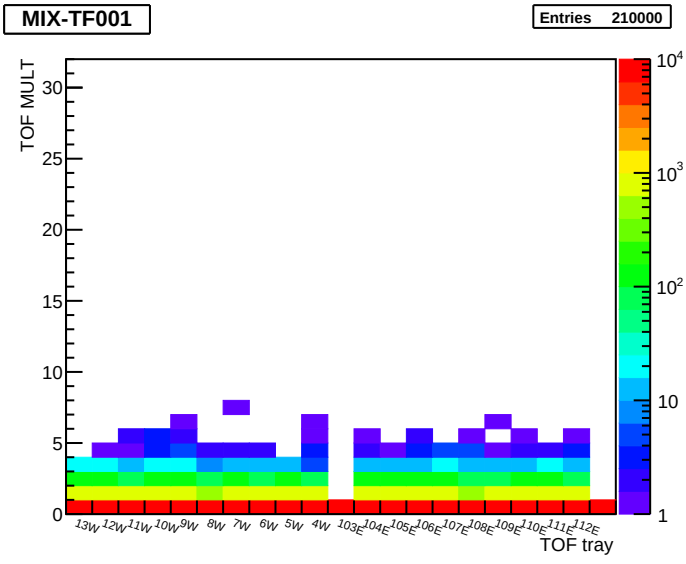

BunchId7Bit (JP2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

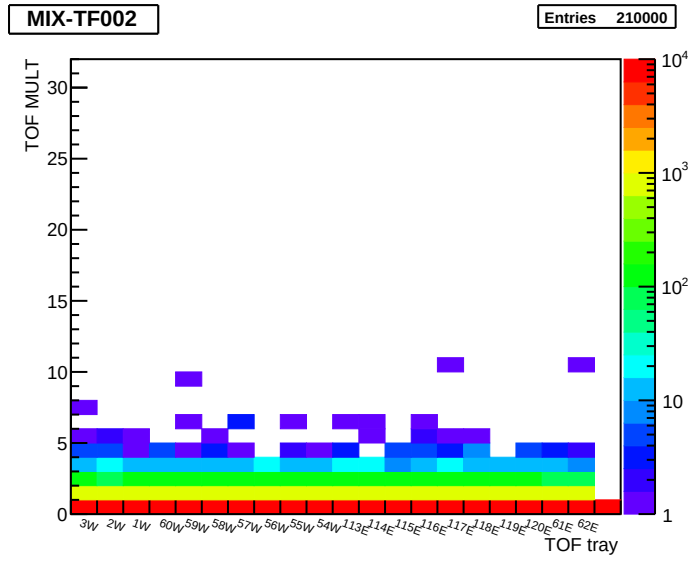

BunchId7Bit (BBC-TAC)
Entries 8051
10^{2}
0 20 40 60 80 100 120

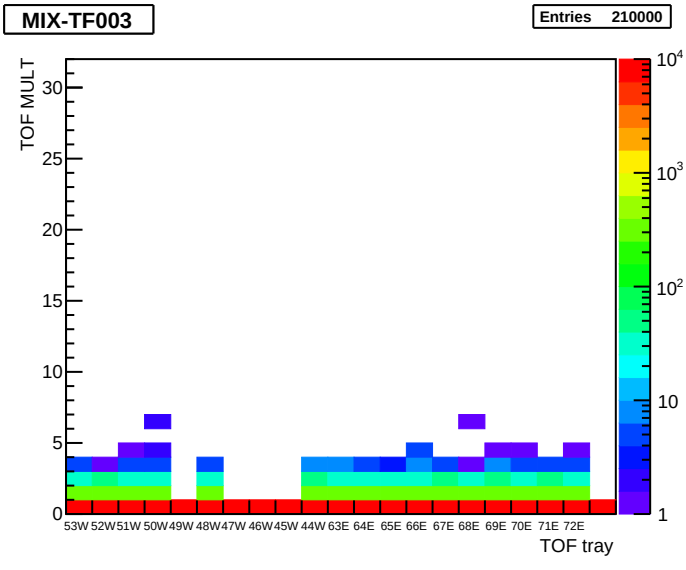

BunchId7Bit (VPD-TAC)
Entries 10000
10^{2}
0 20 40 60 80 100 120

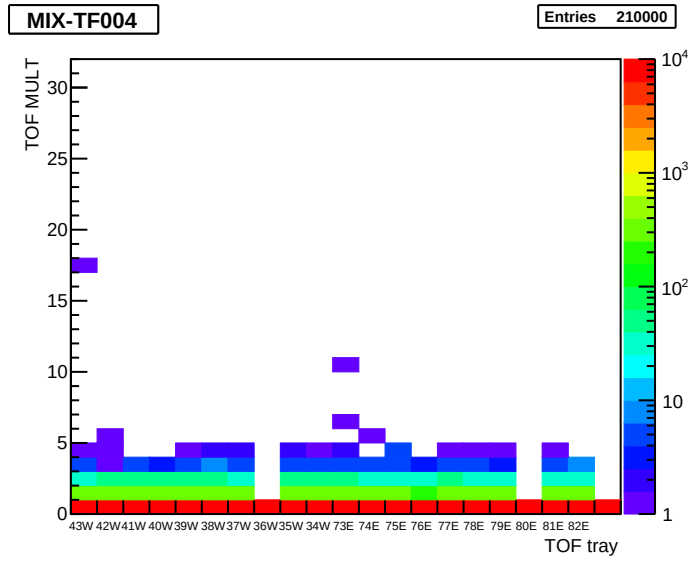

BunchId7Bit (MTD-th2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

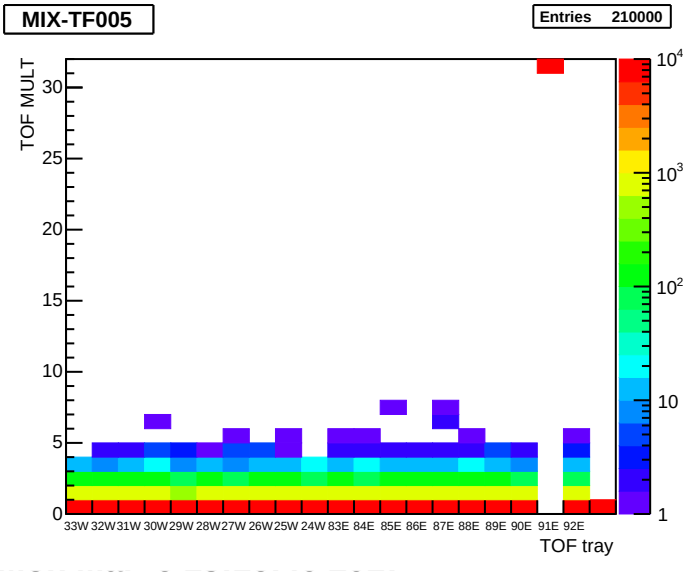

BunchId7Bit (RPET)
Entries 1574
10
0 20 40 60 80 100 120

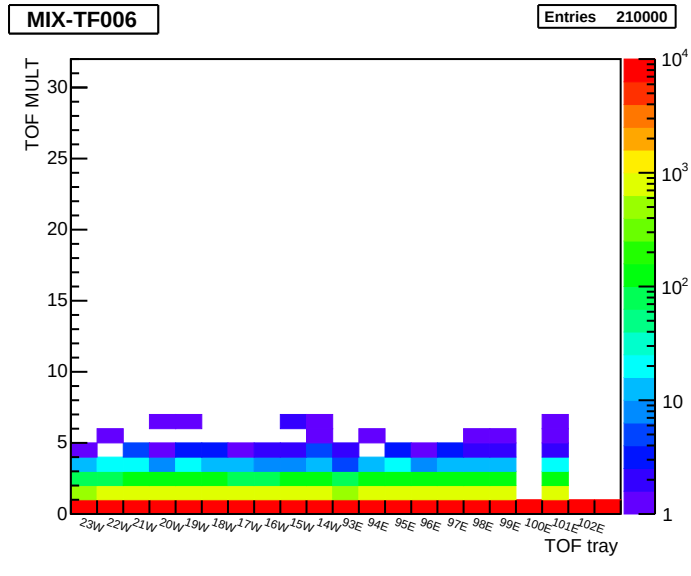

MIX-TF001
Entries 210000
TOF MULT
TOF tray

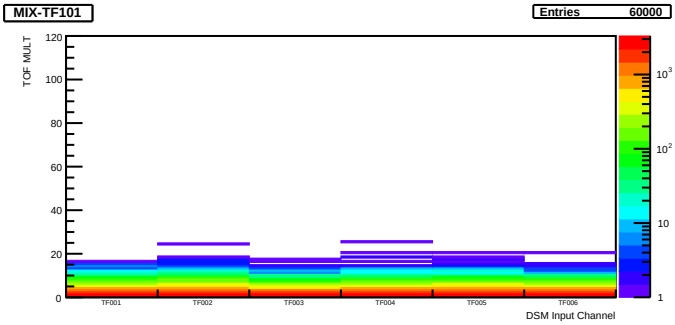

MIX-TF002
Entries 210000
TOF MULT
TOF tray

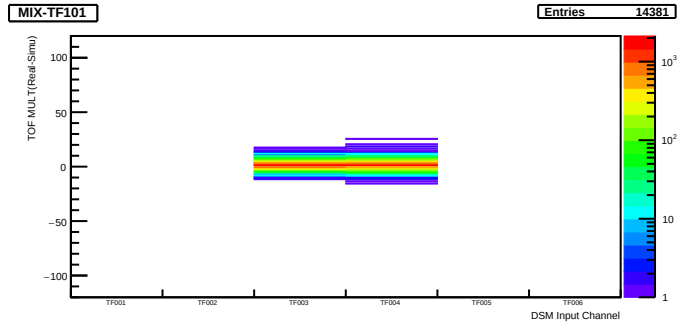

MIX-TF003
Entries 210000
TOF MULT
TOF tray

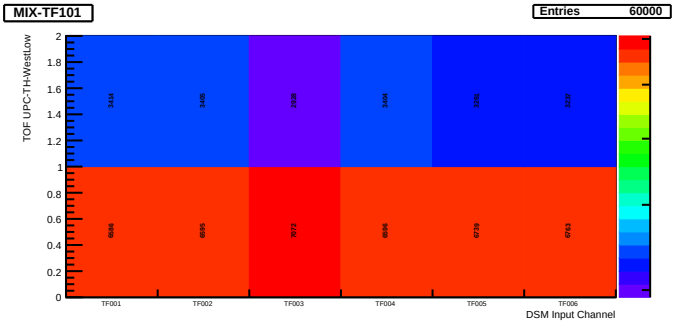

MIX-TF004
Entries 210000
TOF MULT
TOF tray

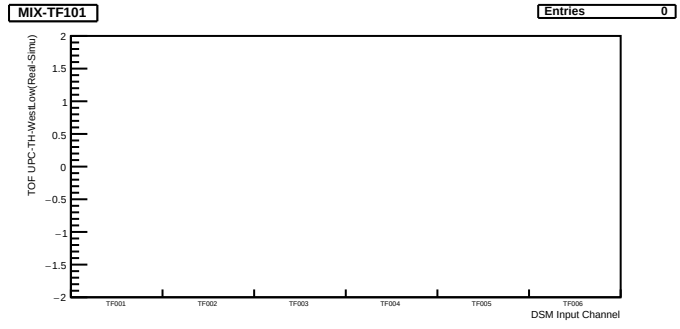

MIX-TF005
Entries 210000
TOF MULT
TOF tray

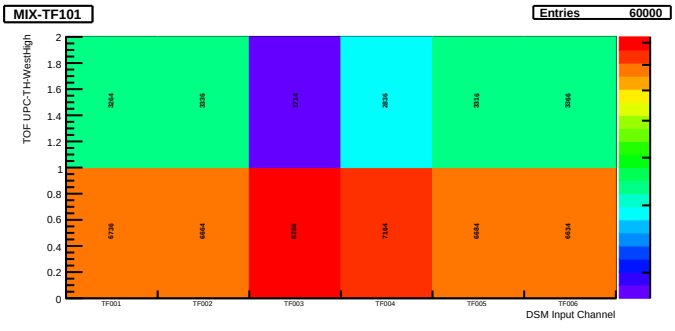

MIX-TF006
Entries 210000
TOF MULT
TOF tray

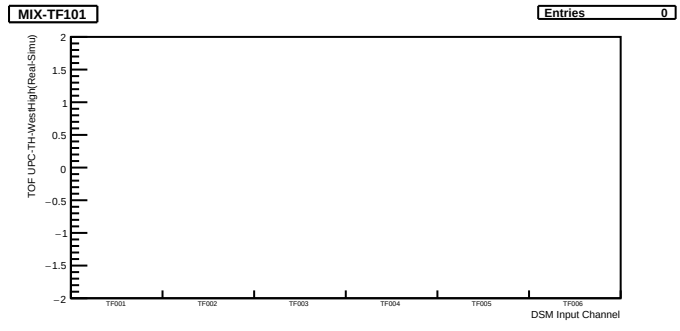

MIX-TF101
Entries 60000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

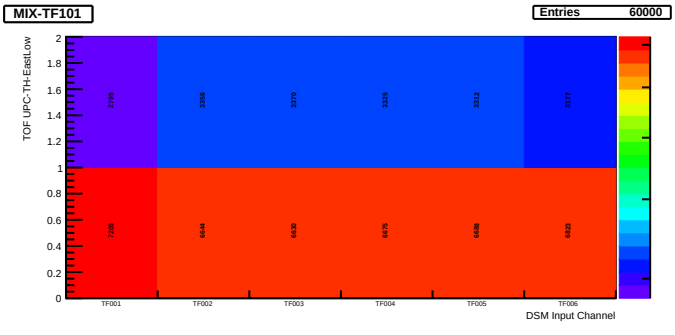

MIX-TF101
Entries 14381
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

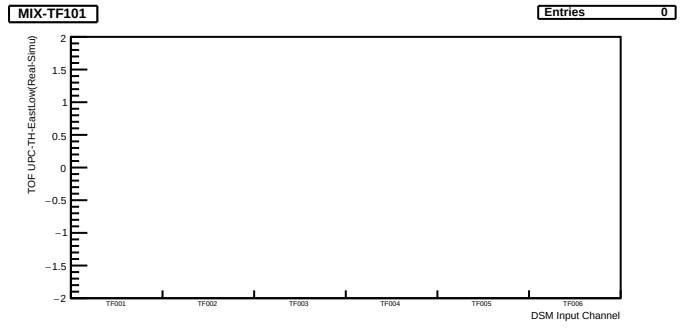

MIX-TF101
Entries 60000
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

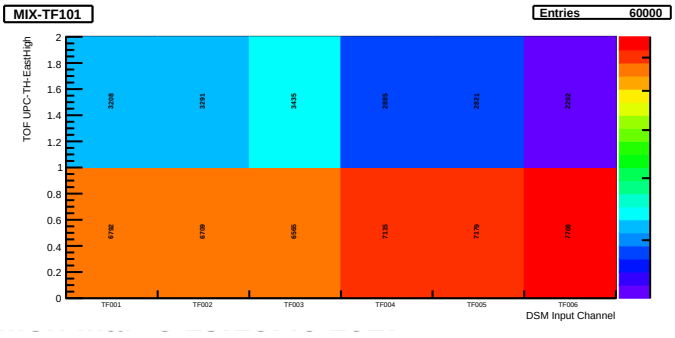

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

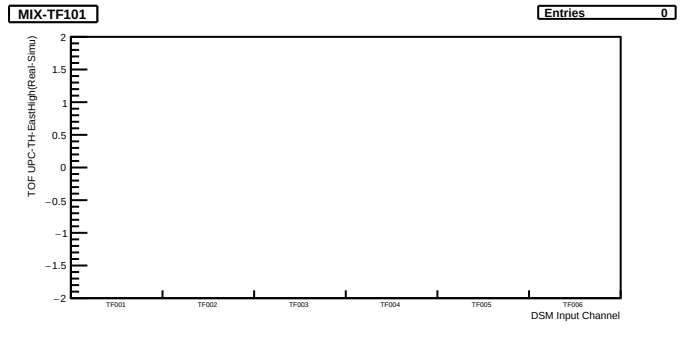

MIX-TF101
Entries 60000
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

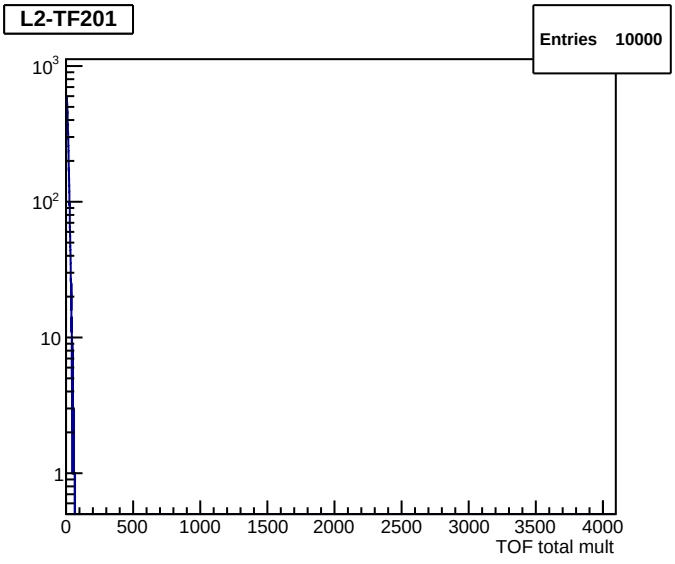

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

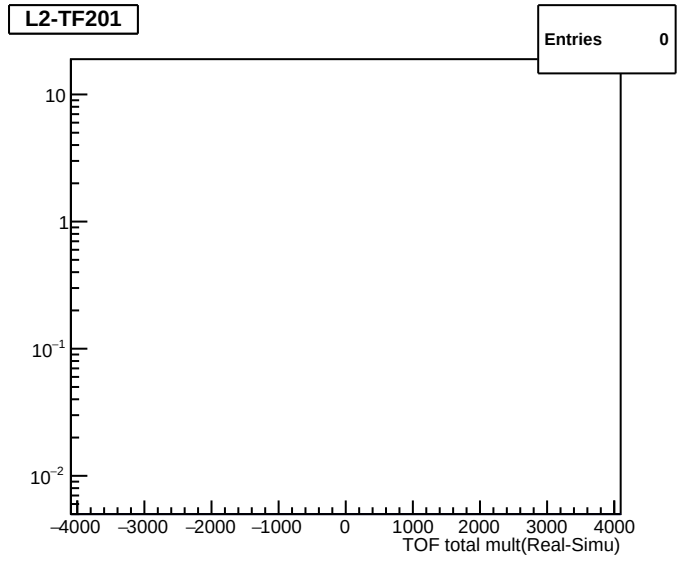

MIX-TF101
Entries 60000
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

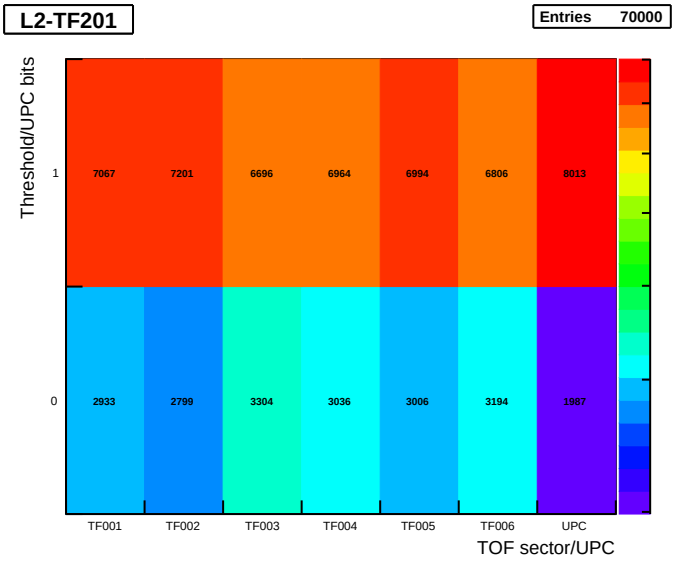

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

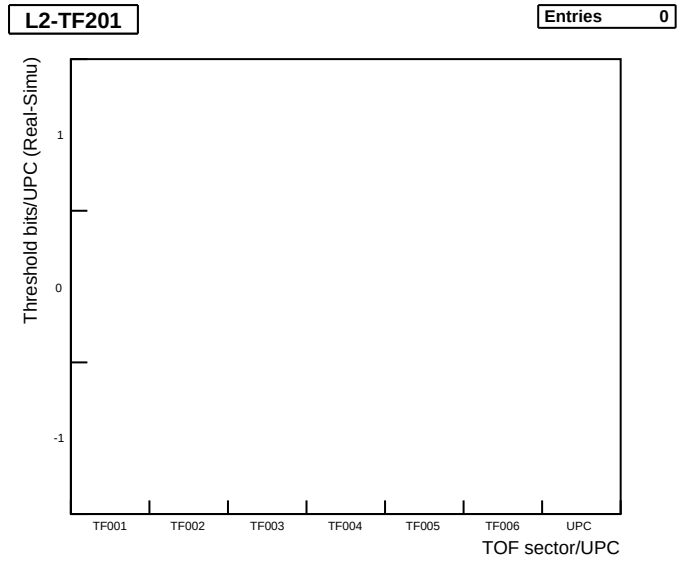

MIX-TF101
Entries 60000
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

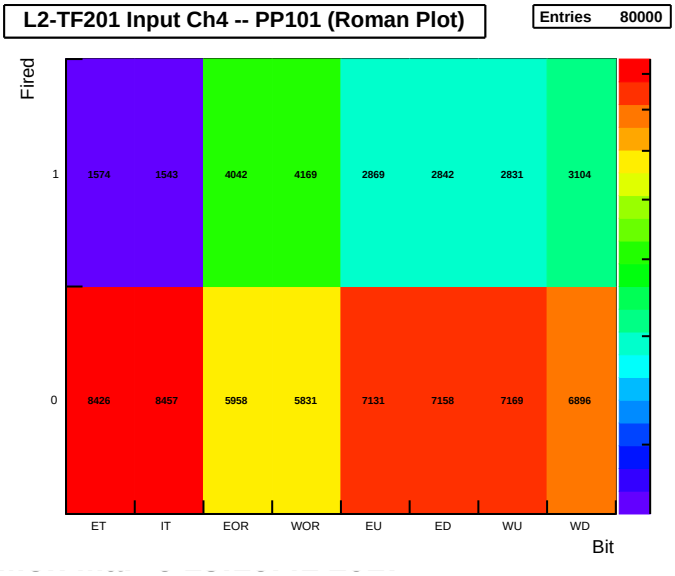

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

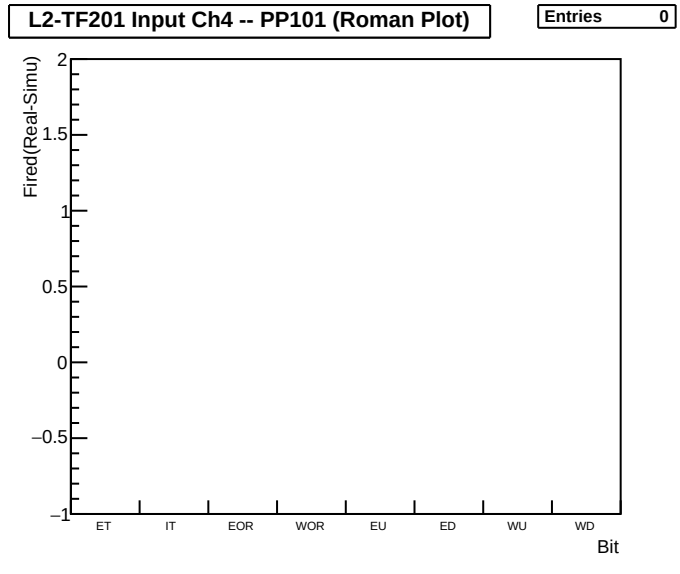

L2-TF201
Entries 10000
TOF total mult

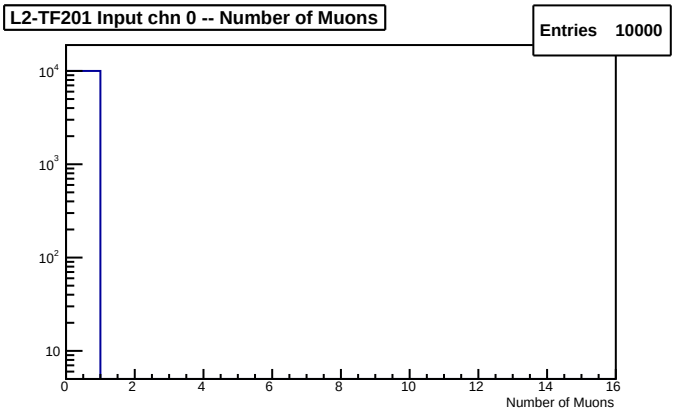

L2-TF201
Entries 0
TOF total mult(Real-Simu)

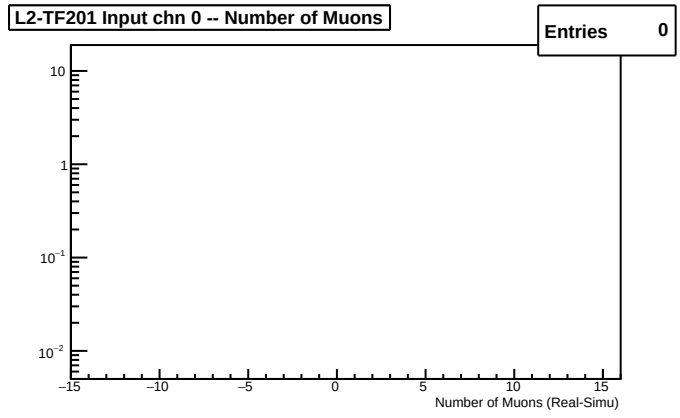

L2-TF201
Entries 70000
Threshold/UPC bits
7067 7201 6696 6964 6994 6806 8013
2933 2799 3304 3036 3006 3194 1987
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

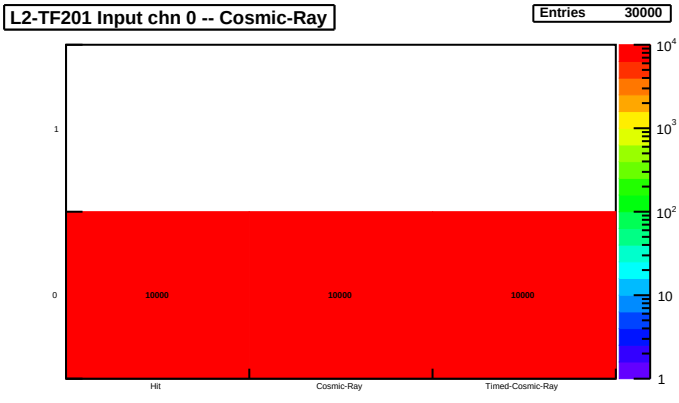

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

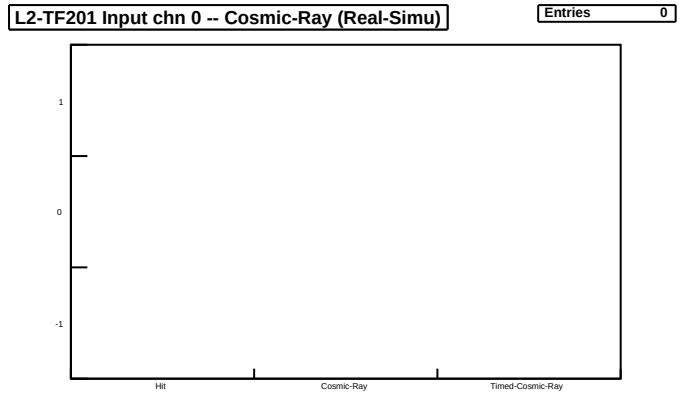

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 80000
Fired
1574 1543 4042 4169 2869 2842 2831 3104
8426 8457 5958 5831 7131 7158 7169 6896
ET IT EOR WOR EU ED WU WD
Bit

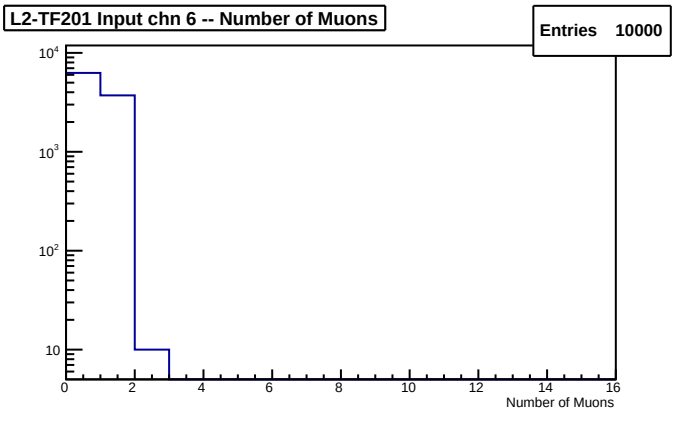

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 0
Fired(Real-Simu)
ET IT EOR WOR EU ED WU WD
Bit

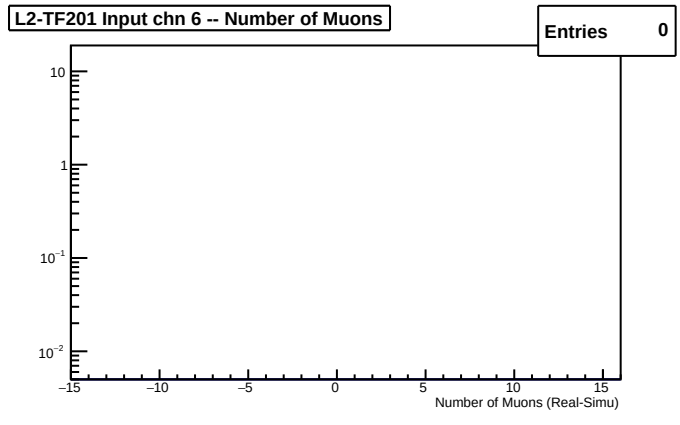

L2-TF201 Input chn 0 -- Number of Muons
Entries 10000
Number of Muons

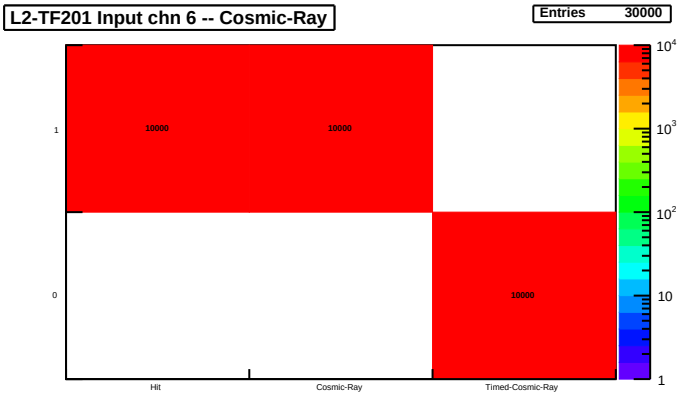

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

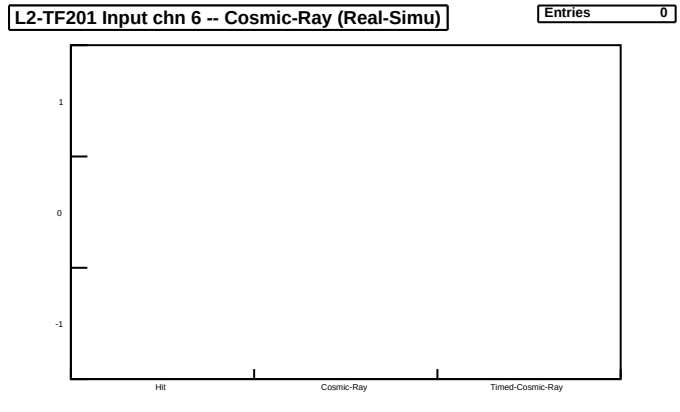

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 30000
10000
10000
10000
Hit
Cosmic-Ray
Timed-Cosmic-Ray

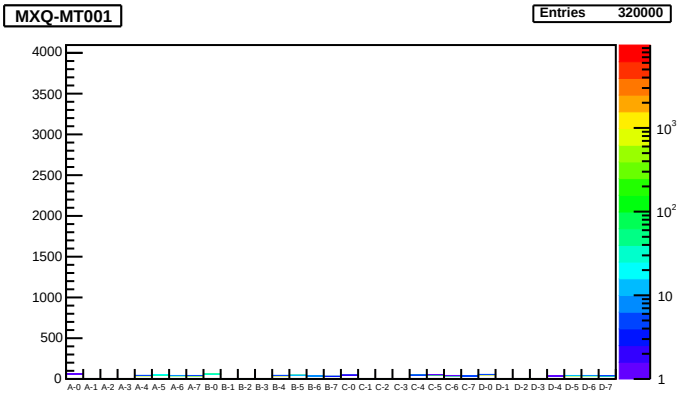

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

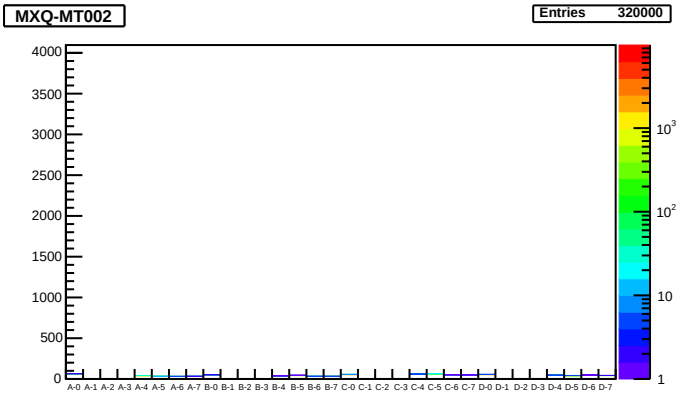

L2-TF201 Input chn 6 -- Number of Muons
Entries 10000
Number of Muons

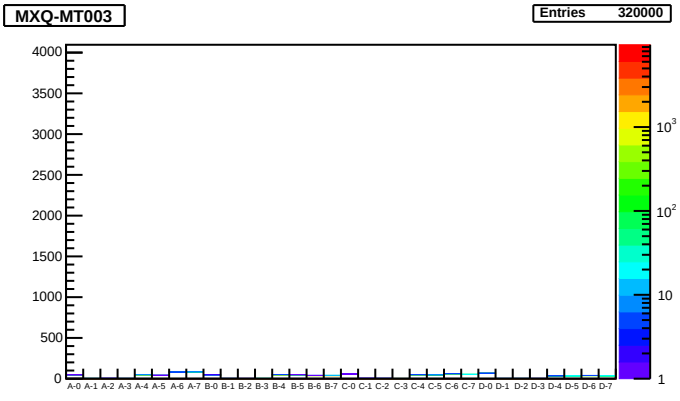

L2-TF201 Input chn 6 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

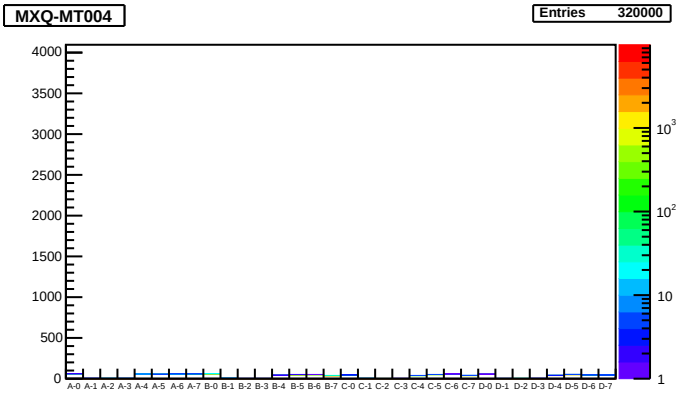

L2-TF201 Input chn 6 -- Cosmic-Ray
Entries 30000
10000
10000
10000
Hit
Cosmic-Ray
Timed-Cosmic-Ray

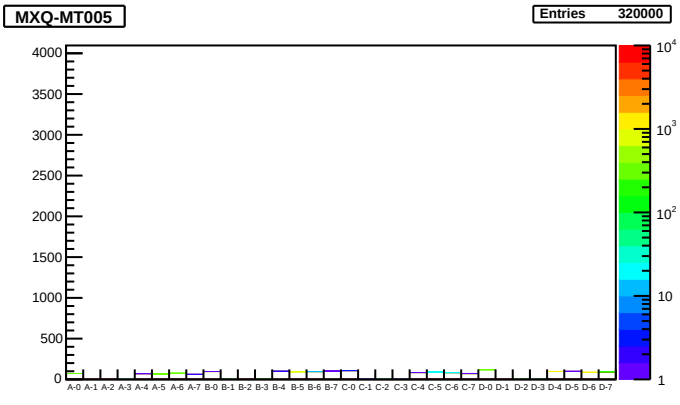

L2-TF201 Input chn 6 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

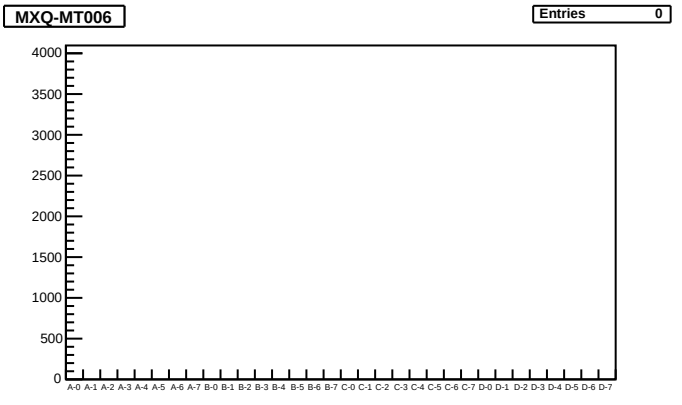

MXQ-MT001
Entries 320000

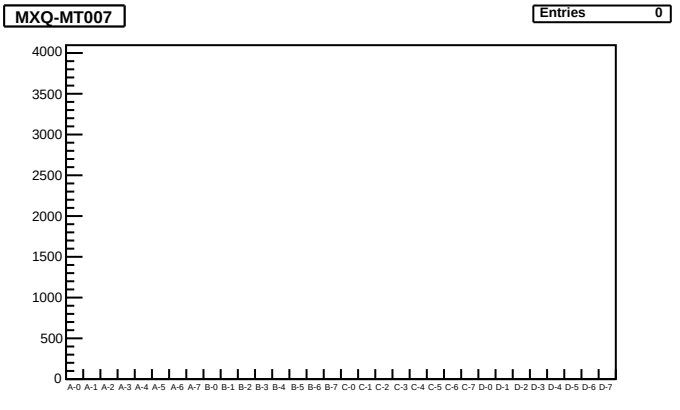

MXQ-MT002
Entries 320000

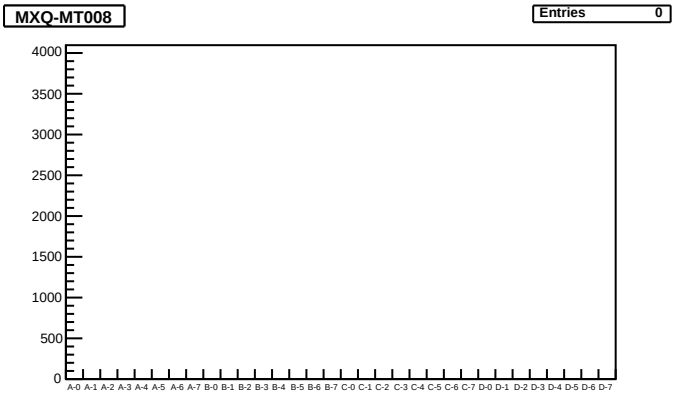

MXQ-MT003
Entries 320000

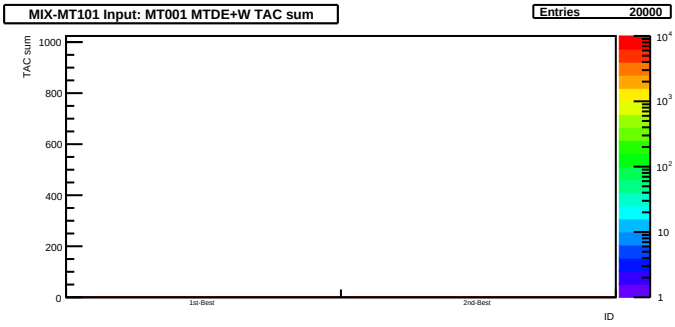

MXQ-MT004
Entries 320000

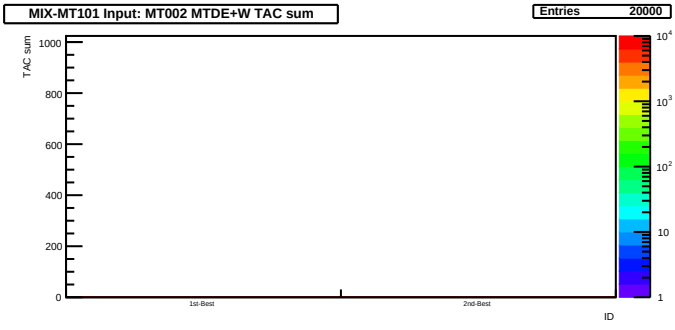

MXQ-MT005
Entries 320000

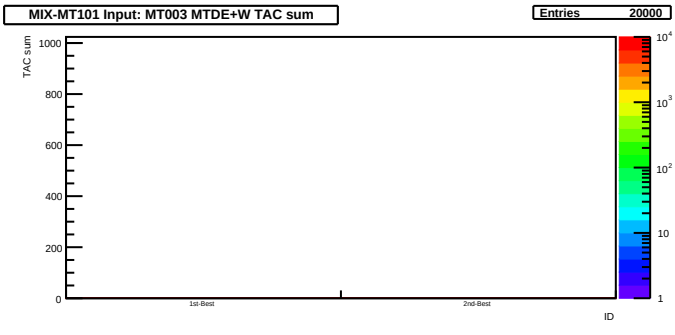

MXQ-MT006
Entries 0

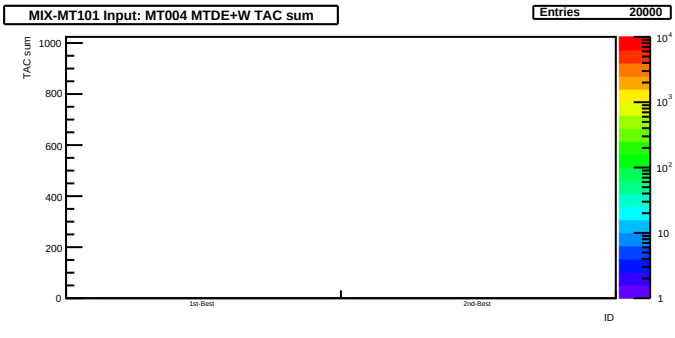

MXQ-MT007
Entries 0

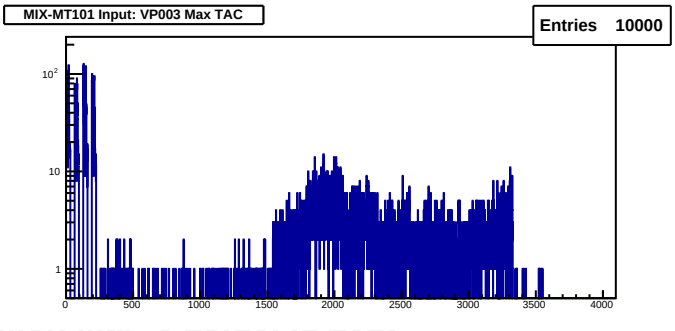

MXQ-MT008
Entries 0

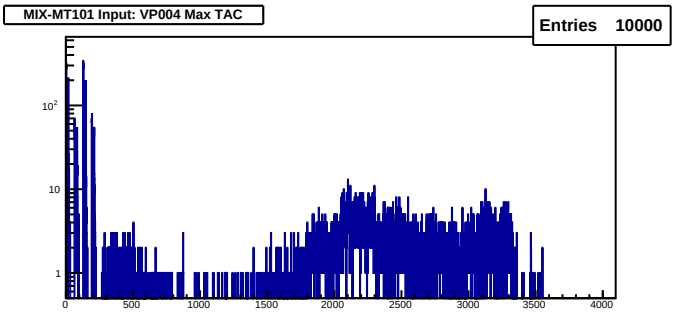

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

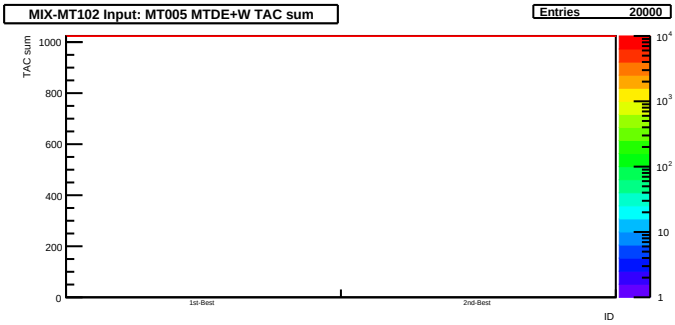

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

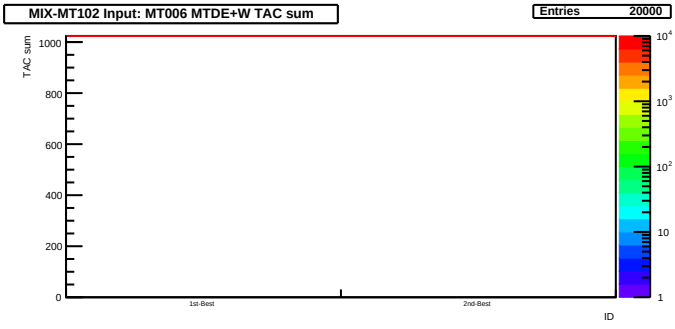

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

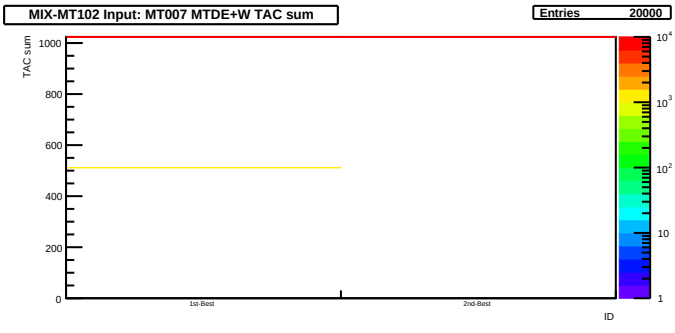

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

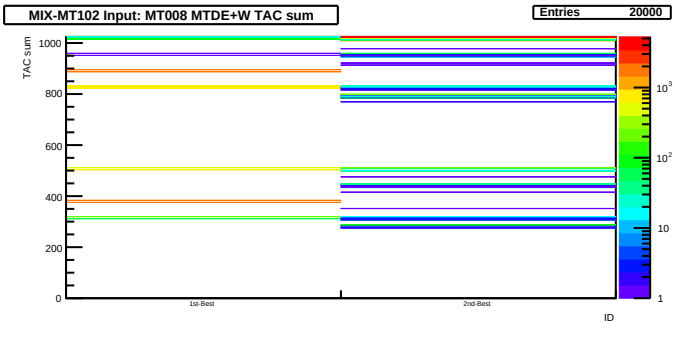

MIX-MT101 Input: VP003 Max TAC
Entries 10000

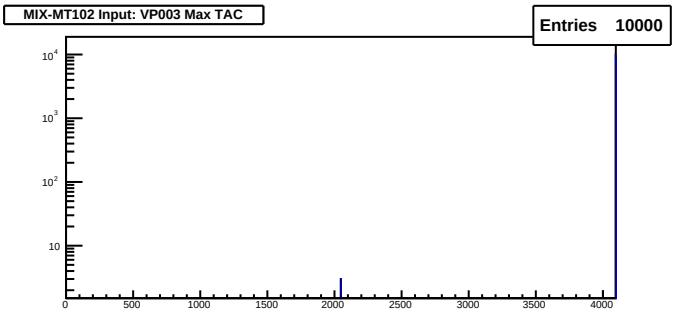

MIX-MT101 Input: VP004 Max TAC
Entries 10000

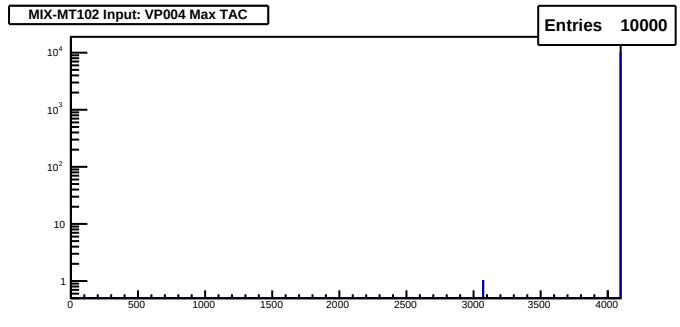

MIX-MT102 Input: MT005 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

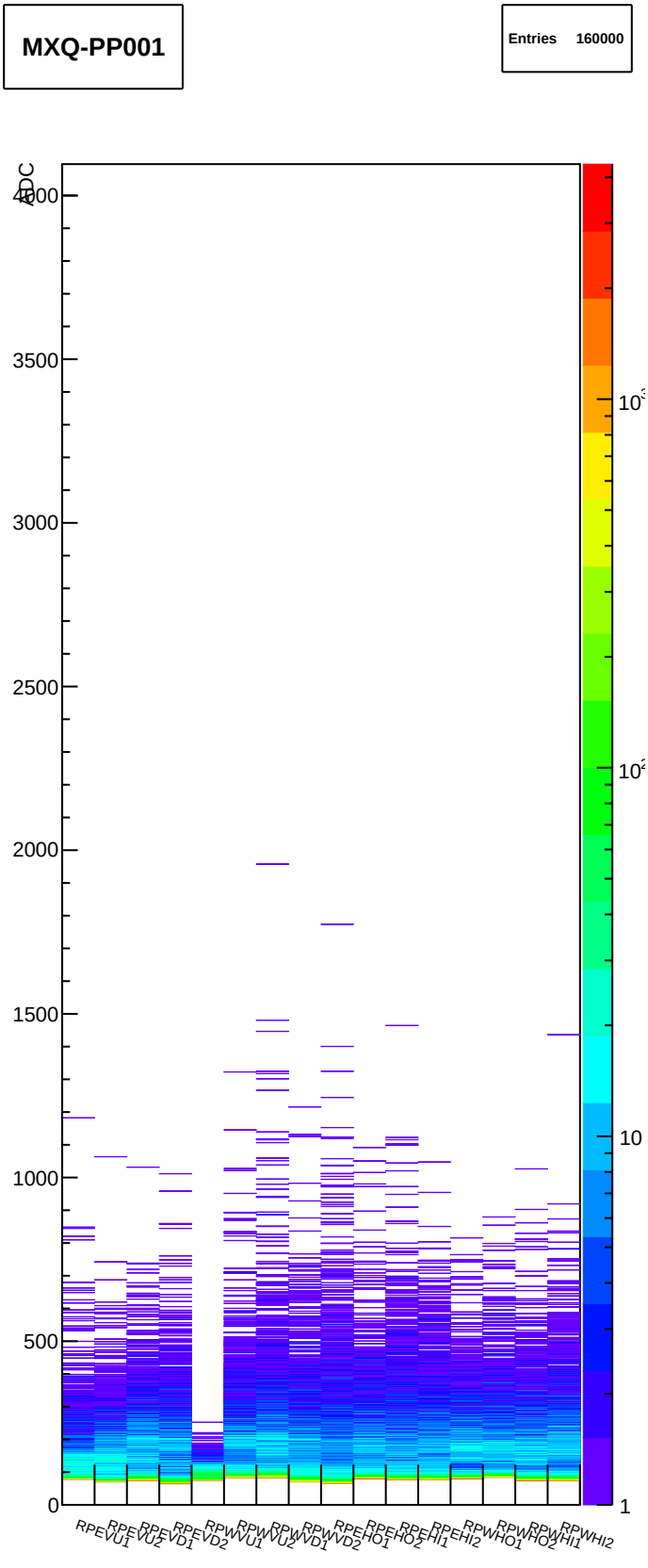

MIX-MT102 Input: MT006 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

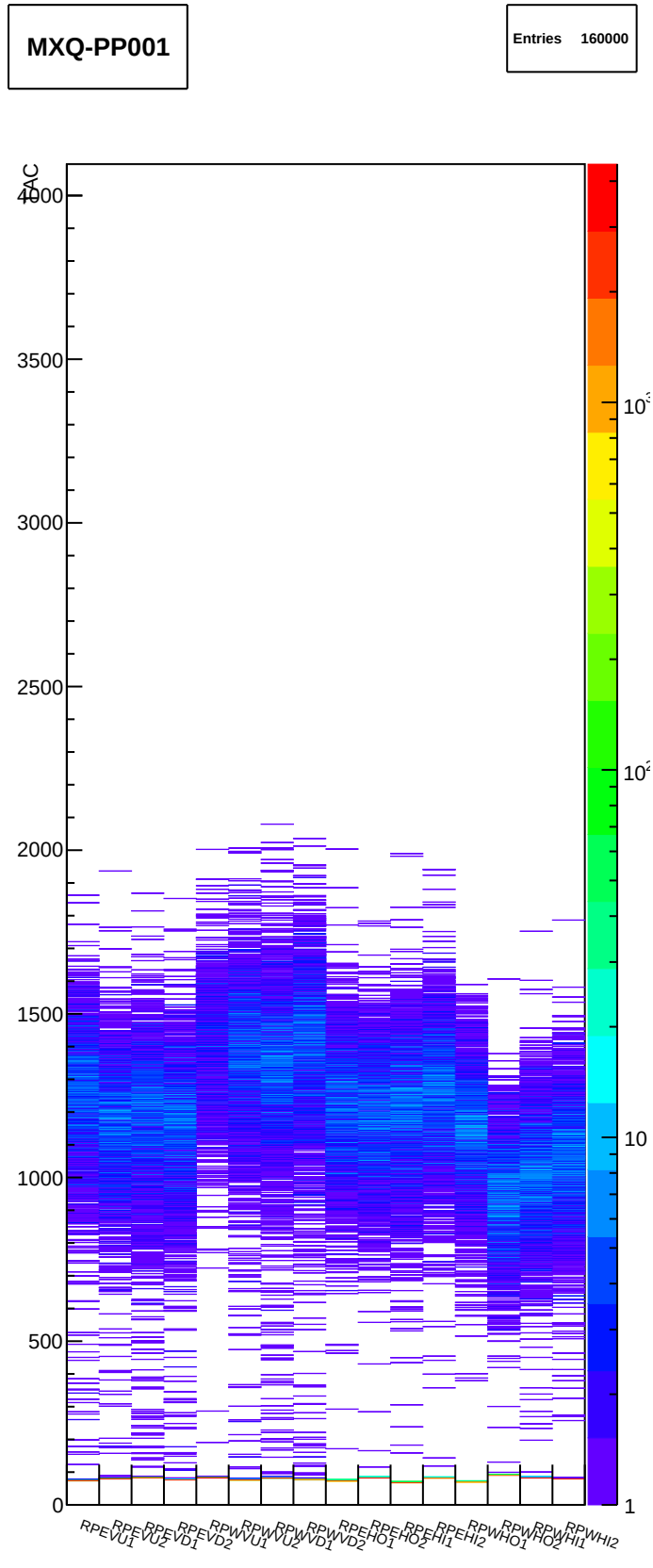

MIX-MT102 Input: MT007 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

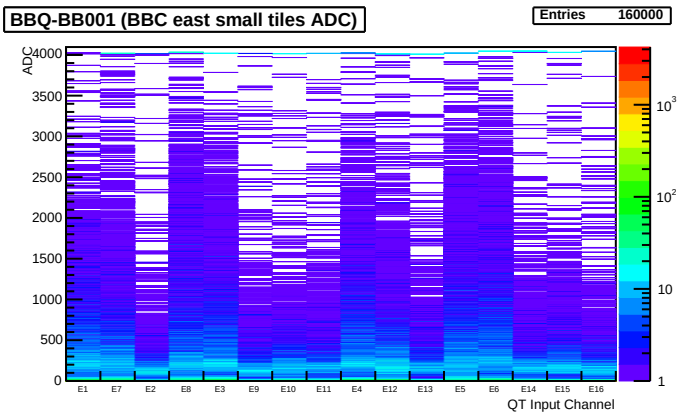

MIX-MT102 Input: MT008 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

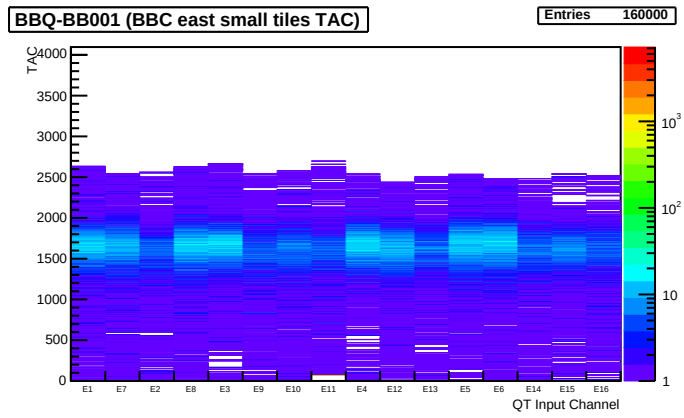

MIX-MT102 Input: VP003 Max TAC
Entries 10000

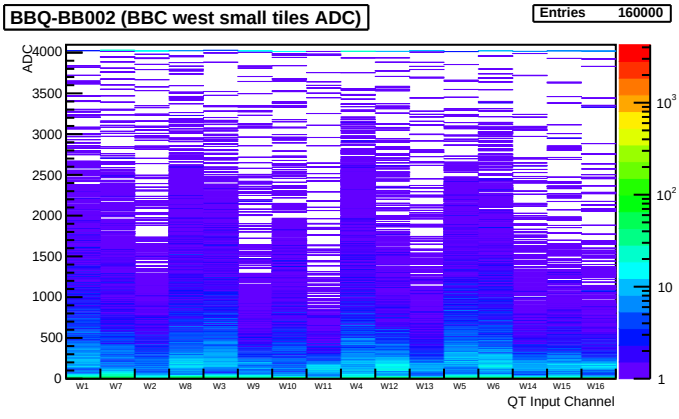

MIX-MT102 Input: VP004 Max TAC
Entries 10000

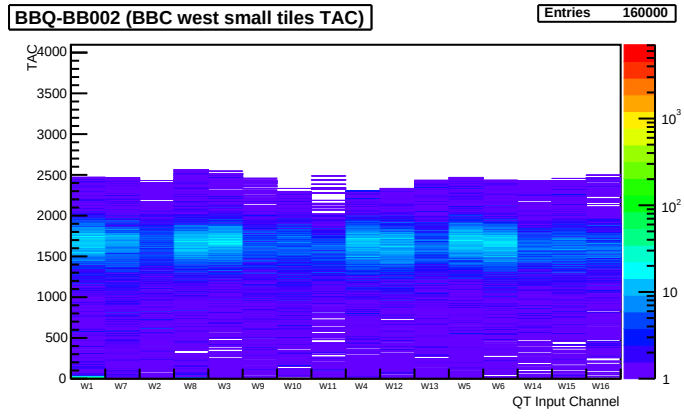

MXQ-PP001
Entries 160000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

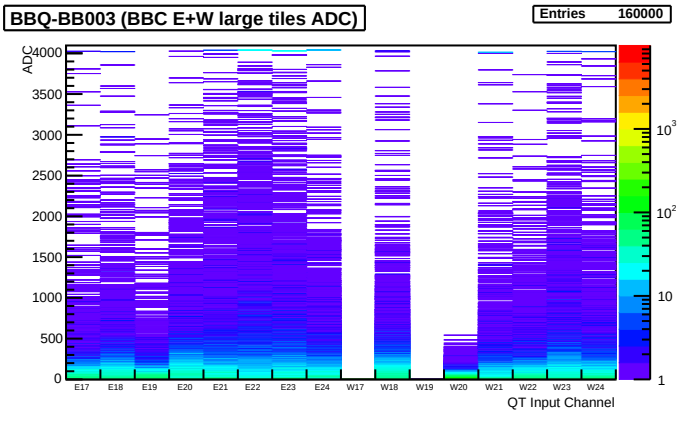

MXQ-PP001
Entries 160000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

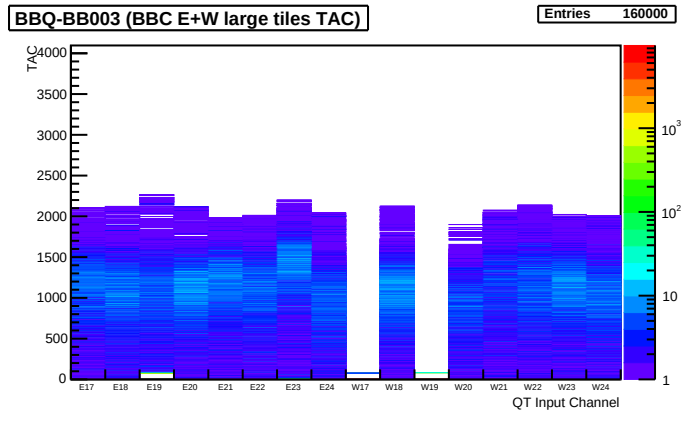

BBQ-BB001 (BBC east small tiles ADC)
Entries 160000
ADC
QT Input Channel

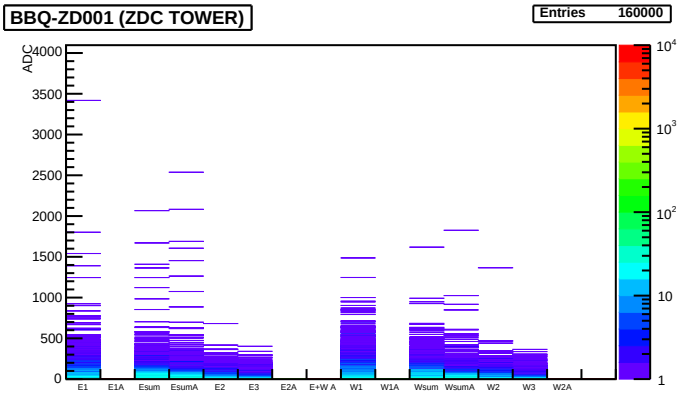

BBQ-BB001 (BBC east small tiles TAC)
Entries 160000
TAC
QT Input Channel

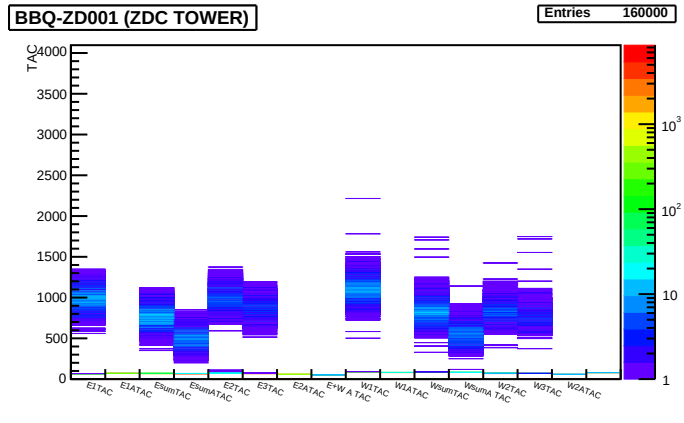

BBQ-BB002 (BBC west small tiles ADC)
Entries 160000
ADC
QT Input Channel

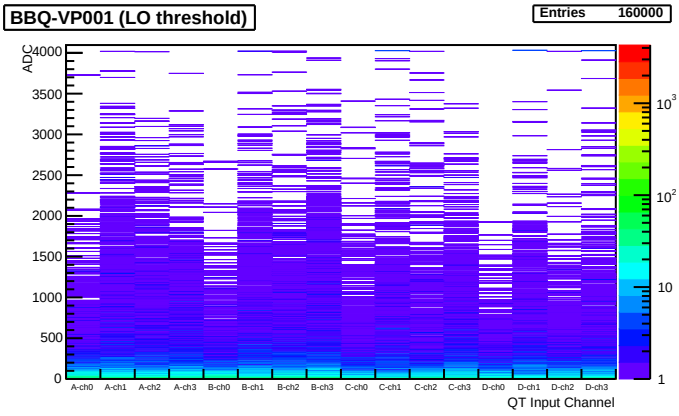

BBQ-BB002 (BBC west small tiles TAC)
Entries 160000
TAC
QT Input Channel

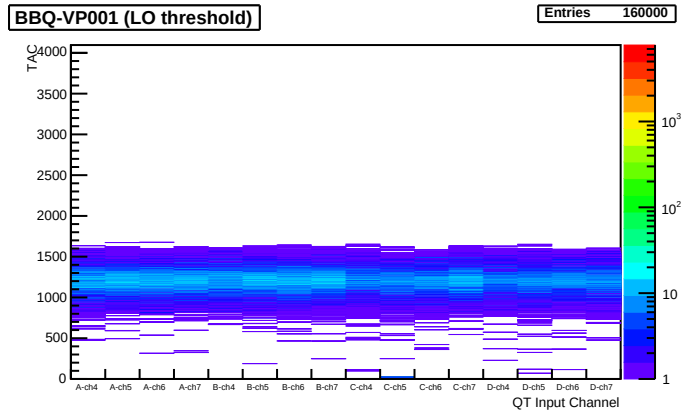

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 160000
ADC
QT Input Channel

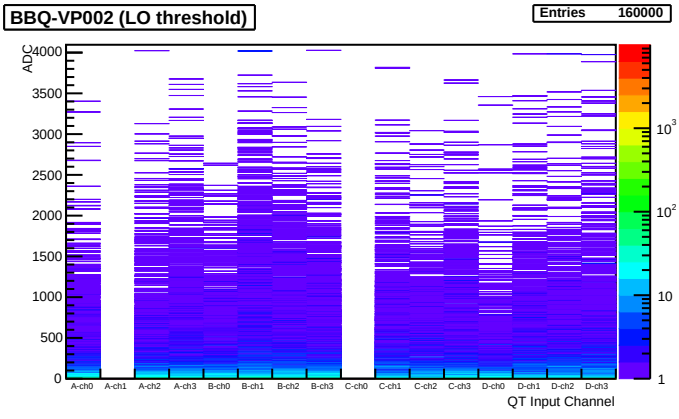

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 160000
TAC
QT Input Channel

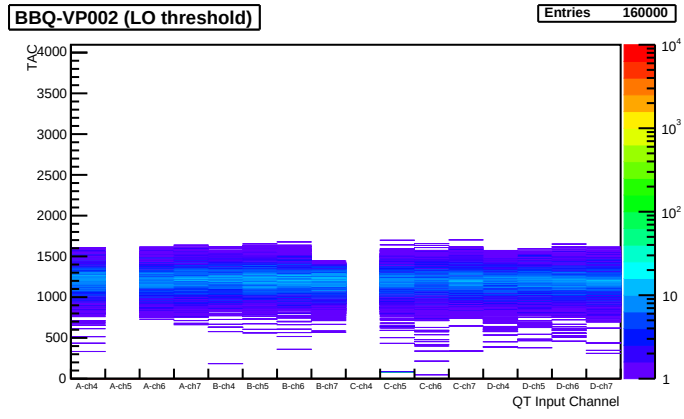

BBQ-ZD001 (ZDC TOWER)
Entries 160000
ADC

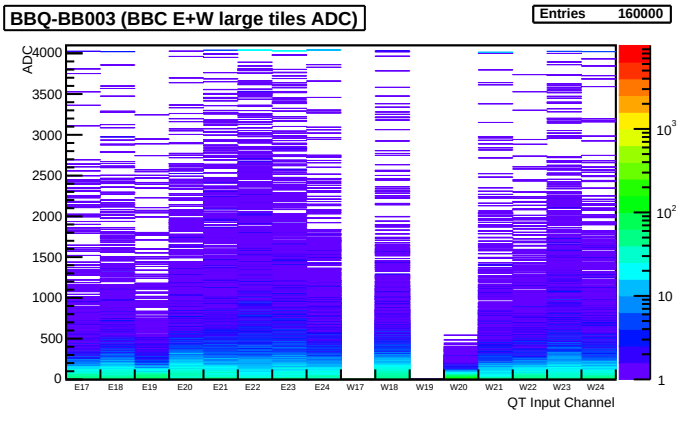

BBQ-ZD001 (ZDC TOWER)
Entries 160000
TAC

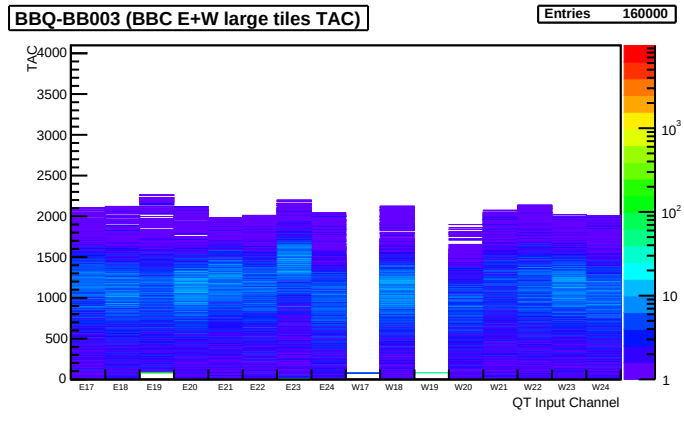

BBQ-VP001 (LO threshold)
Entries 160000
ADC
QT Input Channel

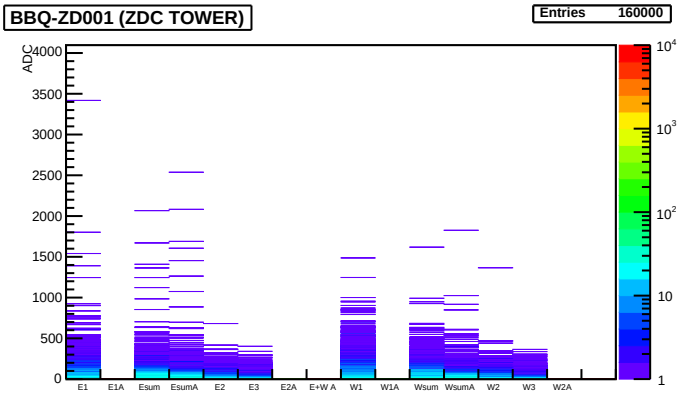

BBQ-VP001 (LO threshold)
Entries 160000
TAC
QT Input Channel

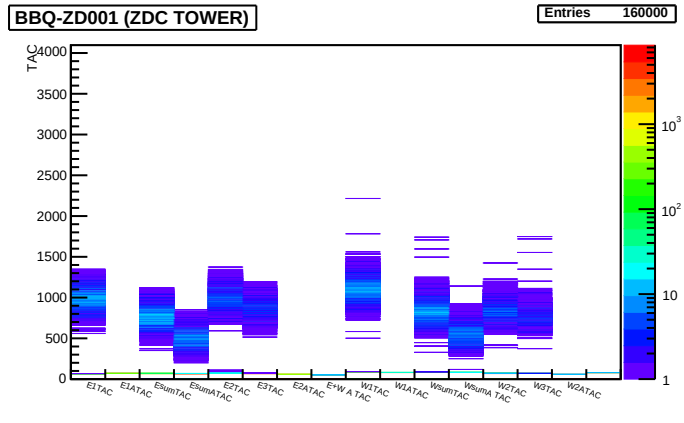

BBQ-VP002 (LO threshold)
Entries 160000
ADC
QT Input Channel

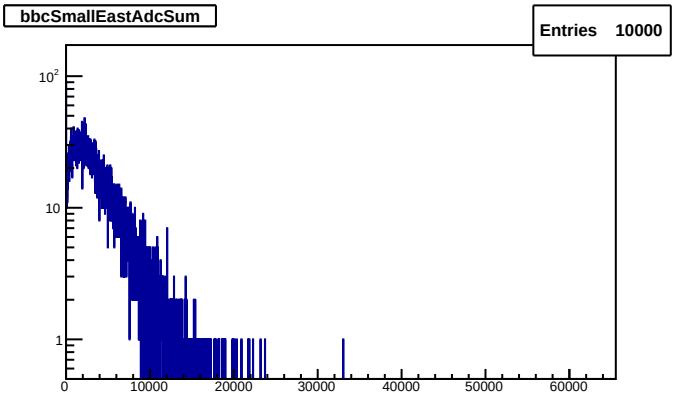

BBQ-VP002 (LO threshold)
Entries 160000
TAC
QT Input Channel

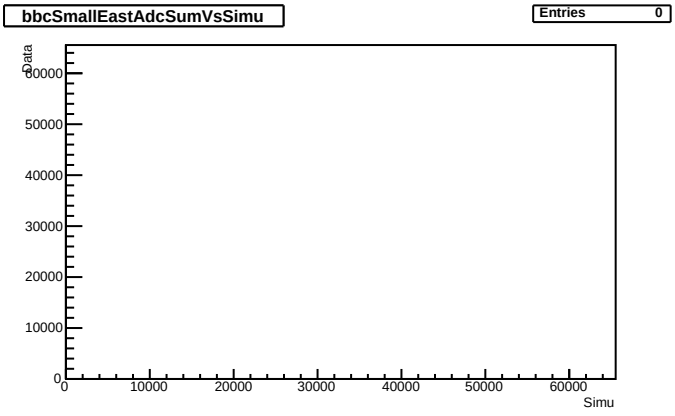

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 160000
ADC
QT Input Channel

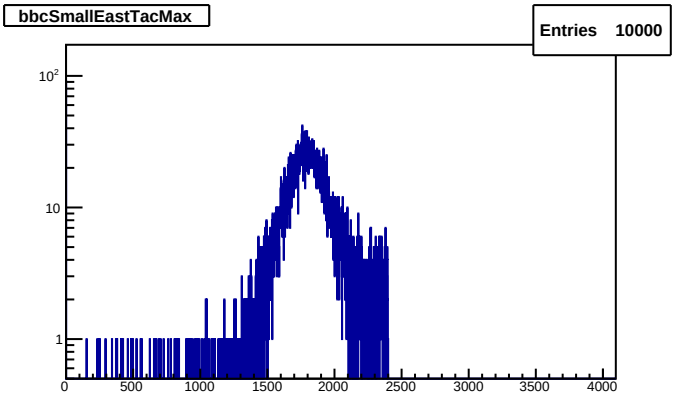

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 160000
TAC
QT Input Channel

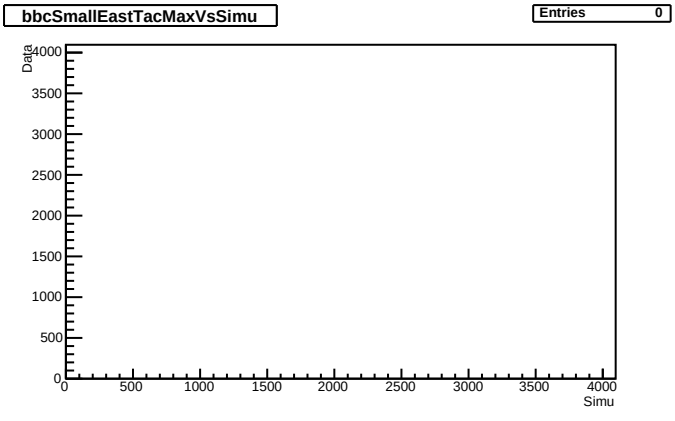

BBQ-ZD001 (ZDC TOWER)
Entries 160000
ADC

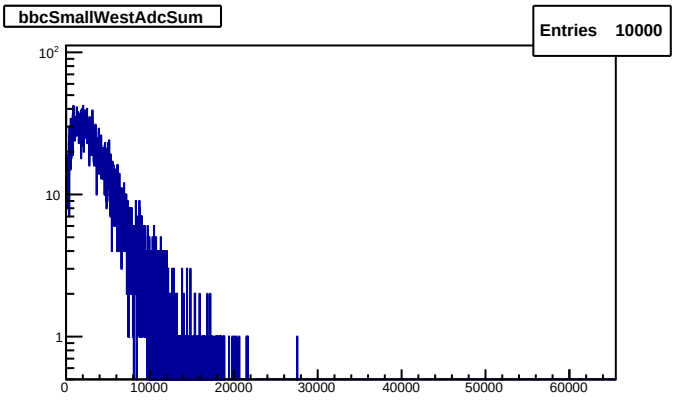

BBQ-ZD001 (ZDC TOWER)
Entries 160000
TAC

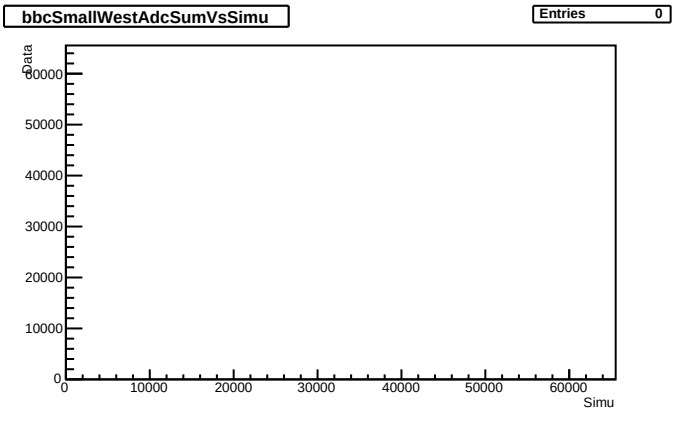

bbcSmallEastAdcSum
Entries 10000

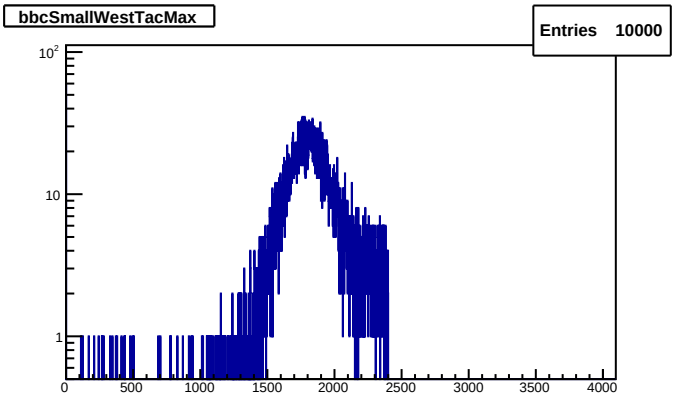

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

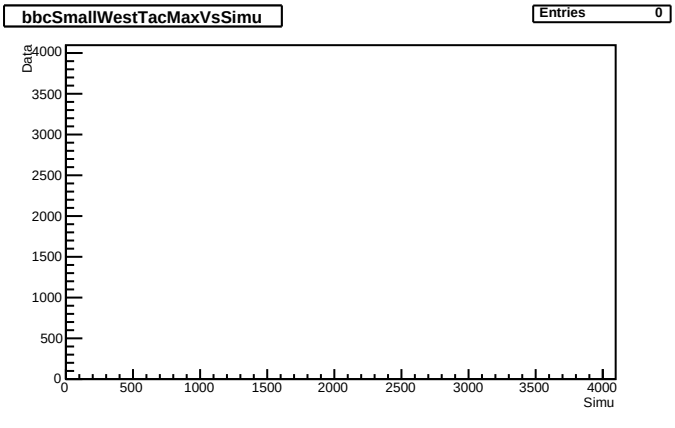

bbcSmallEastTacMax
Entries 10000

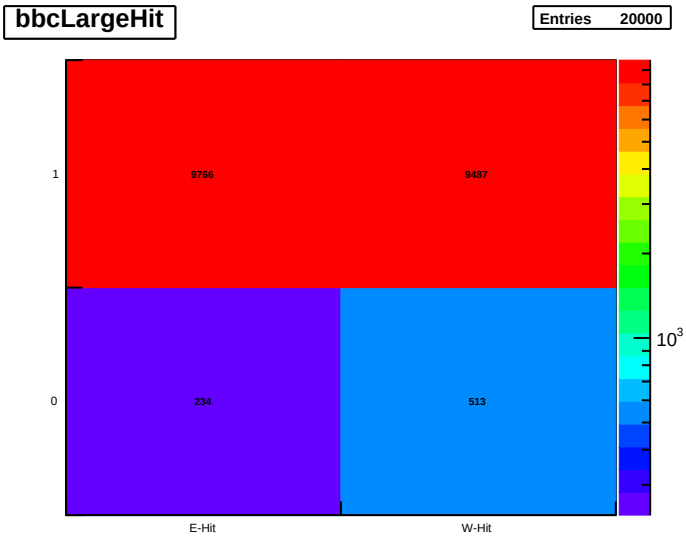

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

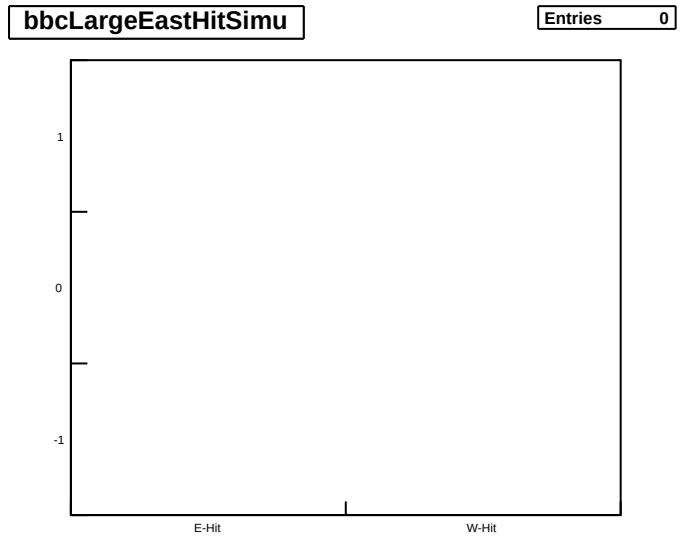

bbcSmallWestAdcSum
Entries 10000

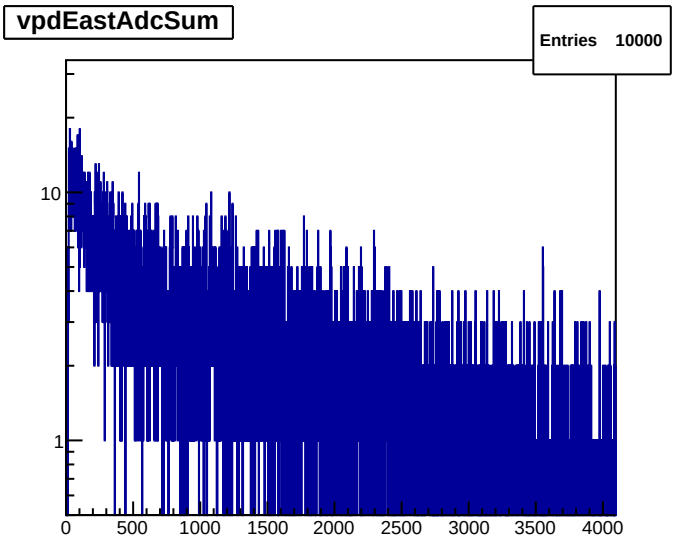

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

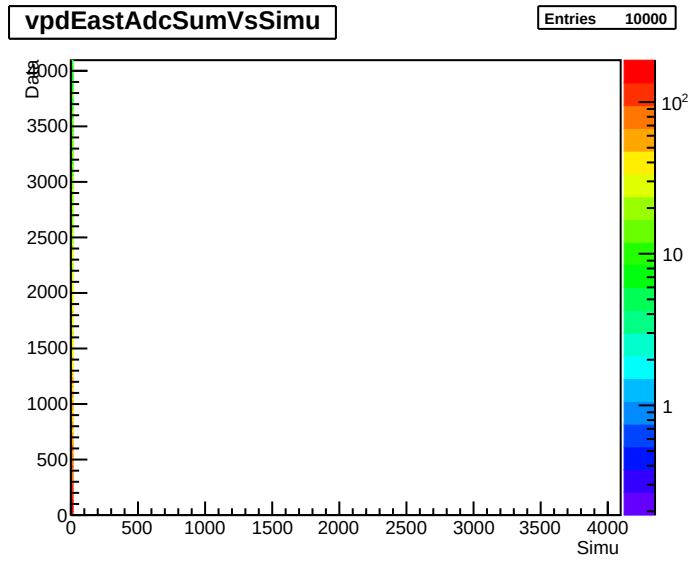

bbcSmallWestTacMax
Entries 10000

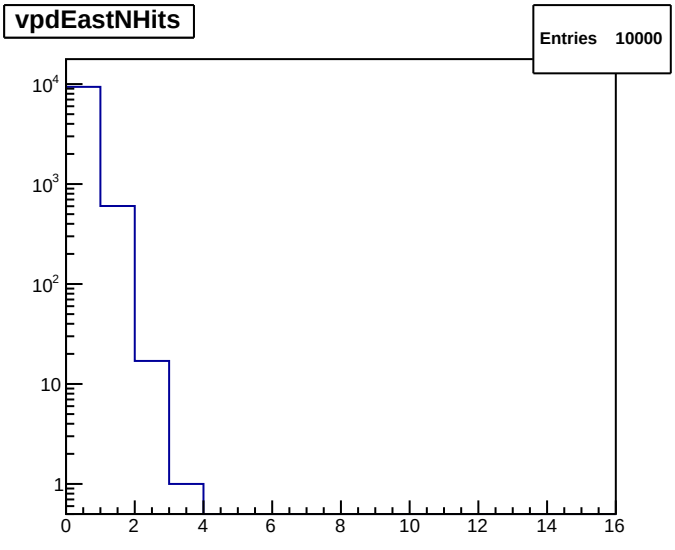

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

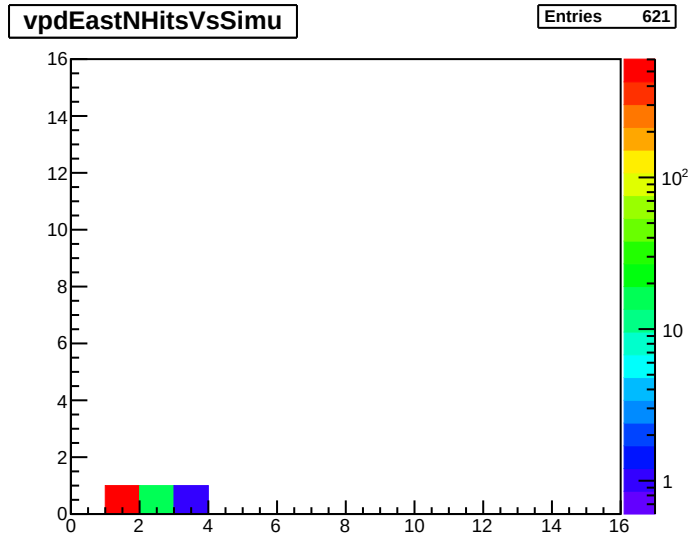

bbcLargeHit
Entries 20000
1
0
9766
9487
234
513
E-Hit
W-Hit
10^3

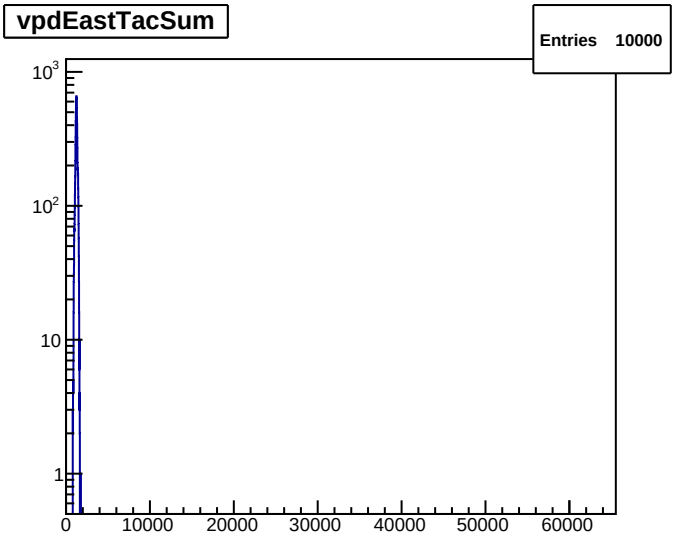

bbcLargeEastHitSimu
Entries 0
1
0
-1
E-Hit
W-Hit

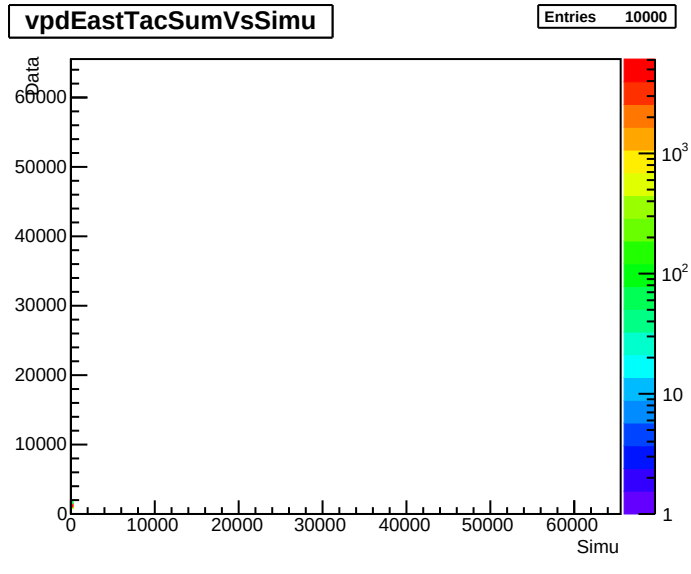

vpdEastAdcSum
Entries 10000

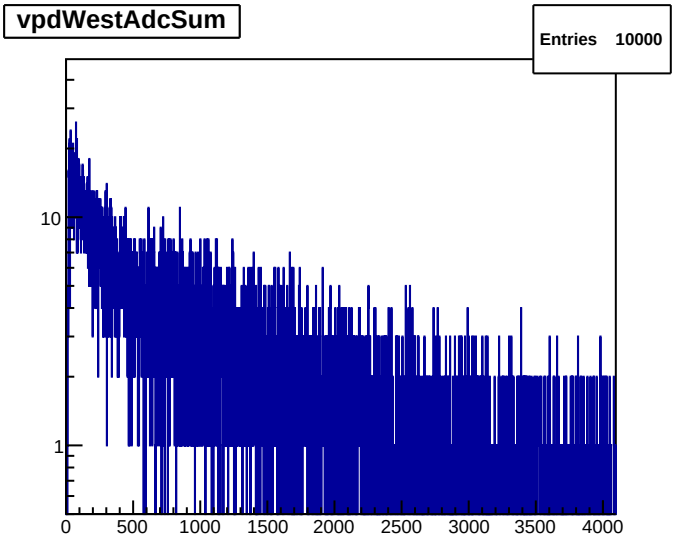

vpdEastAdcSumVsSimu
Entries 10000
Data
Simu

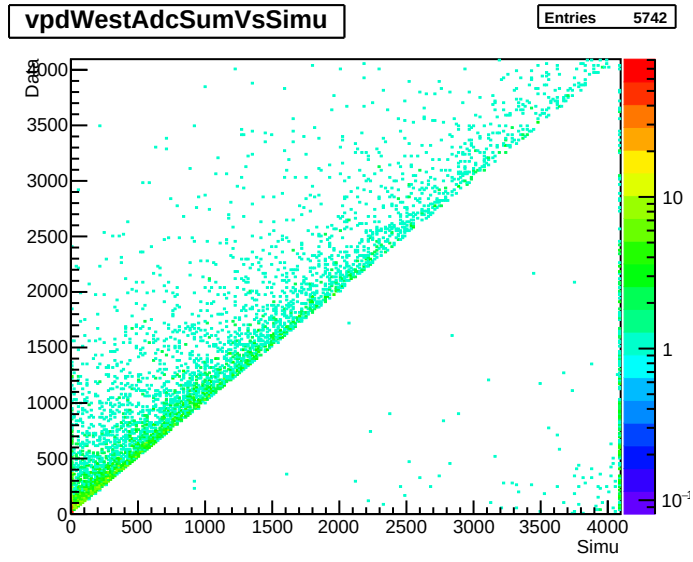

vpdEastNHits
Entries 10000

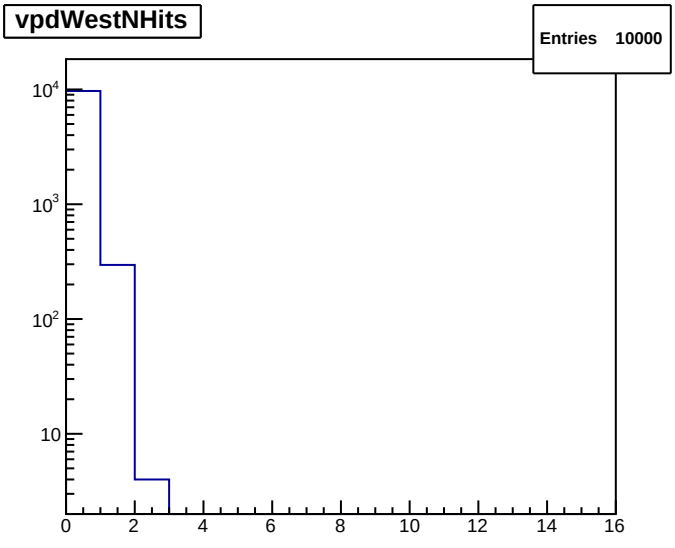

vpdEastNHitsVsSimu
Entries 621

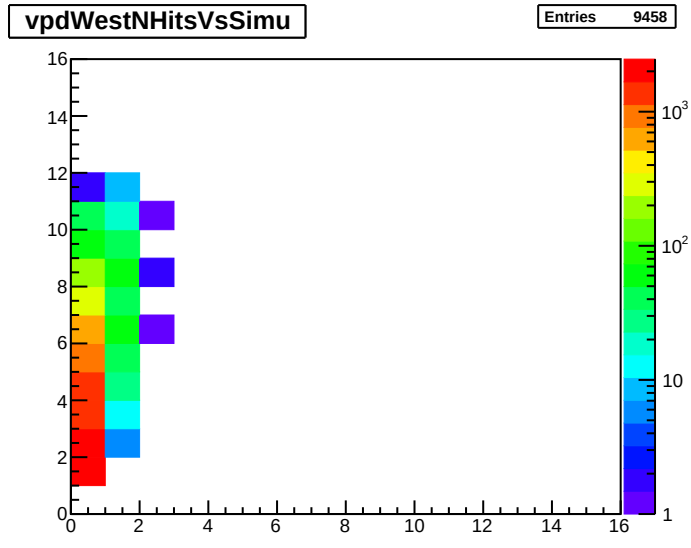

vpdEastTacSum
Entries 10000

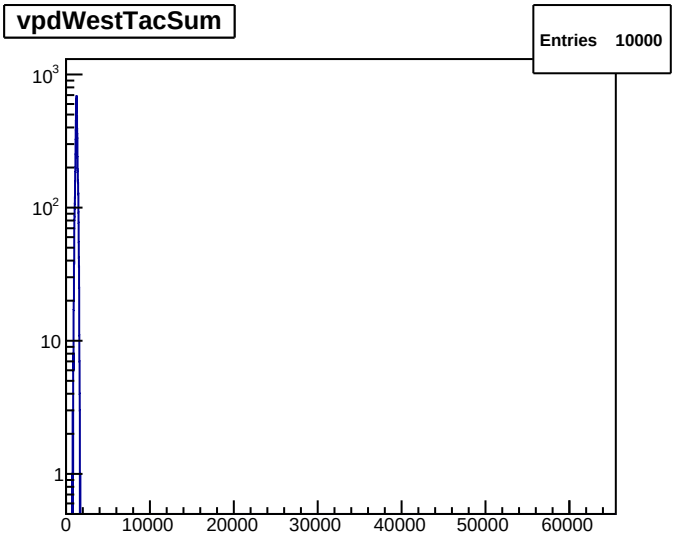

vpdEastTacSumVsSimu
Entries 10000
Data
Simu

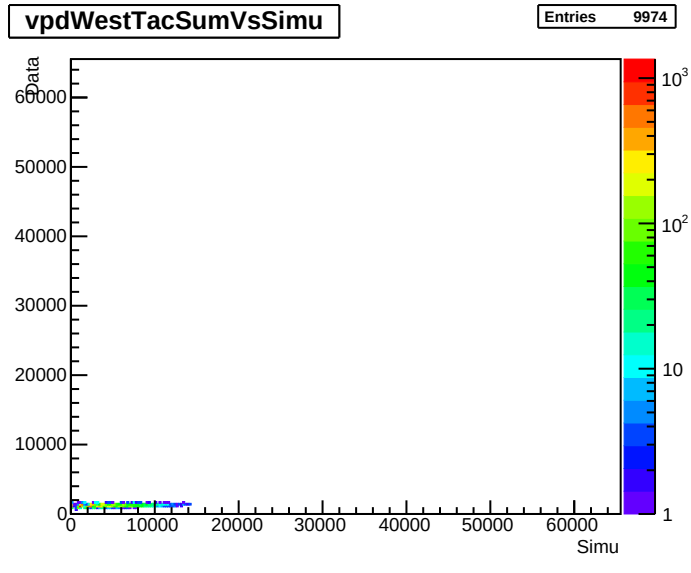

vpdWestAdcSum
Entries 10000

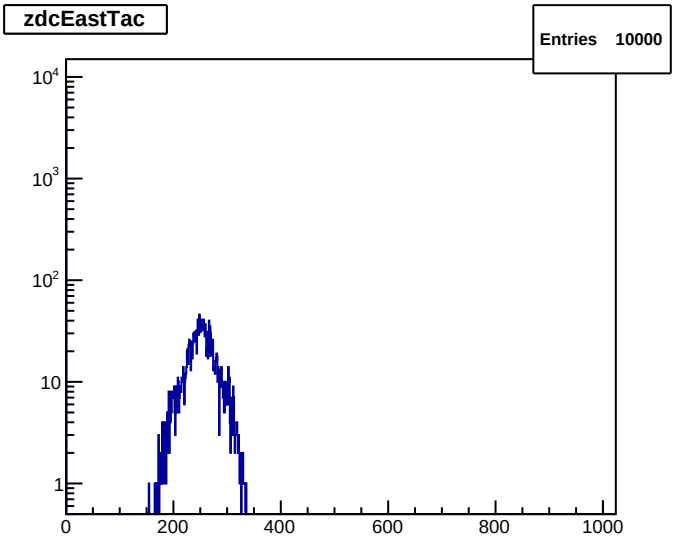

vpdWestAdcSumVsSimu
Entries 5742
Data
Simu

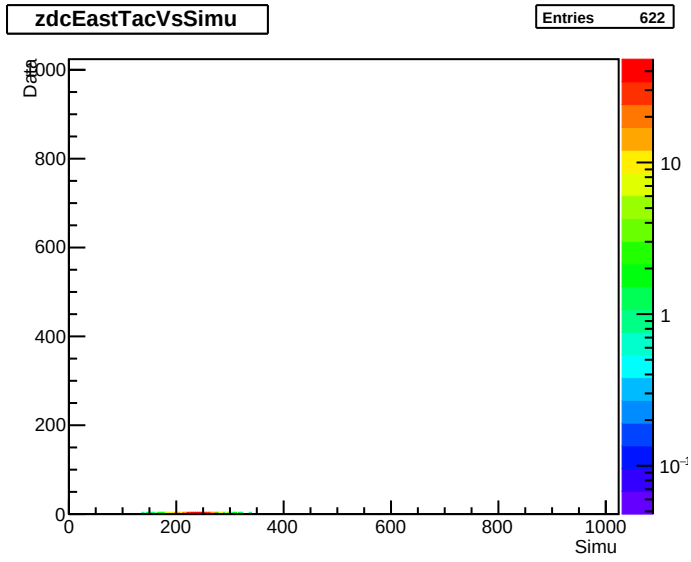

vpdWestNHits
Entries 10000

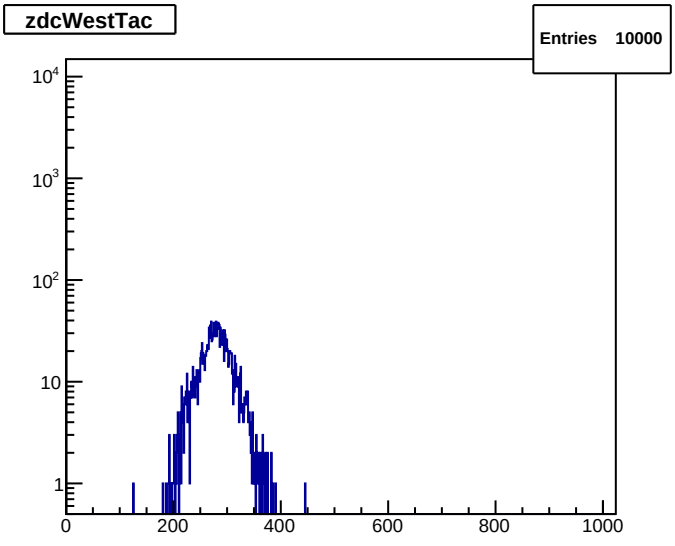

vpdWestNHitsVsSimu
Entries 9458

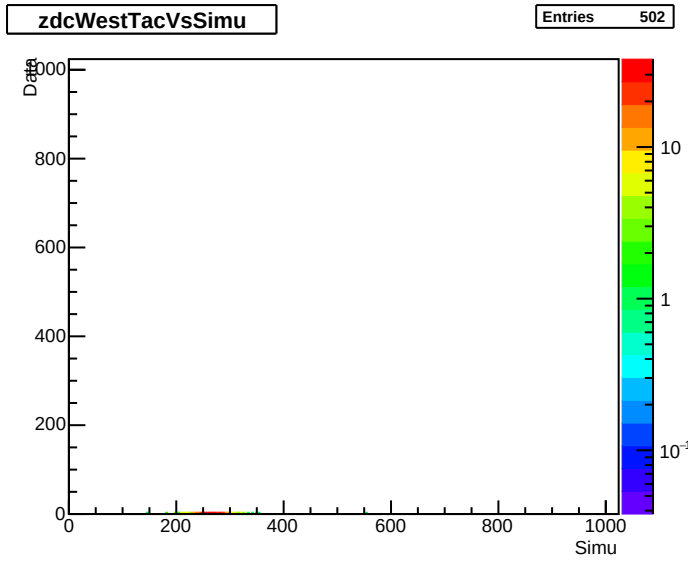

vpdWestTacSum
Entries 10000

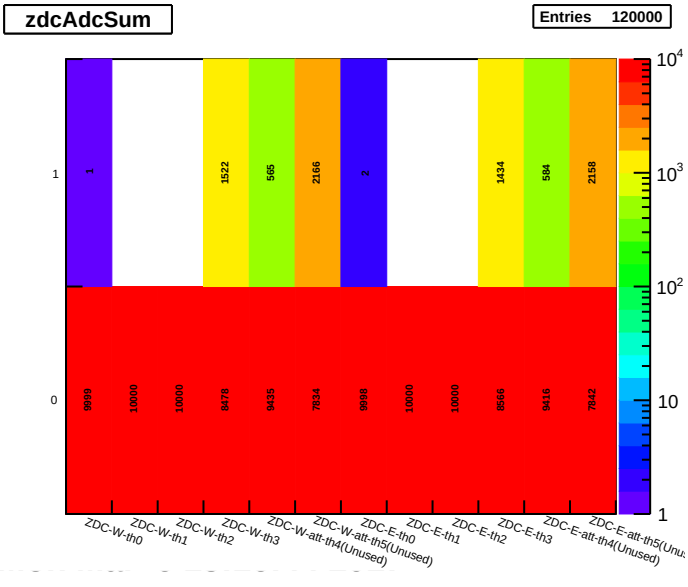

vpdWestTacSumVsSimu
Entries 9974
Data
Simu

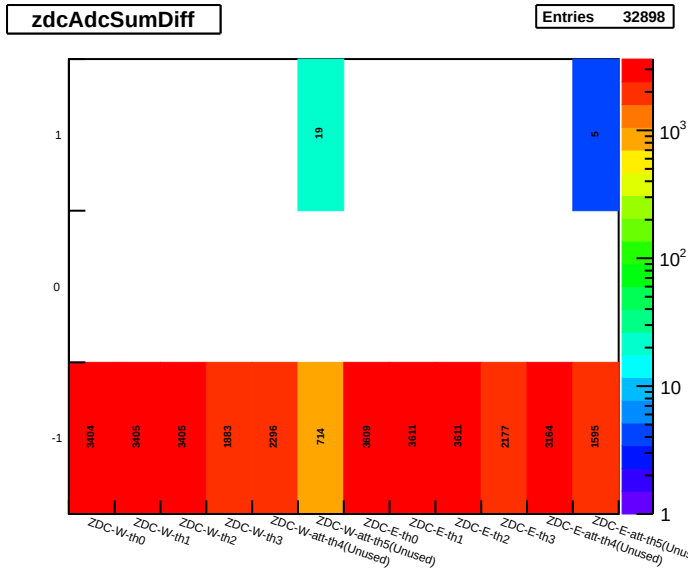

zdcEastTac
Entries 10000

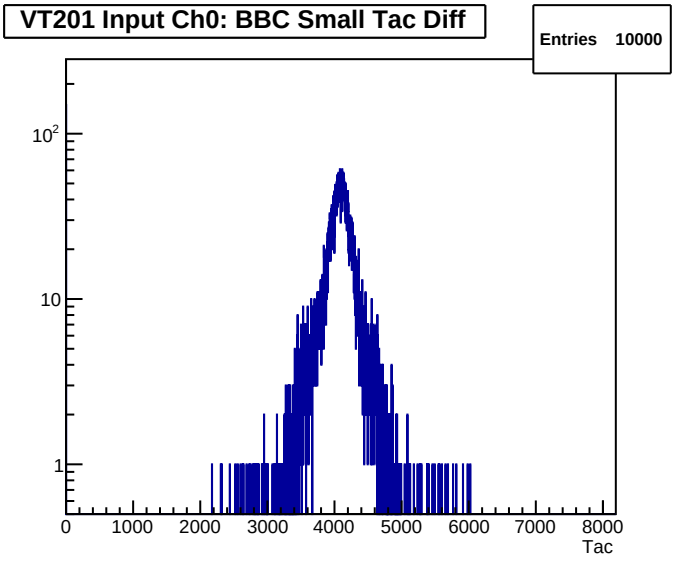

zdcEastTacVsSimu
Entries 622
Data
Simu

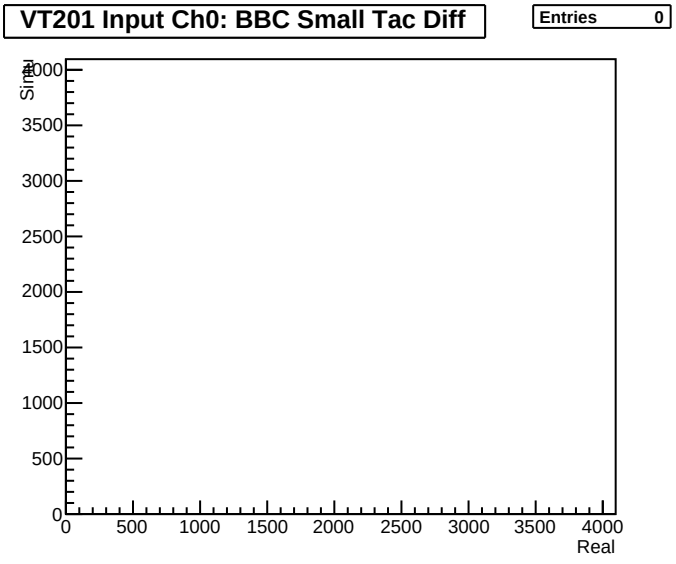

zdcWestTac
Entries 10000

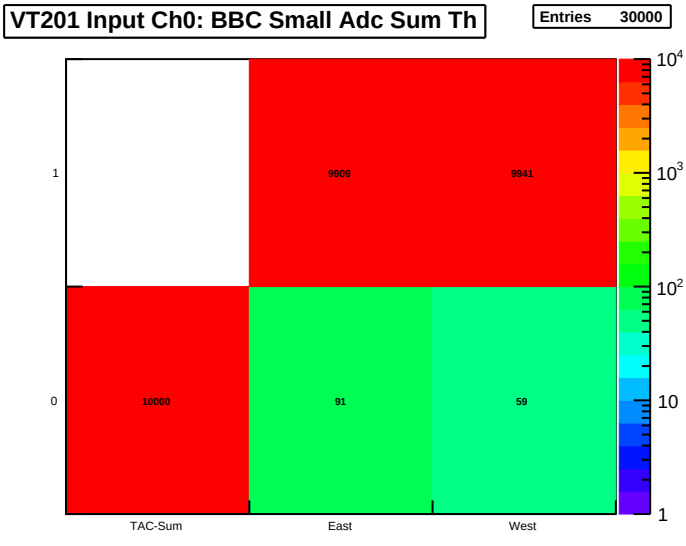

zdcWestTacVsSimu
Entries 502
Data
Simu

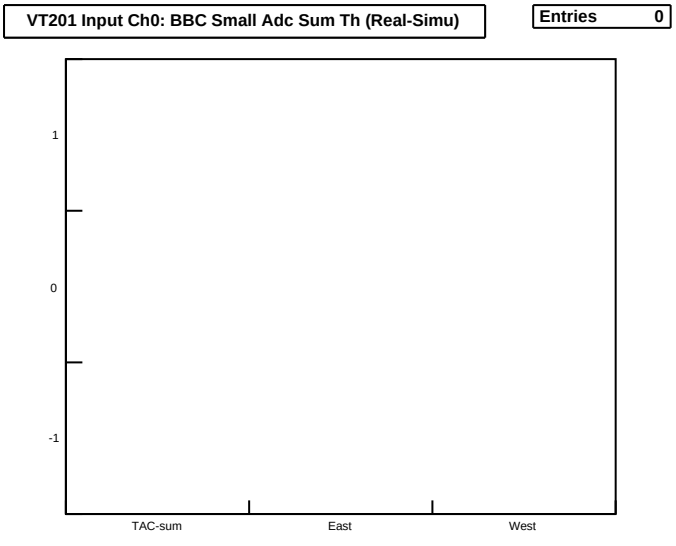

zdcAdcSum
Entries 120000
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

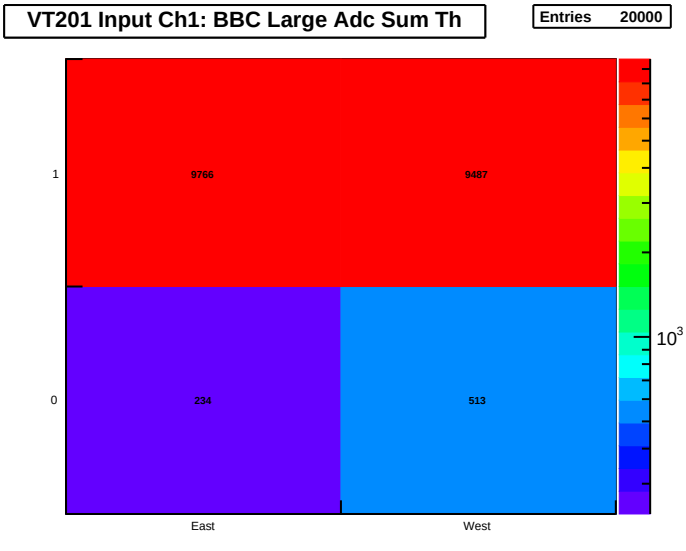

zdcAdcSumDiff
Entries 32898
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

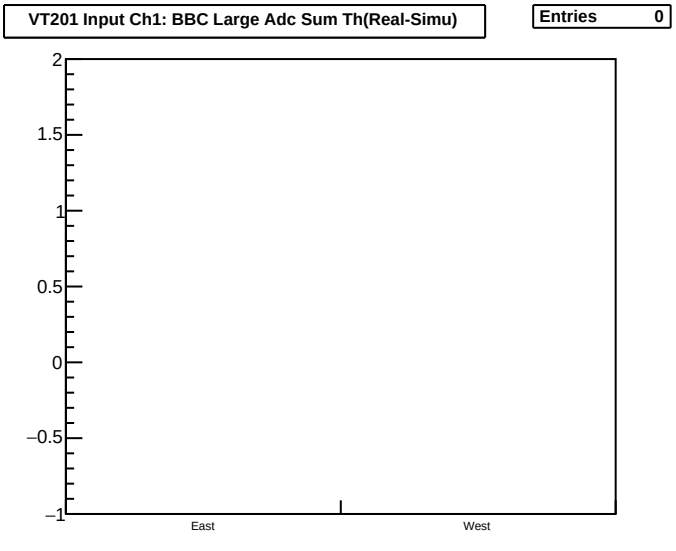

VT201 Input Ch0: BBC Small Tac Diff
Entries 10000
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

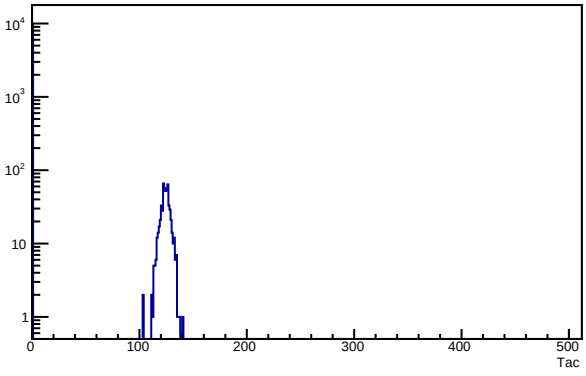

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 30000
9909
9941
10000
91
59
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

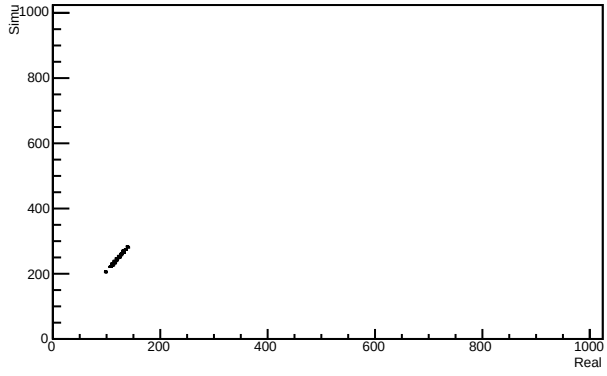

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 20000
9766
9487
234
513
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

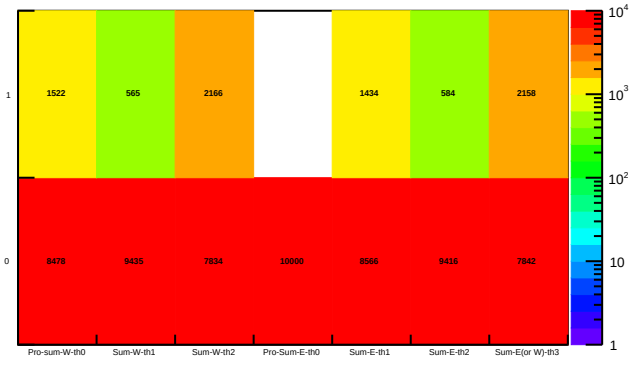
VT201 Input Ch2: ZDC Tac Diff



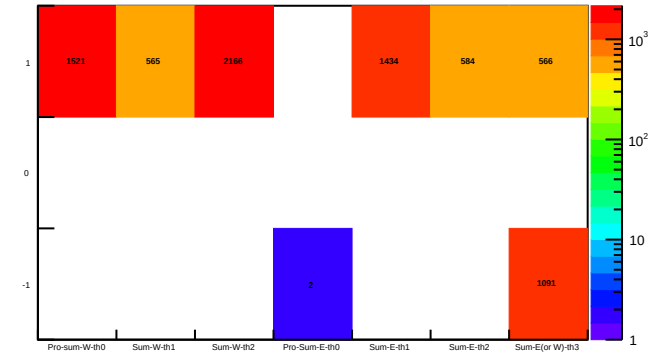
VT201 Input Ch2: ZDC Tac Diff



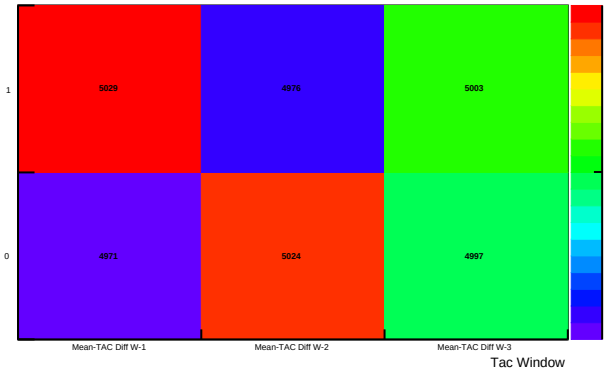
VT201 Input Ch2: ZDC Adc Sum Th



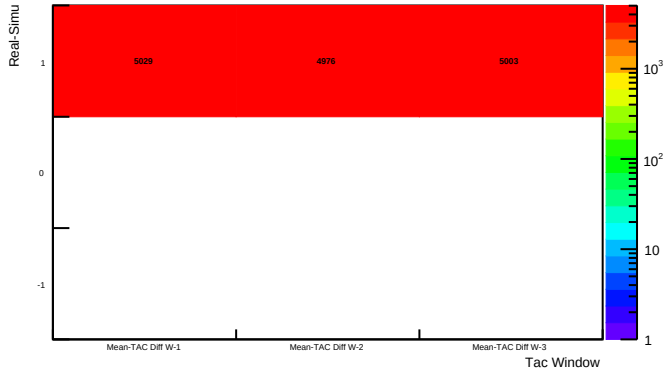
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



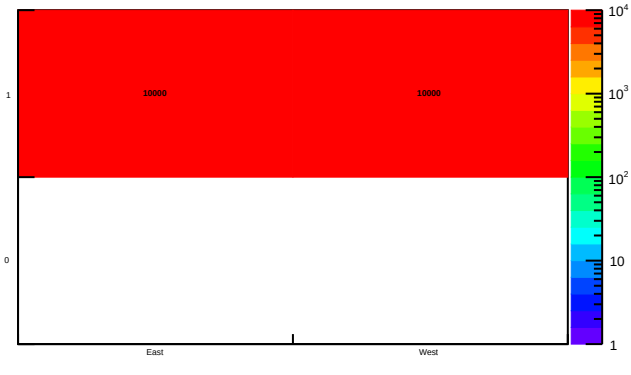
VT201 Input Ch4: VPD Tac Diff



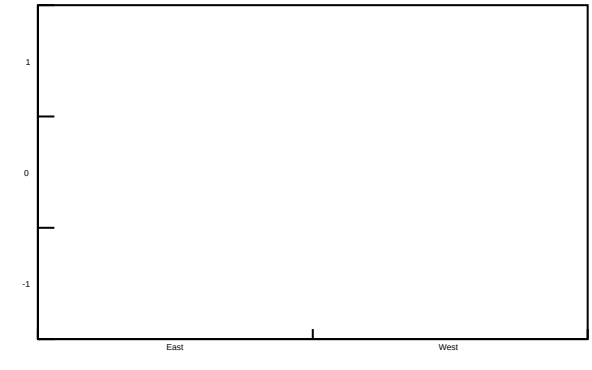
VT201 Input Ch4: VPD Tac Diff

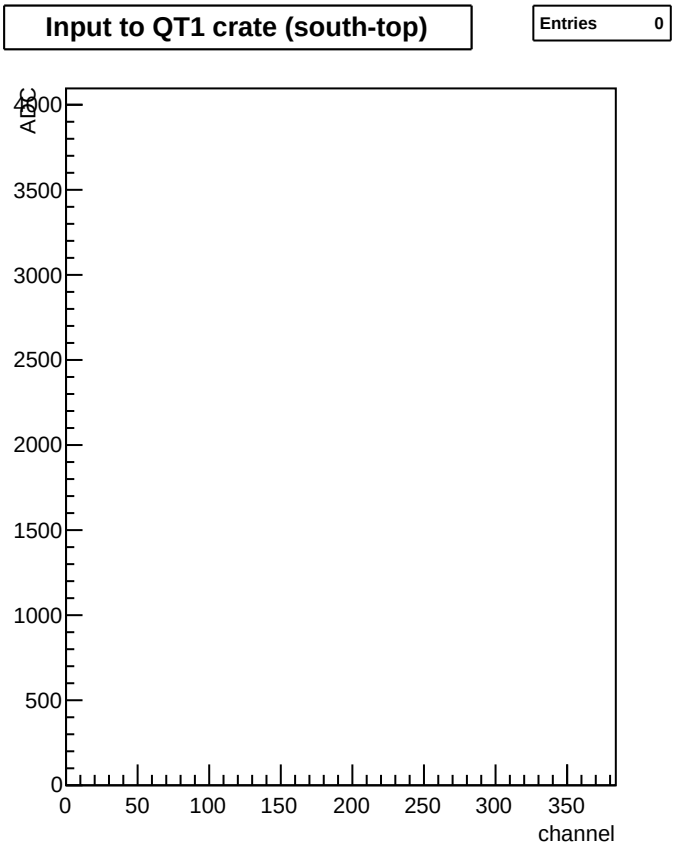


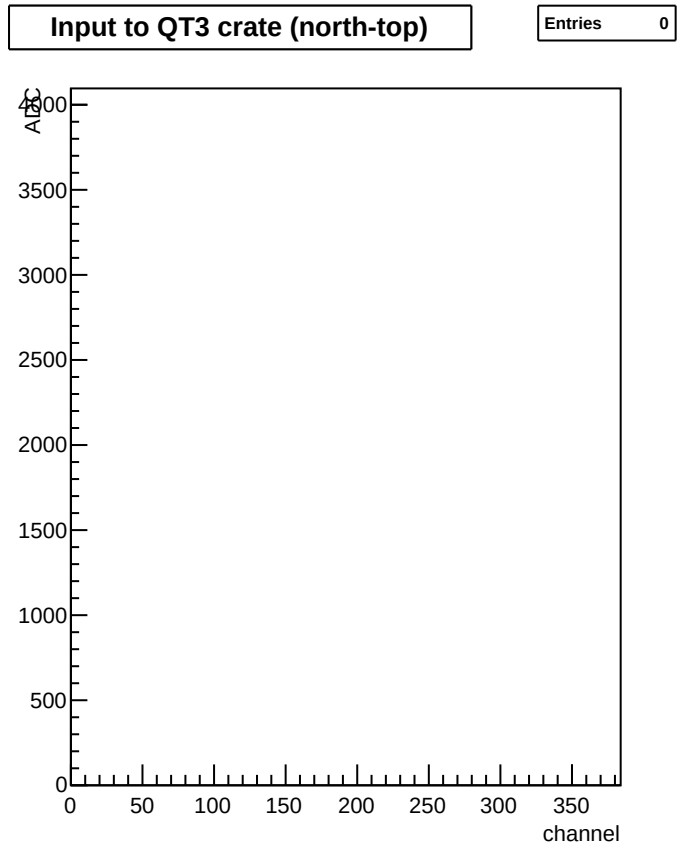
VT201 Input Ch4: VPD Adc Sum Th

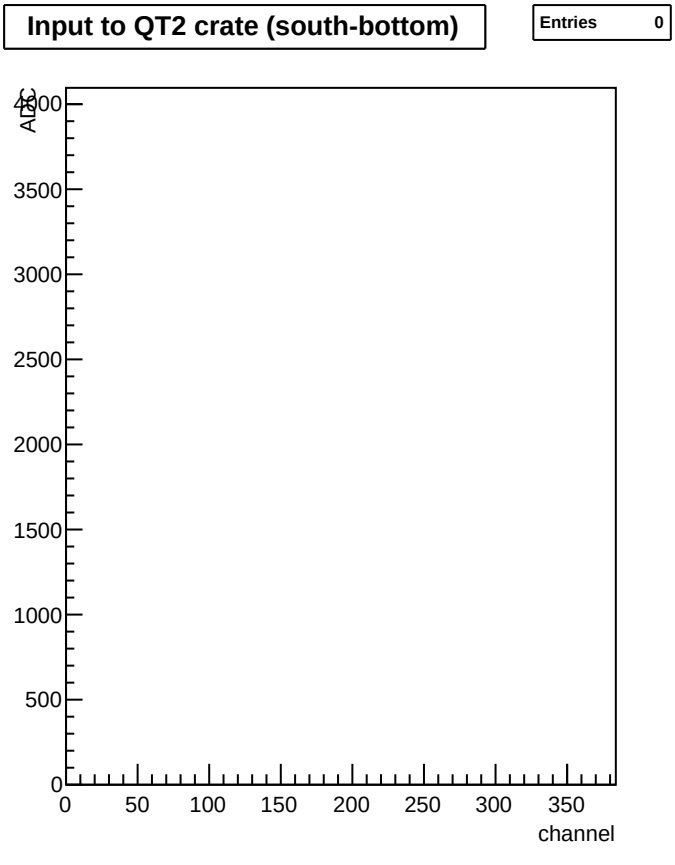


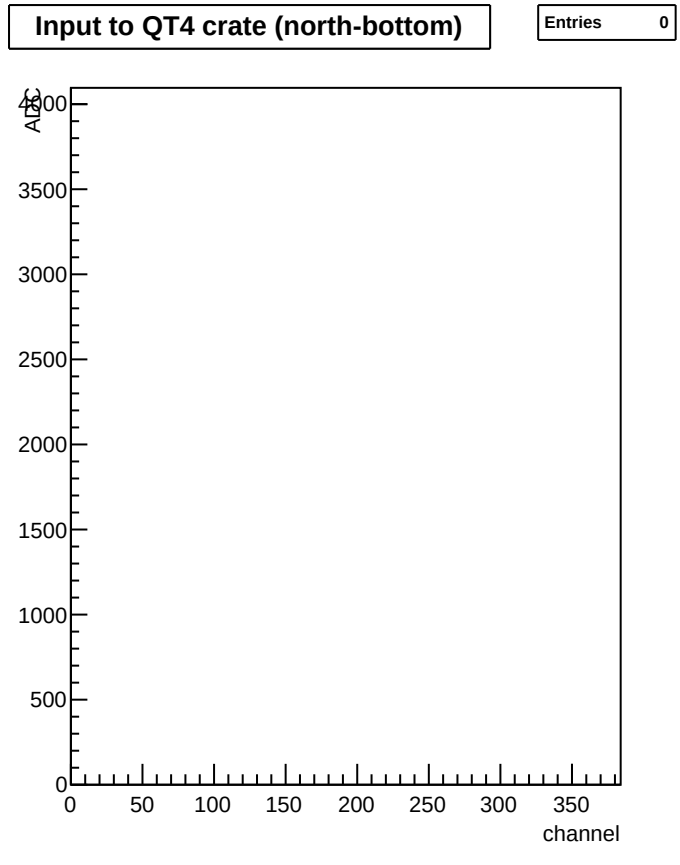
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

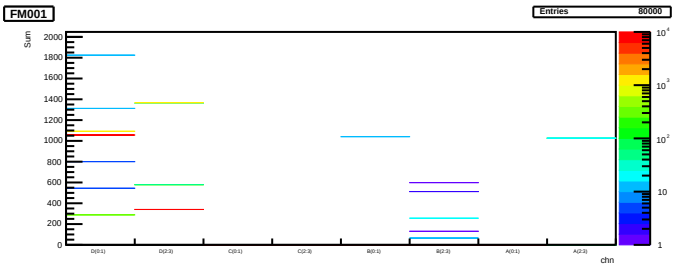


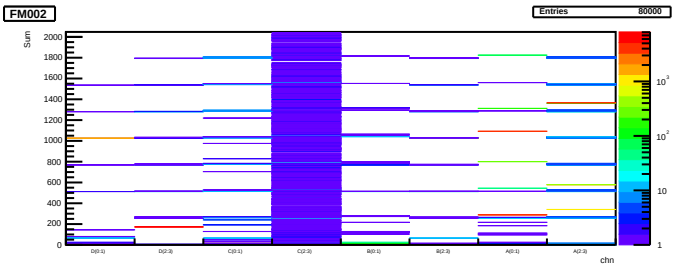

Input to QT1 crate (south-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

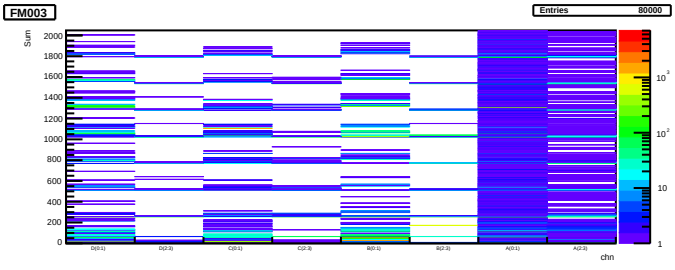

Input to QT3 crate (north-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

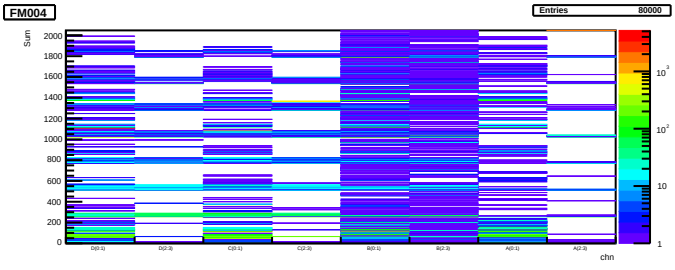

Input to QT2 crate (south-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

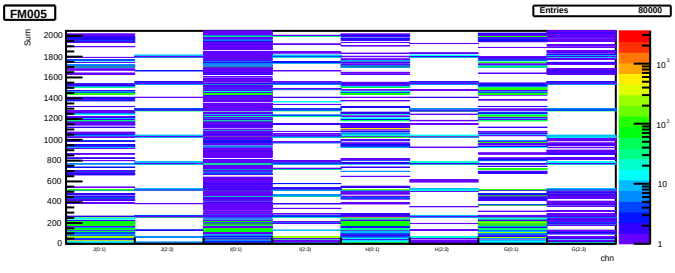

Input to QT4 crate (north-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

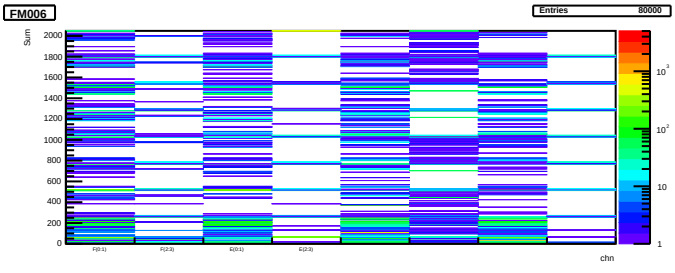

FM001
Entries 80000
Sum
chn

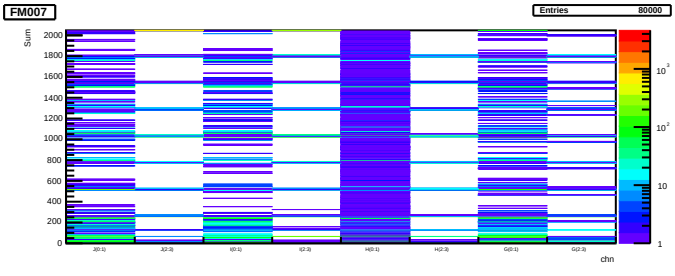

FM002
Entries 80000
Sum
chn

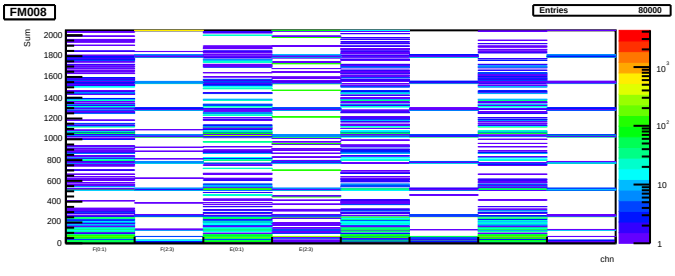

FM003
Entries 80000
Sum
chn

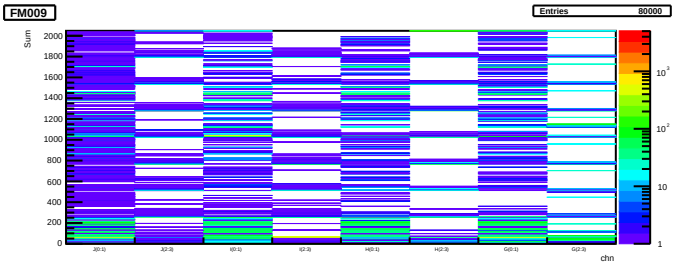

FM004
Entries 80000
Sum
chn

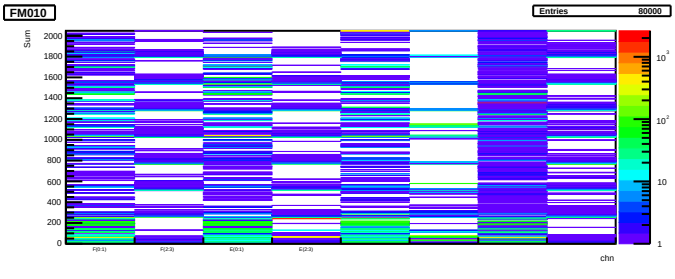

FM005
Entries 80000
Sum
chn

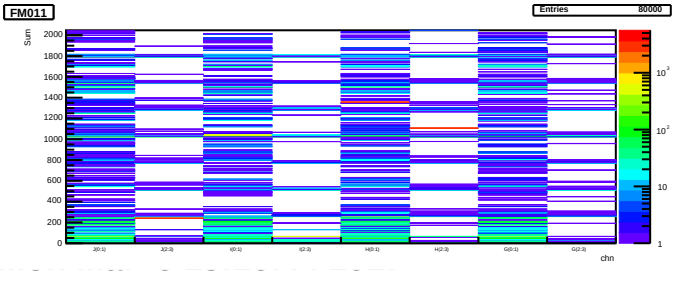

FM006
Entries 80000
Sum
chn

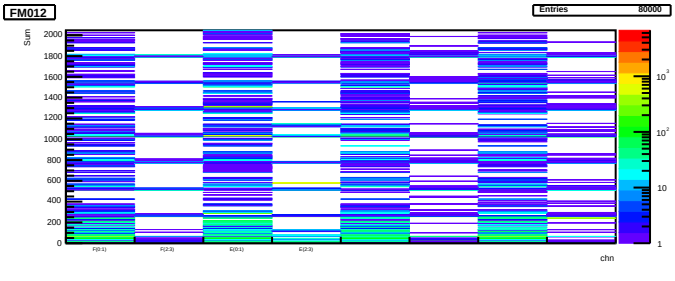

FM007
Entries 80000
Sum
chn

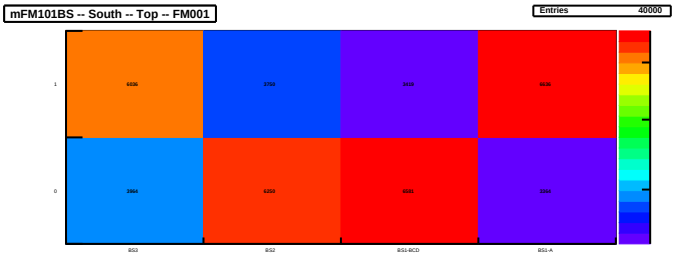

FM008
Entries 80000
Sum
chn

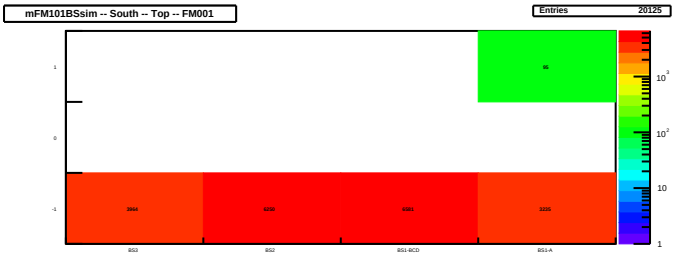

FM009
Entries 80000
Sum
chn

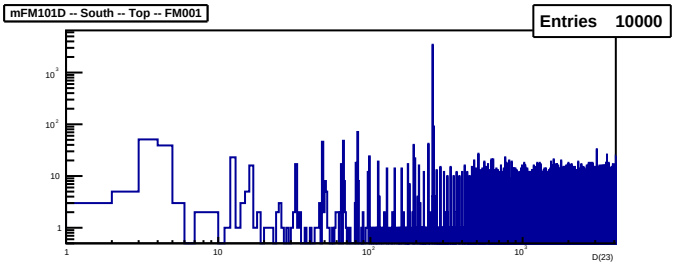

FM010
Entries 80000
Sum
chn

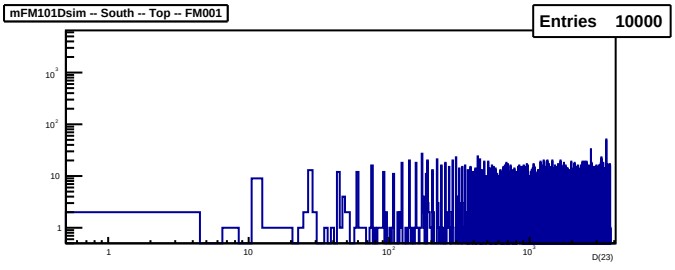

FM011
Entries 80000
Sum
chn

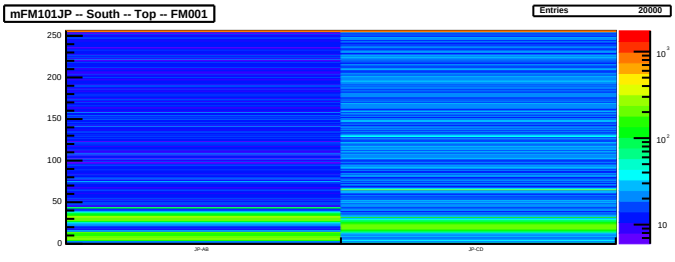

FM012
Entries 80000
Sum
chn

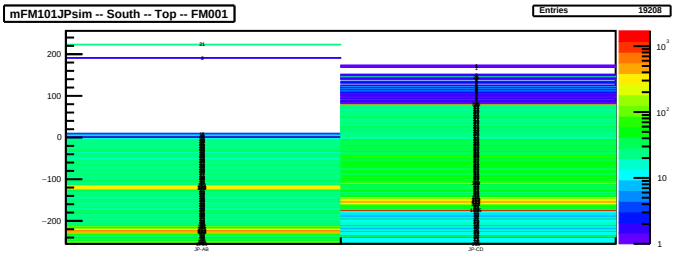

mFM101BS -- South -- Top -- FM001
Entries 40000

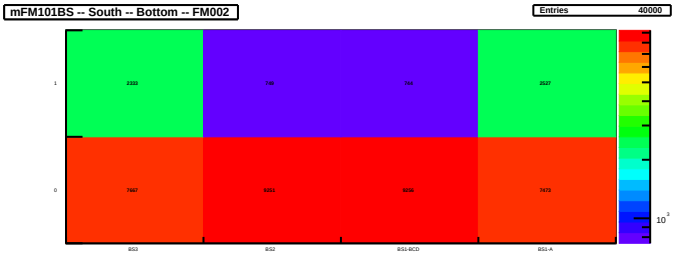

mFM101BSsim -- South -- Top -- FM001
Entries 20125

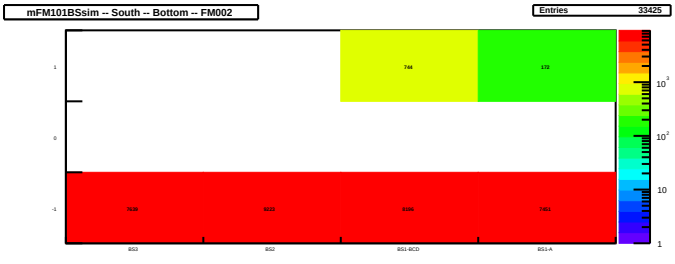

mFM101D -- South -- Top -- FM001
Entries 10000
D(23)

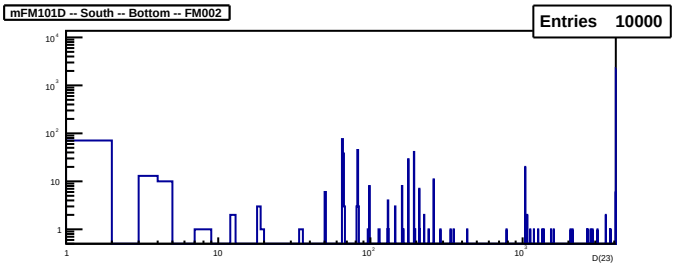

mFM101Dsim -- South -- Top -- FM001
Entries 10000
D(23)

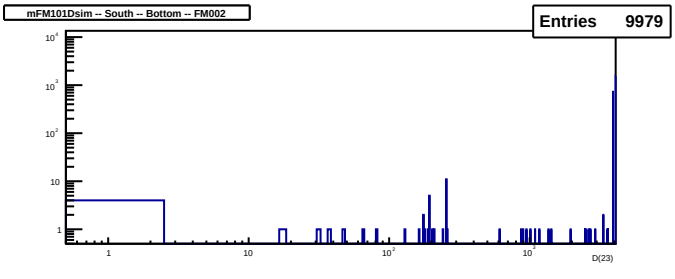

mFM101JP -- South -- Top -- FM001
Entries 20000

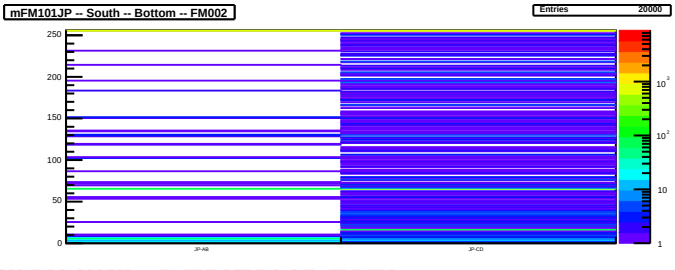

mFM101JPsim -- South -- Top -- FM001
Entries 19208

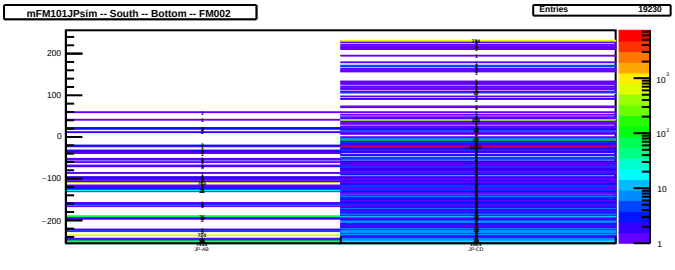

mFM101BS -- South -- Bottom -- FM002
Entries 40000

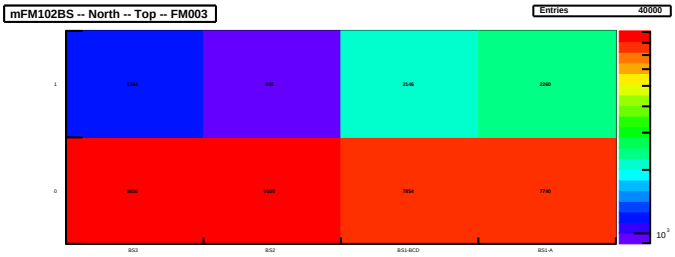

mFM101BSsim -- South -- Bottom -- FM002
Entries 33425

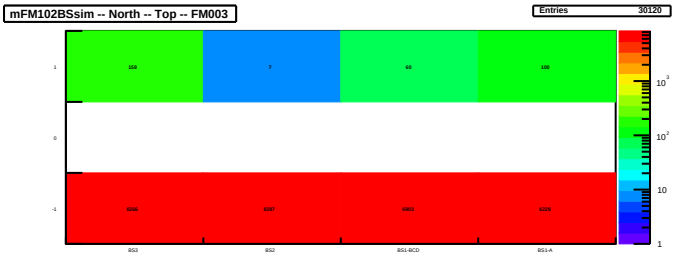

mFM101D -- South -- Bottom -- FM002
Entries 10000
D(23)

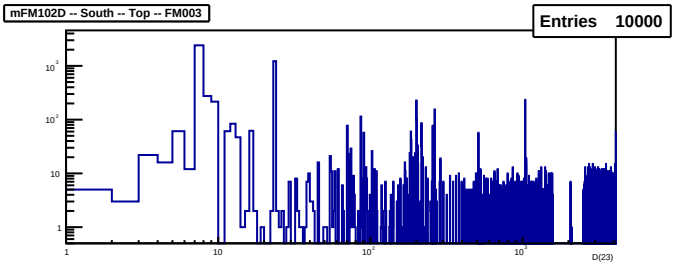

mFM101Dsim -- South -- Bottom -- FM002
Entries 9979
D(23)

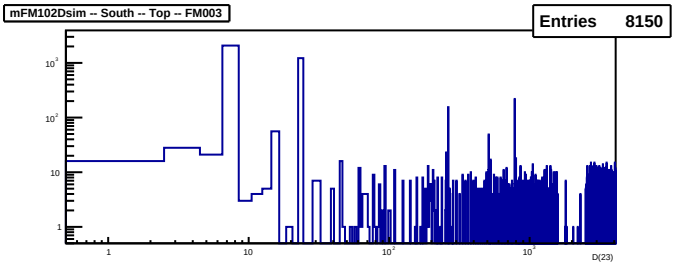

mFM101JP -- South -- Bottom -- FM002
Entries 20000

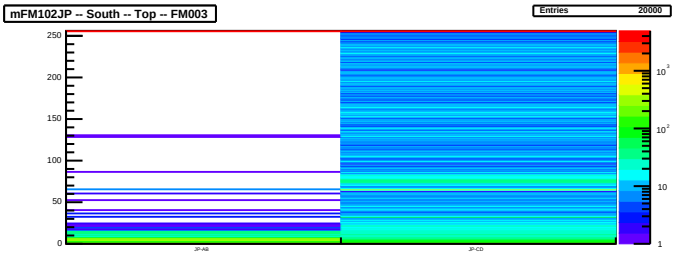

mFM101JPsim -- South -- Bottom -- FM002
Entries 19230

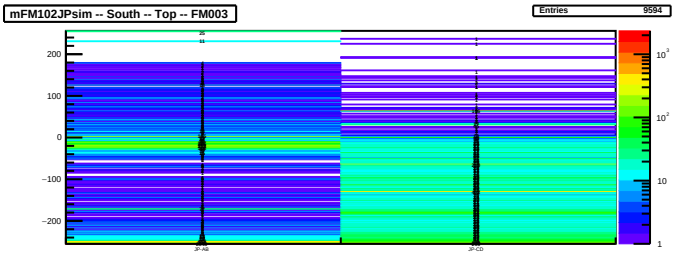

mFM102BS -- North -- Top -- FM003
Entries 40000
BS3
BS2
BS1-BCD
BS1-A

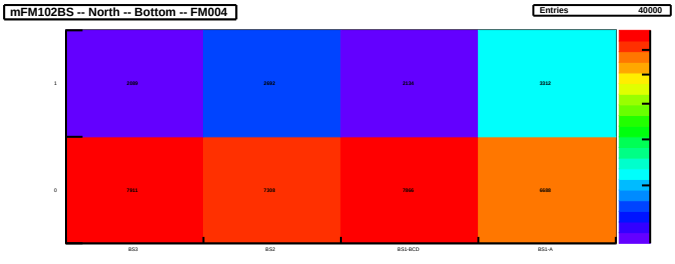

mFM102BSsim -- North -- Top -- FM003
Entries 30120
BS3
BS2
BS1-BCD
BS1-A

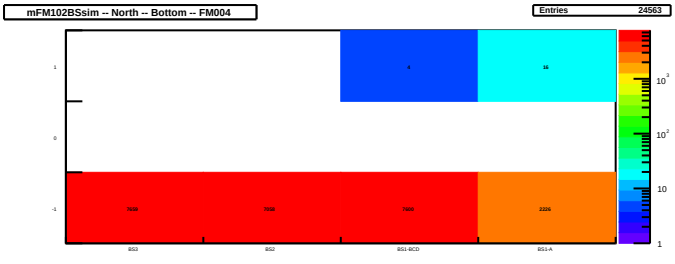

mFM102D -- South -- Top -- FM003
Entries 10000
D(23)

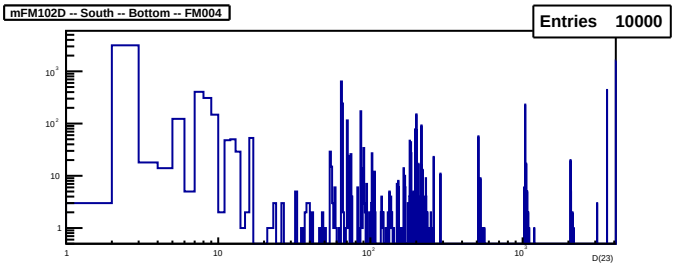

mFM102Dsim -- South -- Top -- FM003
Entries 8150
D(23)

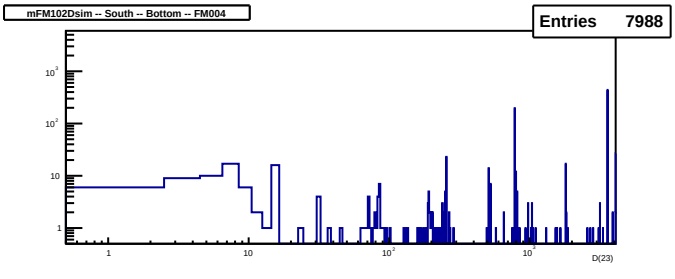

mFM102JP -- South -- Top -- FM003
Entries 20000
JP-AB
JP-CD

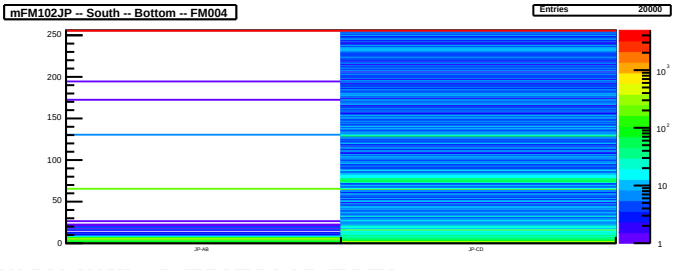

mFM102JPsim -- South -- Top -- FM003
Entries 9594
JP-AB
JP-CD

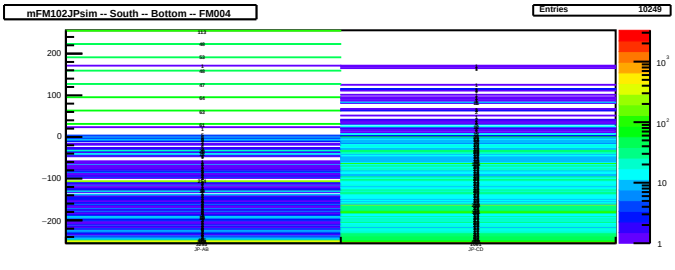

mFM102BS -- North -- Bottom -- FM004
Entries 40000
BS3
BS2
BS1-BCD
BS1-A

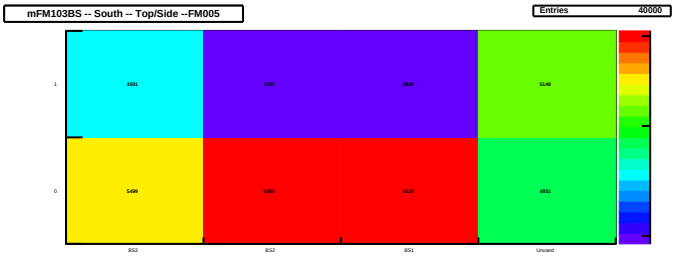

mFM102BSsim -- North -- Bottom -- FM004
Entries 24563
BS3
BS2
BS1-BCD
BS1-A

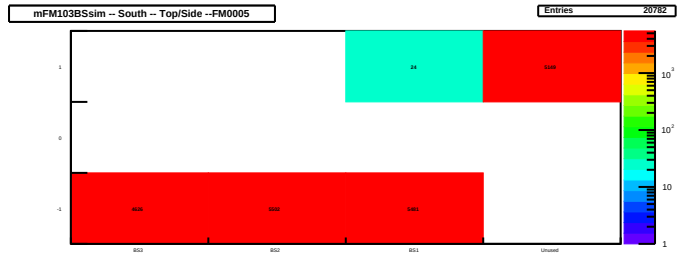

mFM102D -- South -- Bottom -- FM004
Entries 10000
D(23)

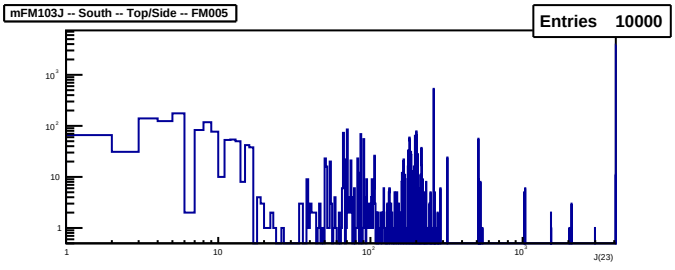

mFM102Dsim -- South -- Bottom -- FM004
Entries 7988
D(23)

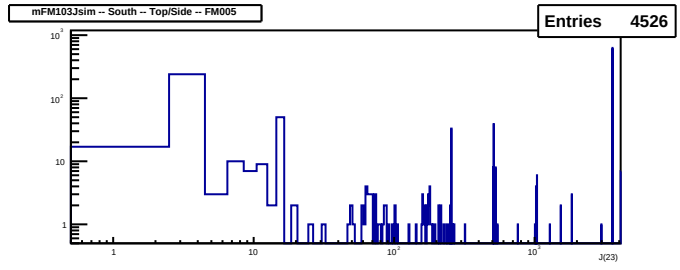

mFM102JP -- South -- Bottom -- FM004
Entries 20000
JP-AB
JP-CD

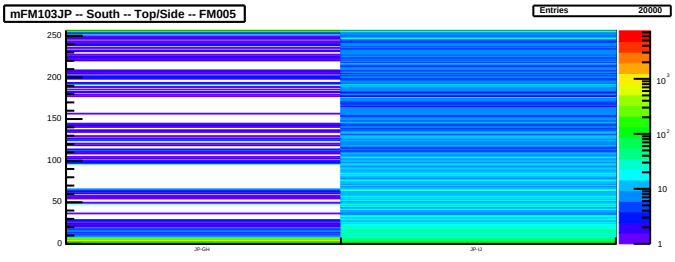

mFM102JPsim -- South -- Bottom -- FM004
Entries 10249
JP-AB
JP-CD

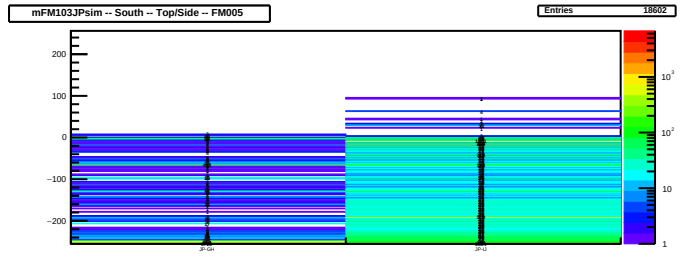

mFM103BS -- South -- Top/Side --FM005
Entries 40000

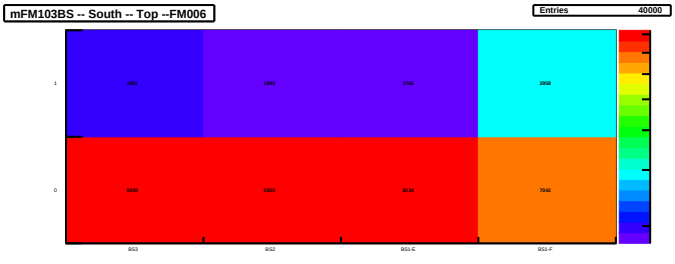

mFM103BSsim -- South -- Top/Side --FM0005
Entries 20782

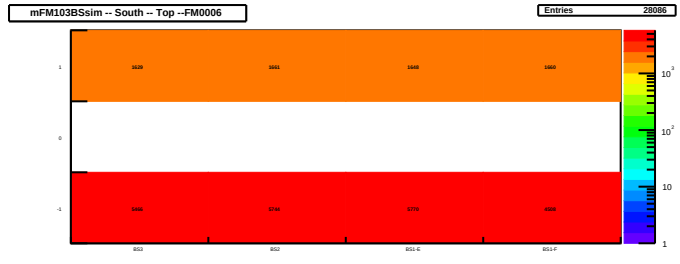

mFM103J -- South -- Top/Side -- FM005
Entries 10000
J(23)

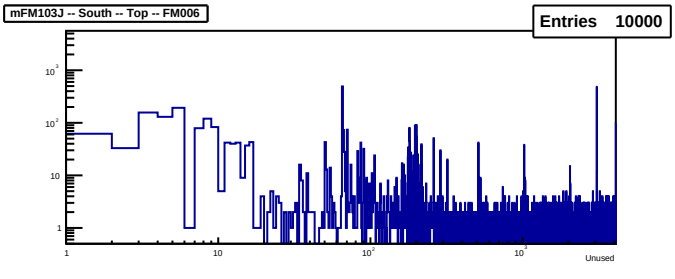

mFM103Jsim -- South -- Top/Side -- FM005
Entries 4526
J(23)

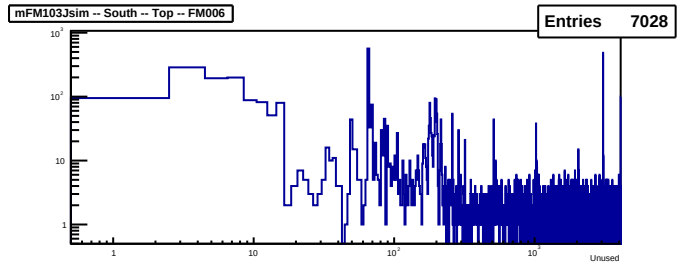

mFM103JP -- South -- Top/Side -- FM005
Entries 20000


mFM103JPsim -- South -- Top/Side -- FM005
Entries 18602


mFM103BS -- South -- Top --FM006
Entries 40000


mFM103BSsim -- South -- Top --FM0006
Entries 28086


mFM103J -- South -- Top -- FM006
Entries 10000
Unused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Entries 7028
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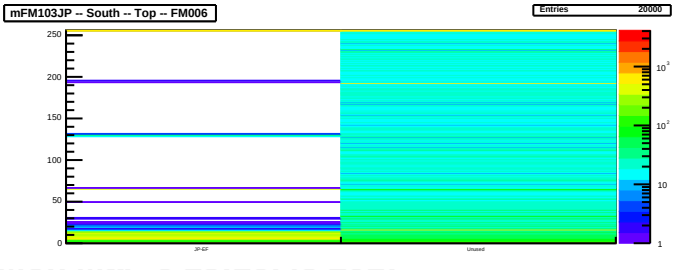
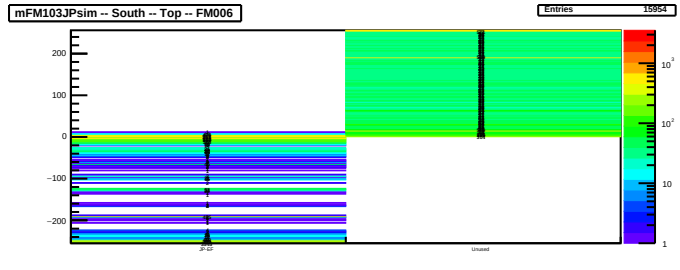
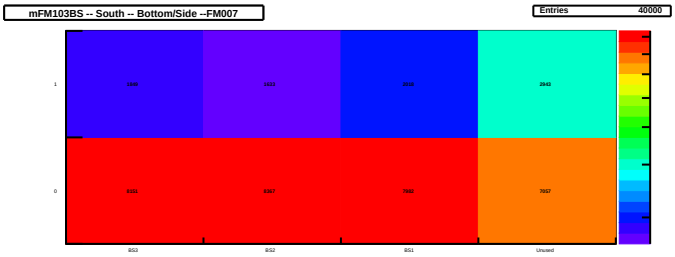
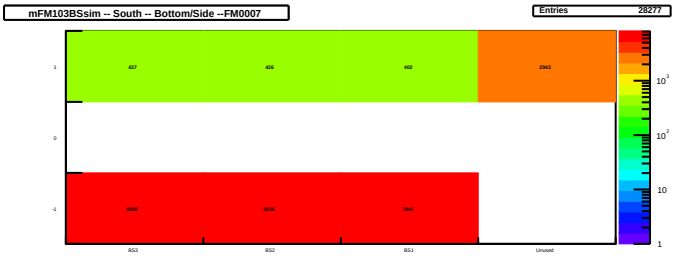
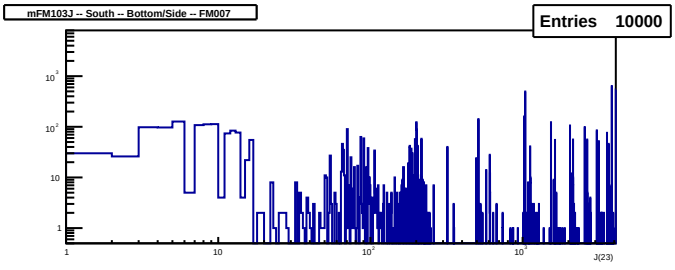
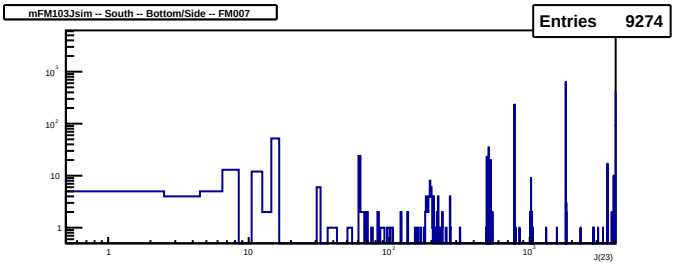

mFM103JP -- South -- Top -- FM006
Entries 20000

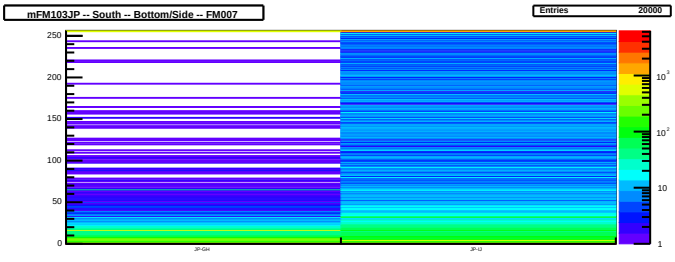

mFM103JPsim -- South -- Top -- FM006
Entries 15954

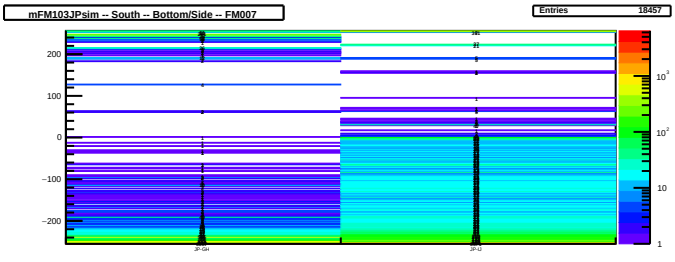

mFM103BS -- South -- Bottom/Side --FM007
Entries 40000

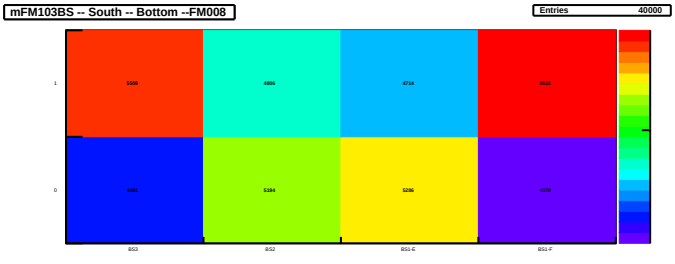

mFM103BSsim -- South -- Bottom/Side --FM0007
Entries 28277

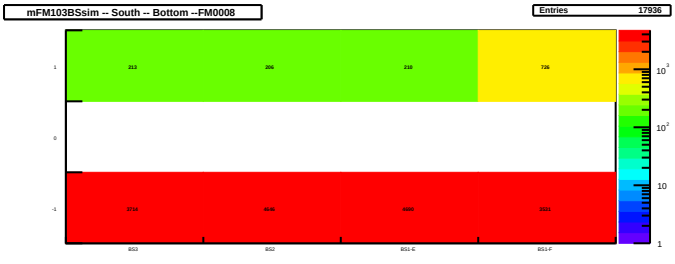

mFM103J -- South -- Bottom/Side -- FM007
Entries 10000
J(23)

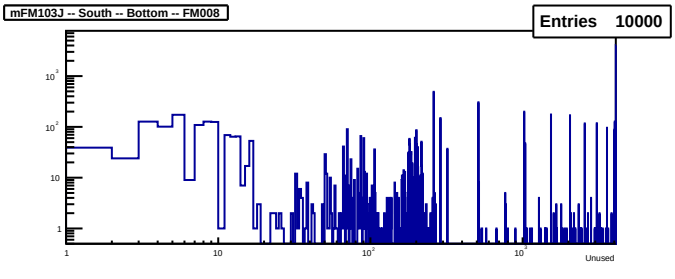

mFM103Jsim -- South -- Bottom/Side -- FM007
Entries 9274
J(23)

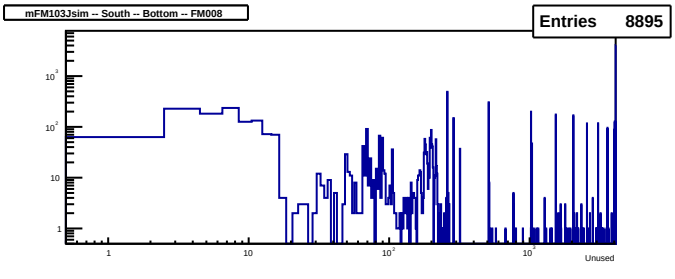

mFM103JP -- South -- Bottom/Side -- FM007
Entries 20000

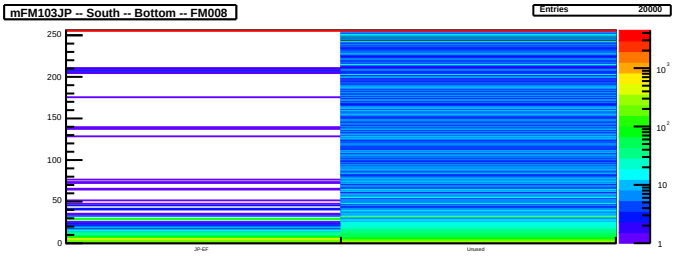

mFM103JPsim -- South -- Bottom/Side -- FM007
Entries 18457

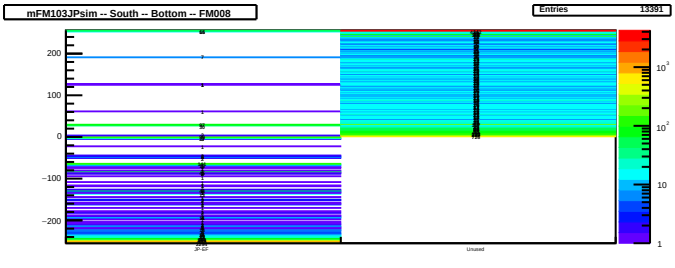

mFM103BS -- South -- Bottom --FM008
Entries 40000


mFM103BSsim -- South -- Bottom --FM0008
Entries 17936


mFM103J -- South -- Bottom -- FM008
Entries 10000
Unused


mFM103Jsim -- South -- Bottom -- FM008
Entries 8895
Unused


mFM103JP -- South -- Bottom -- FM008
Entries 20000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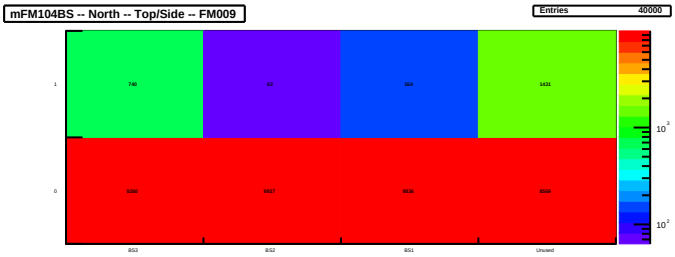
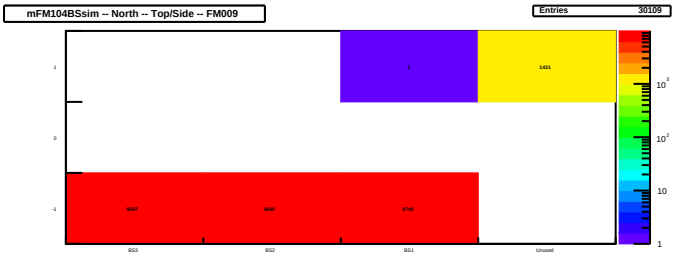
Entries 13391


mFM104BS -- North -- Top/Side -- FM009
Entries 40000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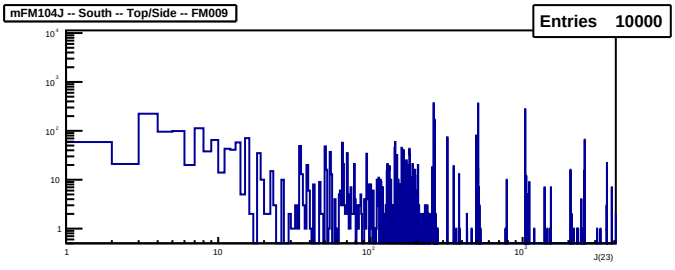
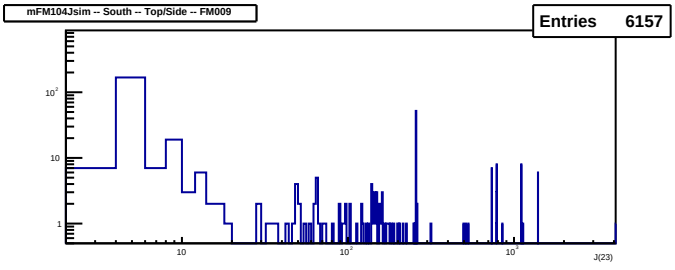
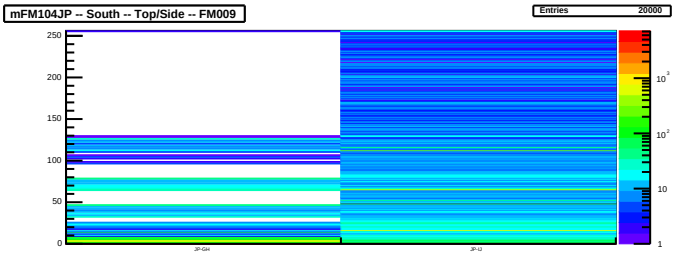
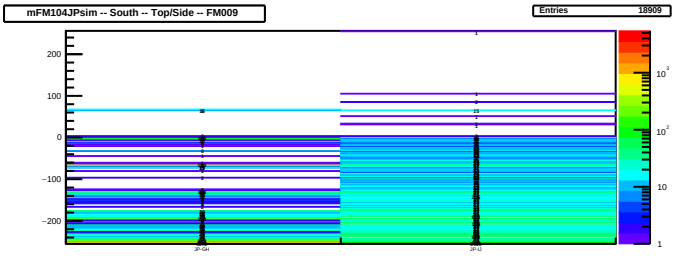
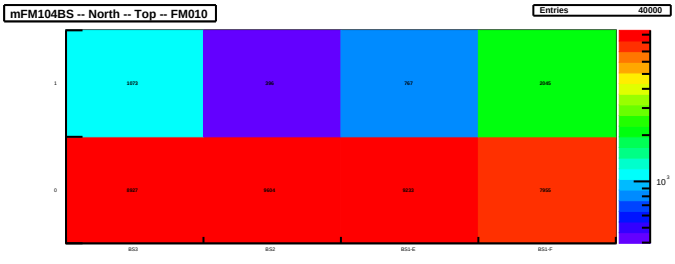
Entries 30109

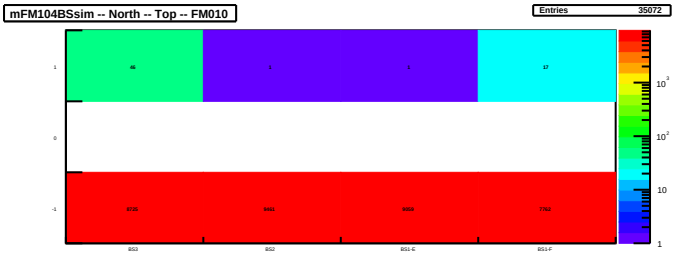

mFM104J -- South -- Top/Side -- FM009
Entries 10000
J(23)


mFM104Jsim -- South -- Top/Side -- FM009
Entries 6157
J(23)


mFM104JP -- South -- Top/Side -- FM009
Entries 20000


mFM104JPsim -- South -- Top/Side -- FM009
Entries 18909


mFM104BS -- North -- Top -- FM010
Entries 40000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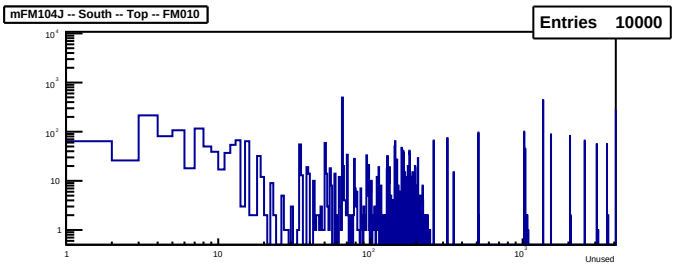
Entries 35072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Entries 10000
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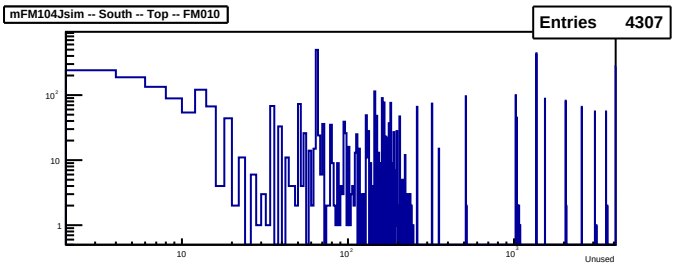
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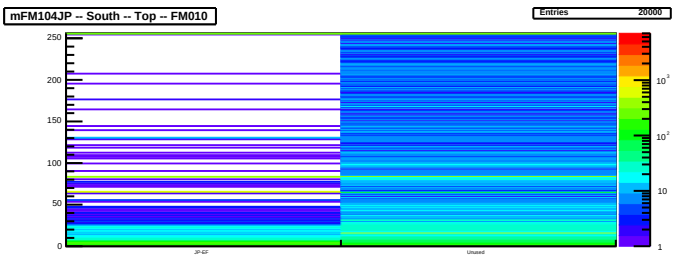
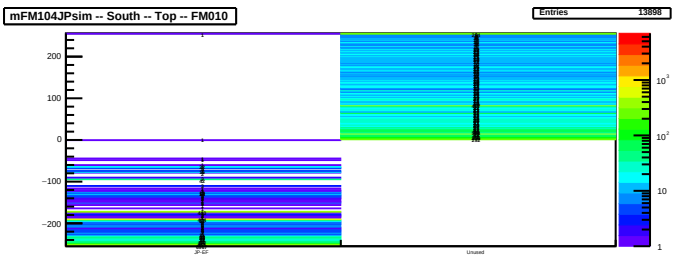
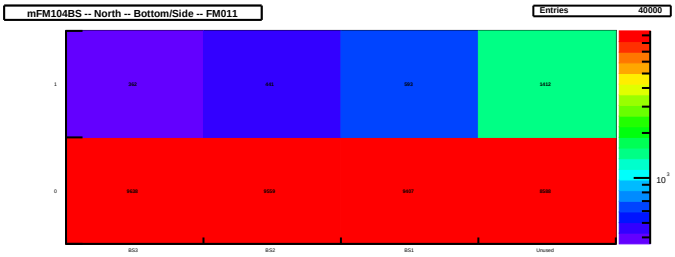
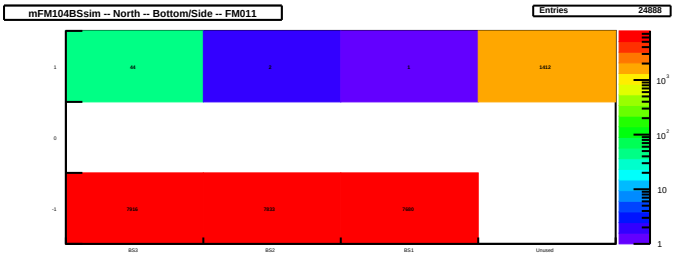
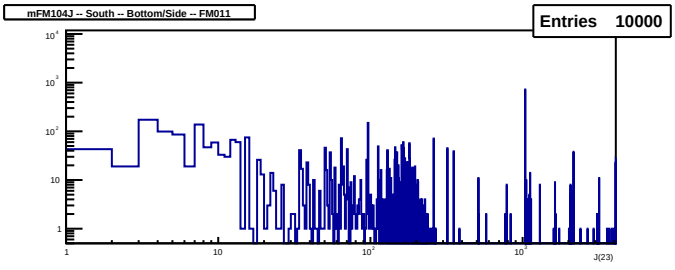
Entries 4307
Unused

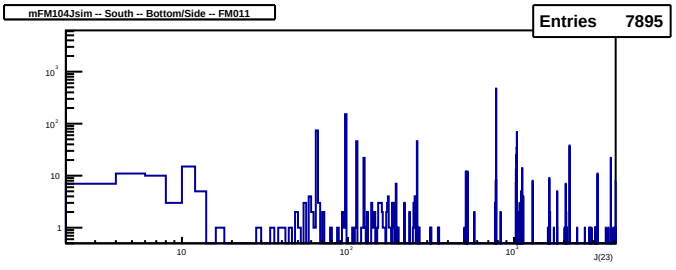

mFM104JP -- South -- Top -- FM010
Entries 20000

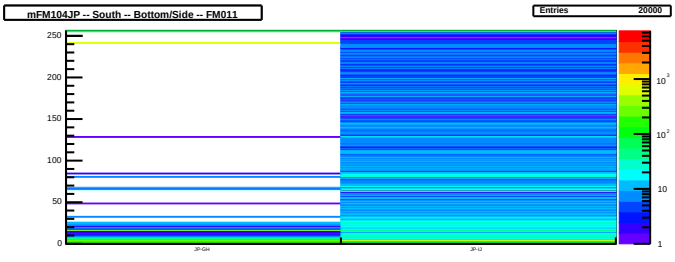

mFM104JPsim -- South -- Top -- FM010
Entries 13898

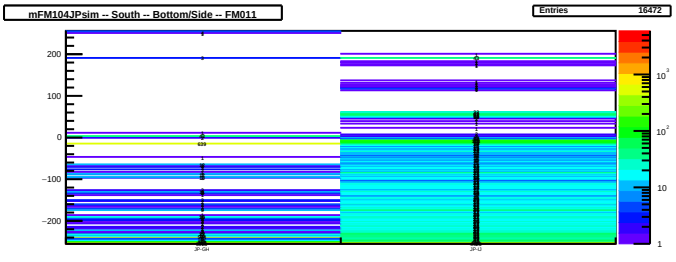

mFM104BS -- North -- Bottom/Side -- FM011
Entries 40000

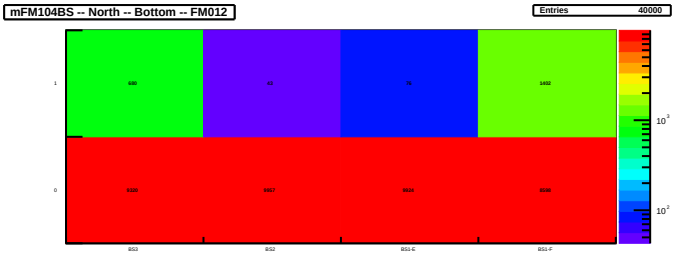

mFM104BSsim -- North -- Bottom/Side -- FM011
Entries 24888

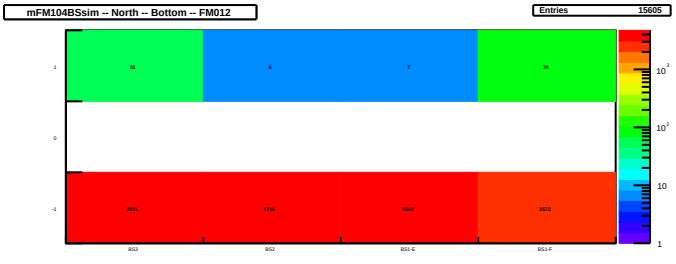

mFM104J -- South -- Bottom/Side -- FM011
Entries 10000
J(23)

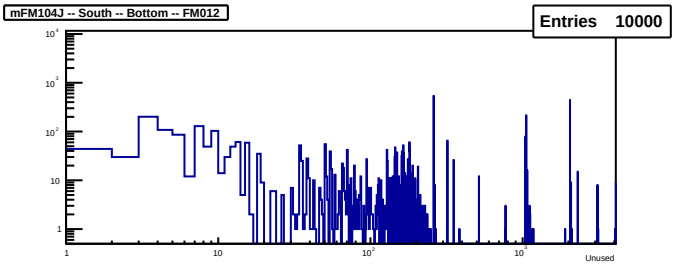

mFM104Jsim -- South -- Bottom/Side -- FM011
Entries 7895
J(23)

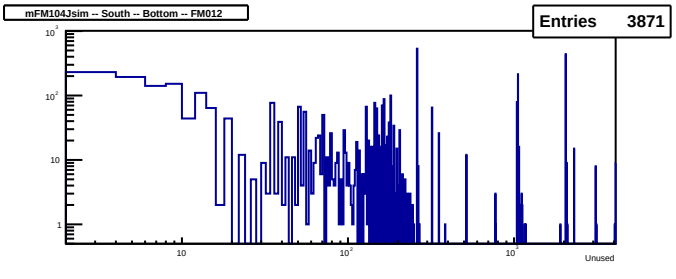

mFM104JP -- South -- Bottom/Side -- FM011
Entries 20000

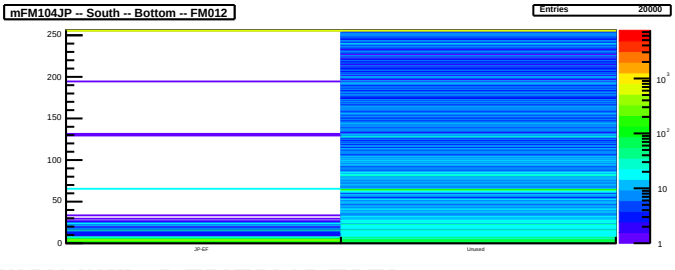

mFM104JPsim -- South -- Bottom/Side -- FM011
Entries 16472

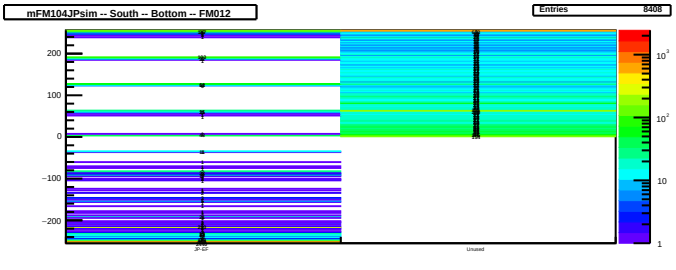

mFM104BS -- North -- Bottom -- FM012
Entries 40000

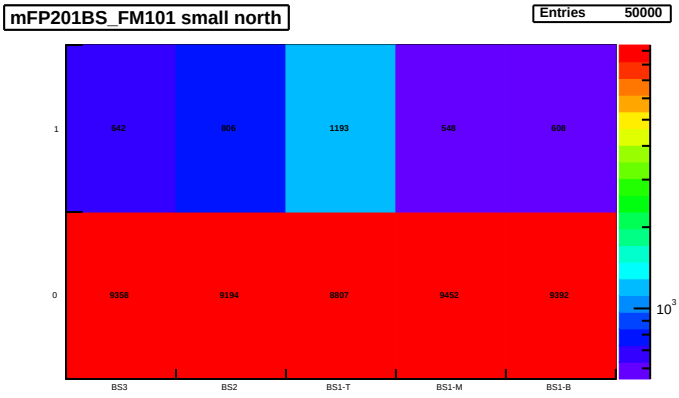

mFM104BSsim -- North -- Bottom -- FM012
Entries 15605

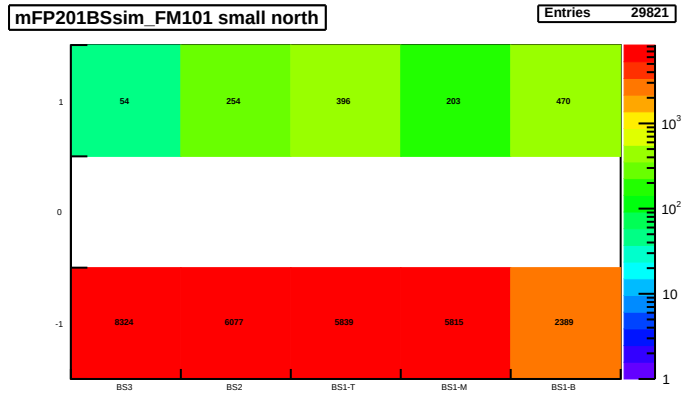

mFM104J -- South -- Bottom -- FM012
Entries 10000
Unused

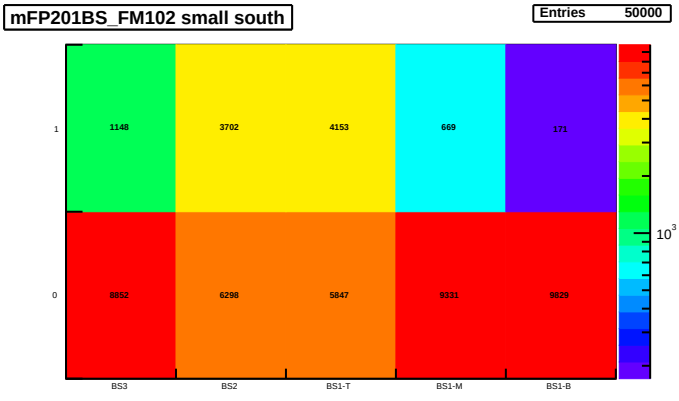

mFM104Jsim -- South -- Bottom -- FM012
Entries 3871
Unused

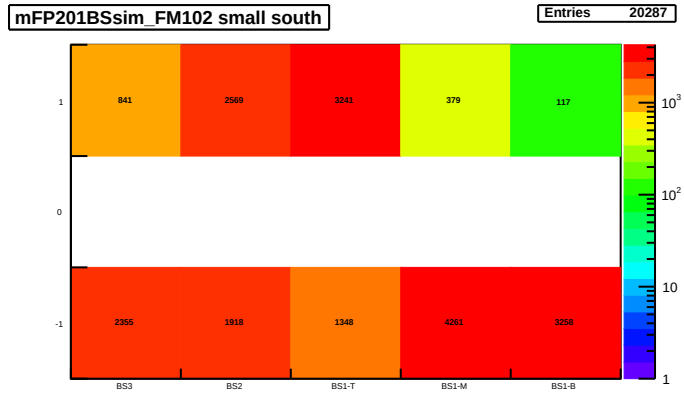

mFM104JP -- South -- Bottom -- FM012
Entries 20000

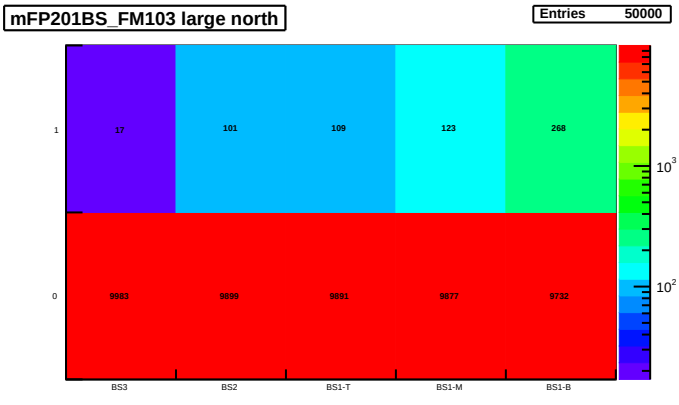

mFM104JPsim -- South -- Bottom -- FM012
Entries 8408

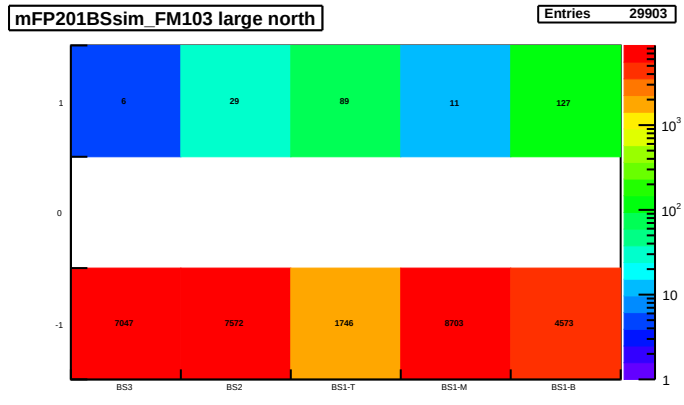

mFP201BS_FM101 small north
Entries 50000
642 806 1193 548 608
9358 9194 8807 9452 9392
BS3 BS2 BS1-T BS1-M BS1-B

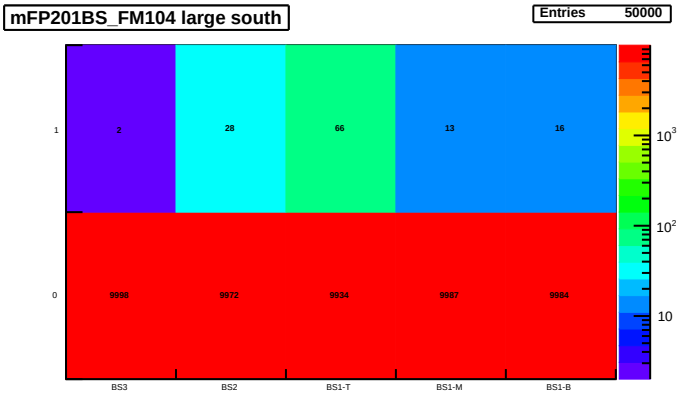

mFP201BSsim_FM101 small north
Entries 29821
54 254 396 203 470
8324 6077 5839 5815 2389
BS3 BS2 BS1-T BS1-M BS1-B

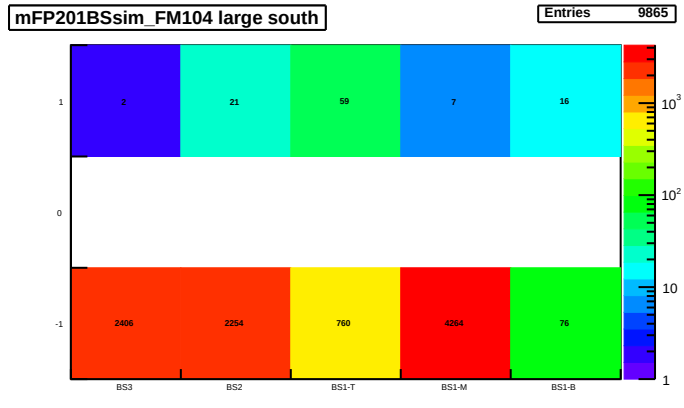

mFP201BS_FM102 small south
Entries 50000
1148 3702 4153 669 171
8852 6298 5847 9331 9829
BS3 BS2 BS1-T BS1-M BS1-B

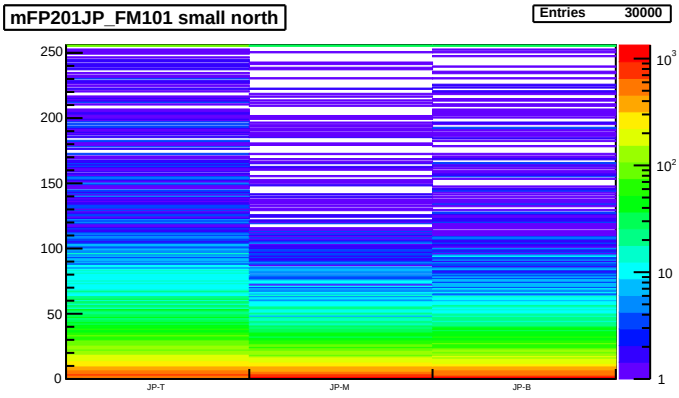

mFP201BSsim_FM102 small south
Entries 20287
841 2569 3241 379 117
2355 1918 1348 4261 3258
BS3 BS2 BS1-T BS1-M BS1-B

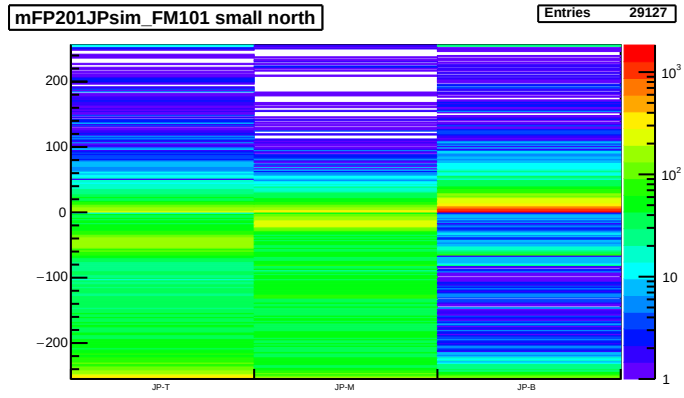

mFP201BS_FM103 large north
Entries 50000
17 101 109 123 268
9983 9899 9891 9877 9732
BS3 BS2 BS1-T BS1-M BS1-B

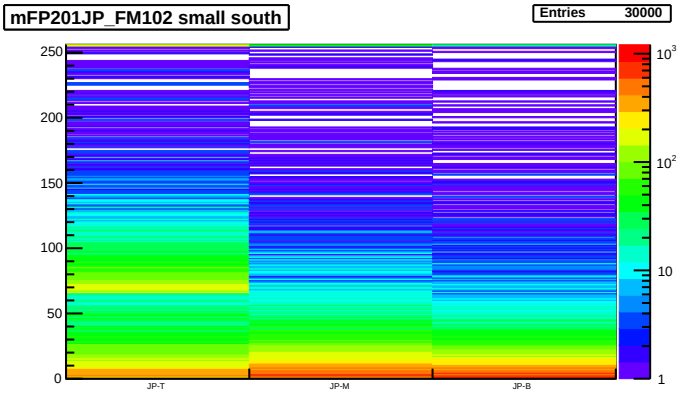

mFP201BSsim_FM103 large north
Entries 29903
6 29 89 11 127
7047 7572 1746 8703 4573
BS3 BS2 BS1-T BS1-M BS1-B

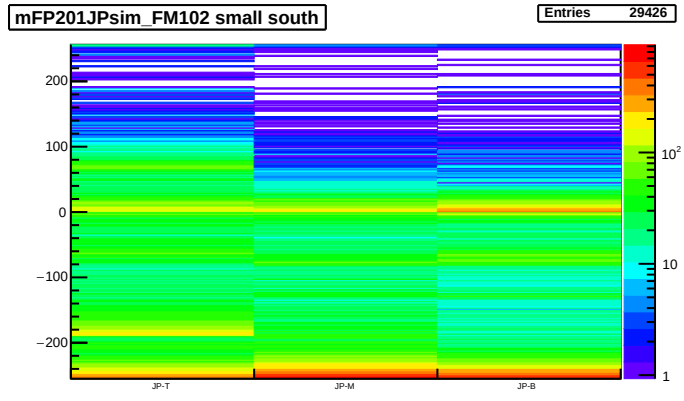

mFP201BS_FM104 large south
Entries 50000
2 28 66 13 16
9998 9972 9934 9987 9984
BS3 BS2 BS1-T BS1-M BS1-B

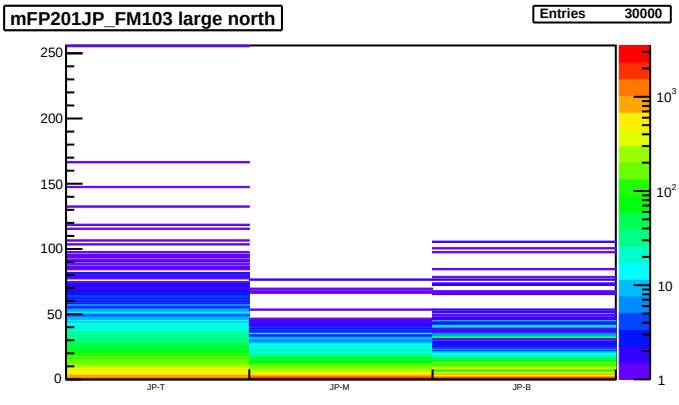

mFP201BSsim_FM104 large south
Entries 9865
2 21 59 7 16
2406 2254 760 4264 76
BS3 BS2 BS1-T BS1-M BS1-B

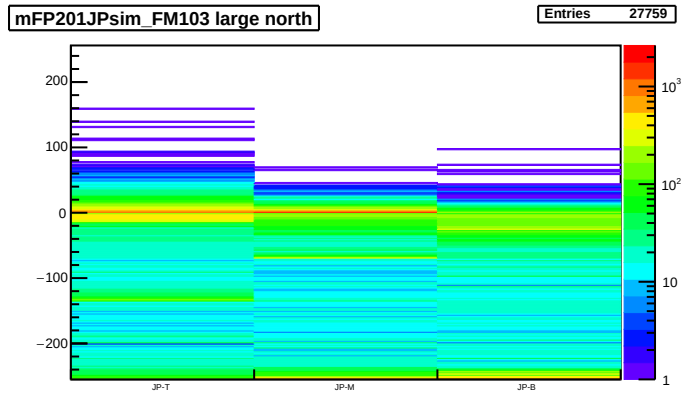

mFP201JP_FM101 small north
Entries 30000
JP-T
JP-M
JP-B

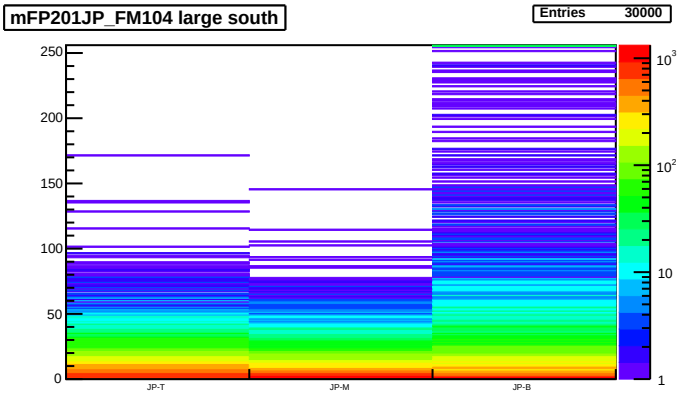

mFP201JPsim_FM101 small north
Entries 29127
JP-T
JP-M
JP-B

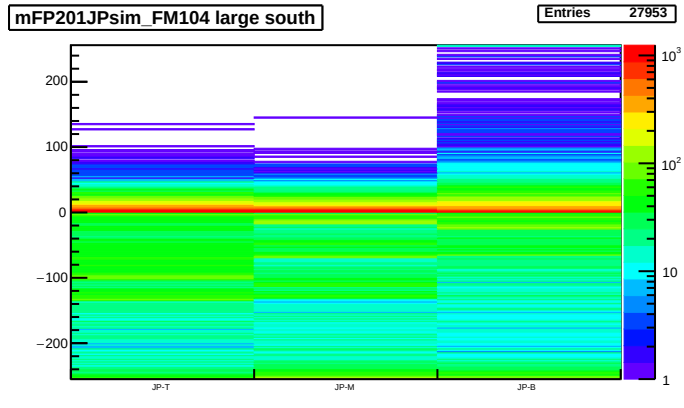

mFP201JP_FM102 small south
Entries 30000
JP-T
JP-M
JP-B

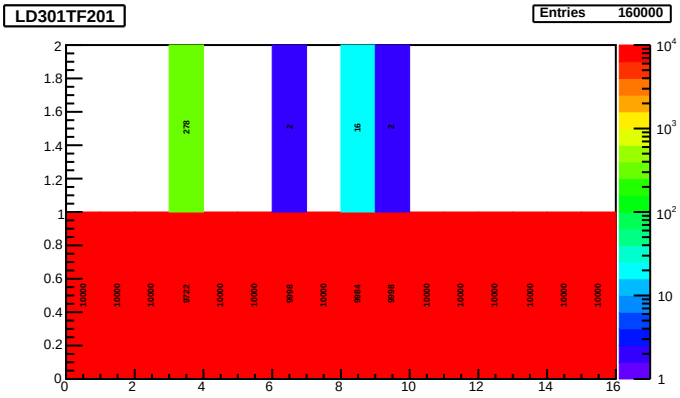

mFP201JPsim_FM102 small south
Entries 29426
JP-T
JP-M
JP-B

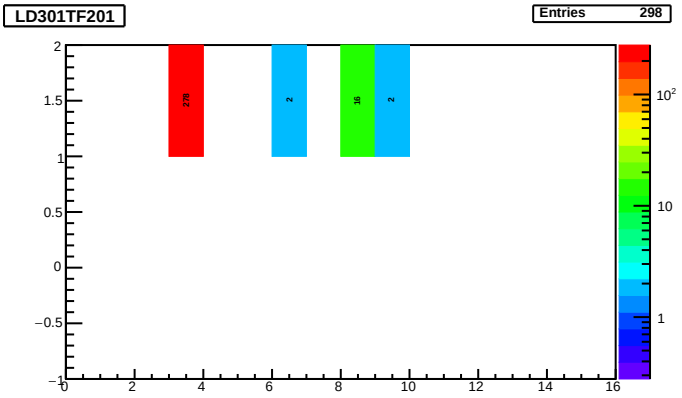

mFP201JP_FM103 large north
Entries 30000
JP-T
JP-M
JP-B

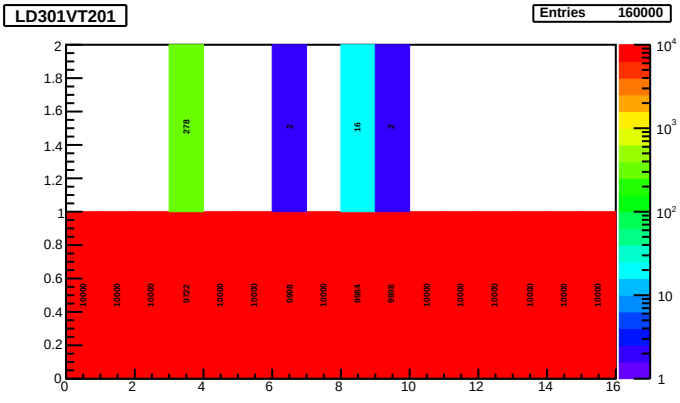

mFP201JPsim_FM103 large north
Entries 27759
JP-T
JP-M
JP-B

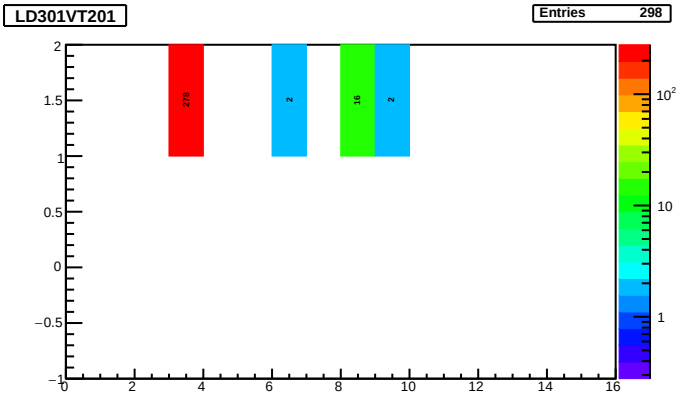

mFP201JP_FM104 large south
Entries 30000
JP-T
JP-M
JP-B

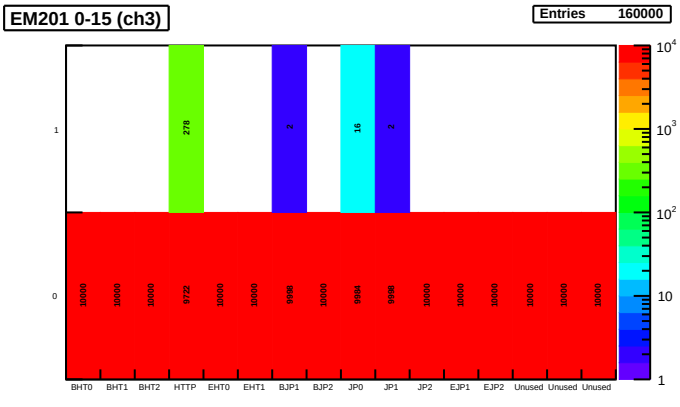

mFP201JPsim_FM104 large south
Entries 27953
JP-T
JP-M
JP-B

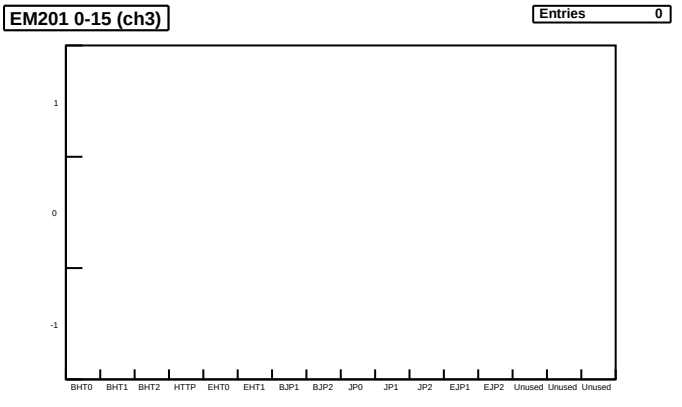

LD301TF201
Entries 160000

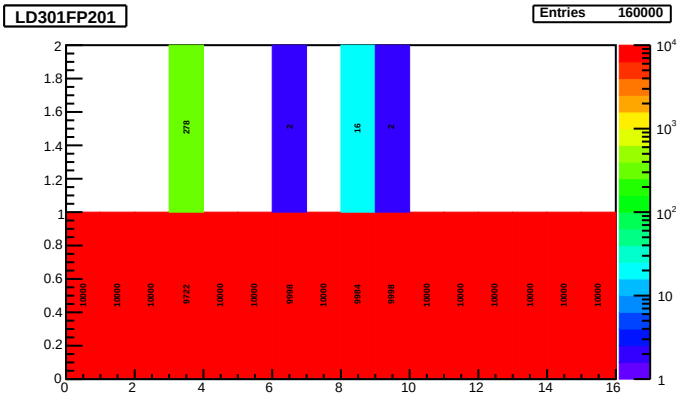

LD301TF201
Entries 298

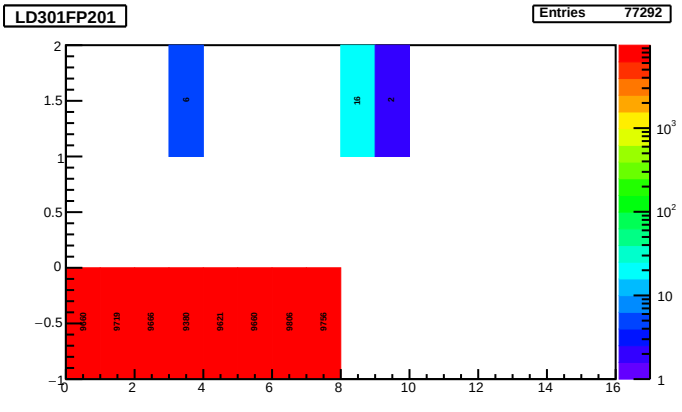

LD301VT201
Entries 160000


LD301VT201
Entries 298


EM201 0-15 (ch3)
Entries 160000
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 HTTP EHT0 EHT1 BJP1 BJP2 JP0 JP1 JP2 EJP1 EJP2 Unused Unused Unused


LD301FP201
Entries 160000


LD301FP201
Entries 77292