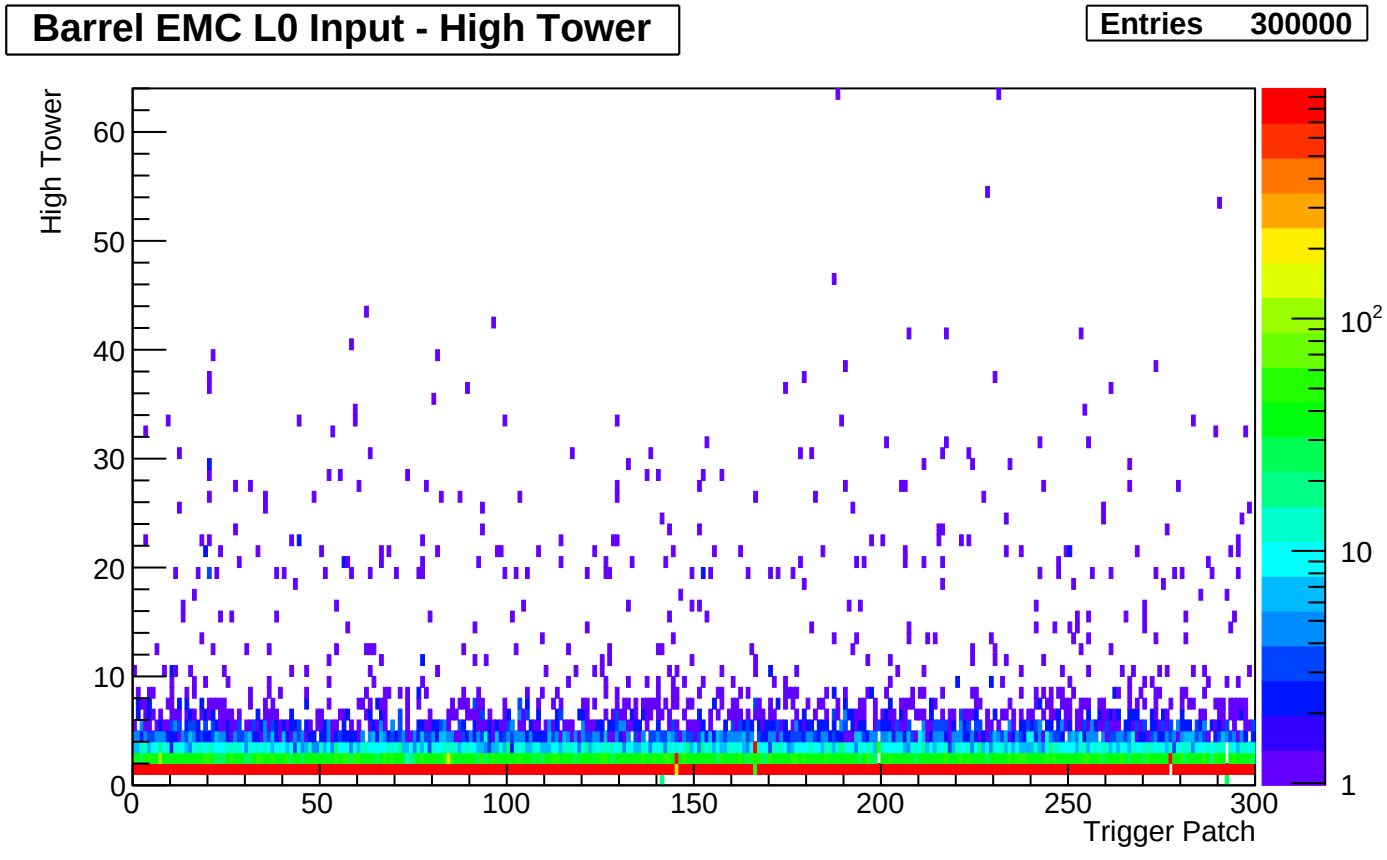
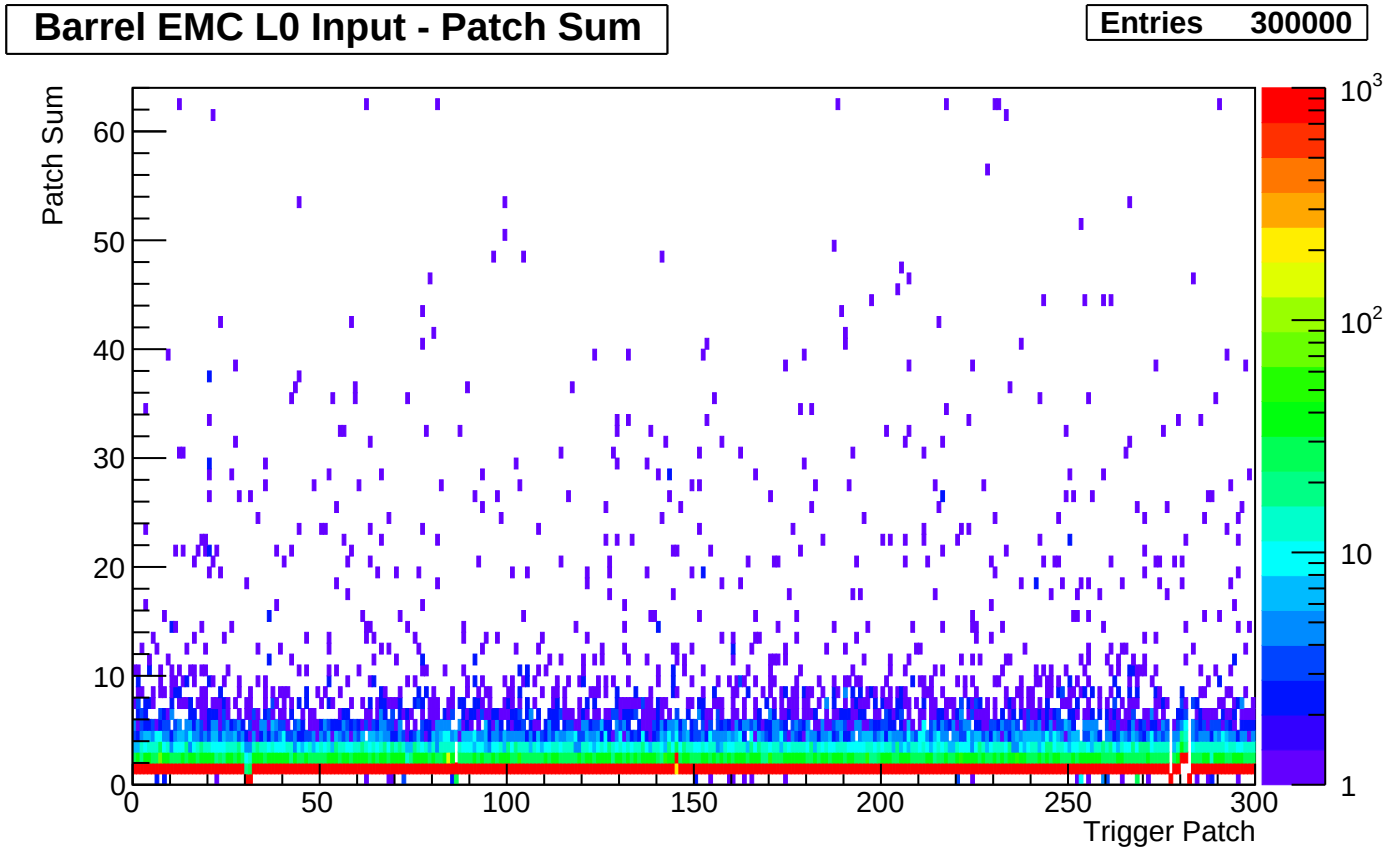
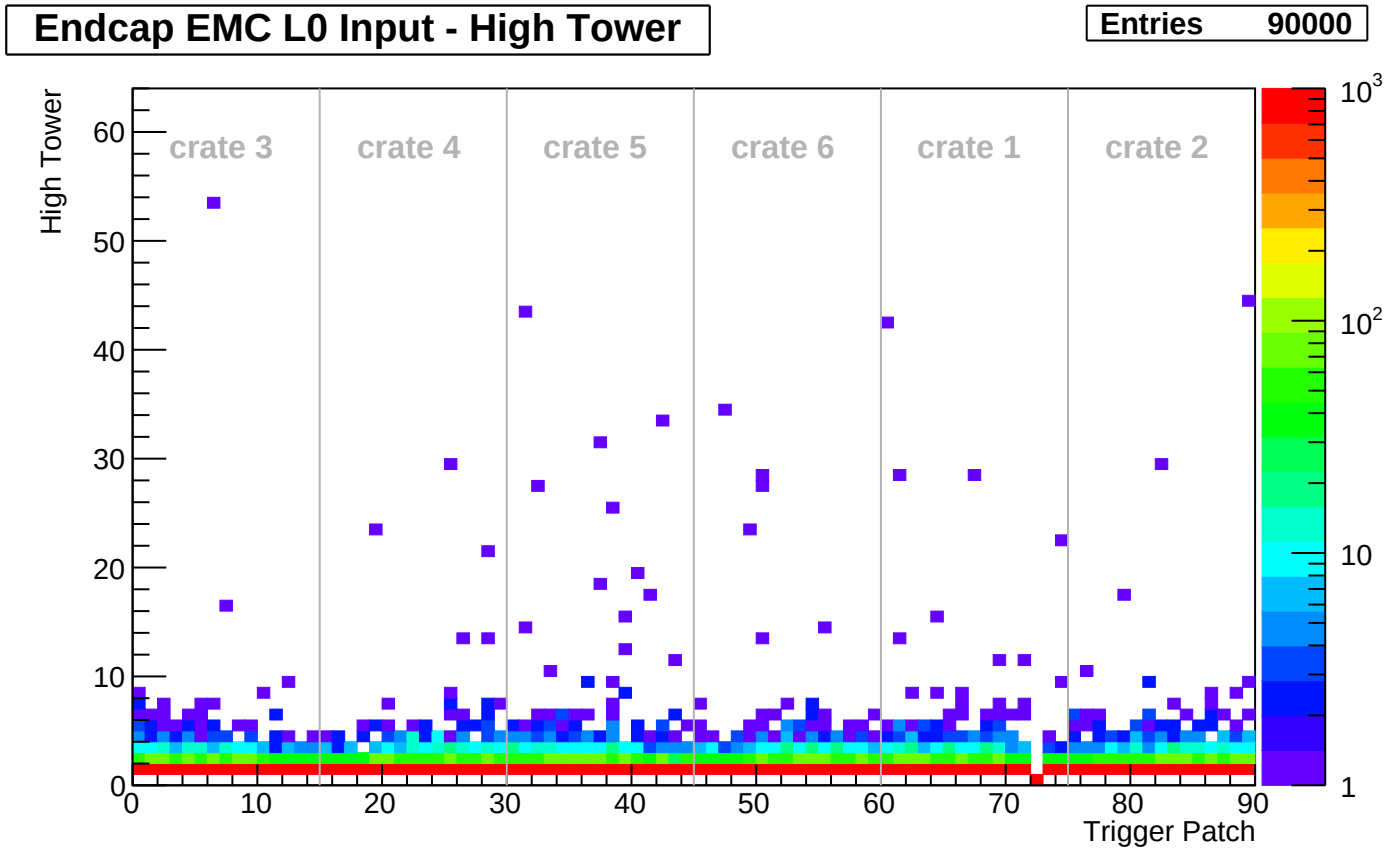
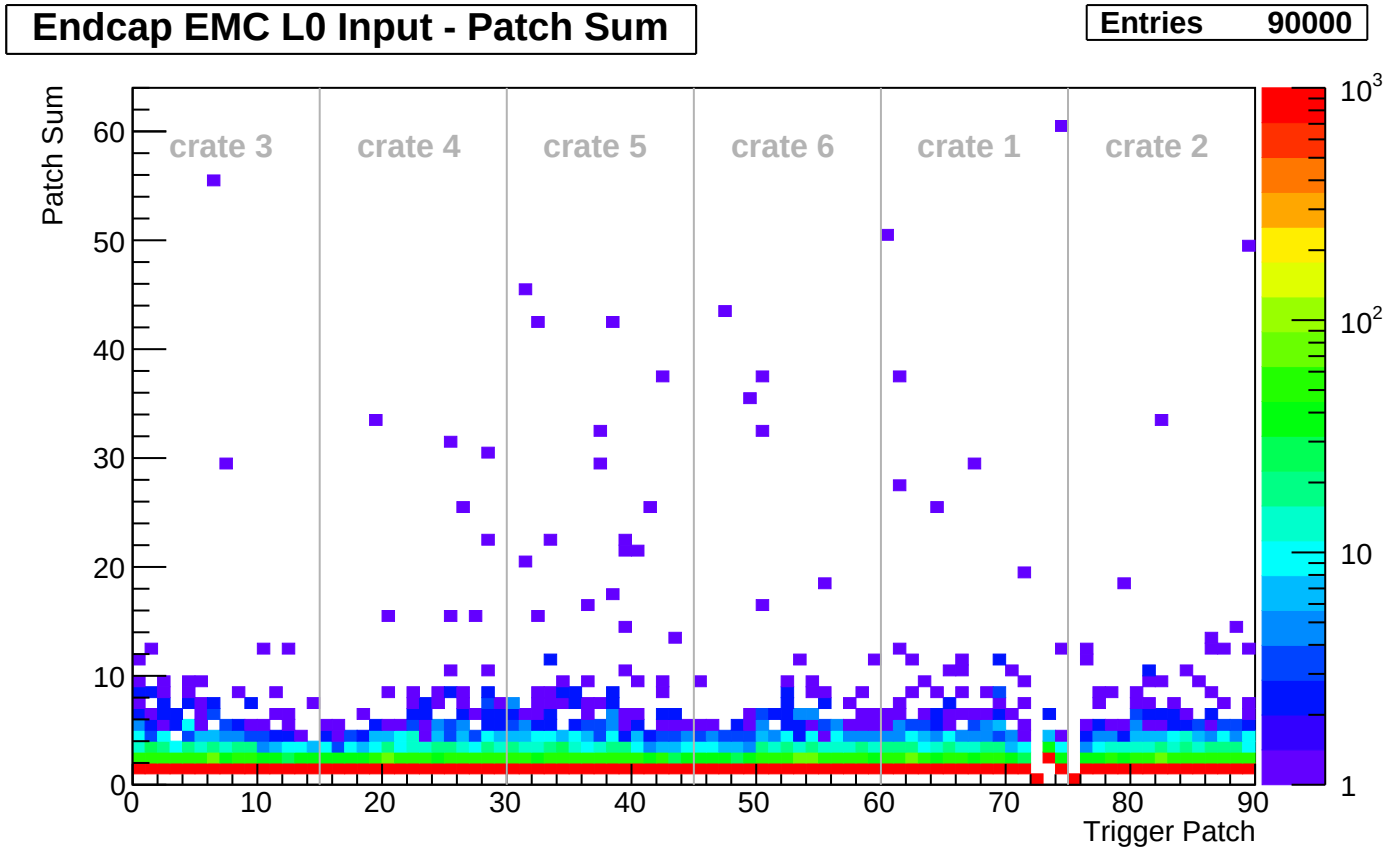
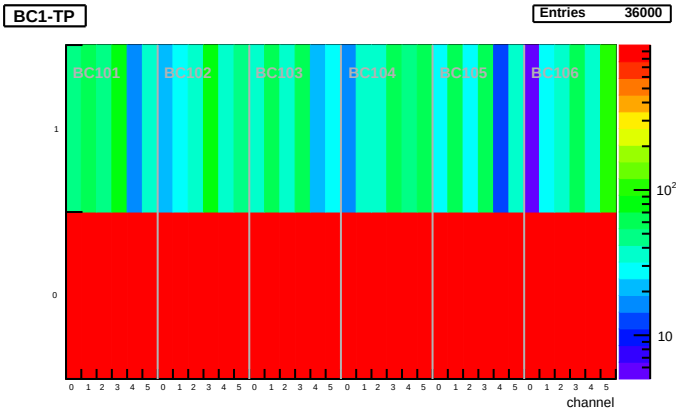
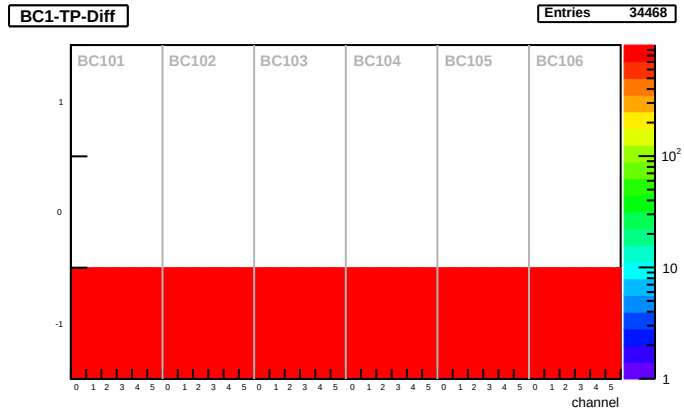

Barrel EMC L0 Input - High Tower
Entries 300000
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2

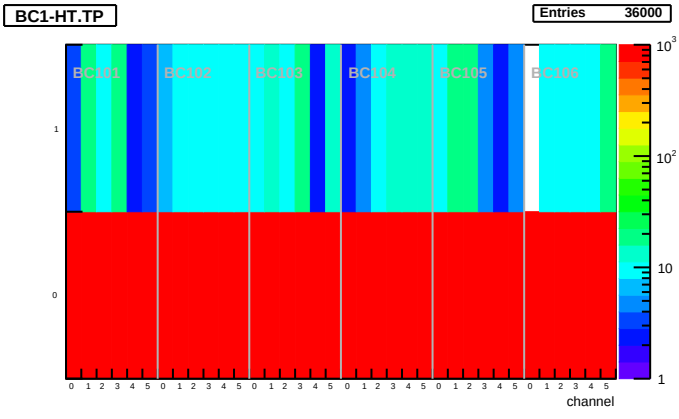

Barrel EMC L0 Input - Patch Sum
Entries 300000
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

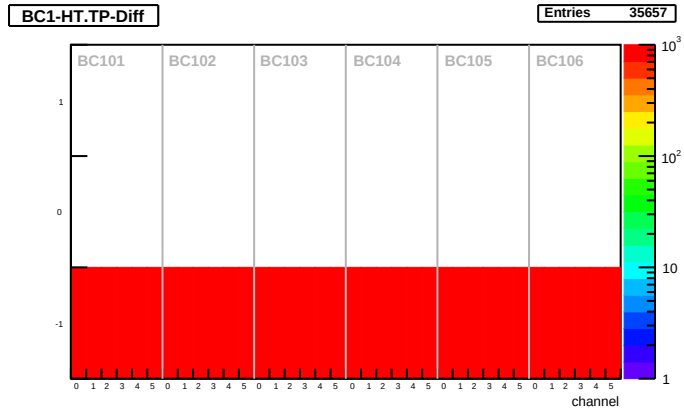

Endcap EMC L0 Input - High Tower
Entries 90000
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3

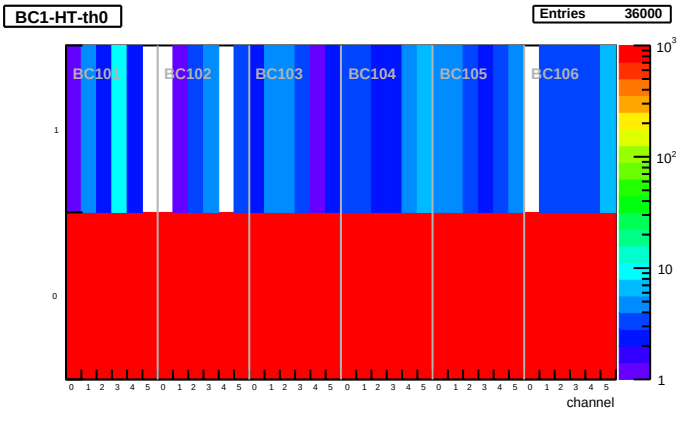

Endcap EMC L0 Input - Patch Sum
Entries 90000
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3

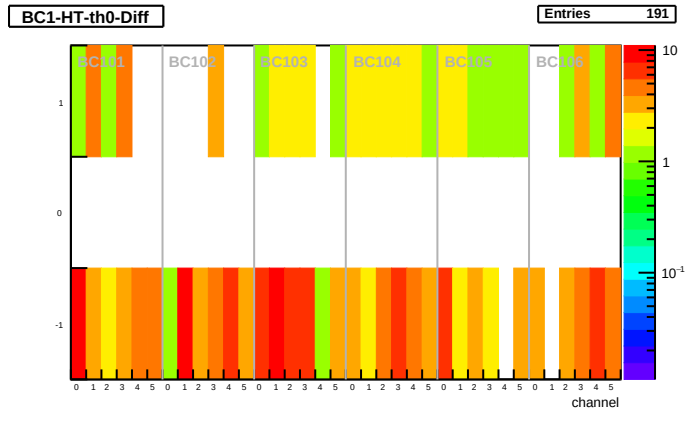

BC1-TP
Entries 36000
BC101
BC102
BC103
BC104
BC105
BC106
channel

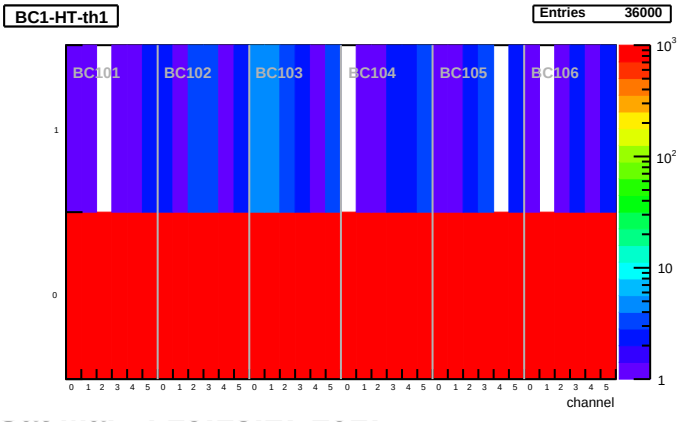

BC1-TP-Diff
Entries 34468
BC101
BC102
BC103
BC104
BC105
BC106
channel

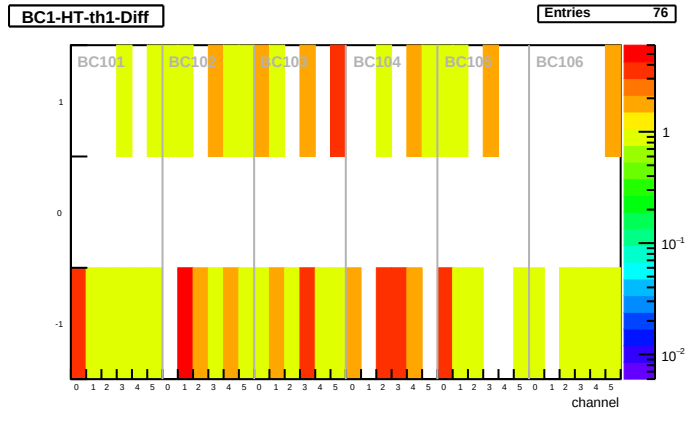

BC1-HT.TP
Entries 36000
BC101
BC102
BC103
BC104
BC105
BC106
channel

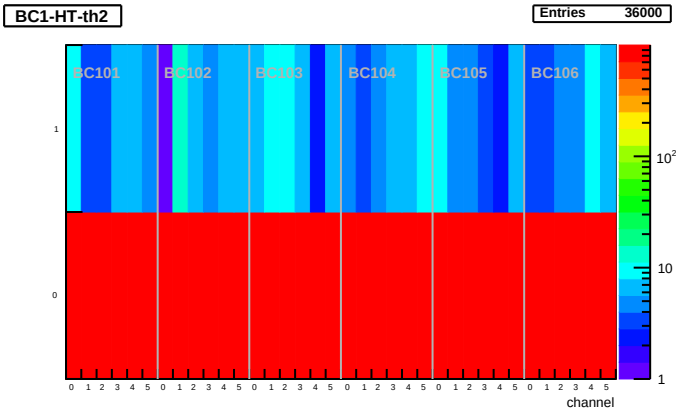

BC1-HT.TP-Diff
Entries 35657
BC101
BC102
BC103
BC104
BC105
BC106
channel

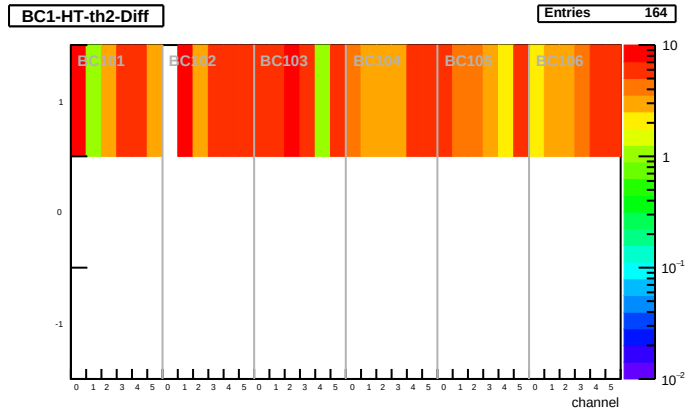

BC1-HT-th0
Entries 36000
BC101
BC102
BC103
BC104
BC105
BC106
channel

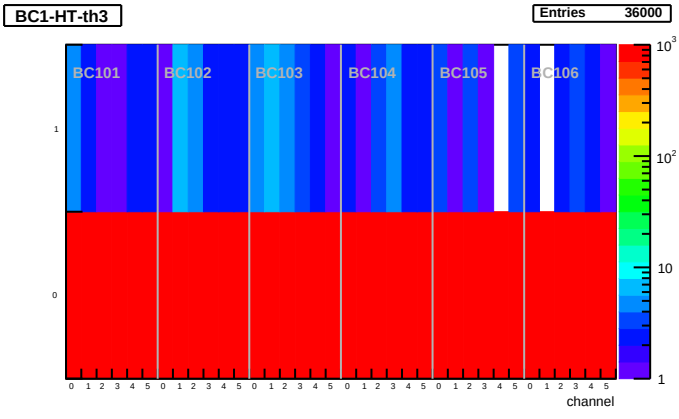

BC1-HT-th0-Diff
Entries 191
BC101
BC102
BC103
BC104
BC105
BC106
channel

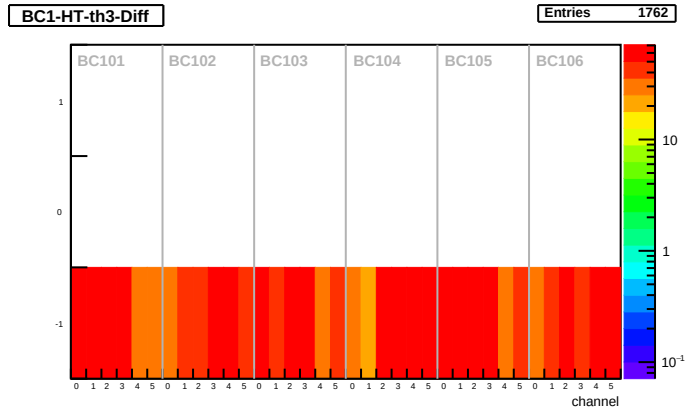

BC1-HT-th1
Entries 36000
BC101
BC102
BC103
BC104
BC105
BC106
channel

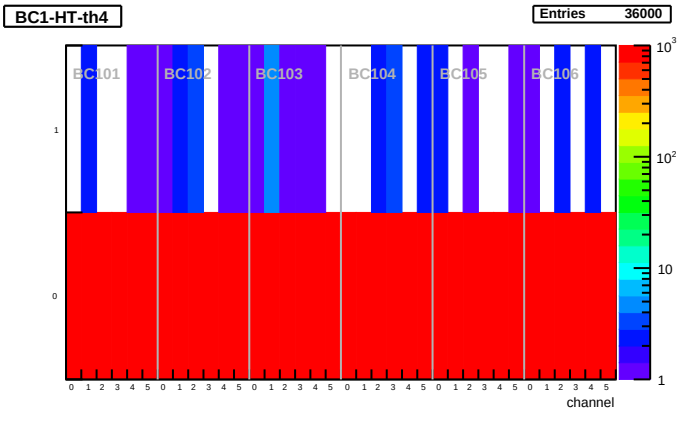

BC1-HT-th1-Diff
Entries 76
BC101
BC102
BC103
BC104
BC105
BC106
channel

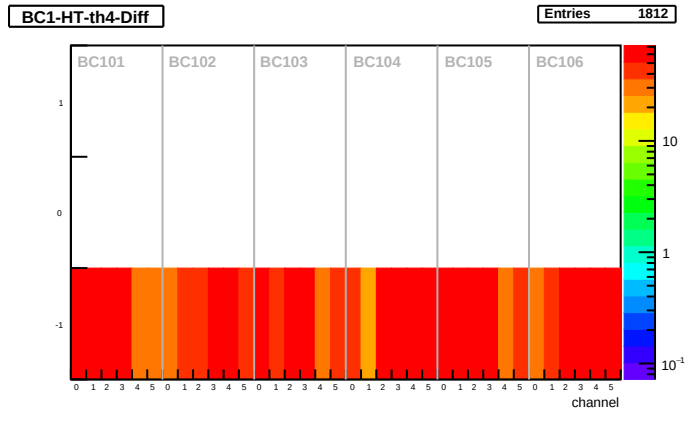

BC1-HT-th2
Entries 36000
BC101
BC102
BC103
BC104
BC105
BC106
channel

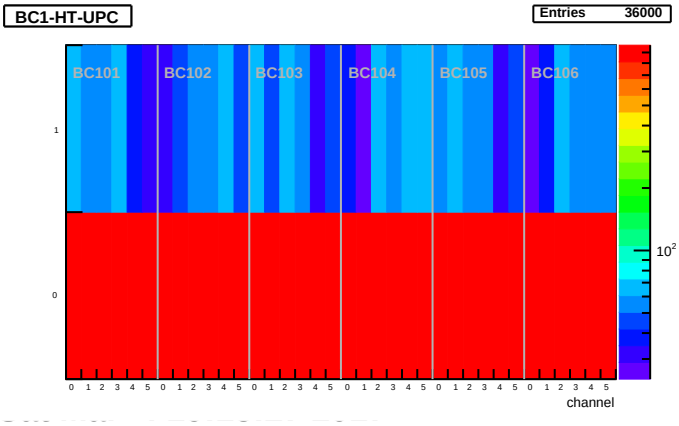

BC1-HT-th2-Diff
Entries 164
BC101
BC102
BC103
BC104
BC105
BC106
channel

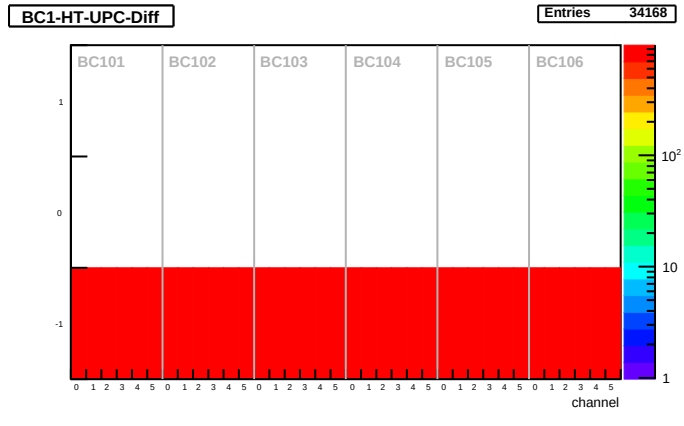

BC1-HT-th3
Entries 36000
BC101
BC102
BC103
BC104
BC105
BC106
channel

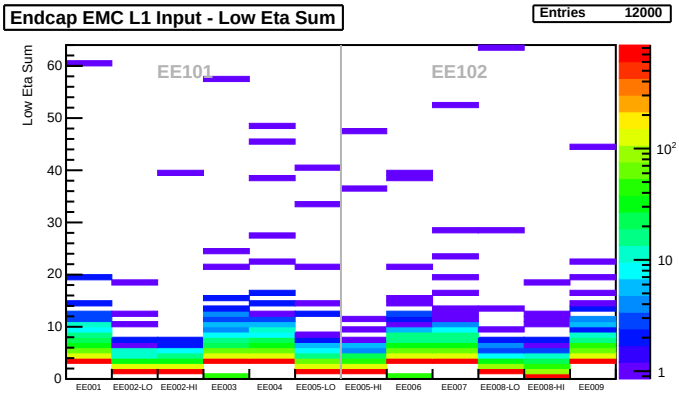

BC1-HT-th3-Diff
Entries 1762
BC101
BC102
BC103
BC104
BC105
BC106
channel

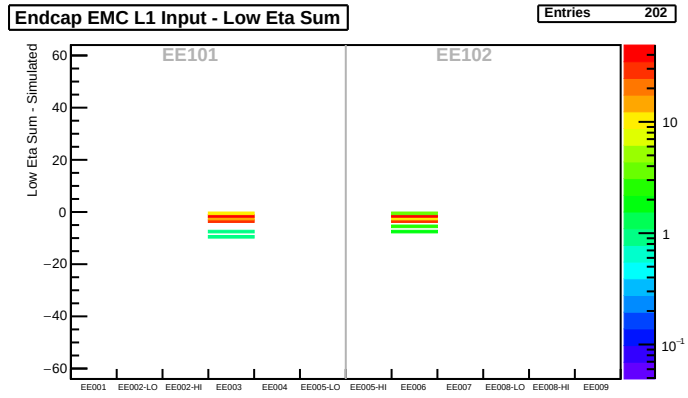

BC1-HT-th4
Entries 36000
BC101
BC102
BC103
BC104
BC105
BC106
channel

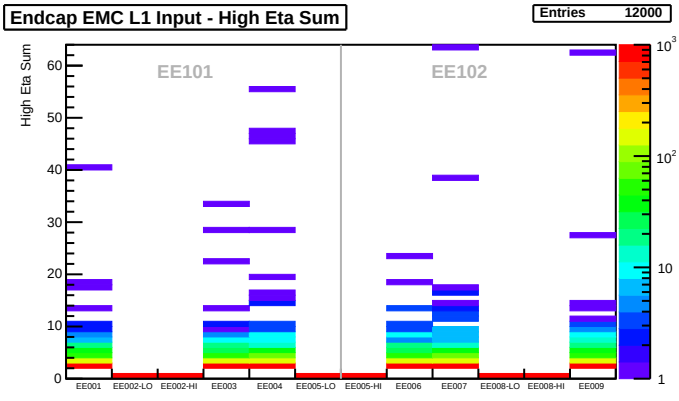

BC1-HT-th4-Diff
Entries 1812
BC101
BC102
BC103
BC104
BC105
BC106
channel

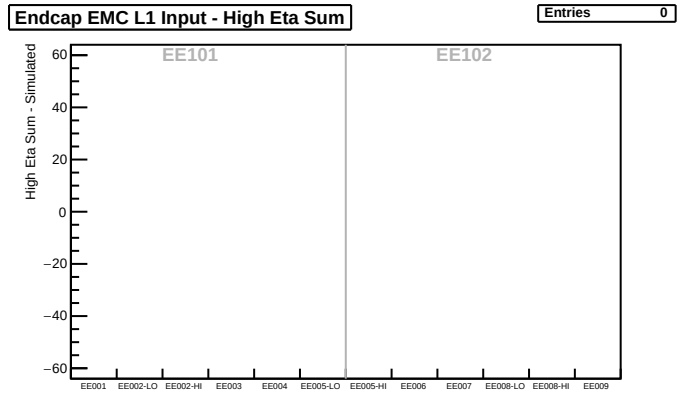

BC1-HT-UPC
Entries 36000
BC101
BC102
BC103
BC104
BC105
BC106
channel

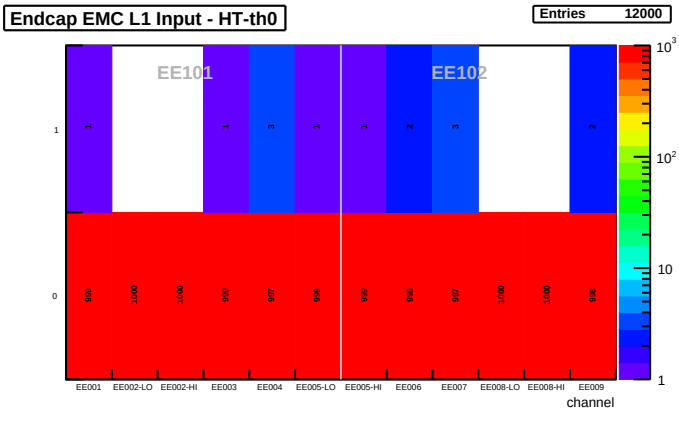

BC1-HT-UPC-Diff
Entries 34168
BC101
BC102
BC103
BC104
BC105
BC106
channel

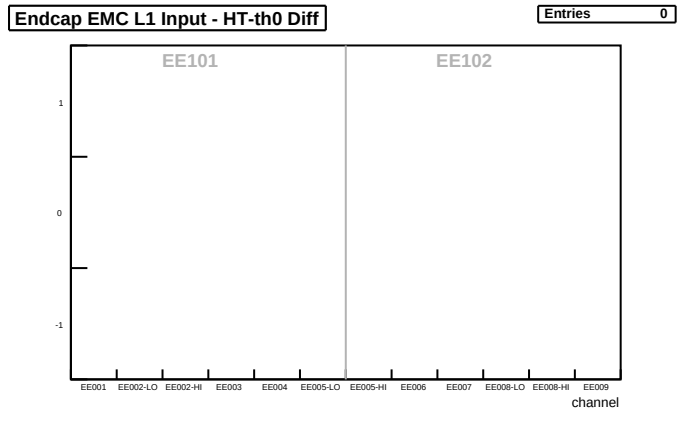

Endcap EMC L1 Input - Low Eta Sum
Entries 12000
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

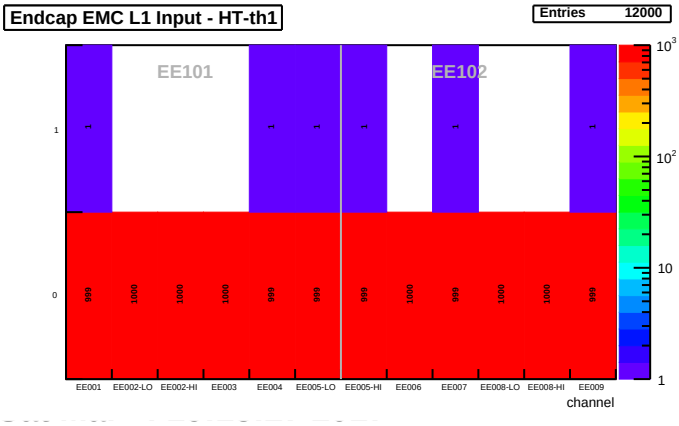

Endcap EMC L1 Input - Low Eta Sum
Entries 202
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

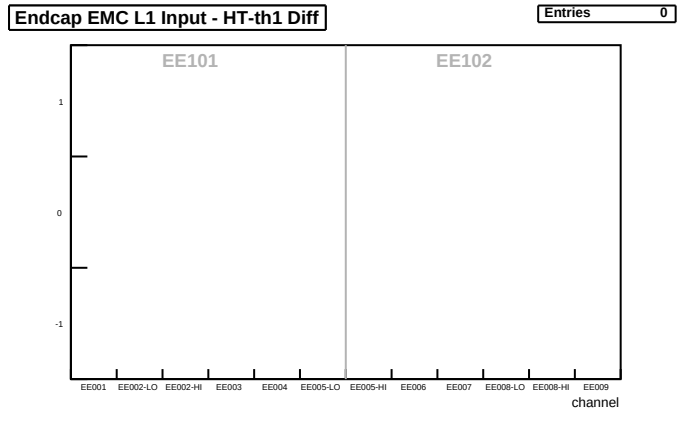

Endcap EMC L1 Input - High Eta Sum
Entries 12000
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

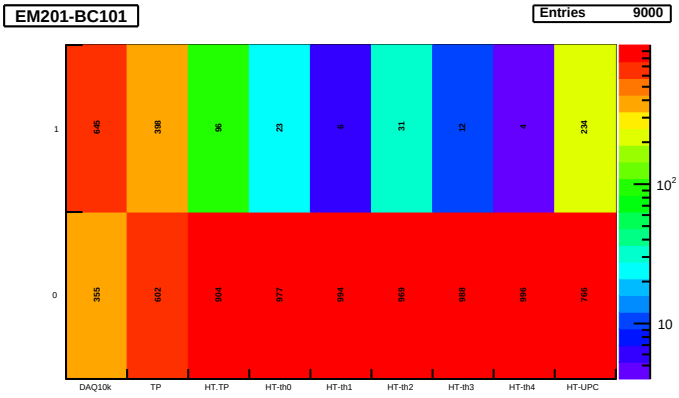

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

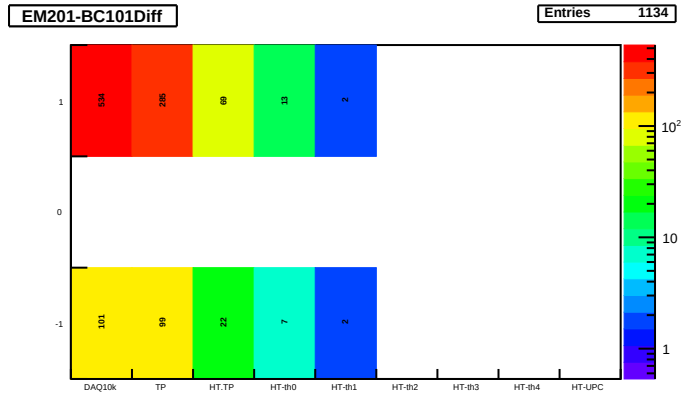

Endcap EMC L1 Input - HT-th0
Entries 12000
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009
channel

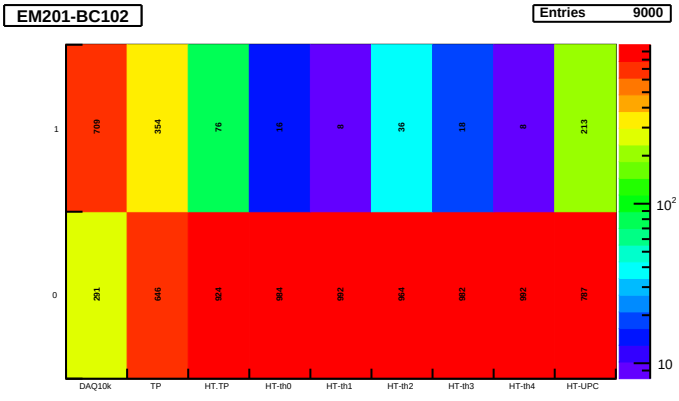

Endcap EMC L1 Input - HT-th0 Diff
Entries 0
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009
channel

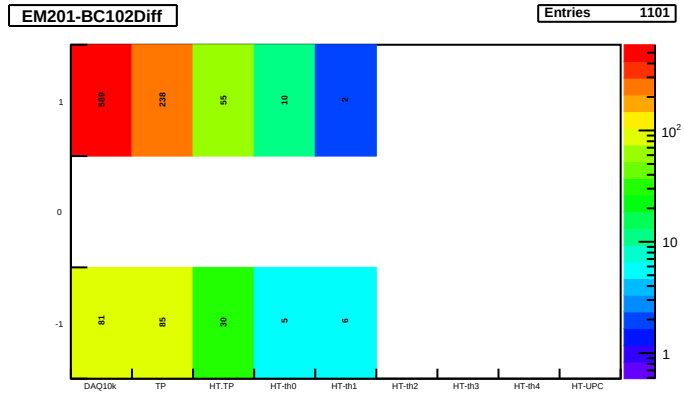

Endcap EMC L1 Input - HT-th1
Entries 12000
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009
channel

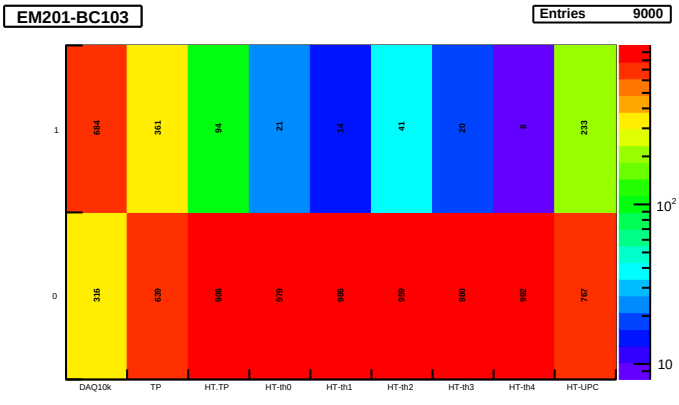

Endcap EMC L1 Input - HT-th1 Diff
Entries 0
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009
channel

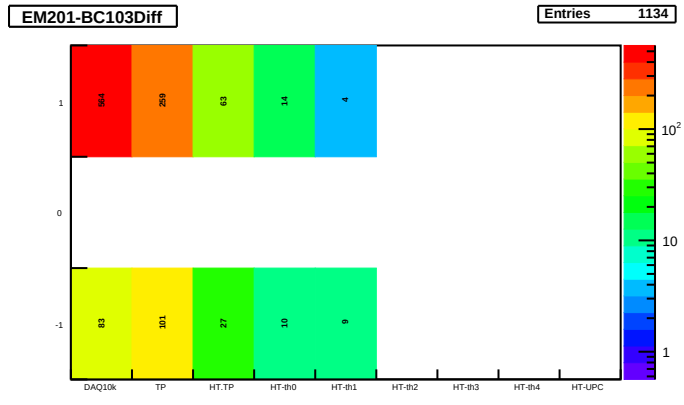

EM201-BC101
Entries 9000
1
0
645 398 96 23 6 31 12 4 234
355 602 904 977 994 969 988 996 766
DAQ10k TP HT.TP HT-th0 HT-th1 HT-th2 HT-th3 HT-th4 HT-UPC
10^2
10

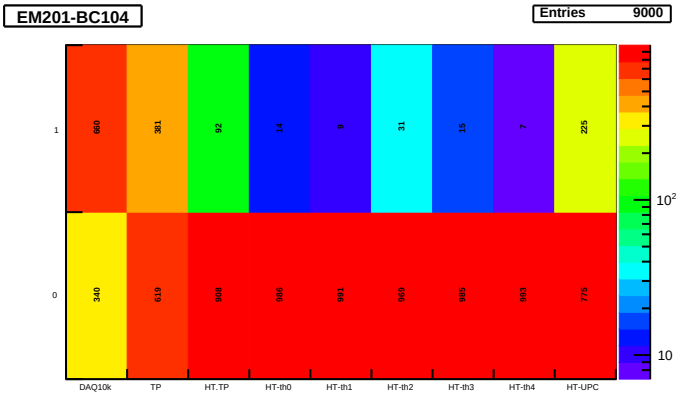

EM201-BC101Diff
Entries 1134
1
0
-1
534 285 69 13 2
101 99 22 7 2
DAQ10k TP HT.TP HT-th0 HT-th1 HT-th2 HT-th3 HT-th4 HT-UPC
10^2
10
1

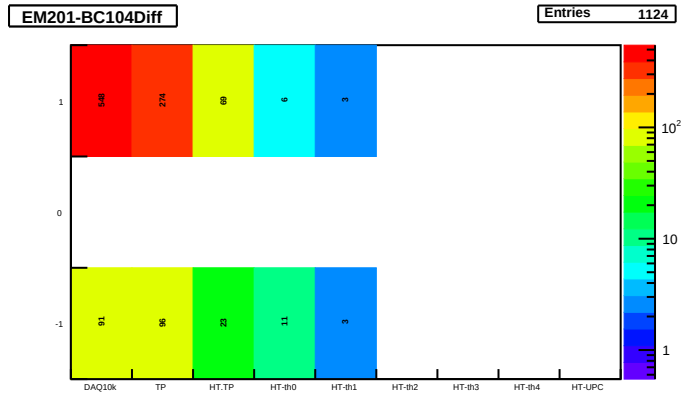

EM201-BC102
Entries 9000
1
0
709 354 76 16 8 36 18 8 213
291 646 924 984 992 964 982 992 787
DAQ10k TP HT.TP HT-th0 HT-th1 HT-th2 HT-th3 HT-th4 HT-UPC
10^2
10


EM201-BC102Diff
Entries 1101
1
0
-1
589 238 55 10 2
81 85 30 5 6
DAQ10k TP HT.TP HT-th0 HT-th1 HT-th2 HT-th3 HT-th4 HT-UPC
10^2
10
1

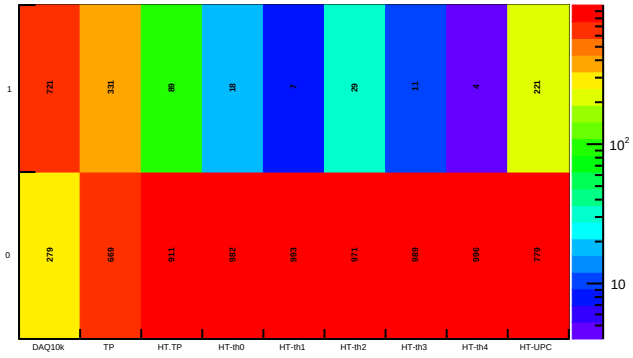

EM201-BC103
Entries 9000
1
0
684 361 94 21 14 41 20 8 233
316 639 906 979 986 959 980 992 767
DAQ10k TP HT.TP HT-th0 HT-th1 HT-th2 HT-th3 HT-th4 HT-UPC
10^2
10


EM201-BC103Diff
Entries 1134
1
0
-1
564 259 63 14 4
83 101 27 10 9
DAQ10k TP HT.TP HT-th0 HT-th1 HT-th2 HT-th3 HT-th4 HT-UPC
10^2
10
1

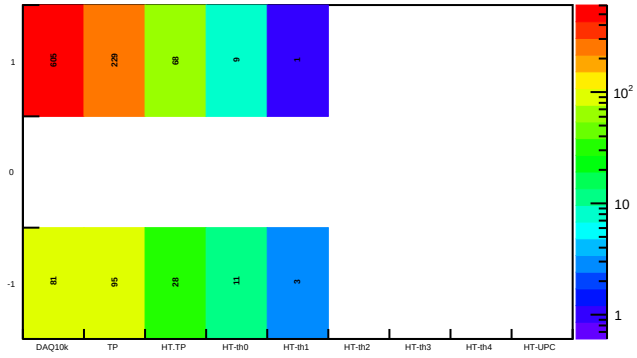

EM201-BC104
Entries 9000
1
0
660 381 92 14 9 31 15 7 225
340 619 908 986 991 969 985 993 775
DAQ10k TP HT.TP HT-th0 HT-th1 HT-th2 HT-th3 HT-th4 HT-UPC
10^2
10


EM201-BC104Diff
Entries 1124
1
0
-1
548 274 69 6 3
91 96 23 11 3
DAQ10k TP HT.TP HT-th0 HT-th1 HT-th2 HT-th3 HT-th4 HT-UPC
10^2
10
1

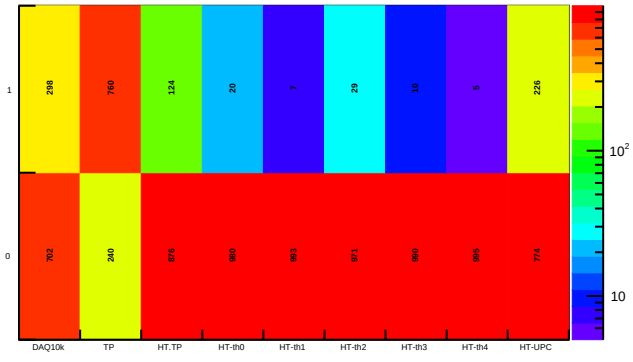
EM201-BC105 Entries 9000



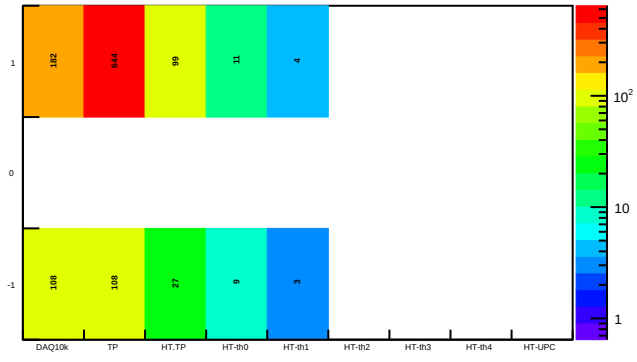
EM201-BC105Diff Entries 1130



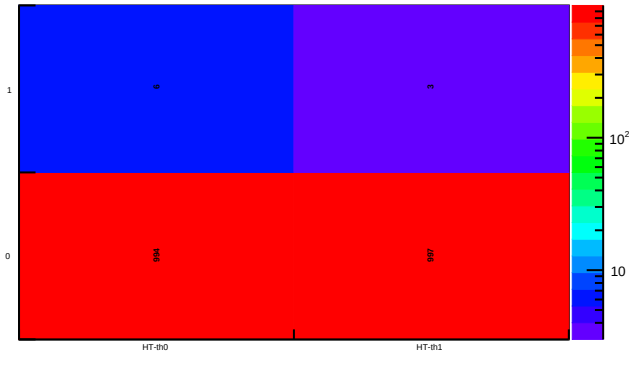
EM201-BC106 Entries 9000



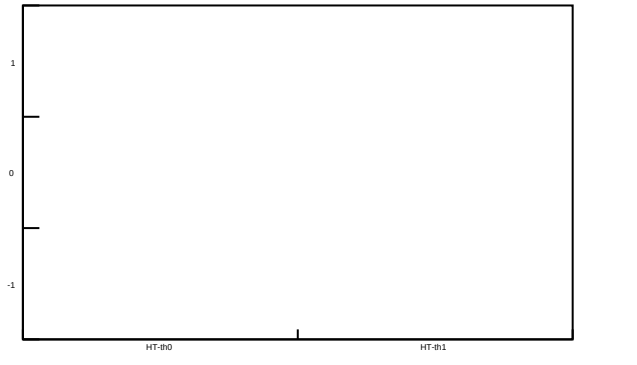
EM201-BC106Diff Entries 1195



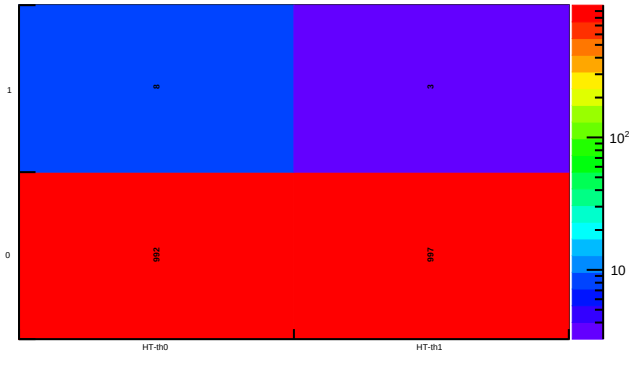
EM201-EE101 Entries 2000



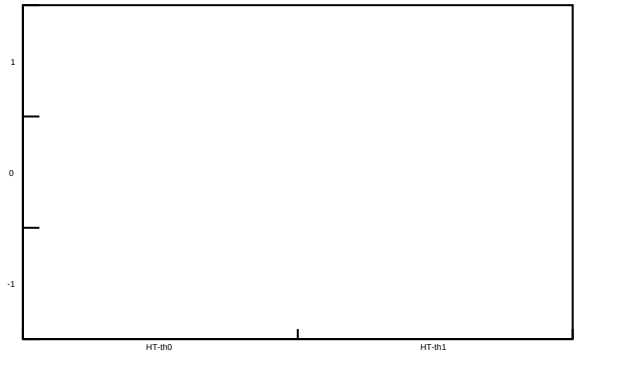
EM201-EE101Diff Entries 0

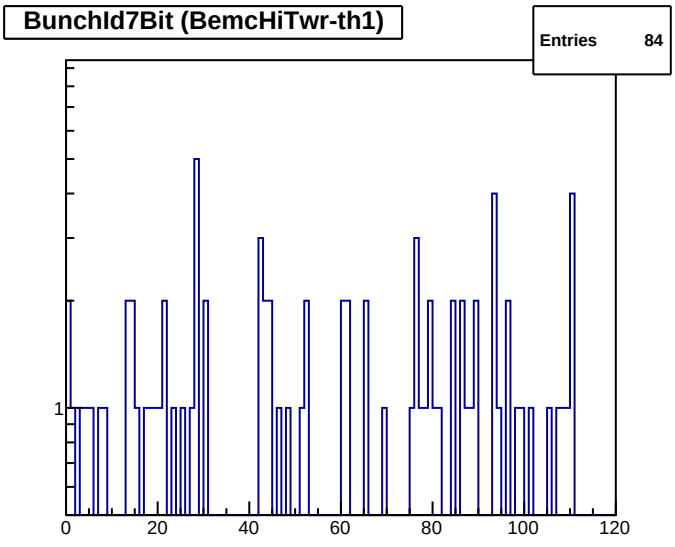


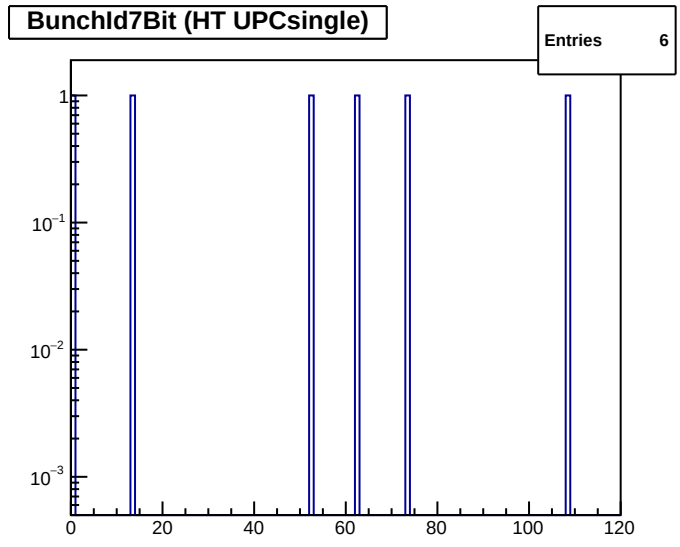
EM201-EE102 Entries 2000

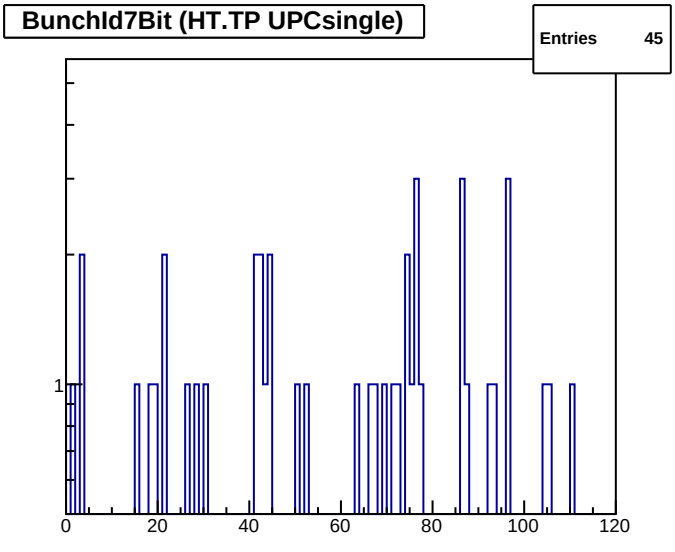


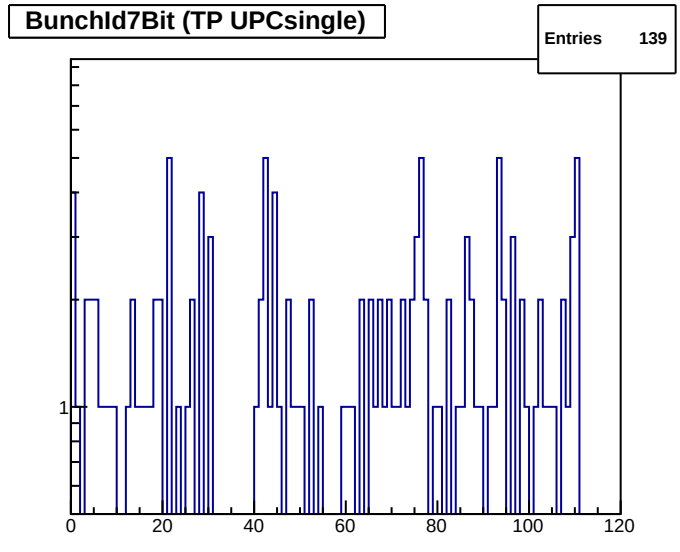
EM201-EE102Diff Entries 0

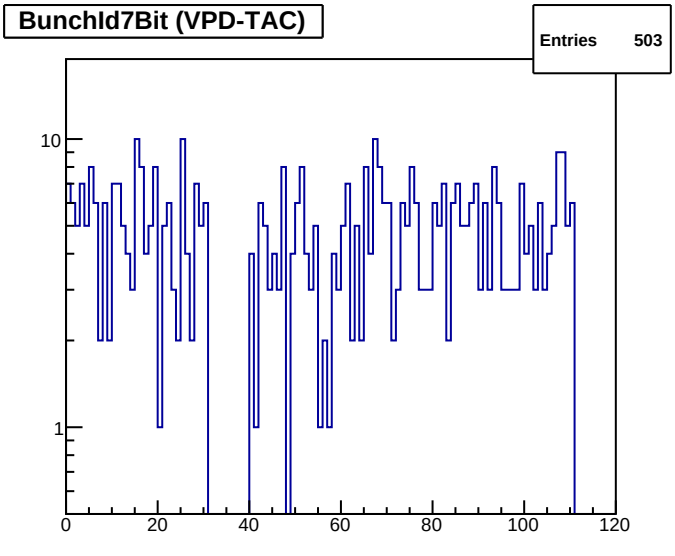


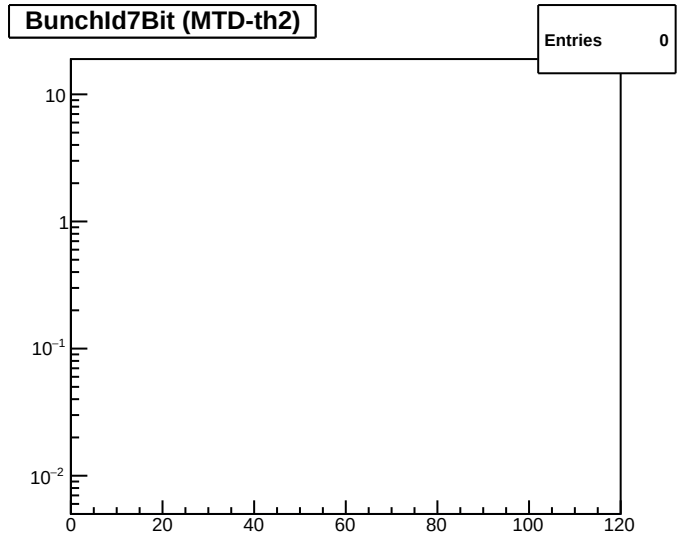

BunchId7Bit (BemcHiTwr-th1)
Entries 84
1
0 20 40 60 80 100 120

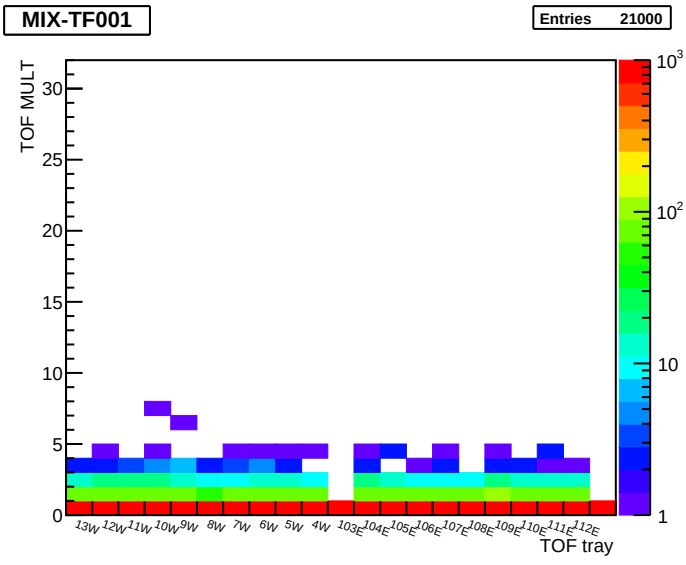

BunchId7Bit (HT UPCsingle)
Entries 6
1
10^{-1}
10^{-2}
10^{-3}
0 20 40 60 80 100 120

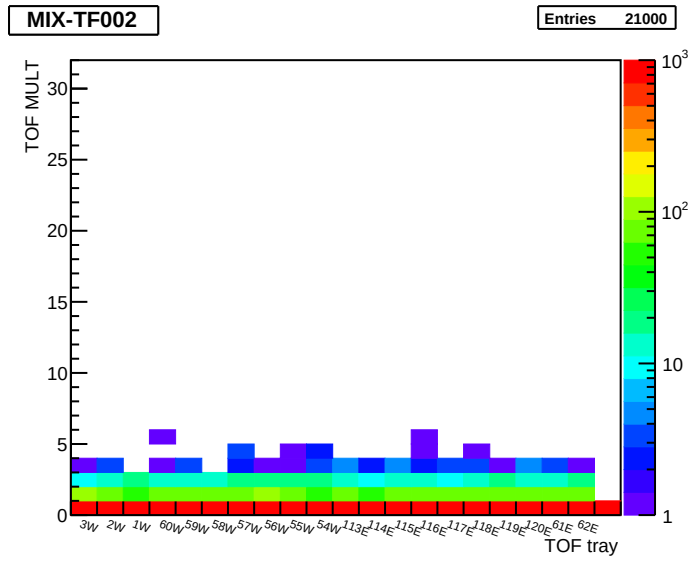

BunchId7Bit (HT.TP UPCsingle)
Entries 45
1
0 20 40 60 80 100 120

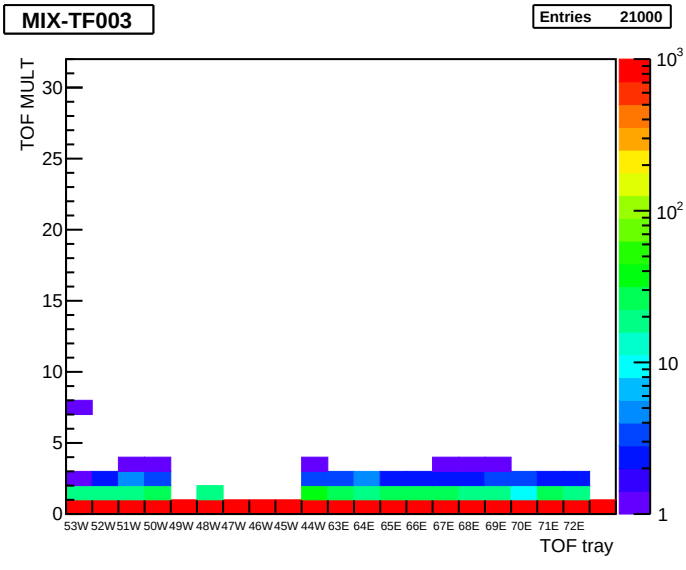

BunchId7Bit (TP UPCsingle)
Entries 139
1
0 20 40 60 80 100 120

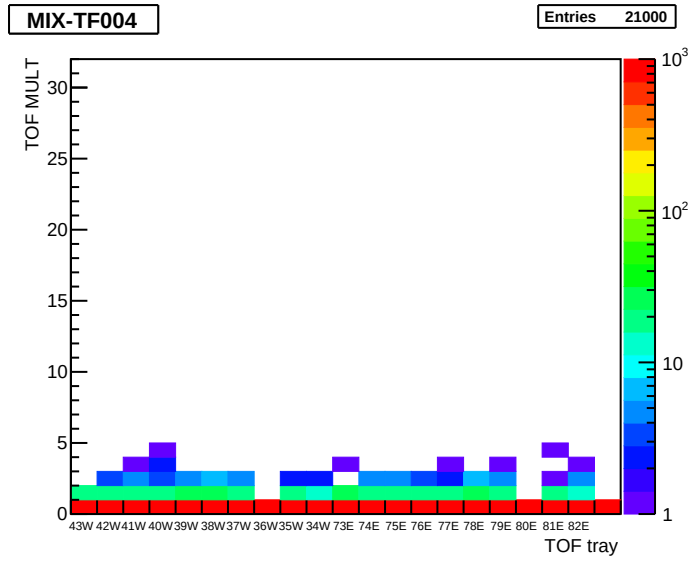

BunchId7Bit (VPD-TAC)
Entries 503
10
1
0 20 40 60 80 100 120

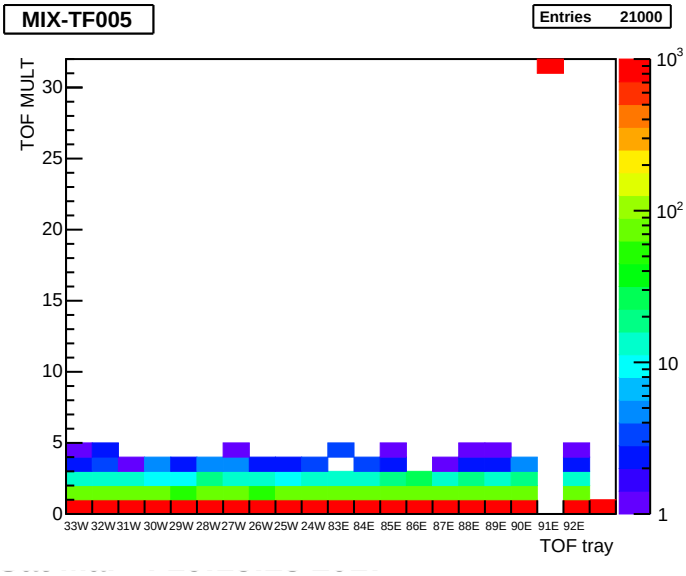

BunchId7Bit (MTD-th2)
Entries 0
10
1
10^{-1}
10^{-2}
0 20 40 60 80 100 120

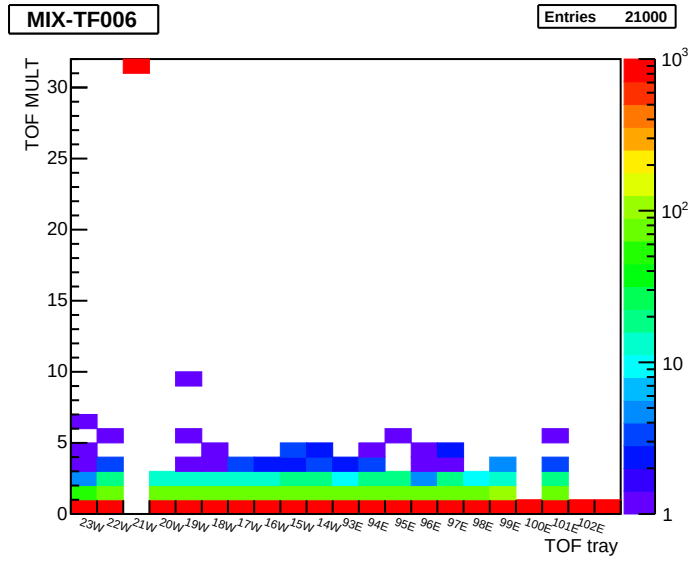

MIX-TF001
Entries 21000
TOF MULT
TOF tray
13W 12W 11W 10W 9W 8W 7W 6W 5W 4W 103E 104E 105E 106E 107E 108E 109E 110E 111E 112E

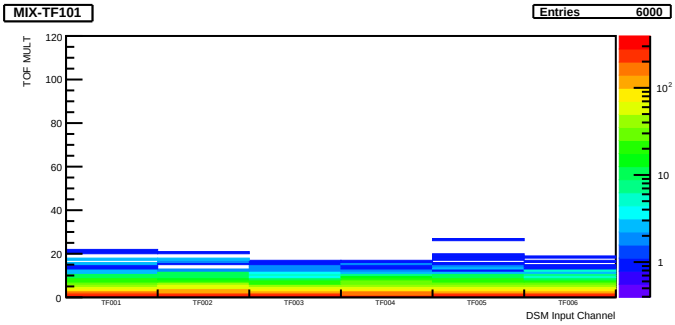

MIX-TF002
Entries 21000
TOF MULT
TOF tray
3W 2W 1W 60W 59W 58W 57W 56W 55W 54W 113E 114E 115E 116E 117E 118E 119E 120E 61E 62E

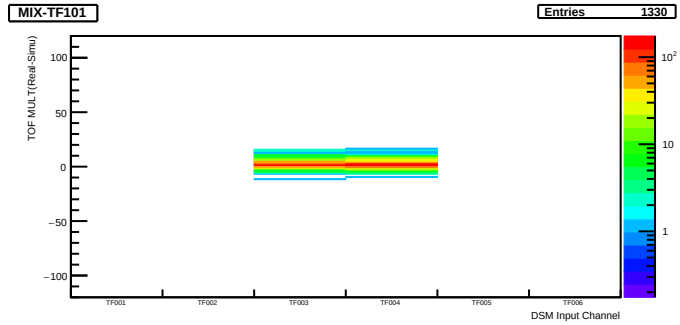

MIX-TF003
Entries 21000
TOF MULT
TOF tray
53W 52W 51W 50W 49W 48W 47W 46W 45W 44W 63E 64E 65E 66E 67E 68E 69E 70E 71E 72E

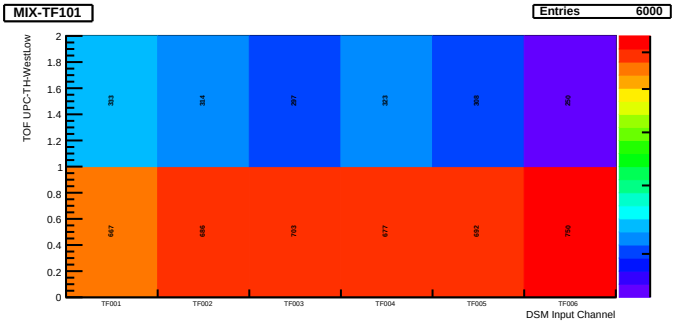

MIX-TF004
Entries 21000
TOF MULT
TOF tray
43W 42W 41W 40W 39W 38W 37W 36W 35W 34W 73E 74E 75E 76E 77E 78E 79E 80E 81E 82E

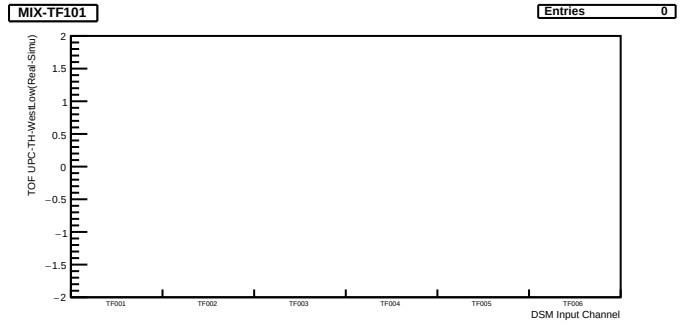

MIX-TF005
Entries 21000
TOF MULT
TOF tray
33W 32W 31W 30W 29W 28W 27W 26W 25W 24W 83E 84E 85E 86E 87E 88E 89E 90E 91E 92E

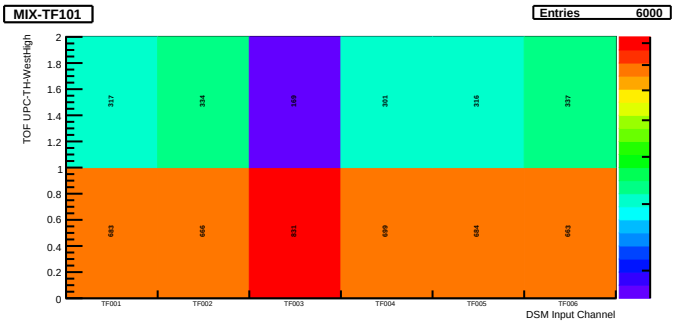

MIX-TF006
Entries 21000
TOF MULT
TOF tray
23W 22W 21W 20W 19W 18W 17W 16W 15W 14W 93E 94E 95E 96E 97E 98E 99E 100E 101E 102E

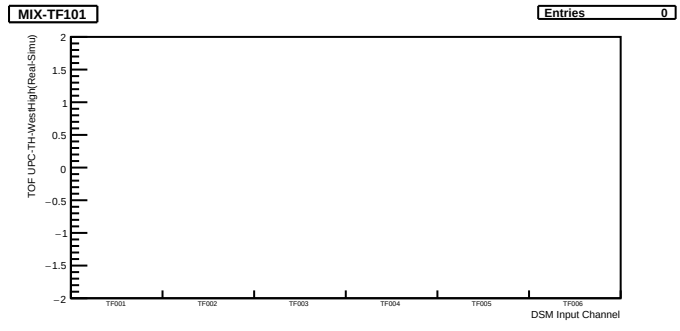

MIX-TF101
Entries 6000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

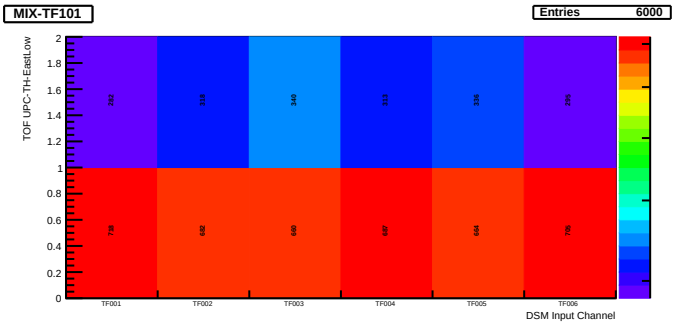

MIX-TF101
Entries 1330
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

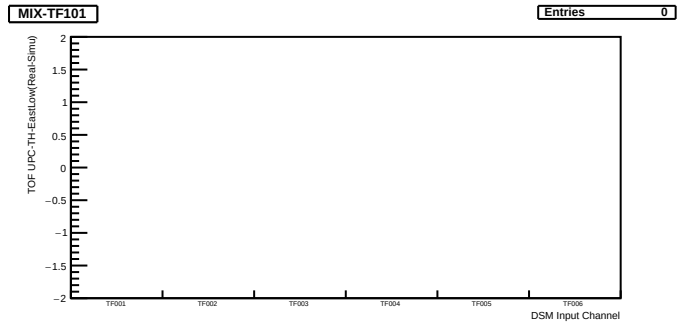

MIX-TF101
Entries 6000
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

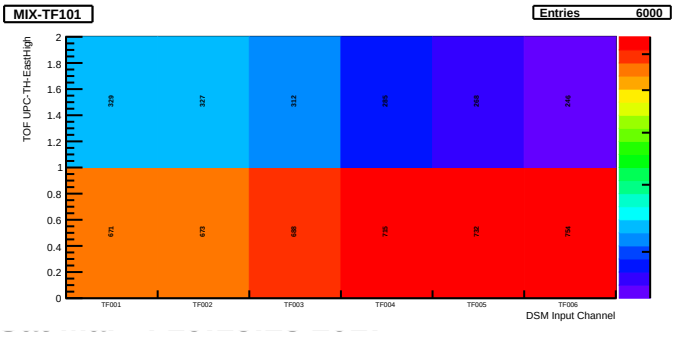

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

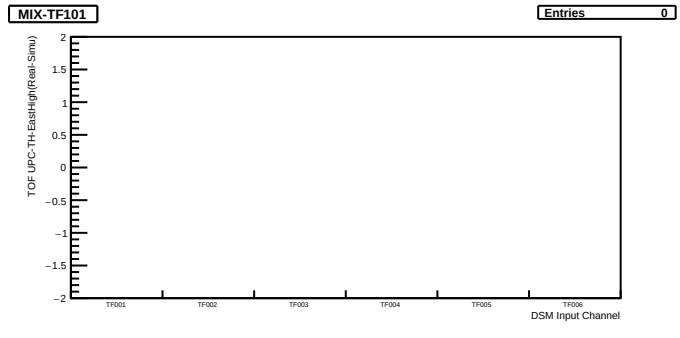

MIX-TF101
Entries 6000
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

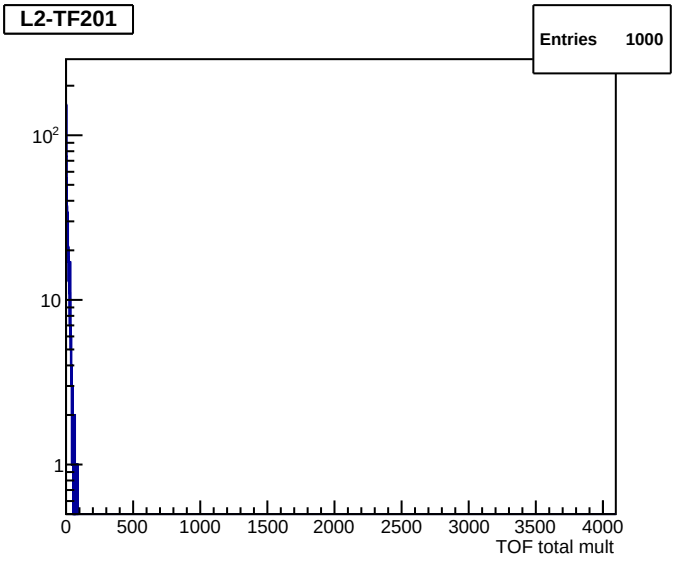

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

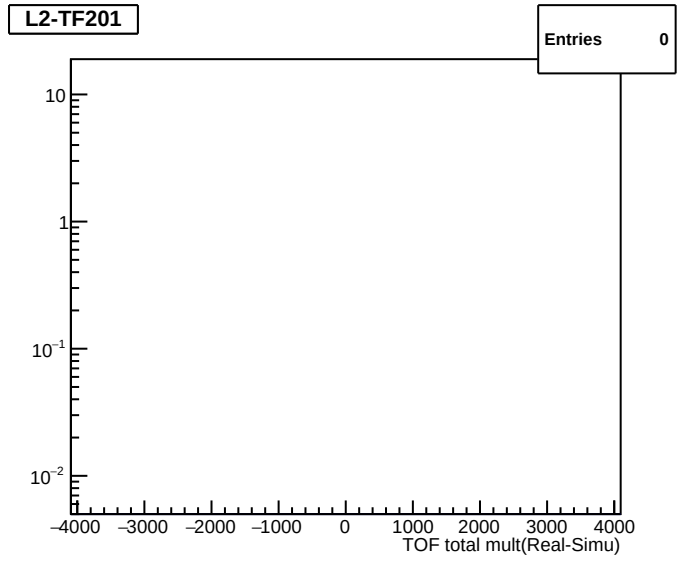

MIX-TF101
Entries 6000
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

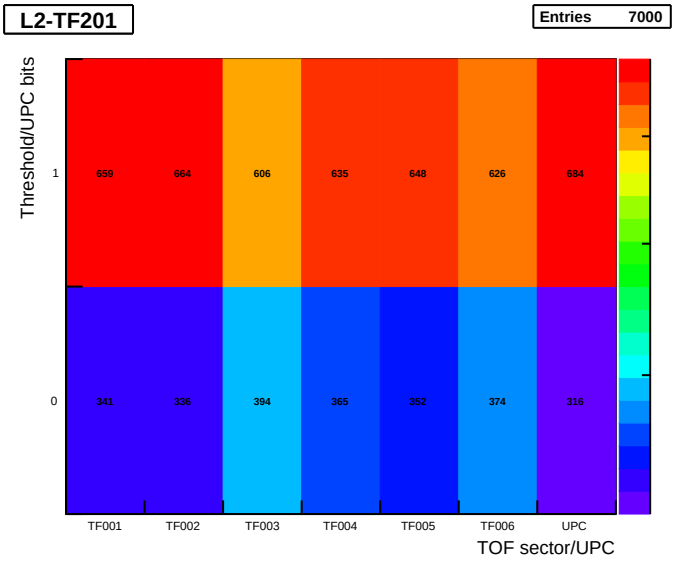

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

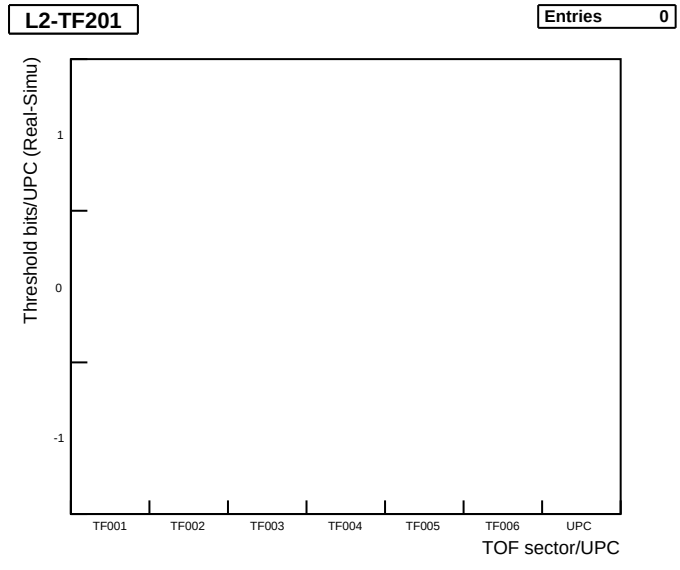

MIX-TF101
Entries 6000
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

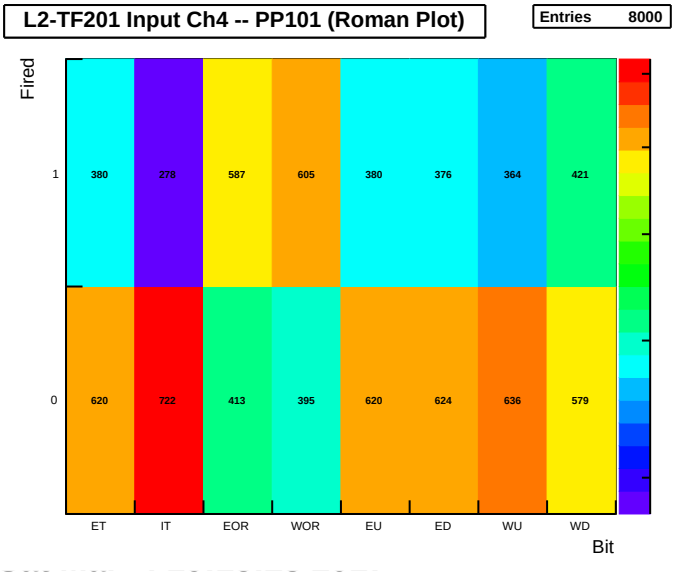

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

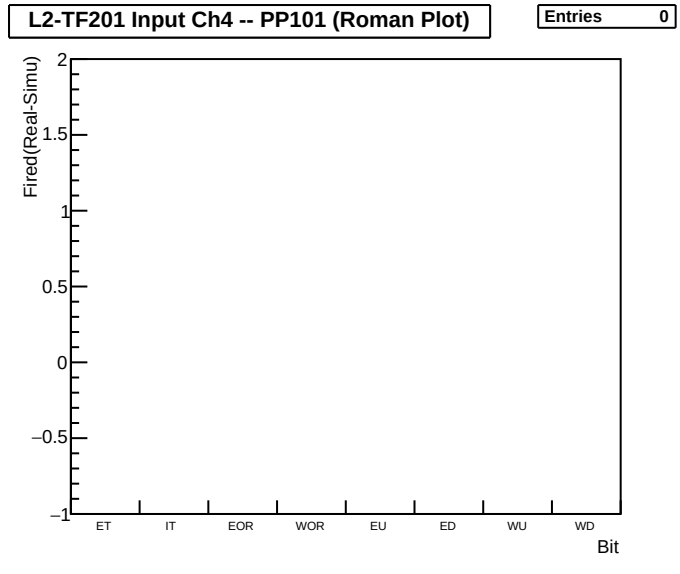

L2-TF201
Entries 1000
10^2
10
1
0 500 1000 1500 2000 2500 3000 3500 4000
TOF total mult

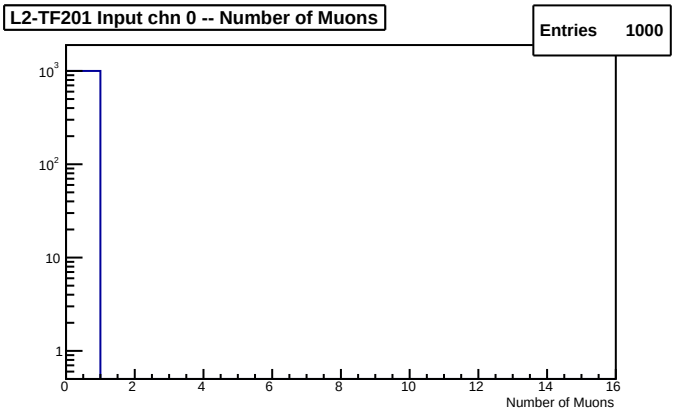

L2-TF201
Entries 0
10
1
10^{-1}
10^{-2}
–4000 –3000 –2000 –1000 0 1000 2000 3000 4000
TOF total mult(Real-Simu)

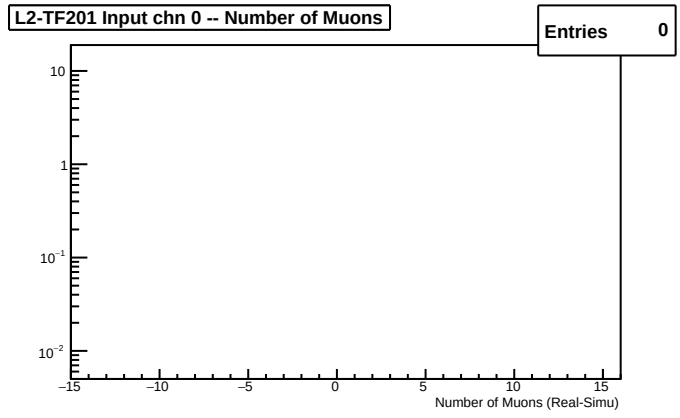

L2-TF201
Entries 7000
Threshold/UPC bits
1
0
659 664 606 635 648 626 684
341 336 394 365 352 374 316
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

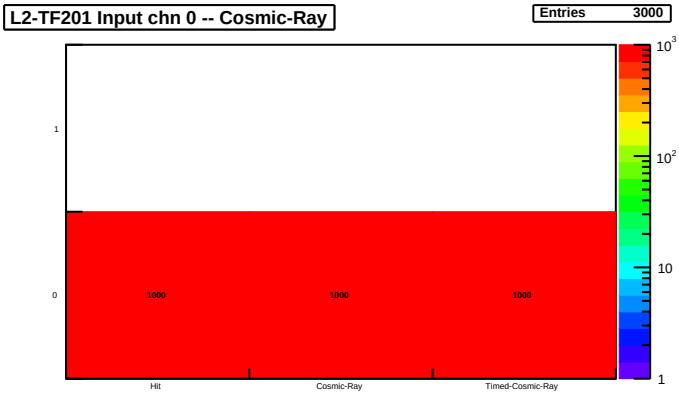

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
1
0
-1
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

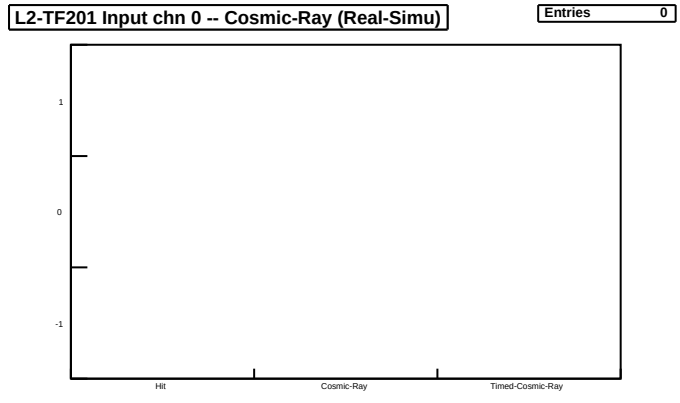

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 8000
Fired
1
0
380 278 587 605 380 376 364 421
620 722 413 395 620 624 636 579
ET IT EOR WOR EU ED WU WD
Bit

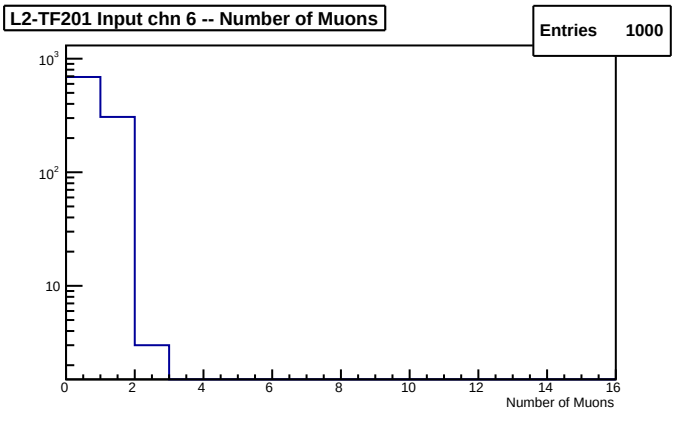

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 0
Fired(Real-Simu)
2
1.5
1
0.5
0
–0.5
–1
ET IT EOR WOR EU ED WU WD
Bit

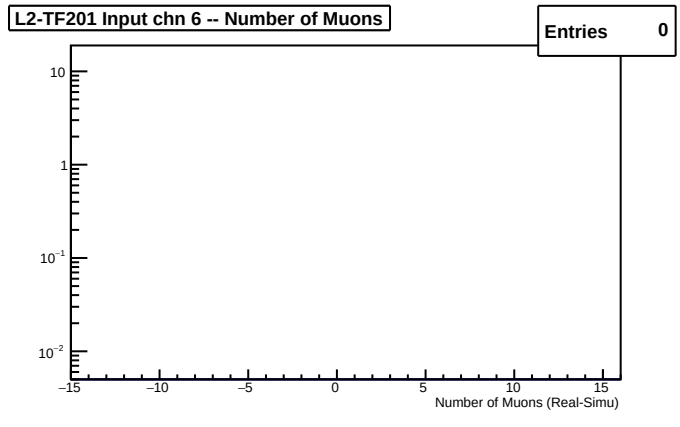

L2-TF201 Input chn 0 -- Number of Muons
Entries 1000
Number of Muons

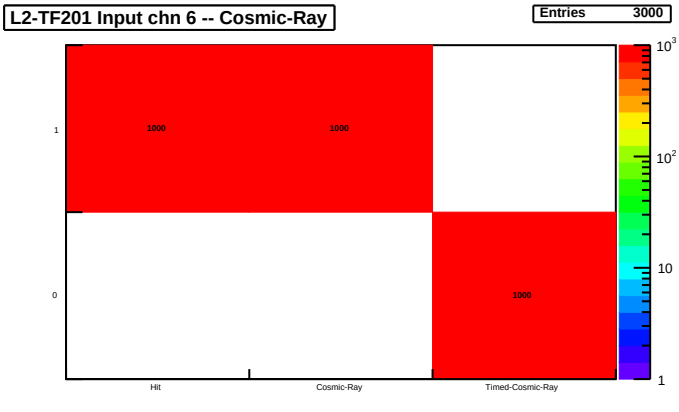

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

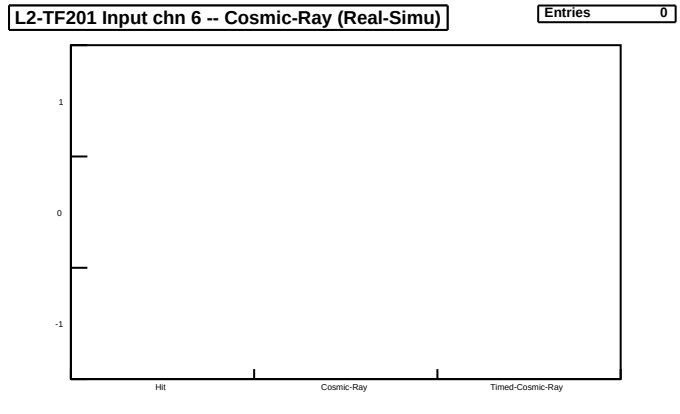

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 3000
1000
1000
1000
Hit
Cosmic-Ray
Timed-Cosmic-Ray

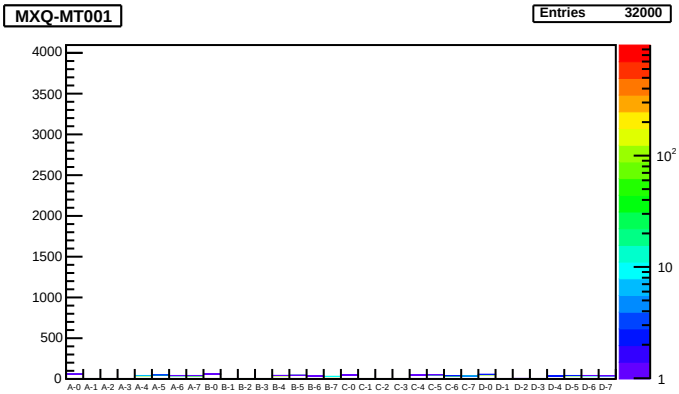

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

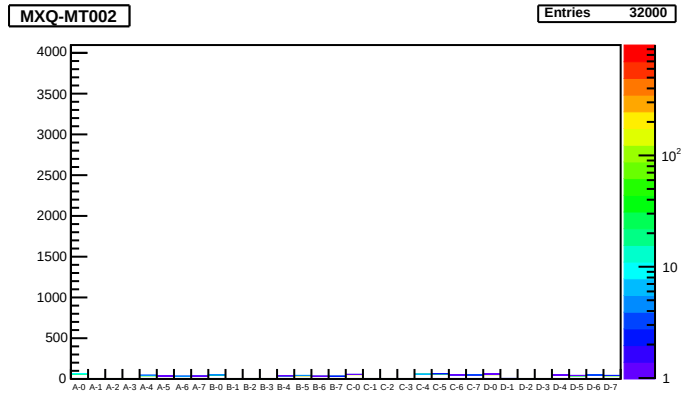

L2-TF201 Input chn 6 -- Number of Muons
Entries 1000
Number of Muons

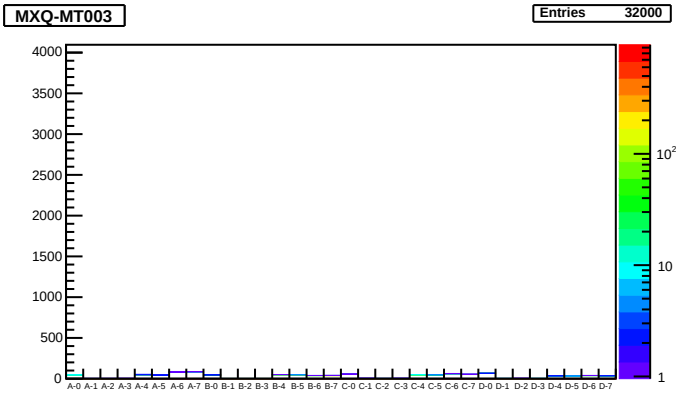

L2-TF201 Input chn 6 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

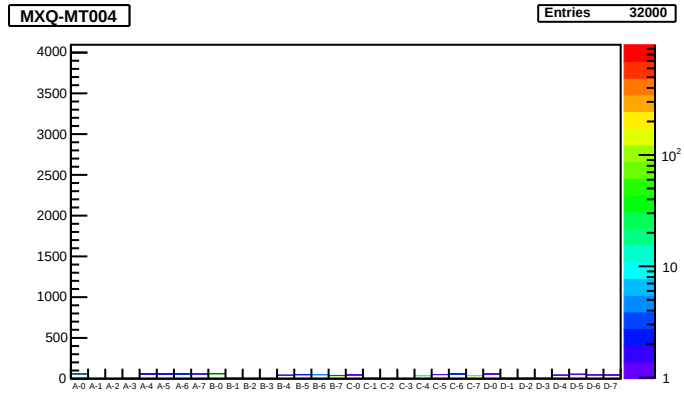

L2-TF201 Input chn 6 -- Cosmic-Ray
Entries 3000
1000
1000
1000
Hit
Cosmic-Ray
Timed-Cosmic-Ray

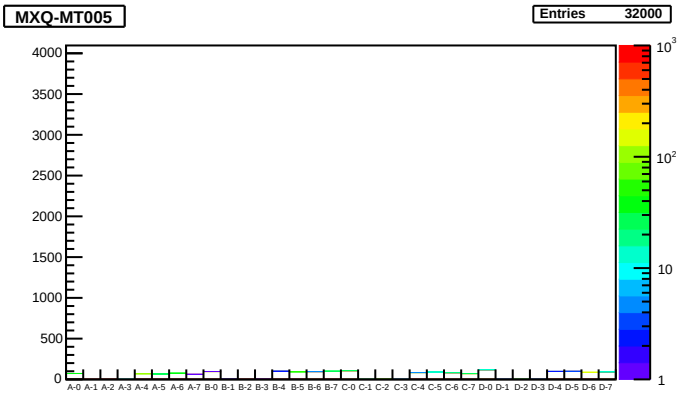

L2-TF201 Input chn 6 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

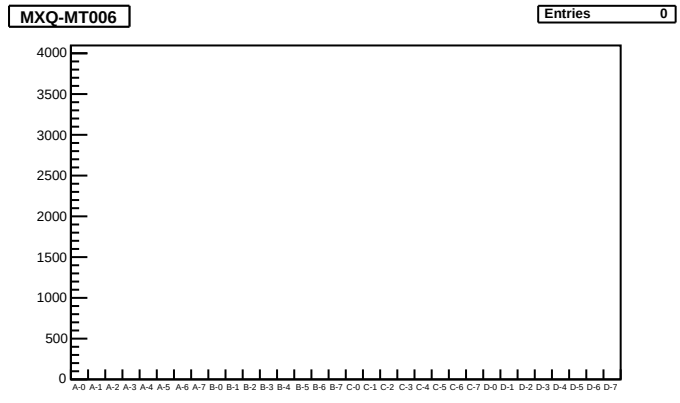

MXQ-MT001
Entries 32000

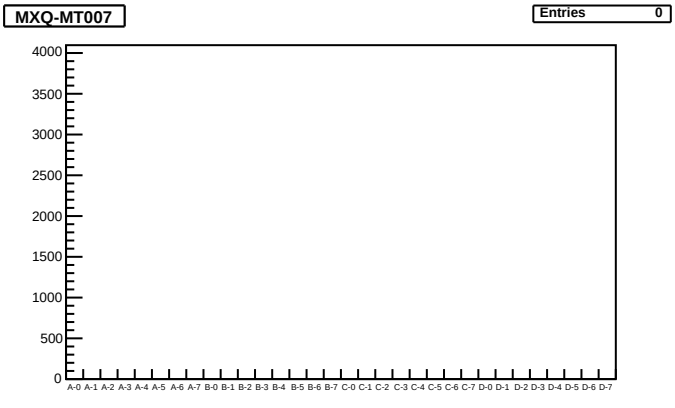

MXQ-MT002
Entries 32000

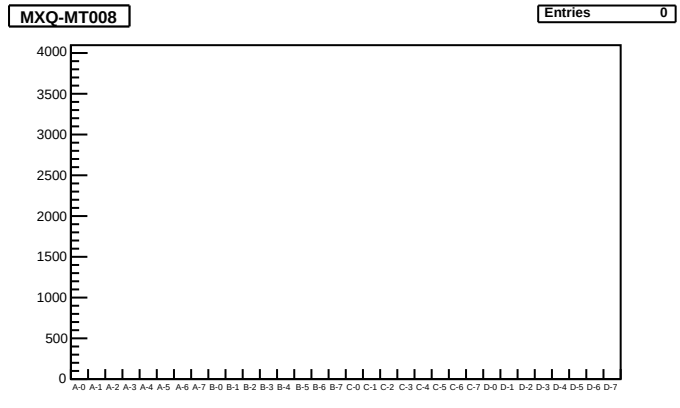

MXQ-MT003
Entries 32000

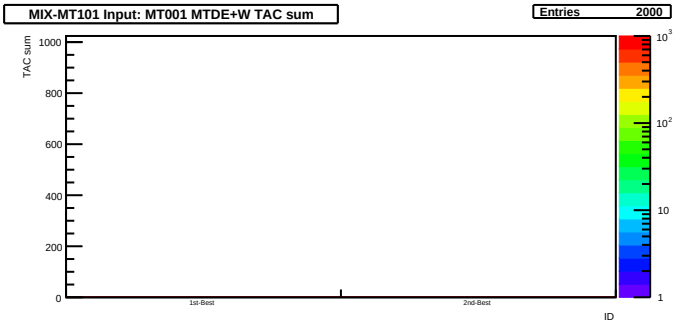

MXQ-MT004
Entries 32000

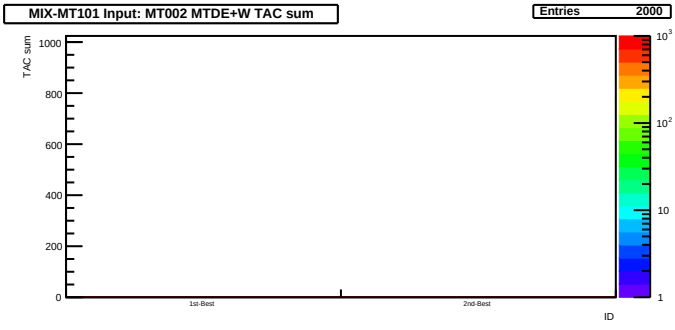

MXQ-MT005
Entries 32000

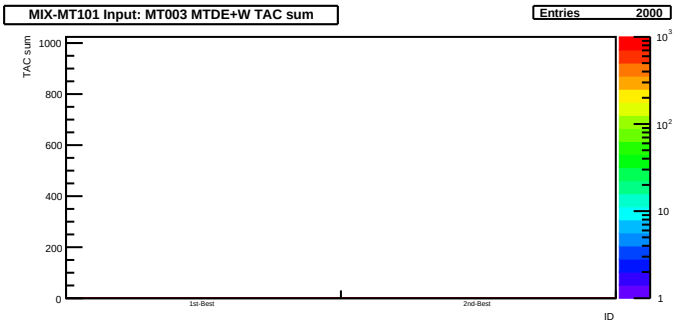

MXQ-MT006
Entries 0

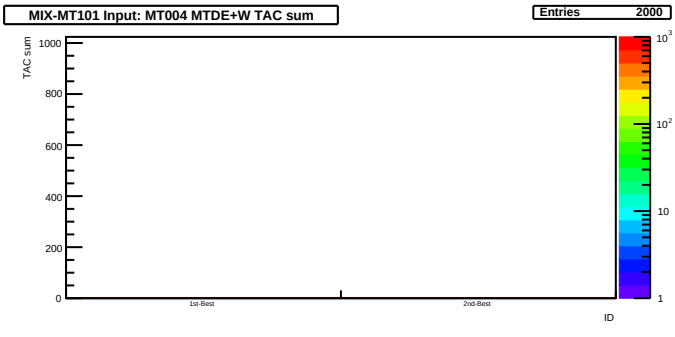

MXQ-MT007
Entries 0

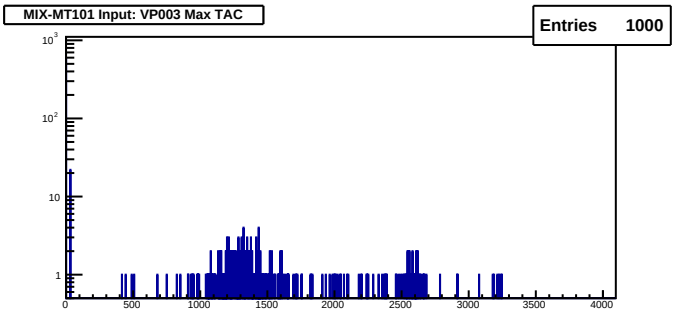

MXQ-MT008
Entries 0

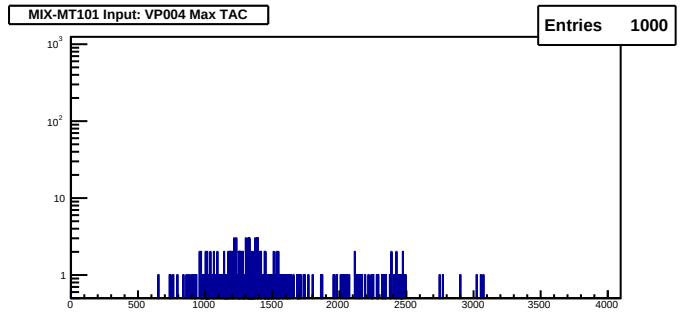

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 2000
TAC sum
1st-Best
2nd-Best
ID

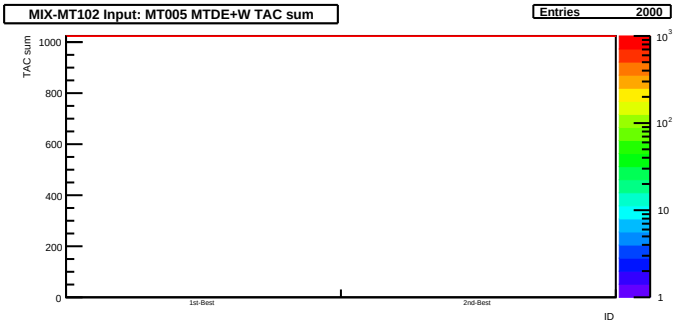

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 2000
TAC sum
1st-Best
2nd-Best
ID

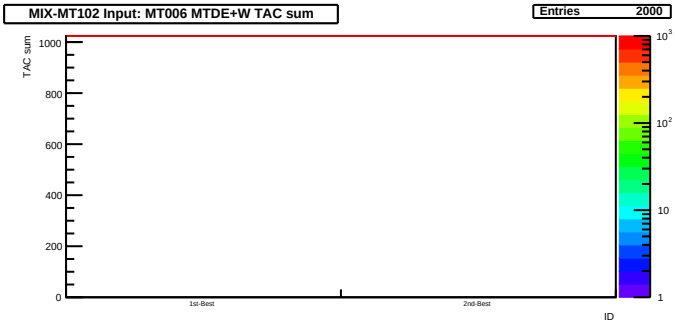

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 2000
TAC sum
1st-Best
2nd-Best
ID

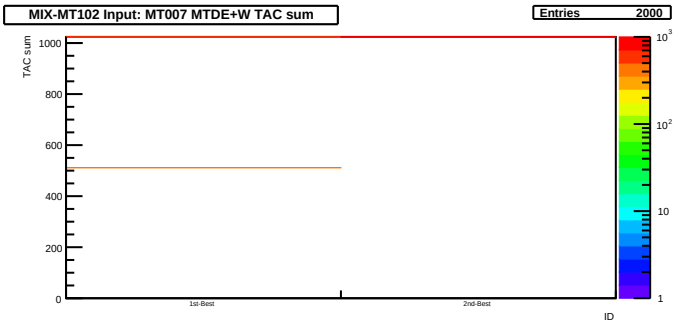

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 2000
TAC sum
1st-Best
2nd-Best
ID

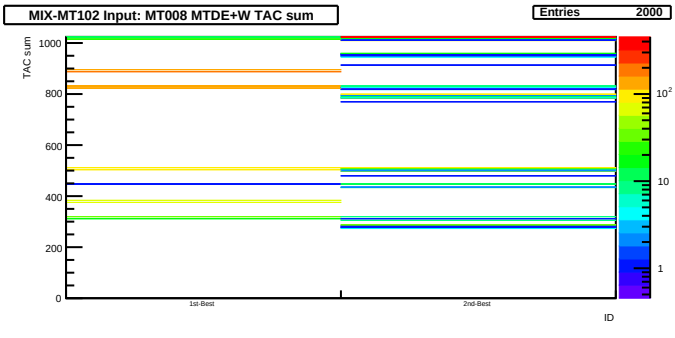

MIX-MT101 Input: VP003 Max TAC
Entries 1000

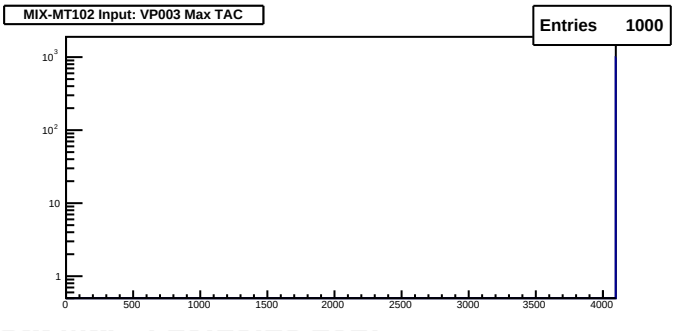

MIX-MT101 Input: VP004 Max TAC
Entries 1000

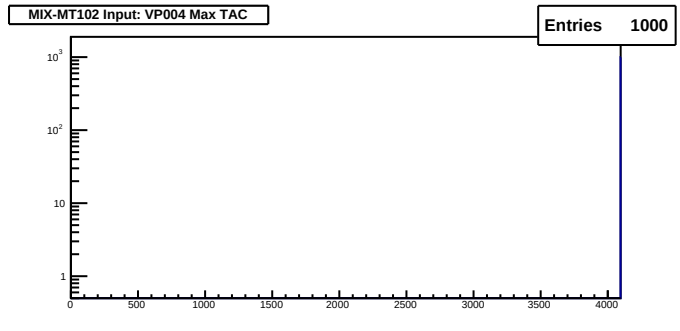

MIX-MT102 Input: MT005 MTDE+W TAC sum
Entries 2000
TAC sum
1st-Best
2nd-Best
ID

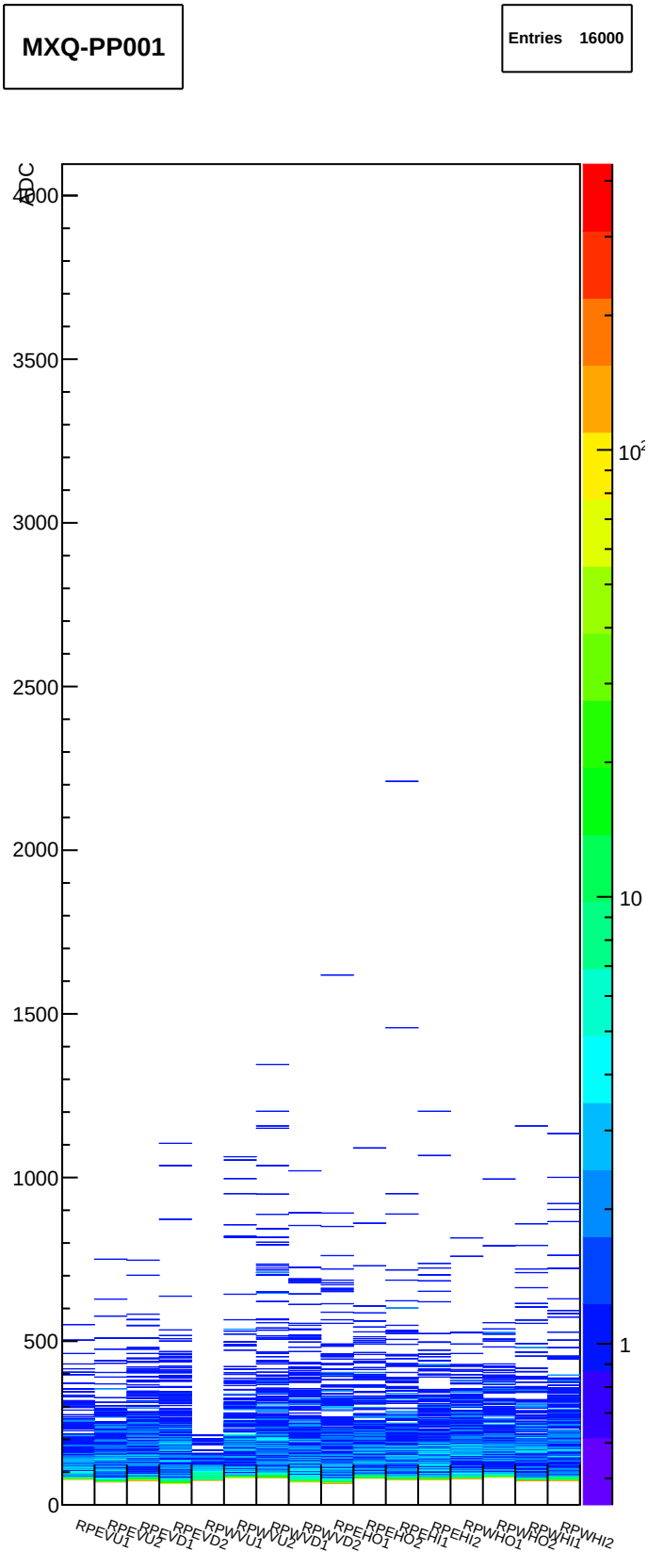

MIX-MT102 Input: MT006 MTDE+W TAC sum
Entries 2000
TAC sum
1st-Best
2nd-Best
ID

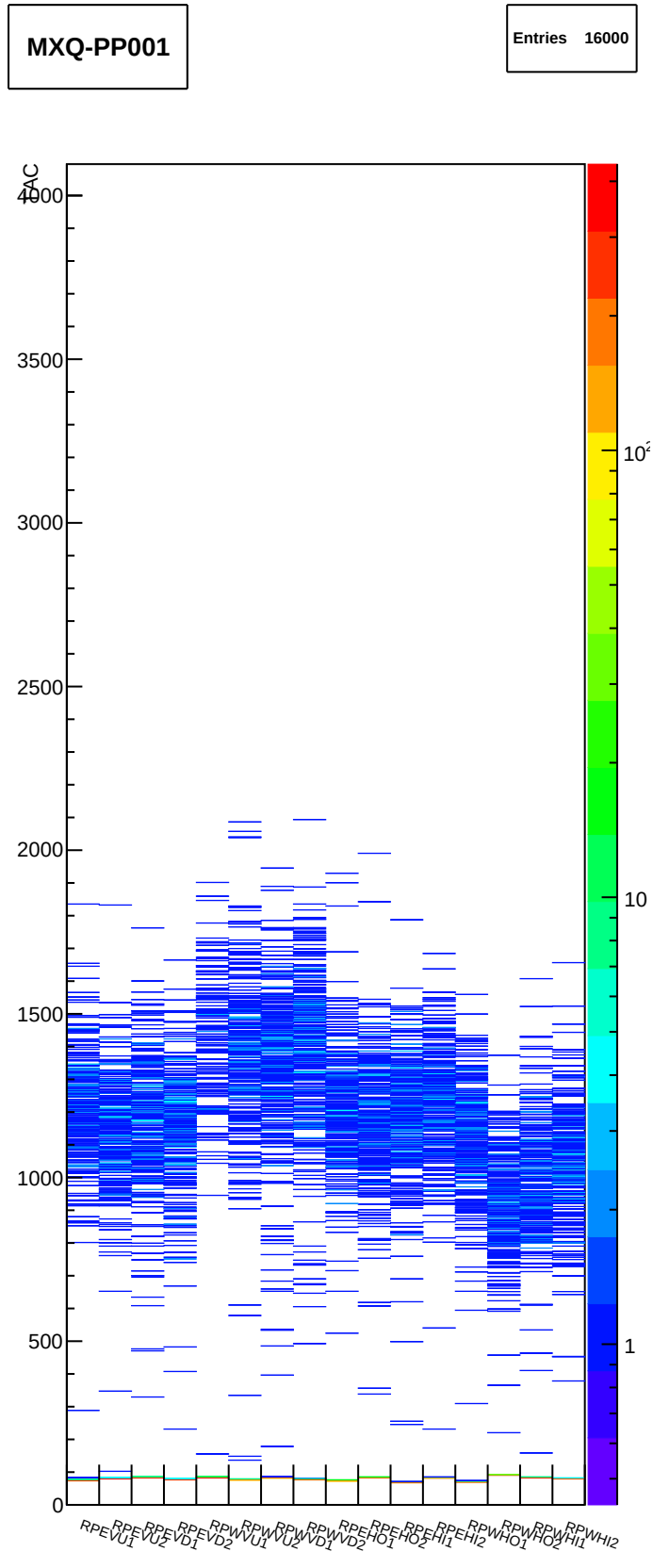

MIX-MT102 Input: MT007 MTDE+W TAC sum
Entries 2000
TAC sum
1st-Best
2nd-Best
ID

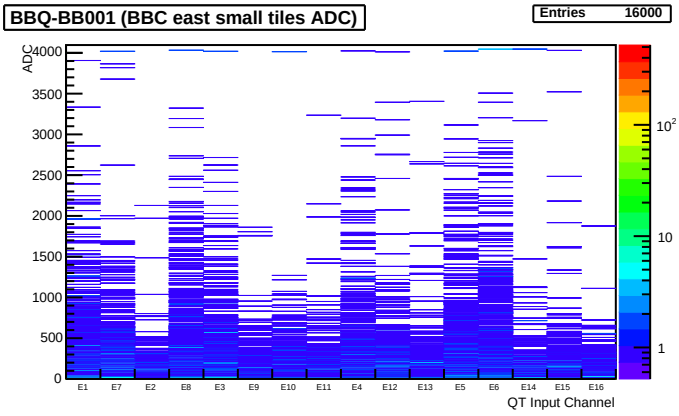

MIX-MT102 Input: MT008 MTDE+W TAC sum
Entries 2000
TAC sum
1st-Best
2nd-Best
ID

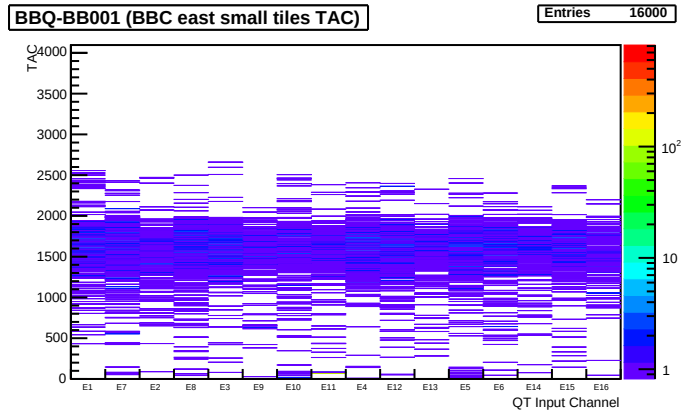

MIX-MT102 Input: VP003 Max TAC
Entries 1000

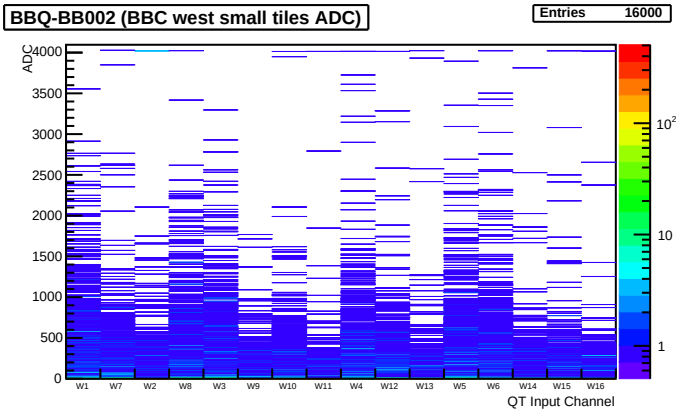

MIX-MT102 Input: VP004 Max TAC
Entries 1000

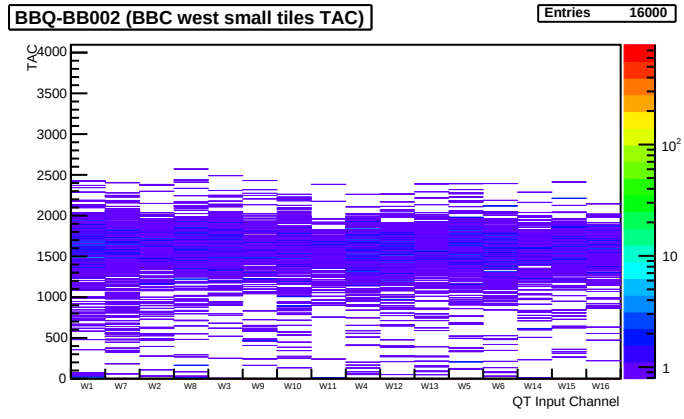

MXQ-PP001
Entries 16000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

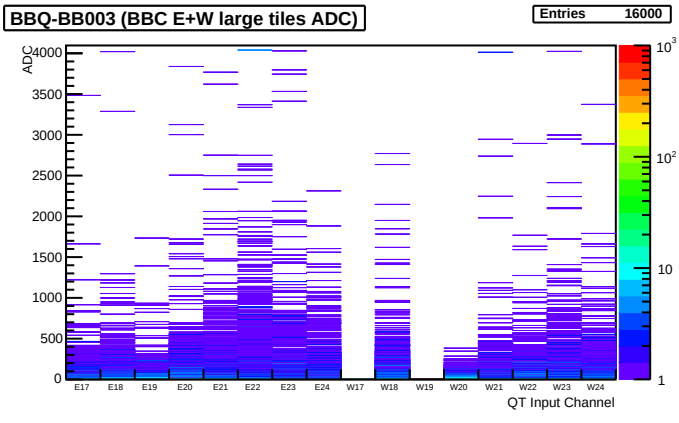

MXQ-PP001
Entries 16000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

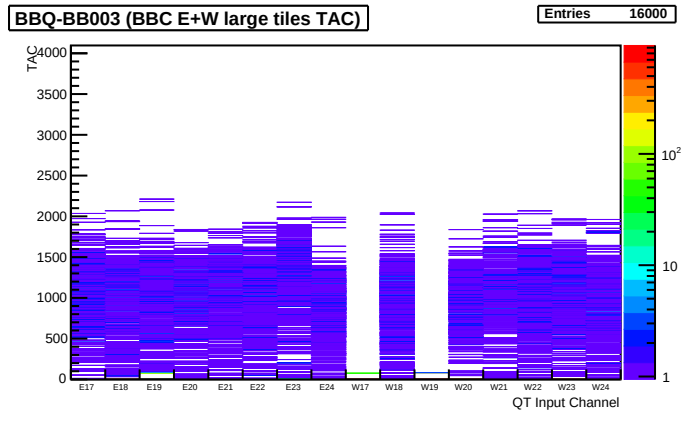

BBQ-BB001 (BBC east small tiles ADC)
Entries 16000
ADC
QT Input Channel

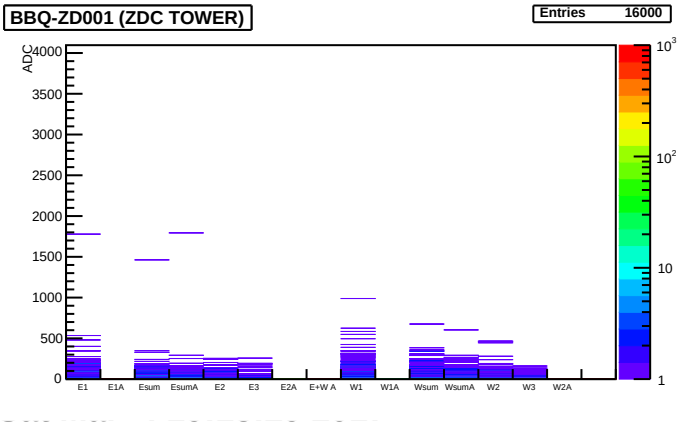

BBQ-BB001 (BBC east small tiles TAC)
Entries 16000
TAC
QT Input Channel

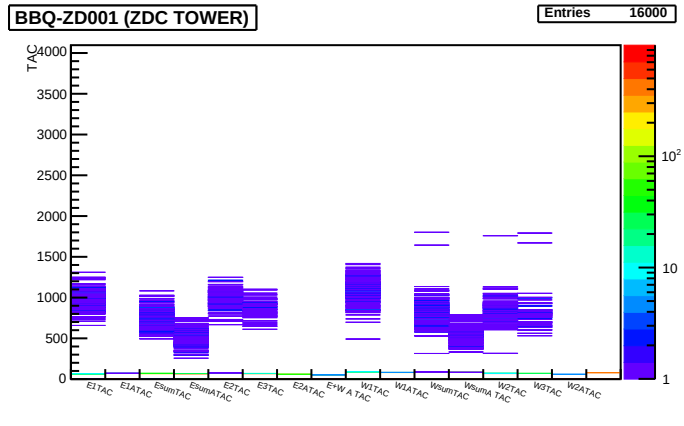

BBQ-BB002 (BBC west small tiles ADC)
Entries 16000
ADC
QT Input Channel

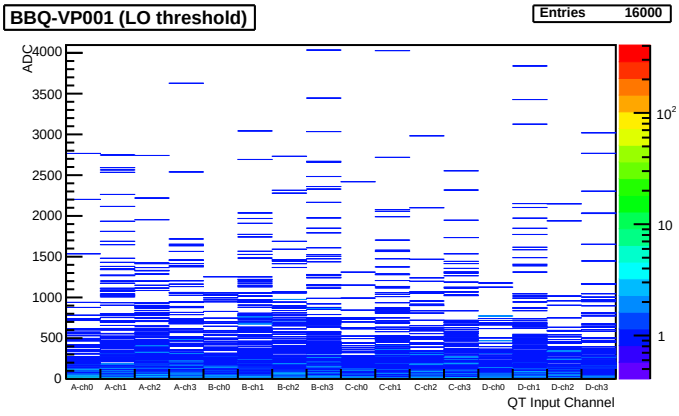

BBQ-BB002 (BBC west small tiles TAC)
Entries 16000
TAC
QT Input Channel

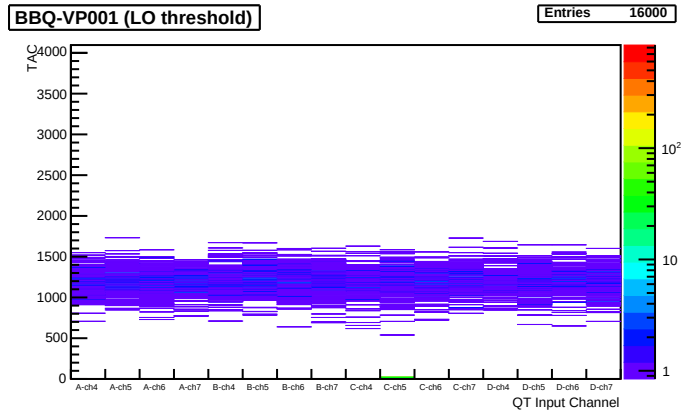

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 16000
ADC
QT Input Channel

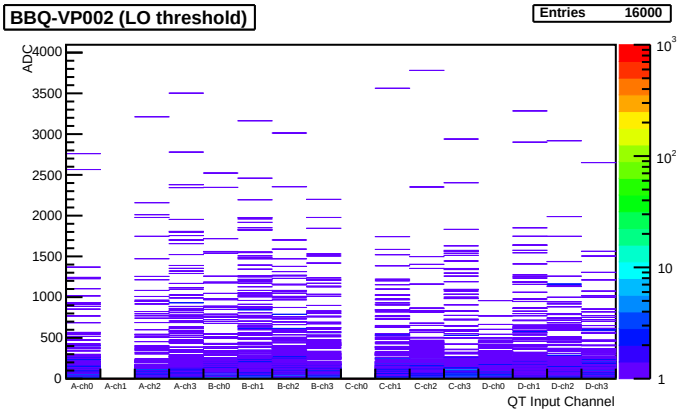

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 16000
TAC
QT Input Channel

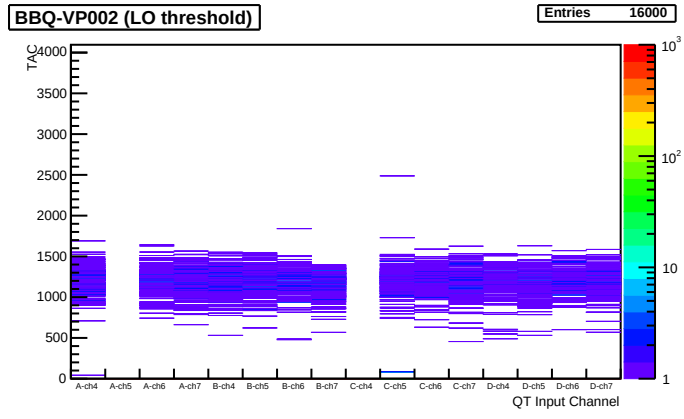

BBQ-ZD001 (ZDC TOWER)
Entries 16000
ADC

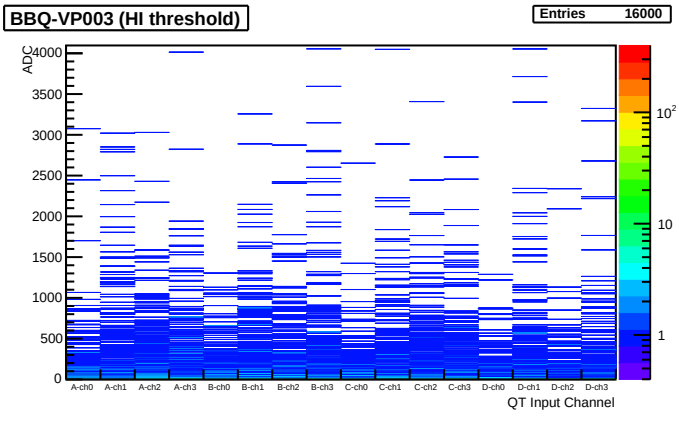

BBQ-ZD001 (ZDC TOWER)
Entries 16000
TAC

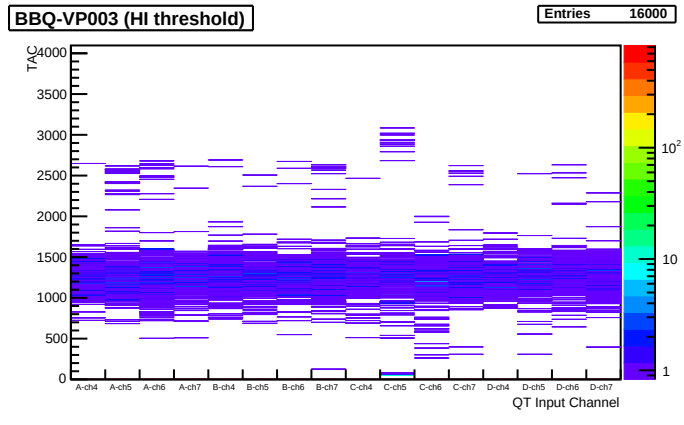

BBQ-VP001 (LO threshold)
Entries 16000
ADC
QT Input Channel

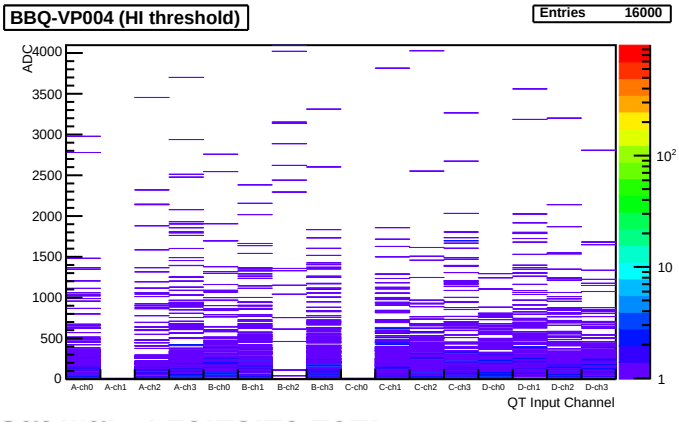

BBQ-VP001 (LO threshold)
Entries 16000
TAC
QT Input Channel

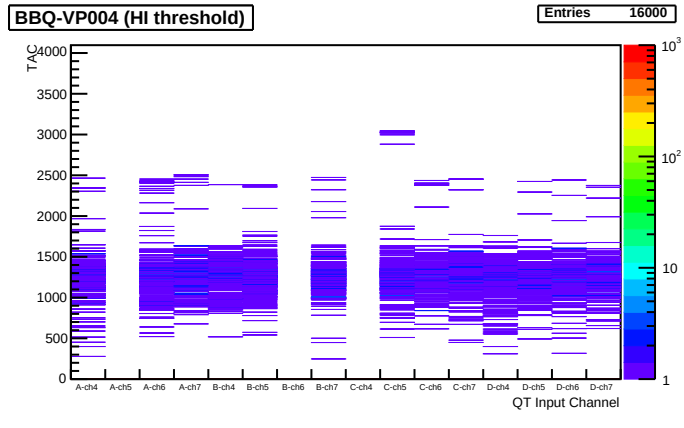

BBQ-VP002 (LO threshold)
Entries 16000
ADC
QT Input Channel

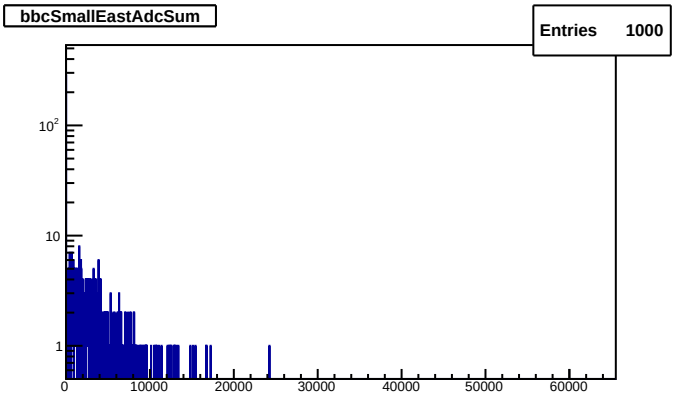

BBQ-VP002 (LO threshold)
Entries 16000
TAC
QT Input Channel

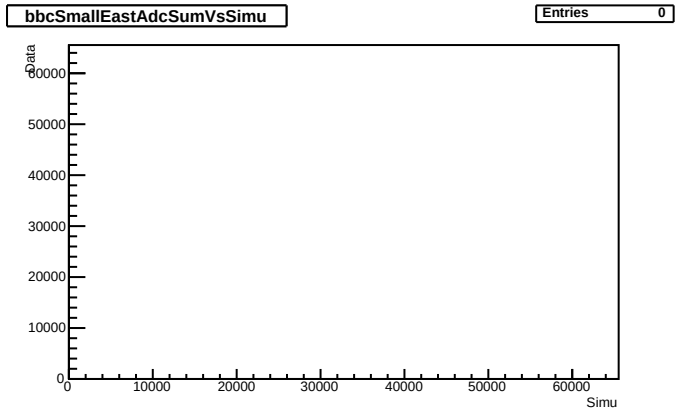

BBQ-VP003 (HI threshold)
Entries 16000
ADC
QT Input Channel

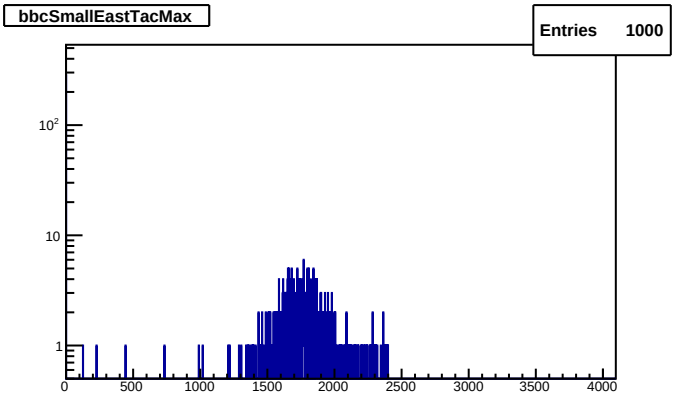

BBQ-VP003 (HI threshold)
Entries 16000
TAC
QT Input Channel

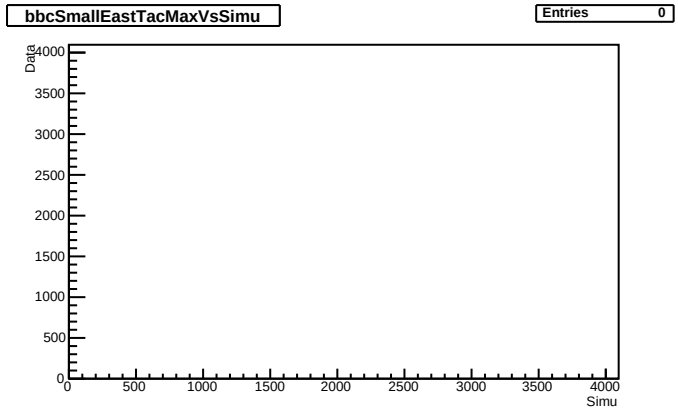

BBQ-VP004 (HI threshold)
Entries 16000
ADC
QT Input Channel

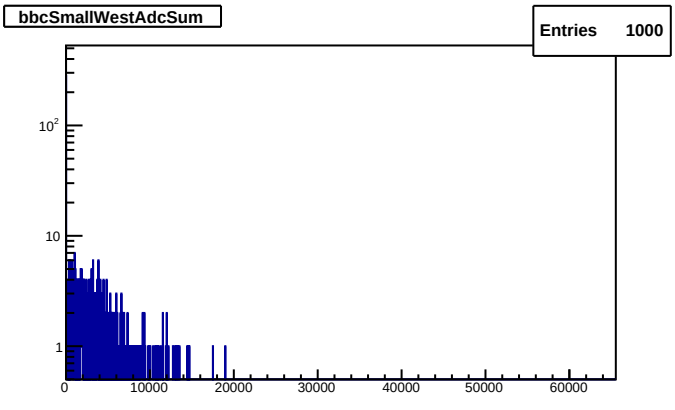

BBQ-VP004 (HI threshold)
Entries 16000
TAC
QT Input Channel

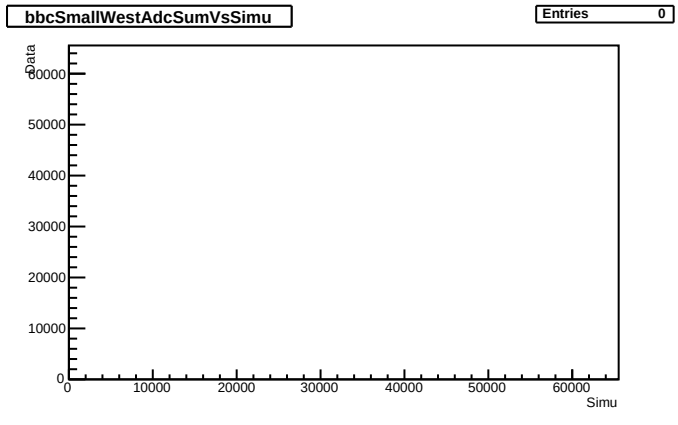

bbcSmallEastAdcSum
Entries 1000

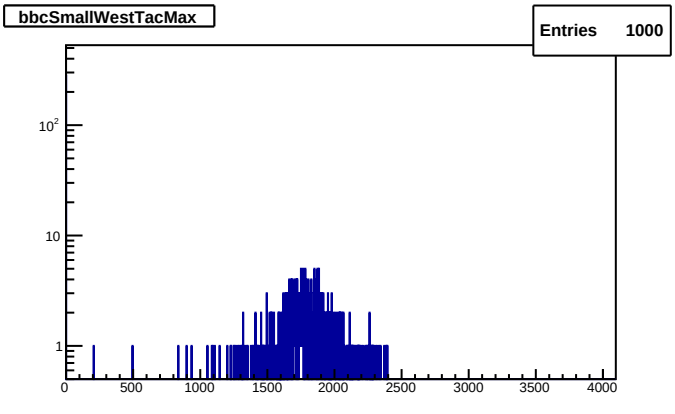

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

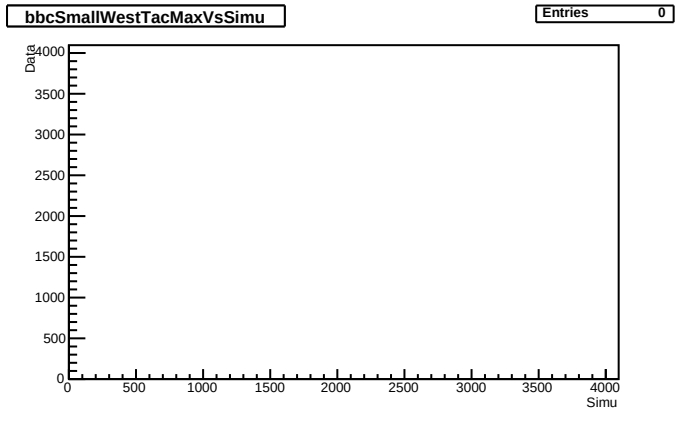

bbcSmallEastTacMax
Entries 1000

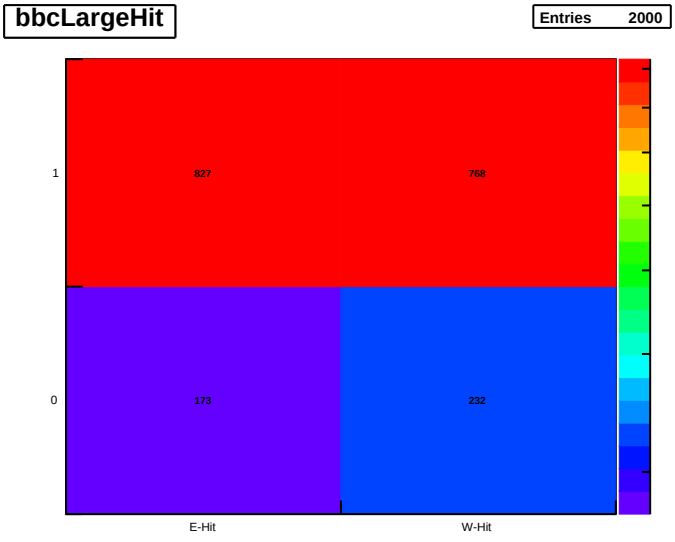

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

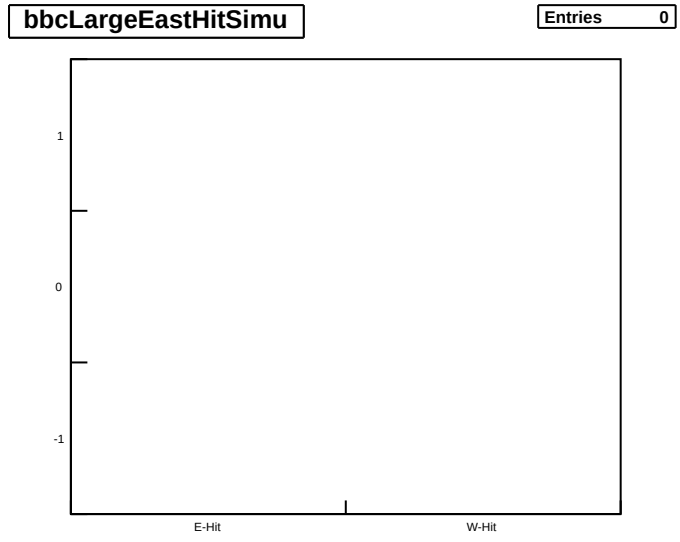

bbcSmallWestAdcSum
Entries 1000

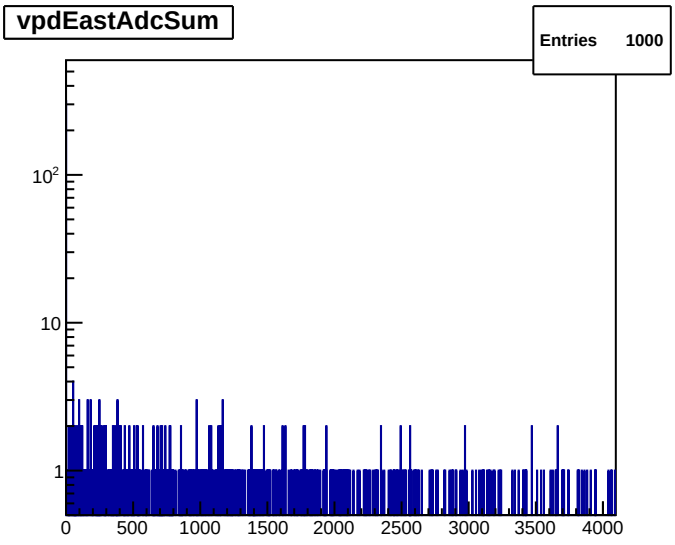

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

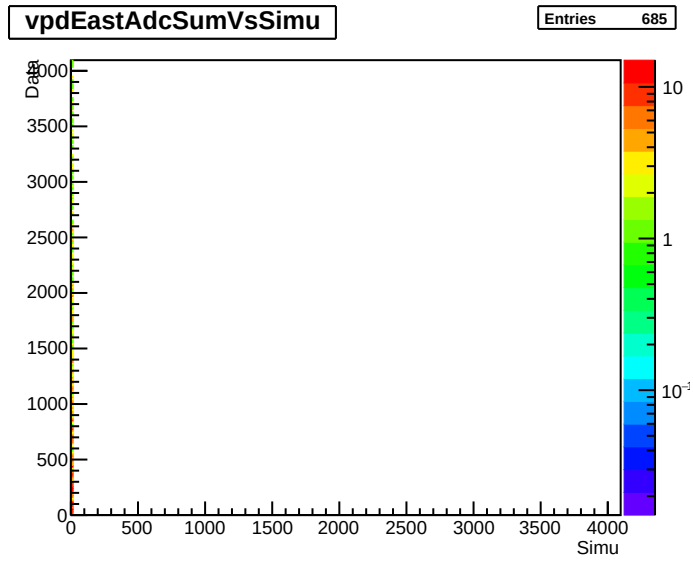

bbcSmallWestTacMax
Entries 1000

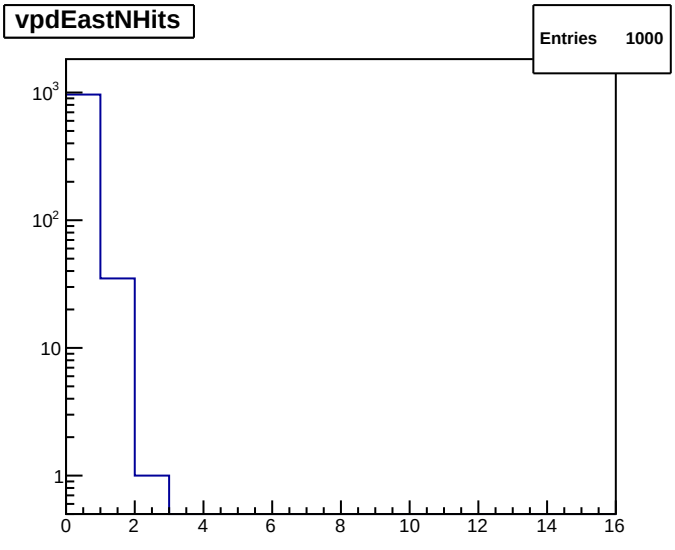

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

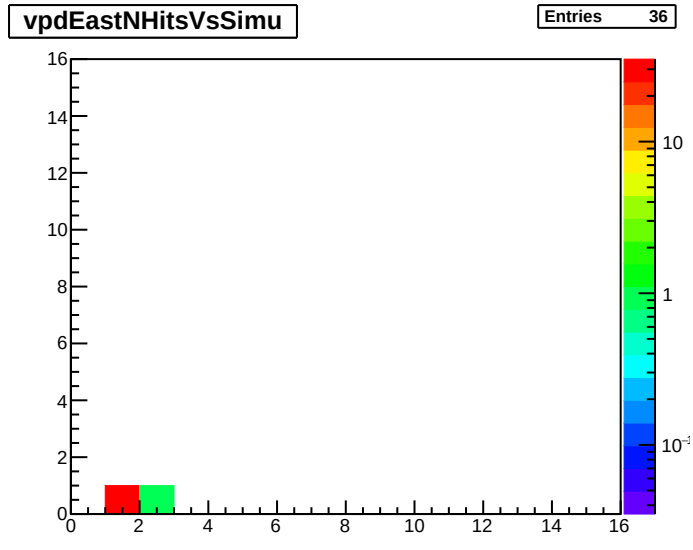

bbcLargeHit
Entries 2000
827
768
173
232
1
0
E-Hit
W-Hit

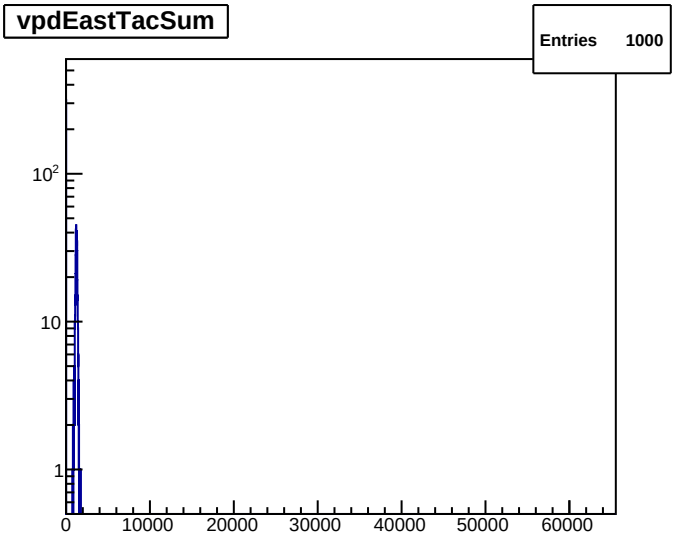

bbcLargeEastHitSimu
Entries 0
1
0
-1
E-Hit
W-Hit

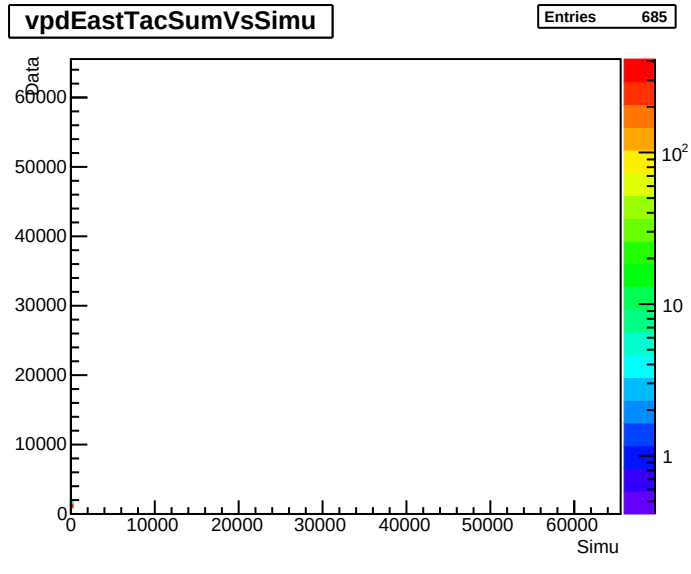

vpdEastAdcSum
Entries 1000

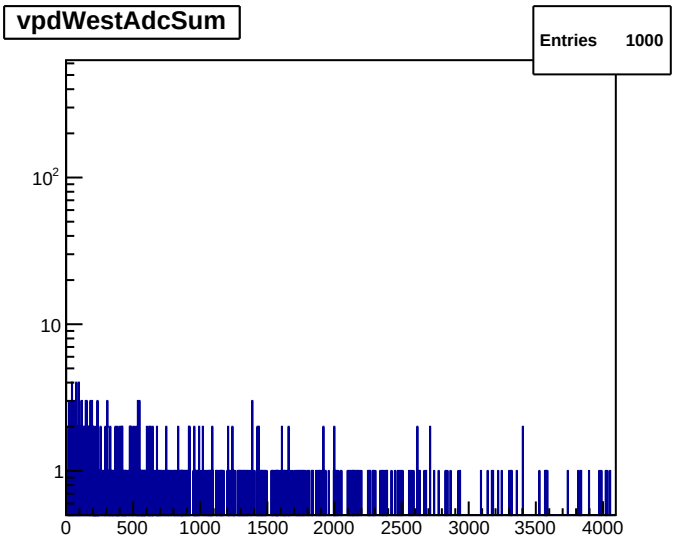

vpdEastAdcSumVsSimu
Entries 685
Data
Simu

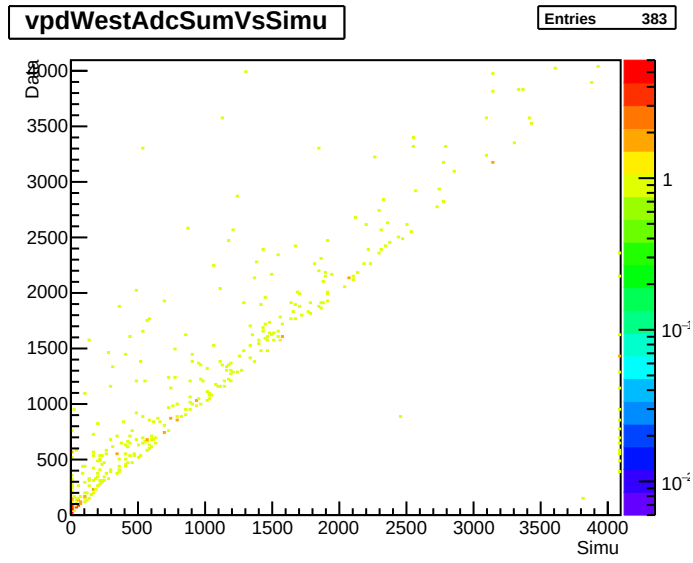

vpdEastNHits
Entries 1000

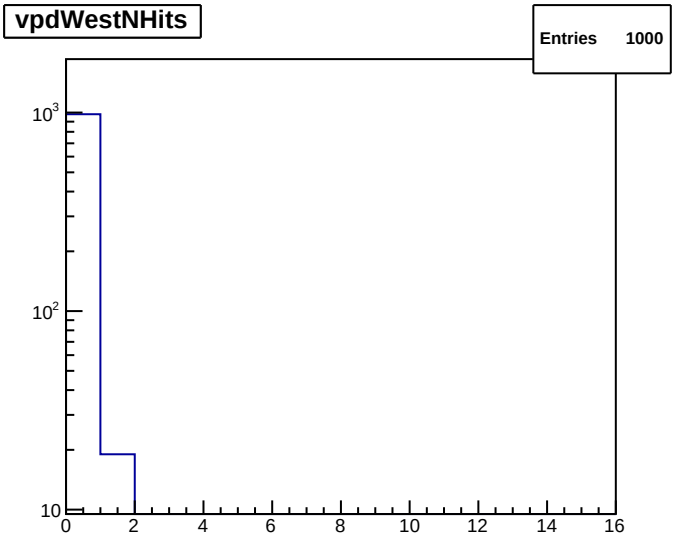

vpdEastNHitsVsSimu
Entries 36

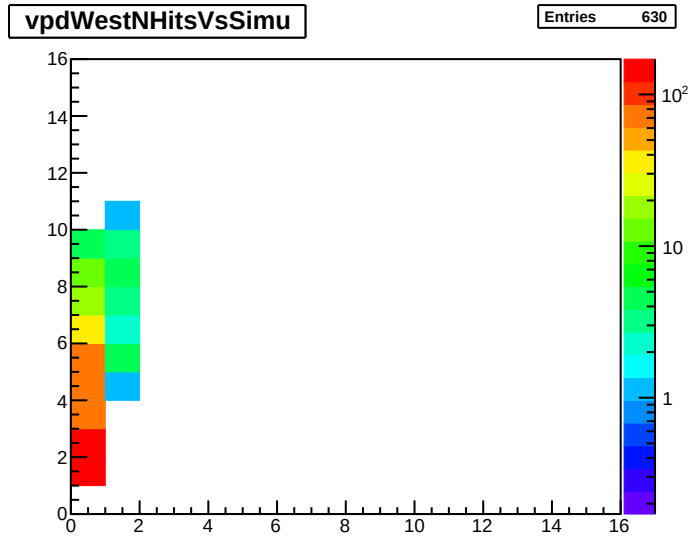

vpdEastTacSum
Entries 1000

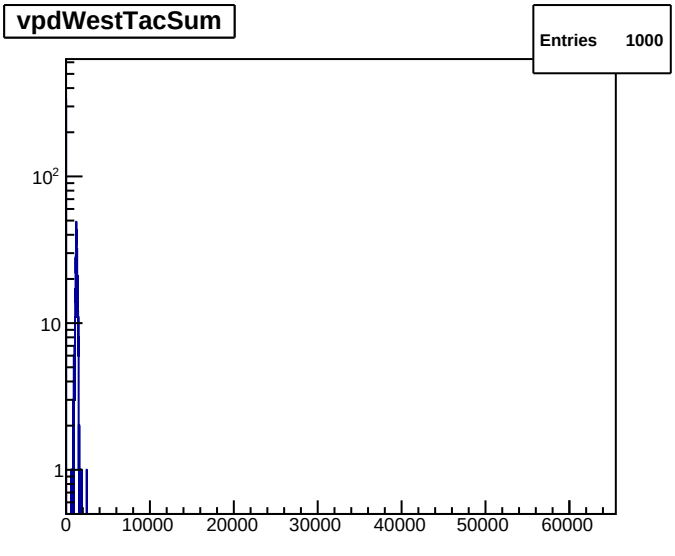

vpdEastTacSumVsSimu
Entries 685
Data
Simu

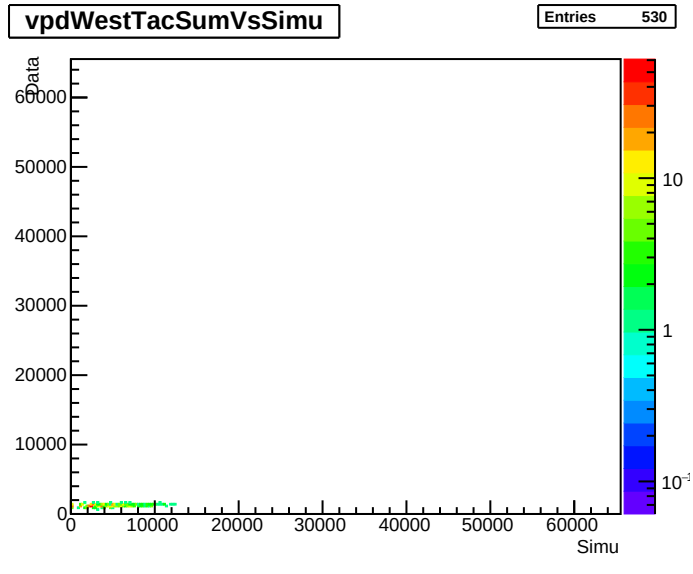

vpdWestAdcSum
Entries 1000

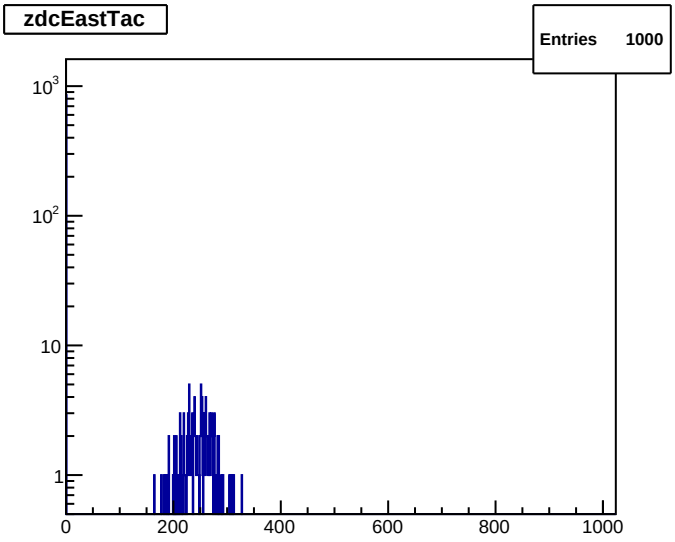

vpdWestAdcSumVsSimu
Entries 383
Data
Simu

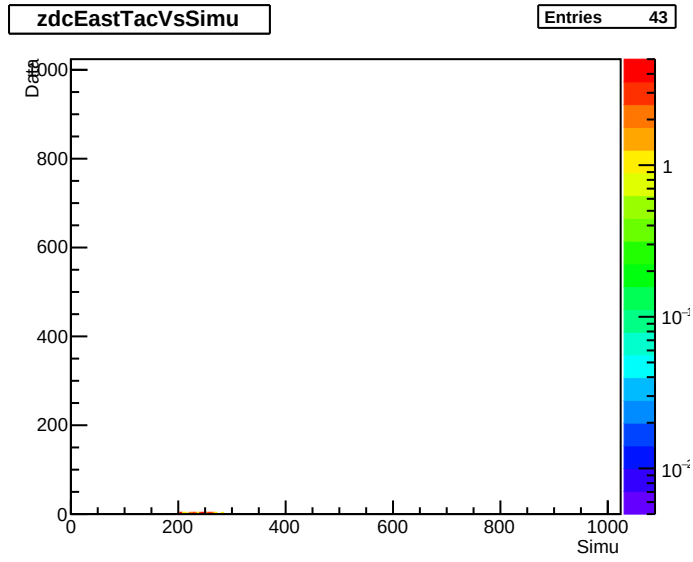

vpdWestNHits
Entries 1000

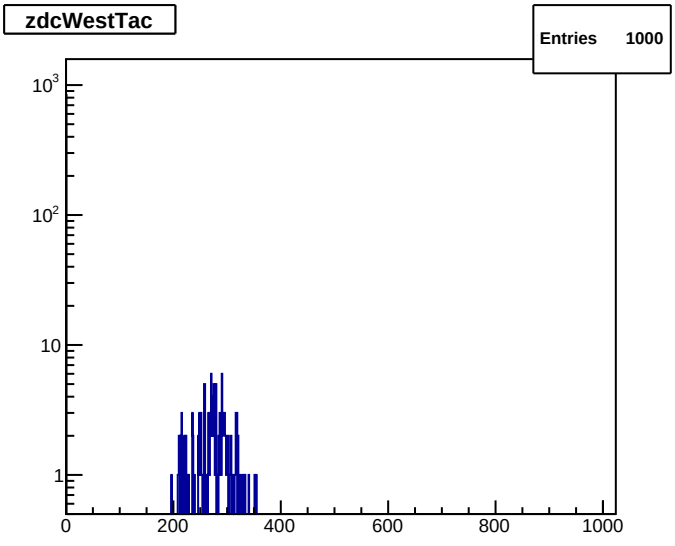

vpdWestNHitsVsSimu
Entries 630

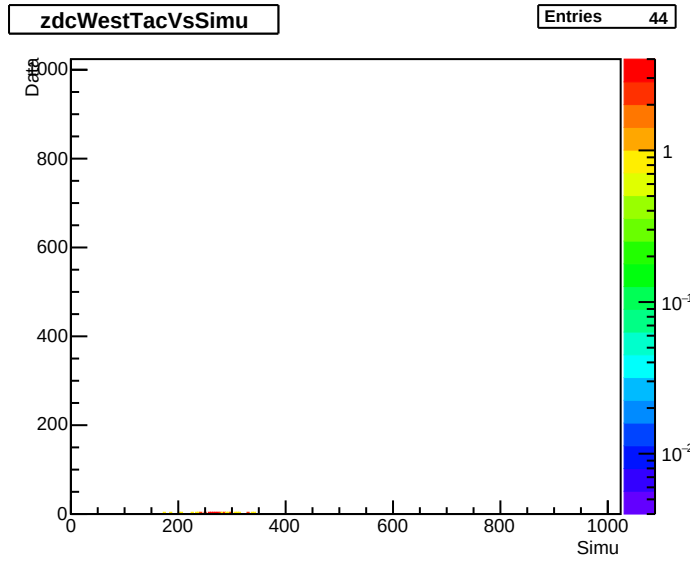

vpdWestTacSum
Entries 1000

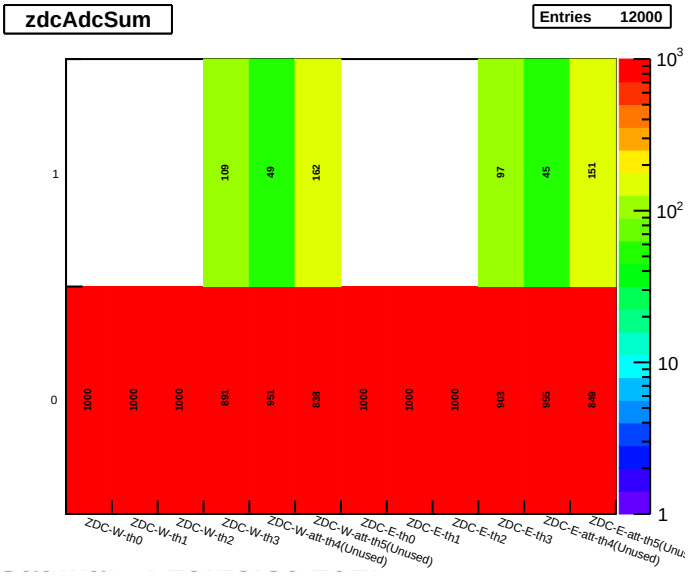

vpdWestTacSumVsSimu
Entries 530
Data
Simu

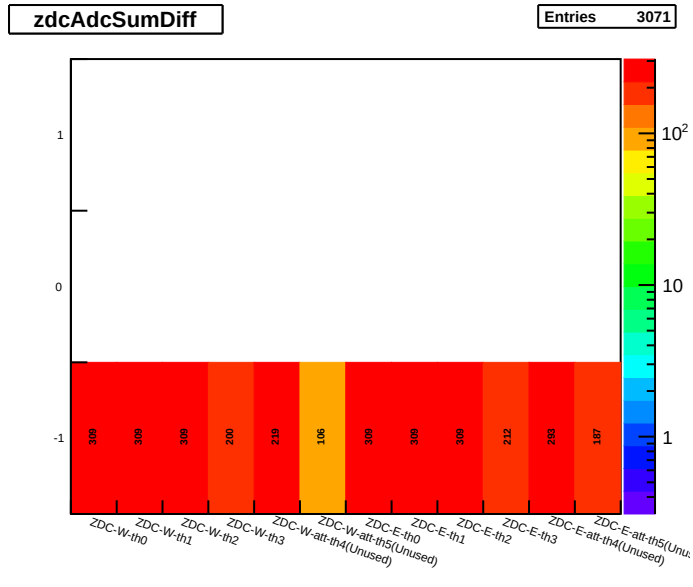

zdcEastTac
Entries 1000

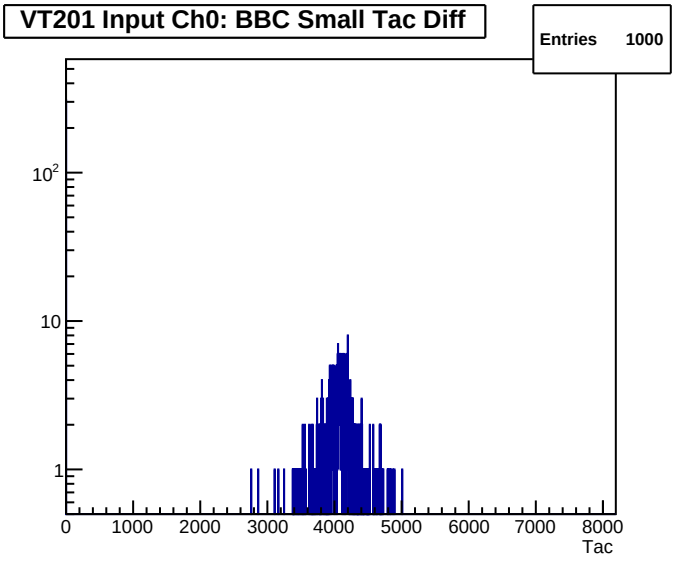

zdcEastTacVsSimu
Entries 43
Data
Simu

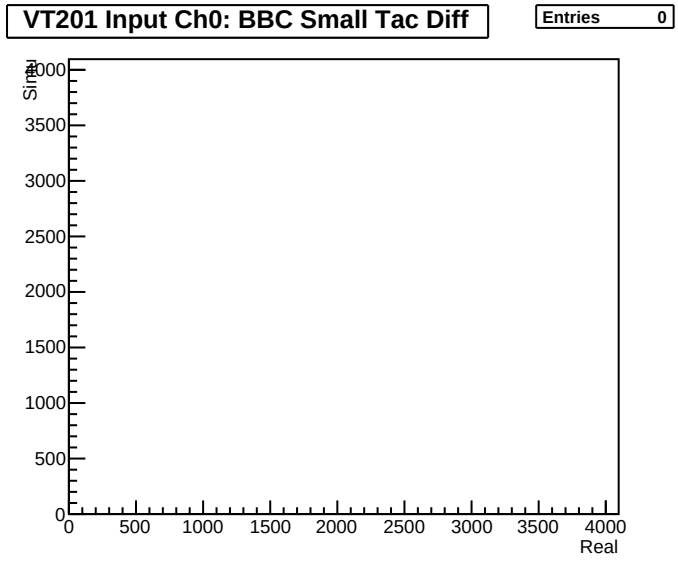

zdcWestTac
Entries 1000

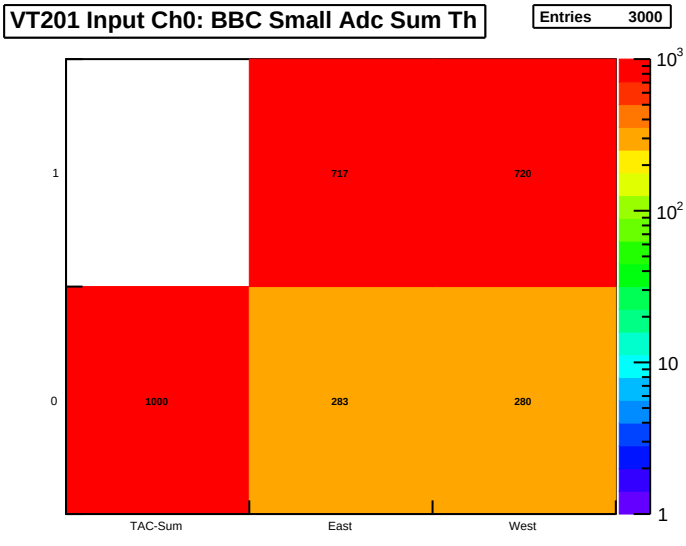

zdcWestTacVsSimu
Entries 44
Data
Simu

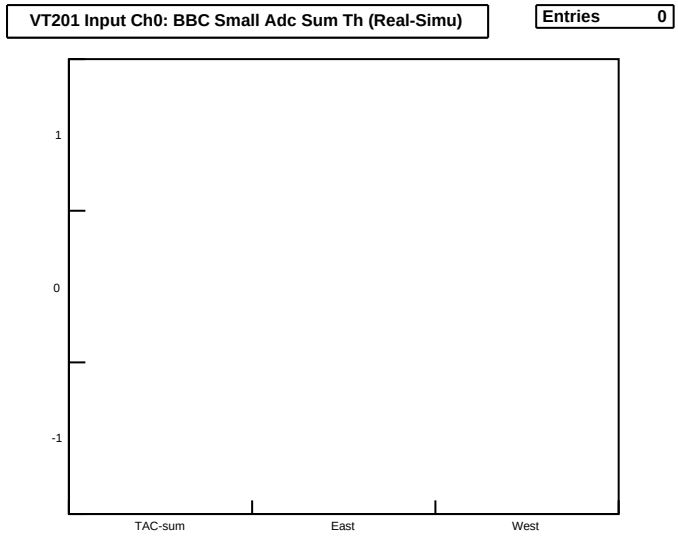

zdcAdcSum
Entries 12000
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

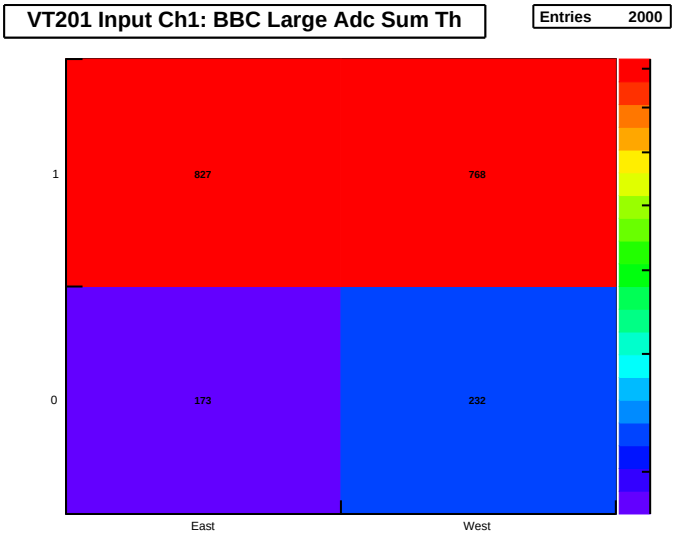

zdcAdcSumDiff
Entries 3071
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)
ZDC-E-att-th5(Unu

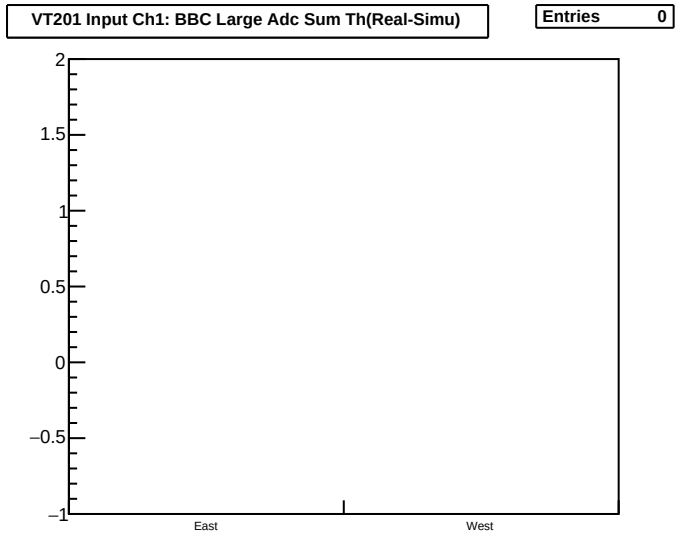

VT201 Input Ch0: BBC Small Tac Diff
Entries 1000
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

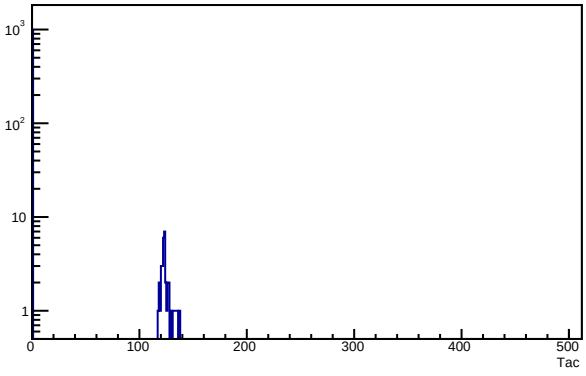

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 3000
717
720
1000
283
280
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

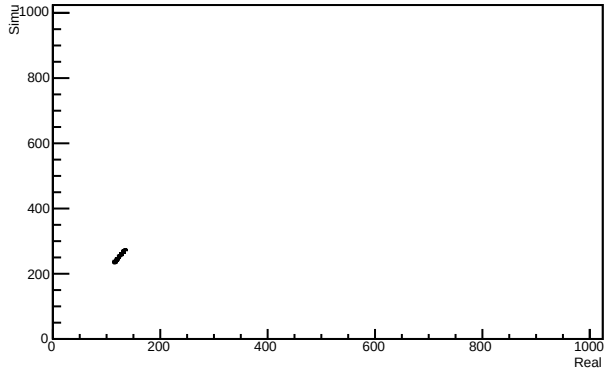

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 2000
827
768
173
232
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

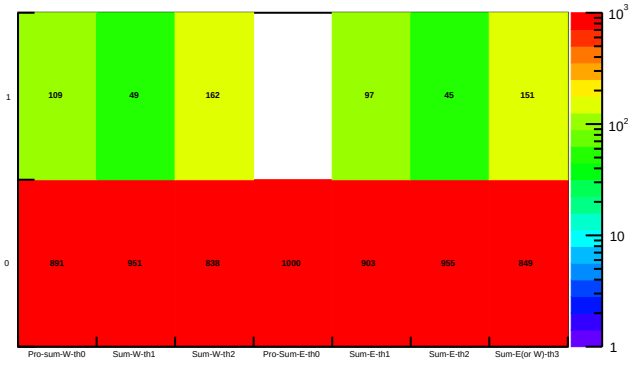
VT201 Input Ch2: ZDC Tac Diff



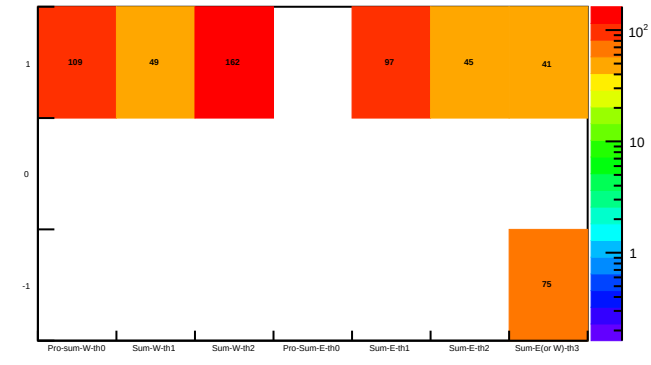
VT201 Input Ch2: ZDC Tac Diff



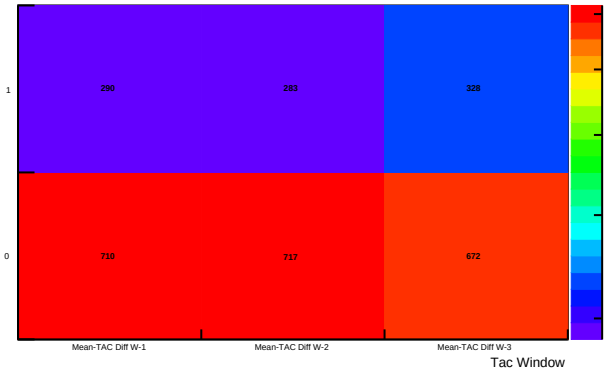
VT201 Input Ch2: ZDC Adc Sum Th



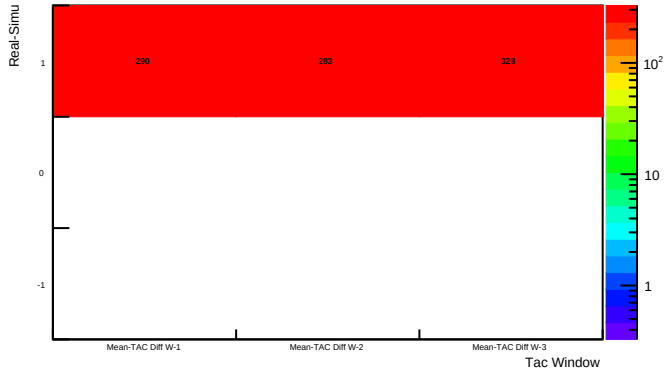
VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)



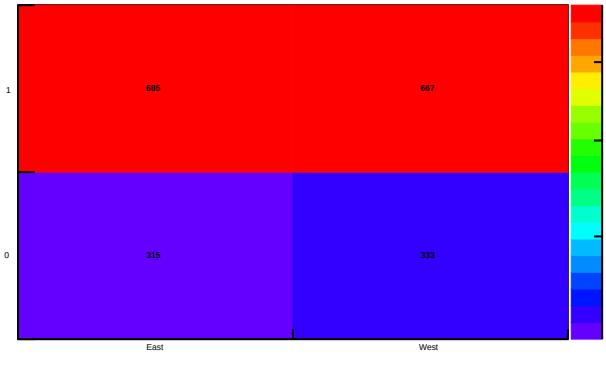
VT201 Input Ch4: VPD Tac Diff



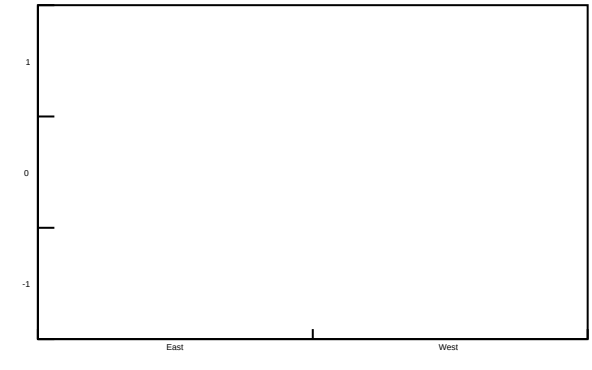
VT201 Input Ch4: VPD Tac Diff

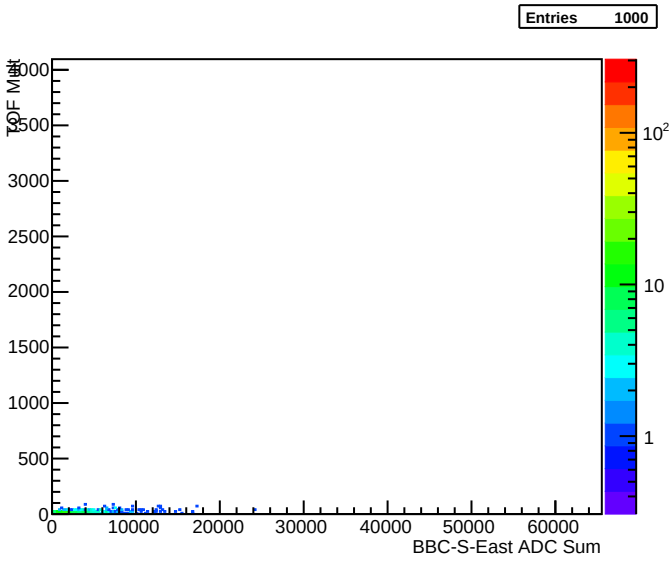


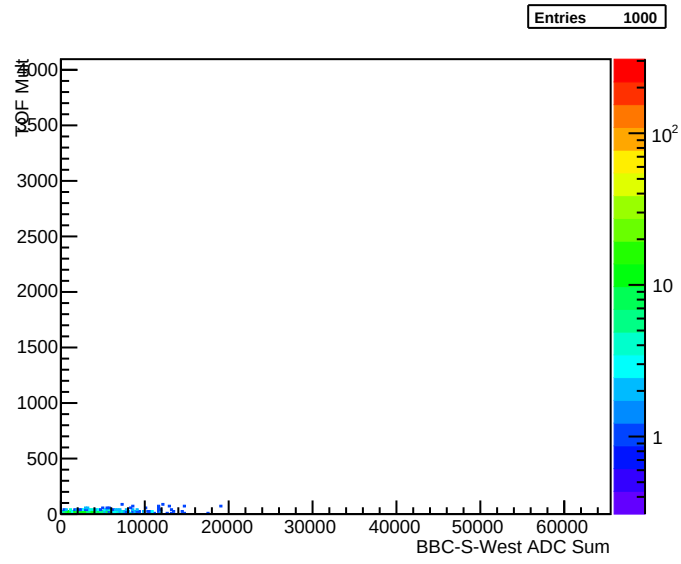
VT201 Input Ch4: VPD Adc Sum Th

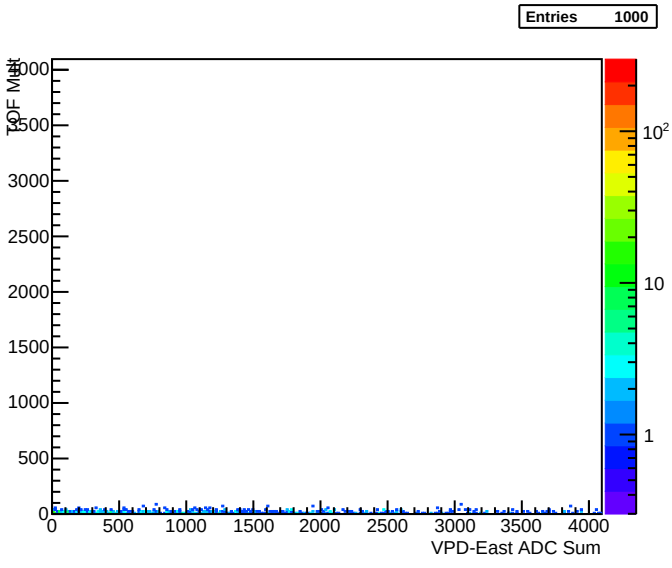


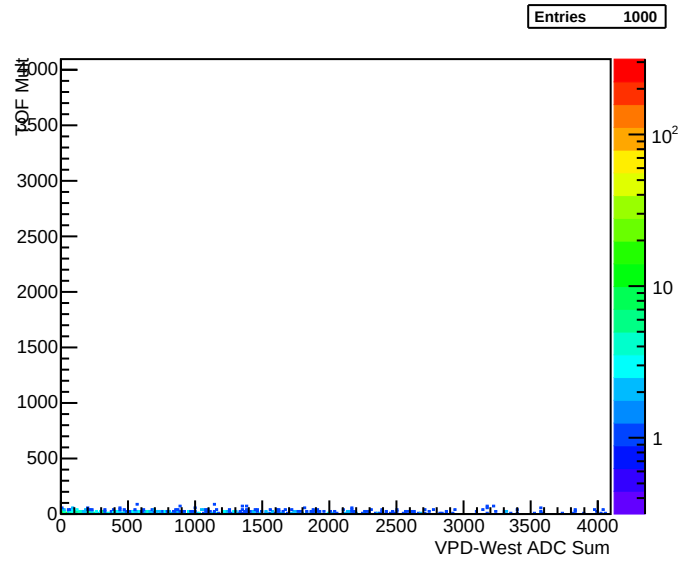
VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)

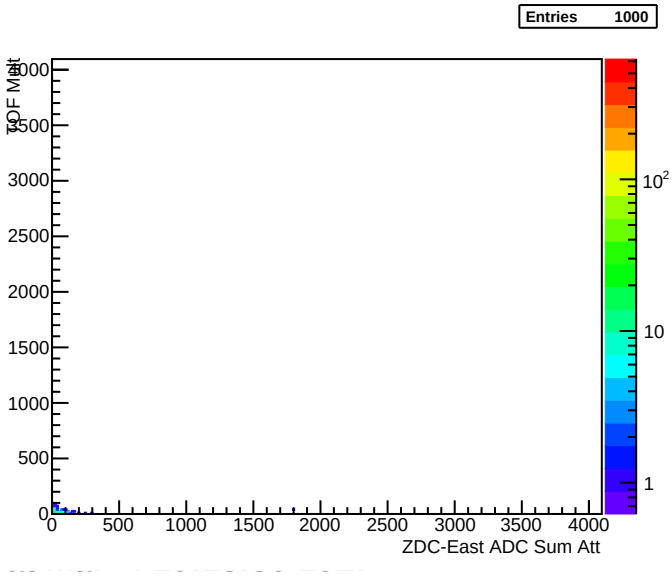


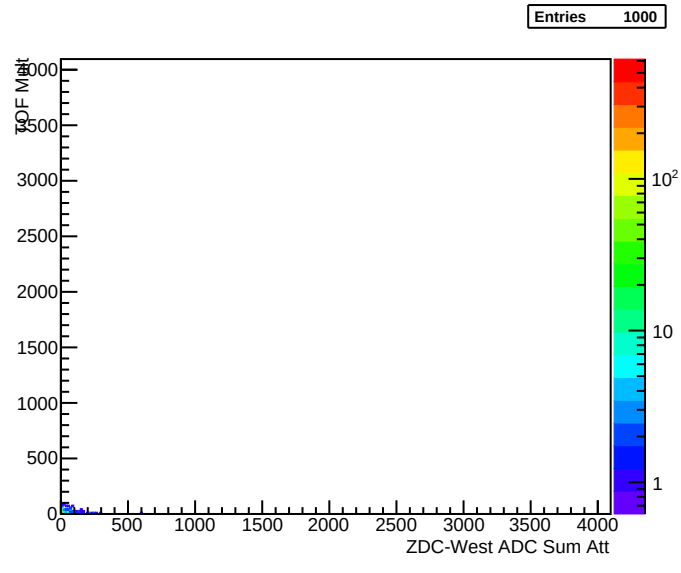

Entries 1000
TOF Mult
BBC-S-East ADC Sum

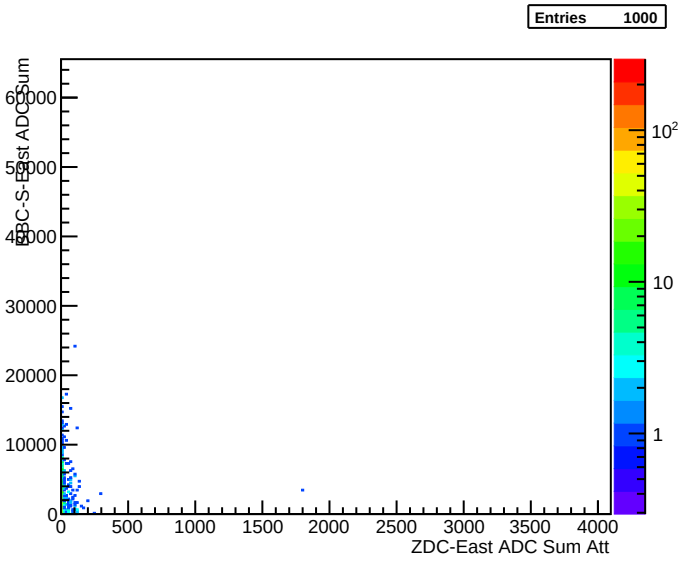

Entries 1000
TOF Mult
BBC-S-West ADC Sum

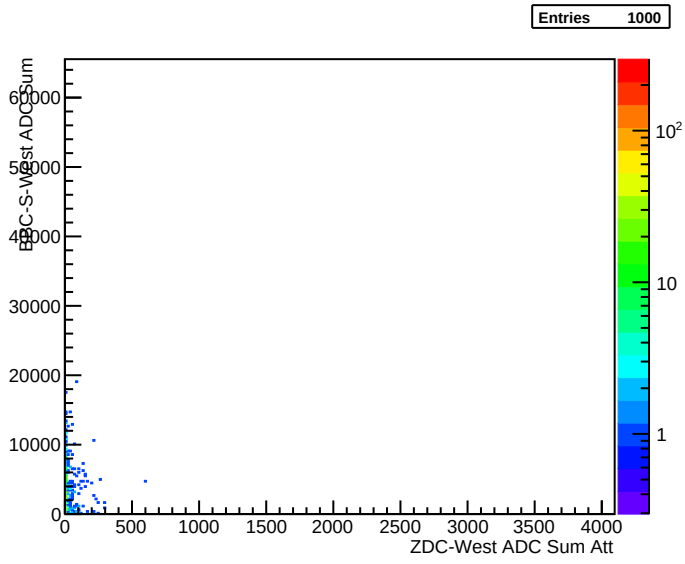

Entries 1000
TOF Mult
VPD-East ADC Sum

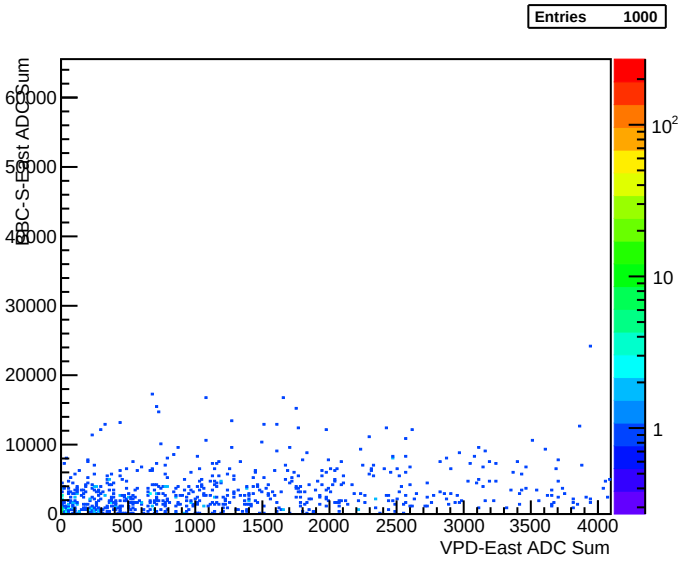

Entries 1000
TOF Mult
VPD-West ADC Sum

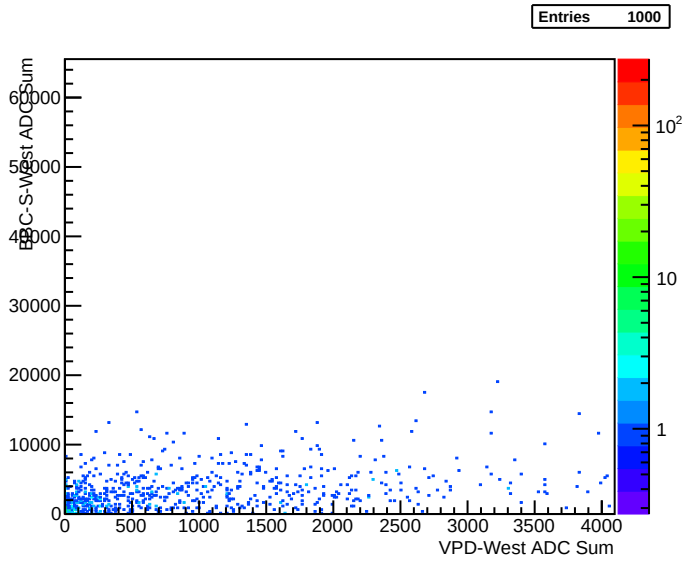

Entries 1000
TOF Mult
ZDC-East ADC Sum Att

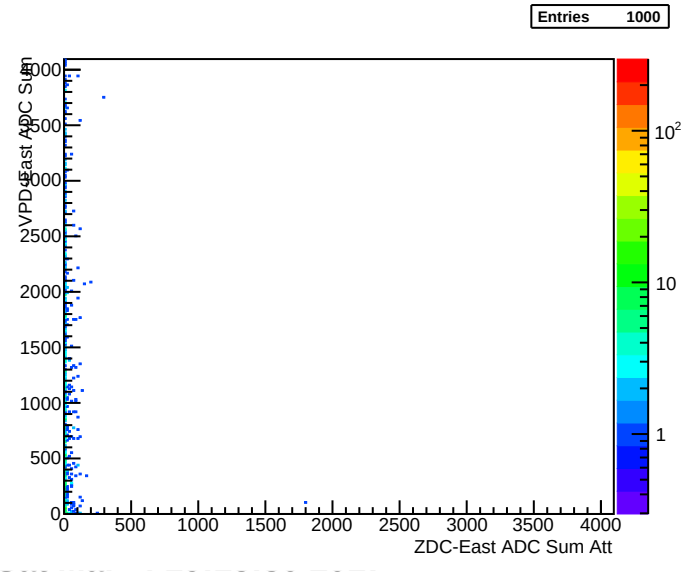

Entries 1000
TOF Mult
ZDC-West ADC Sum Att

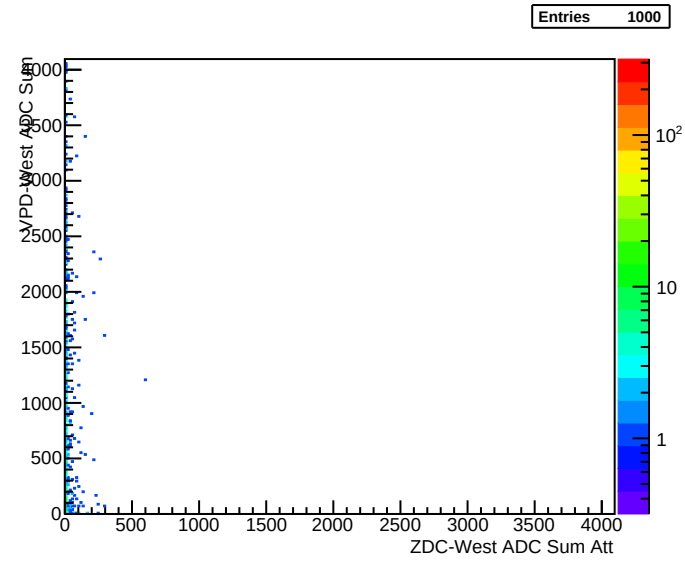

Entries 1000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

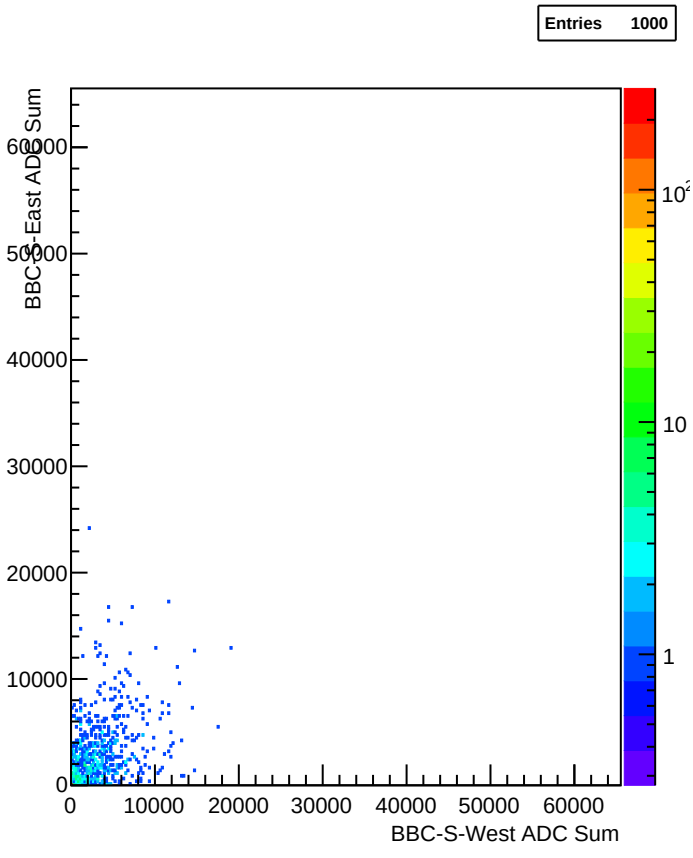

Entries 1000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

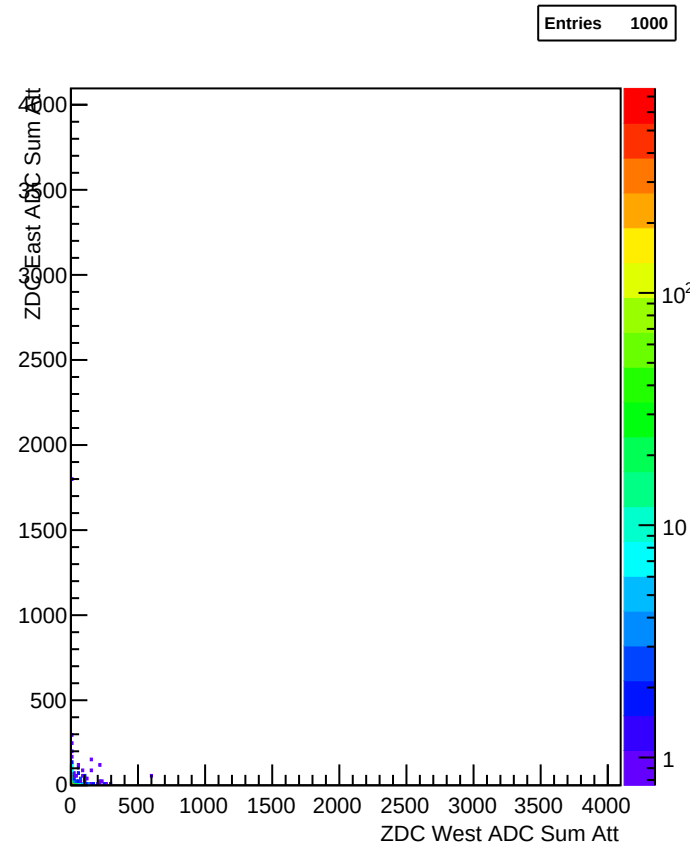

Entries 1000
BBC-S-East ADC Sum
VPD-East ADC Sum

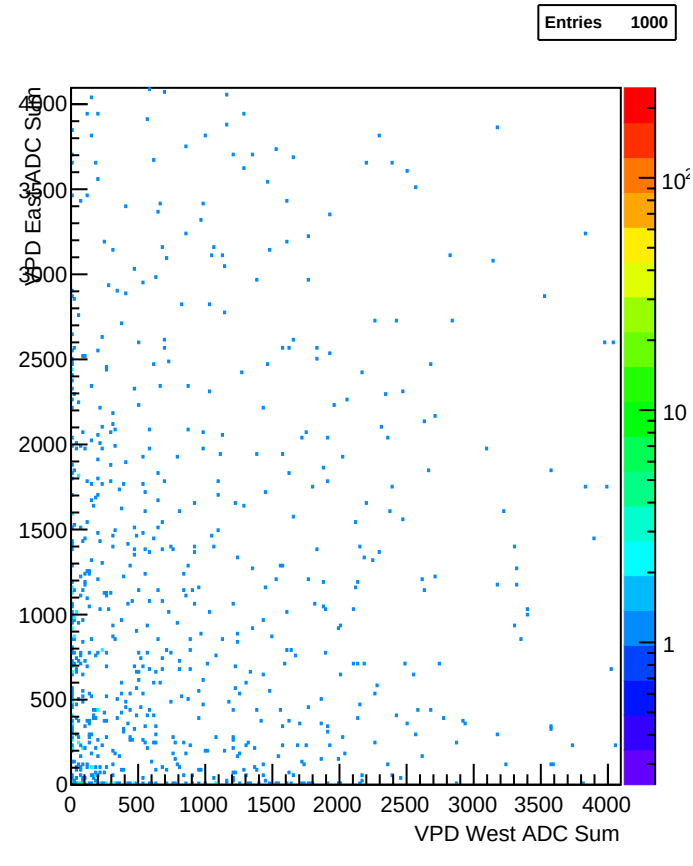

Entries 1000
BBC-S-West ADC Sum
VPD-West ADC Sum

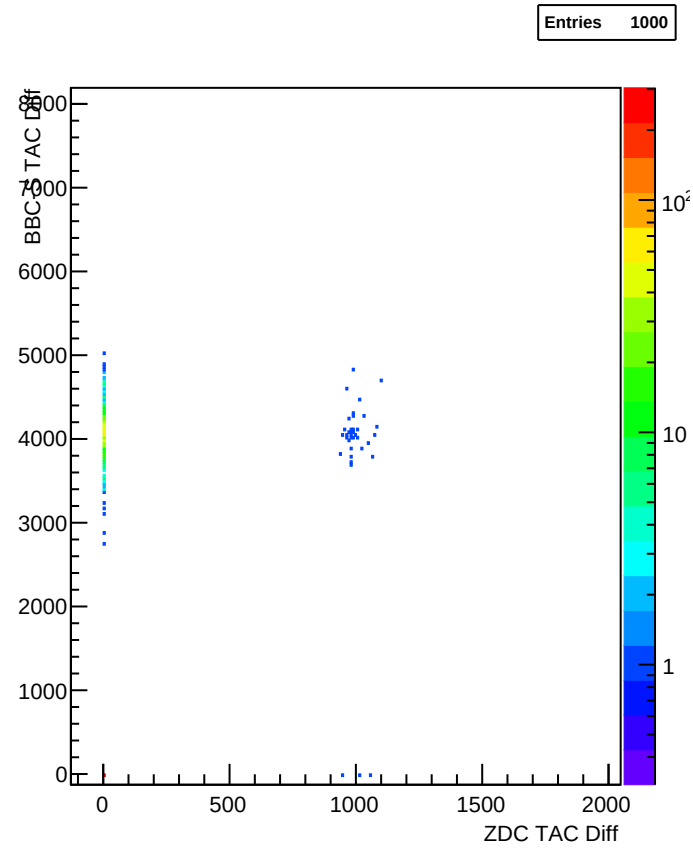

Entries 1000
VPD-East ADC Sum
ZDC-East ADC Sum Att

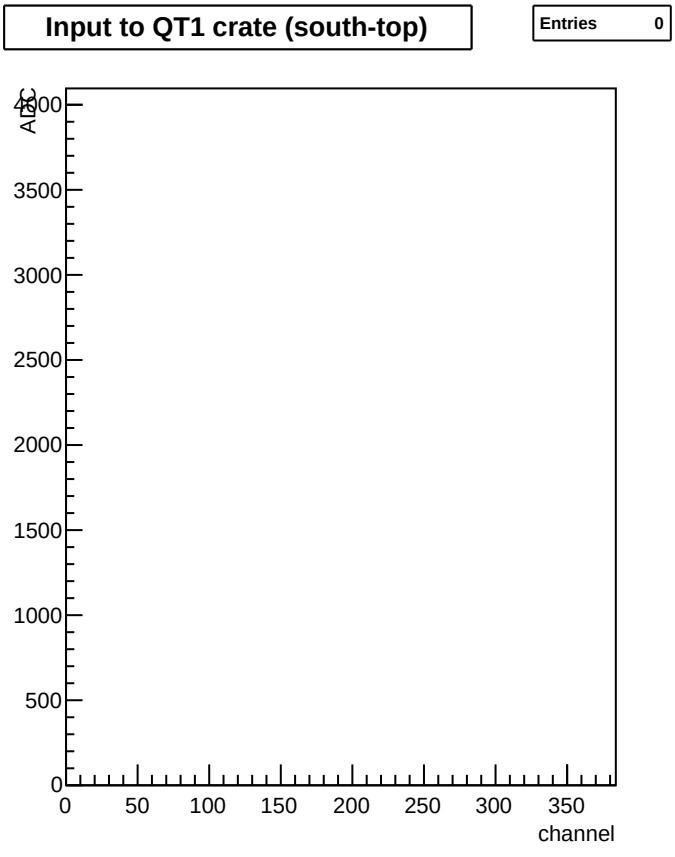

Entries 1000
VPD-West ADC Sum
ZDC-West ADC Sum Att

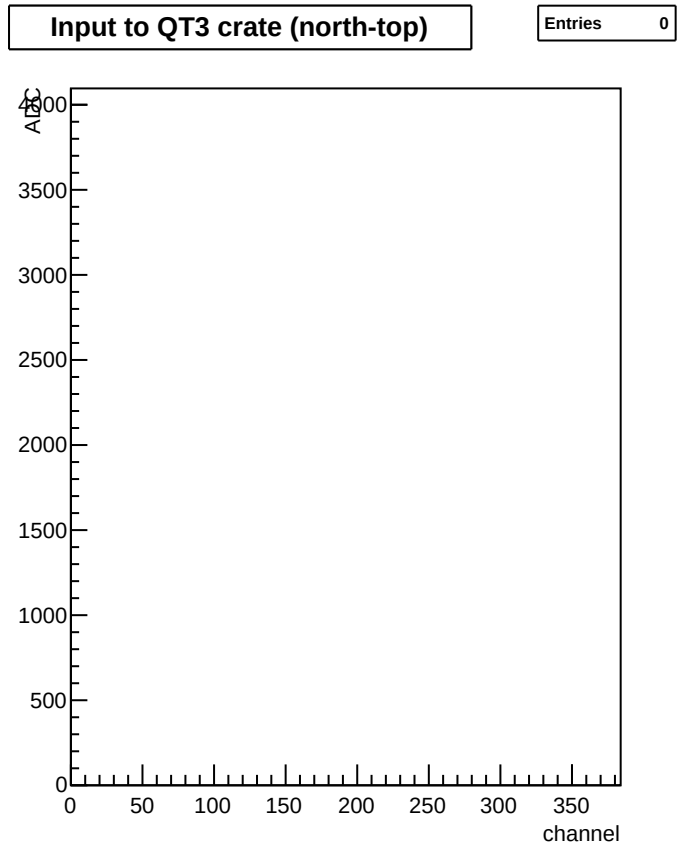

Entries 1000
BBC-S-East ADC Sum
BBC-S-West ADC Sum
0
10000
20000
30000
40000
50000
60000
1
10
10²

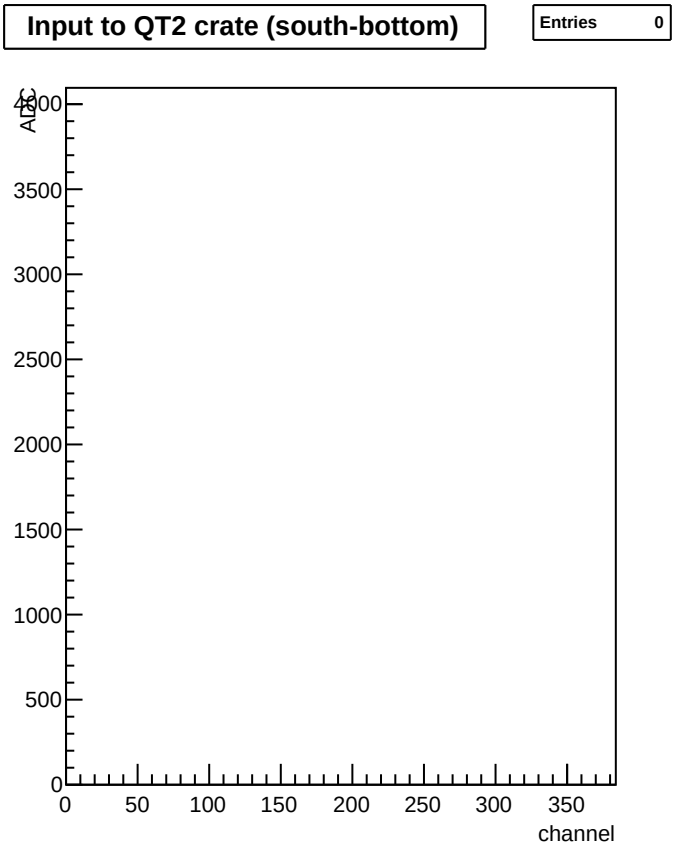

Entries 1000
ZDC East ADC Sum Att
ZDC West ADC Sum Att
0
500
1000
1500
2000
2500
3000
3500
4000
1
10
10²

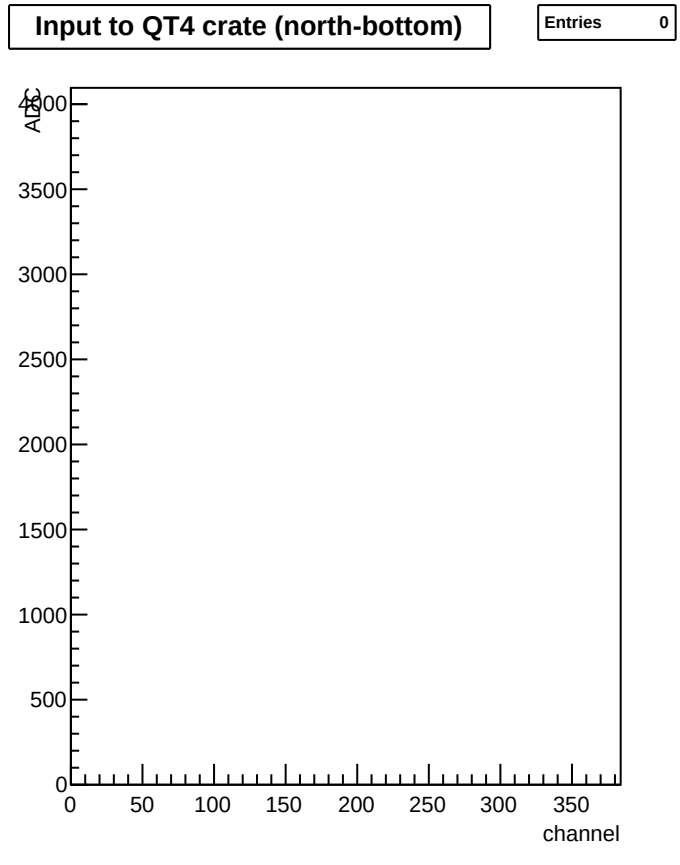

Entries 1000
VPD East ADC Sum
VPD West ADC Sum
0
500
1000
1500
2000
2500
3000
3500
4000
1
10
10²

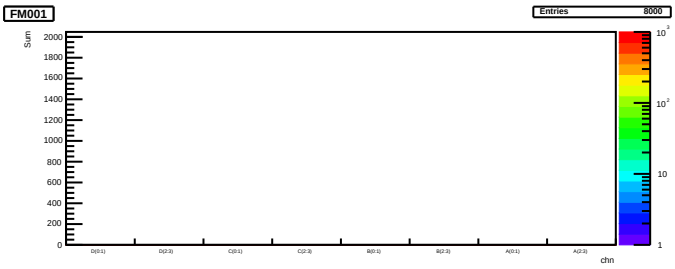

Entries 1000
BBC-S TAC Diff
ZDC TAC Diff
0
500
1000
1500
2000
3000
4000
5000
6000
7000
8000
1
10
10²

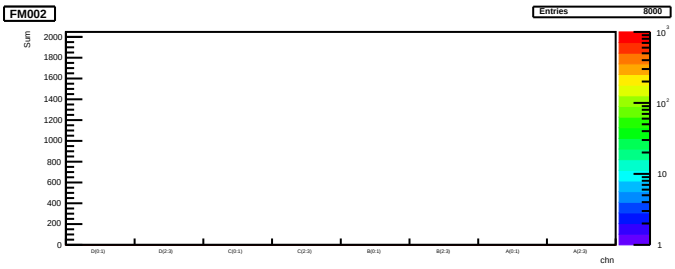

Input to QT1 crate (south-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

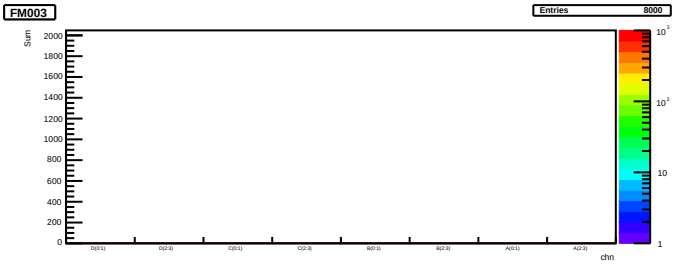

Input to QT3 crate (north-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

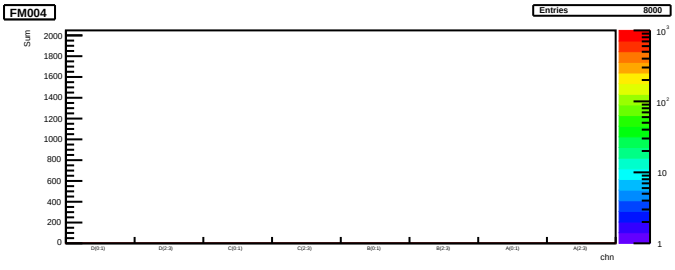

Input to QT2 crate (south-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

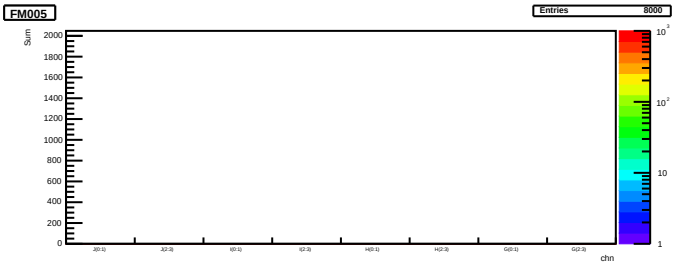

Input to QT4 crate (north-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

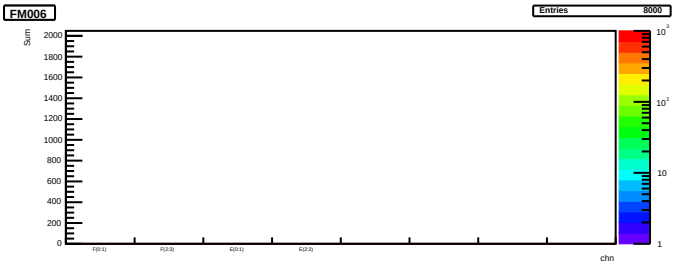

FM001
Entries 8000
Sum
chn

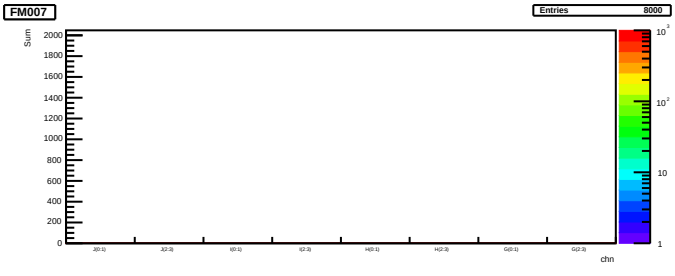

FM002
Entries 8000
Sum
chn

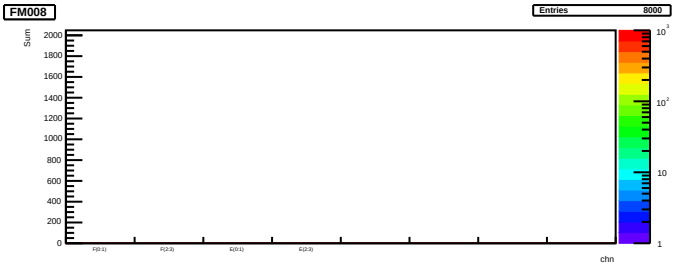

FM003
Entries 8000
Sum
chn

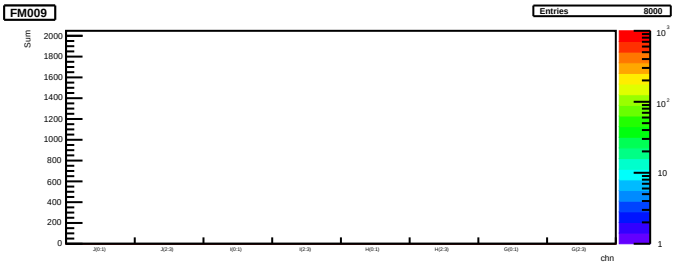

FM004
Entries 8000
Sum
chn

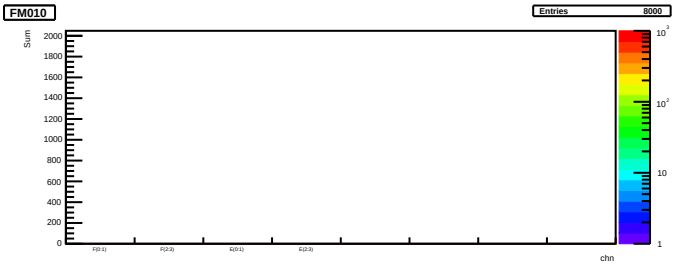

FM005
Entries 8000
Sum
chn

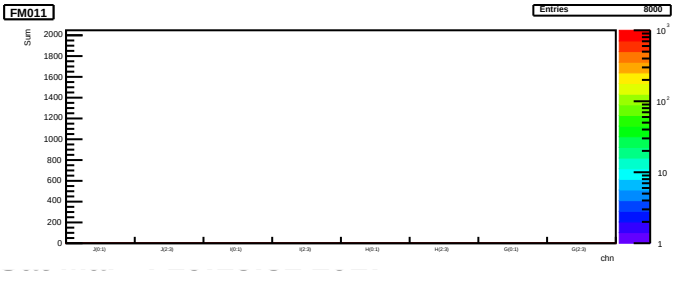

FM006
Entries 8000
Sum
chn

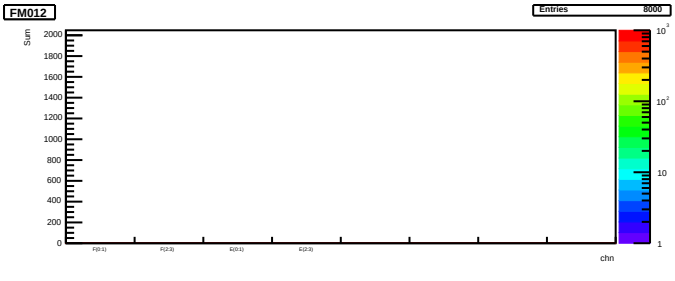

FM007
Entries 8000
Sum
chn

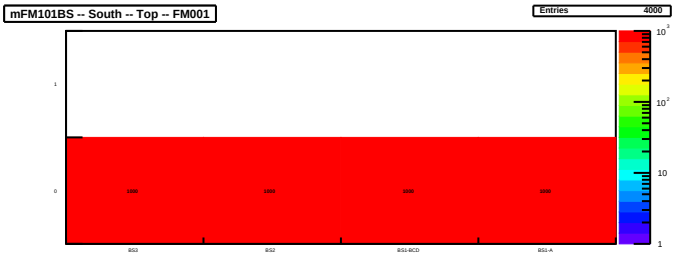

FM008
Entries 8000
Sum
chn

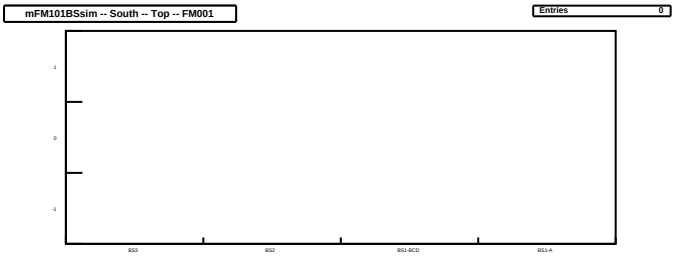

FM009
Entries 8000
Sum
chn

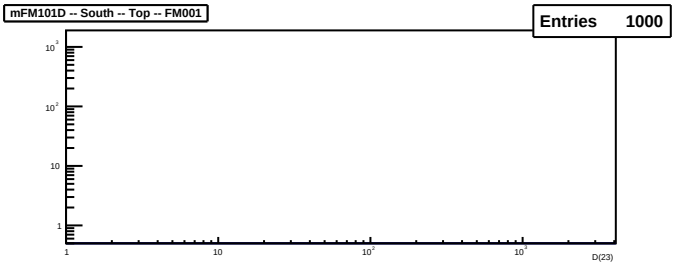

FM010
Entries 8000
Sum
chn

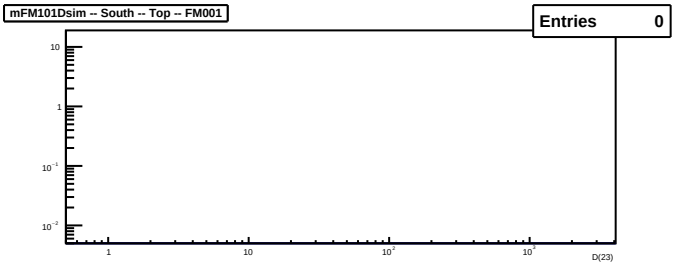

FM011
Entries 8000
Sum
chn

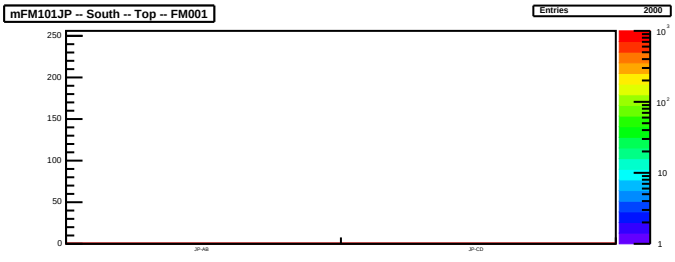

FM012
Entries 8000
Sum
chn


mFM101BS -- South -- Top -- FM001
Entries 4000
1000
1000
1000
1000
BS3
BS2
BS1-BCD
BS1-A

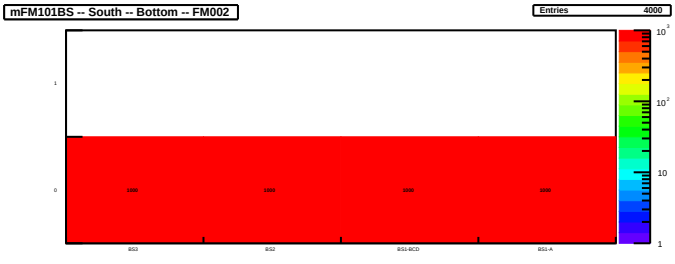

mFM101BSsim -- South -- Top -- FM001
Entries 0
BS3
BS2
BS1-BCD
BS1-A

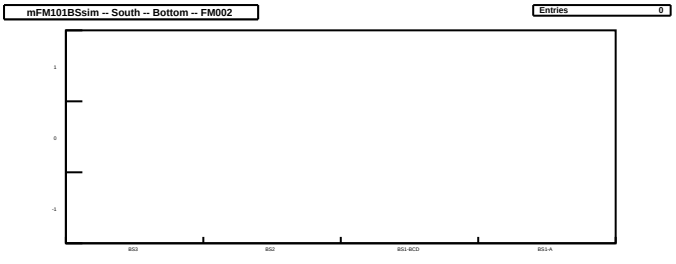

mFM101D -- South -- Top -- FM001
Entries 1000
D(23)

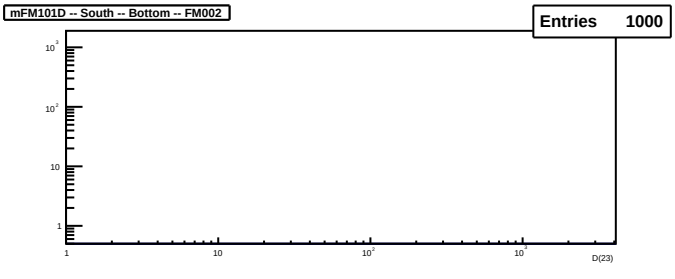

mFM101Dsim -- South -- Top -- FM001
Entries 0
D(23)

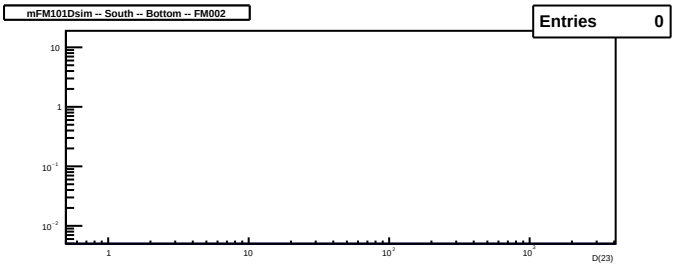

mFM101JP -- South -- Top -- FM001
Entries 2000
JP-AB
JP-CD

mFM101JPsim -- South -- Top -- FM001
Entries 0
JP-AB
JP-CD

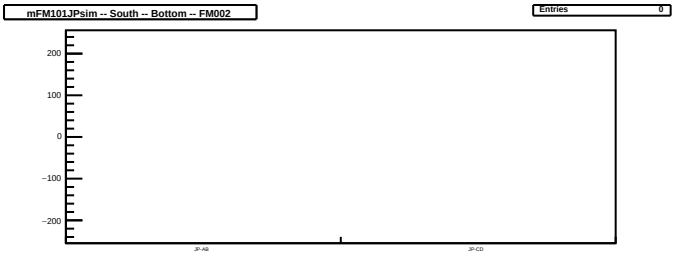

mFM101BS -- South -- Bottom -- FM002
Entries 4000
1000
1000
1000
1000
BS3
BS2
BS1-BCD
BS1-A

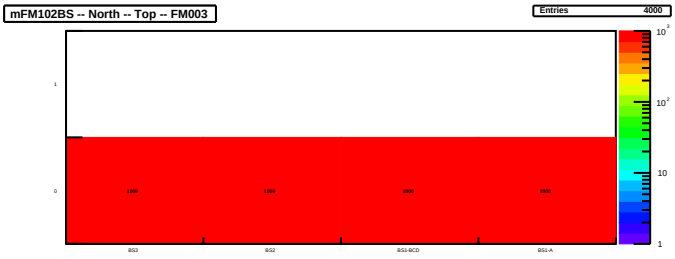

mFM101BSsim -- South -- Bottom -- FM002
Entries 0
BS3
BS2
BS1-BCD
BS1-A


mFM101D -- South -- Bottom -- FM002
Entries 1000
D(23)

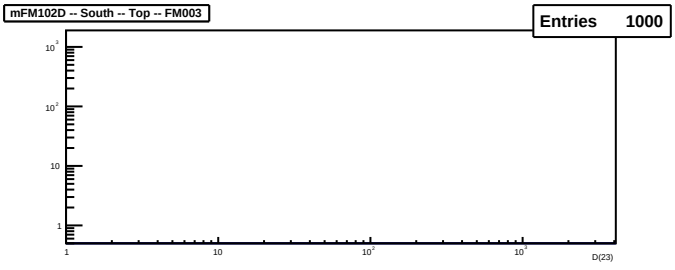

mFM101Dsim -- South -- Bottom -- FM002
Entries 0
D(23)

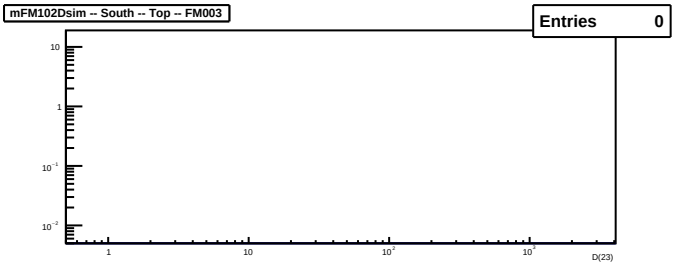
mFM101JP -- South -- Bottom -- FM002
Entries 2000
JP-AB
JP-CD

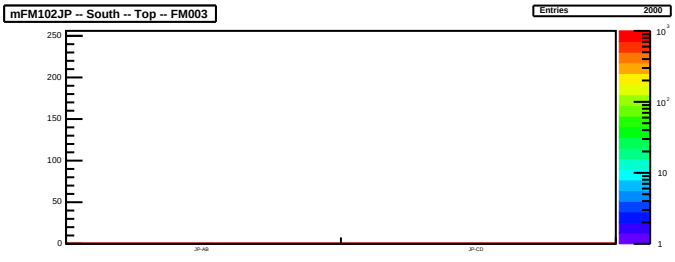

mFM101JPsim -- South -- Bottom -- FM002
Entries 0
JP-AB
JP-CD


mFM102BS -- North -- Top -- FM003
Entries 4000
1000
1000
1000
1000
BS3
BS2
BS1-BCD
BS1-A

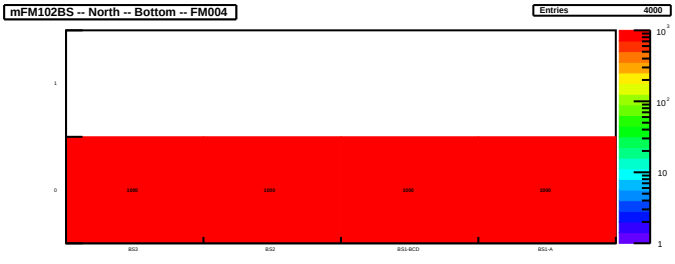
mFM102BSsim -- North -- Top -- FM003
Entries 0
BS3
BS2
BS1-BCD
BS1-A

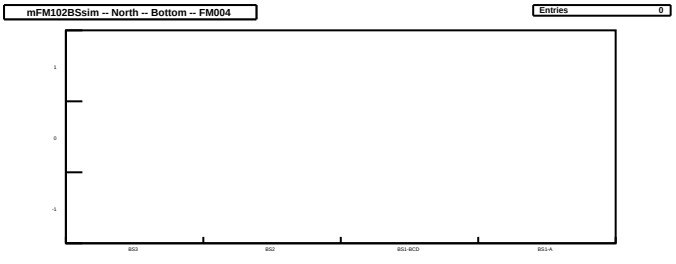

mFM102D -- South -- Top -- FM003
Entries 1000
D(23)

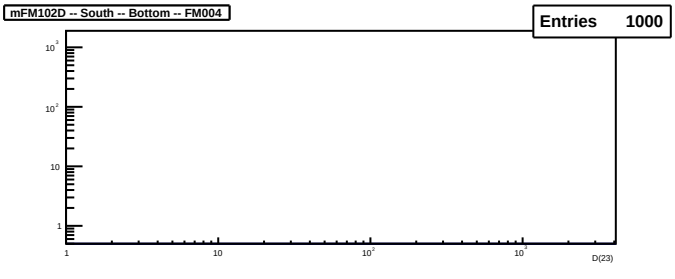

mFM102Dsim -- South -- Top -- FM003
Entries 0
D(23)

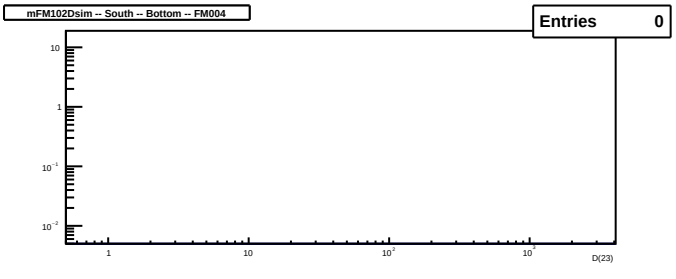

mFM102JP -- South -- Top -- FM003
Entries 2000
JP-AB
JP-CD

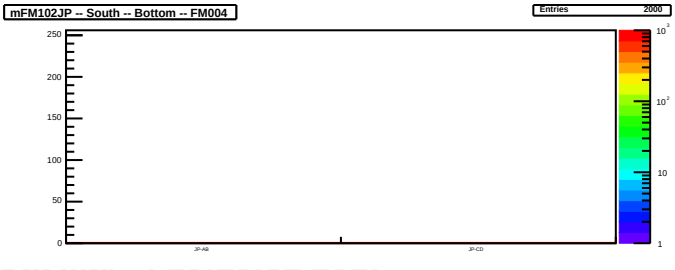
mFM102JPsim -- South -- Top -- FM003
Entries 0
JP-AB
JP-CD

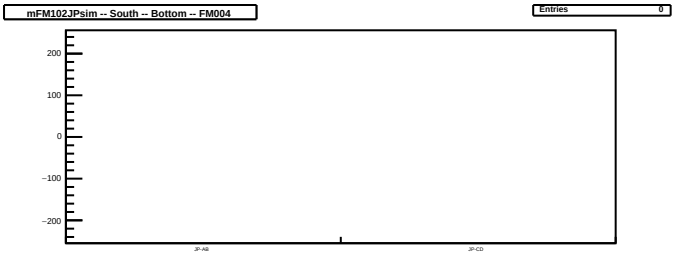

mFM102BS -- North -- Bottom -- FM004
Entries 4000
1000
1000
1000
1000
BS3
BS2
BS1-BCD
BS1-A

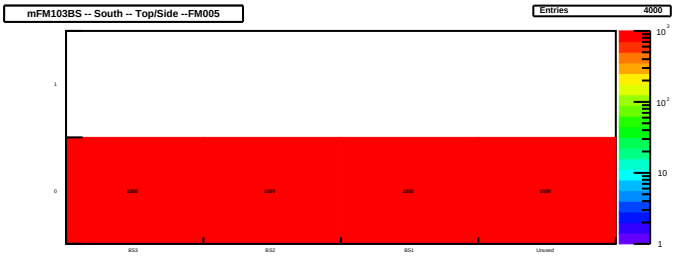

mFM102BSsim -- North -- Bottom -- FM004
Entries 0
BS3
BS2
BS1-BCD
BS1-A

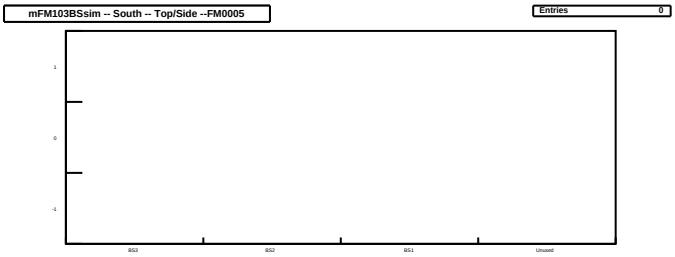

mFM102D -- South -- Bottom -- FM004
Entries 1000
D(23)

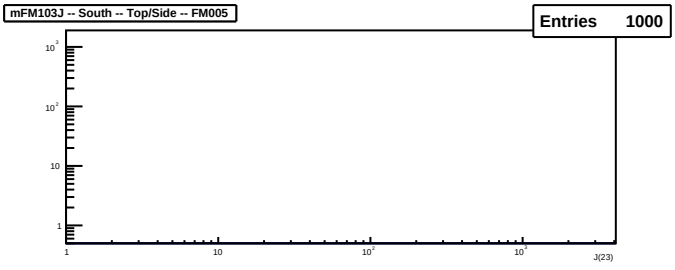

mFM102Dsim -- South -- Bottom -- FM004
Entries 0
D(23)

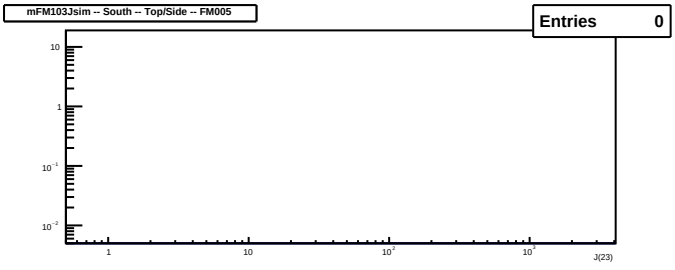

mFM102JP -- South -- Bottom -- FM004
Entries 2000
JP-AB
JP-CD

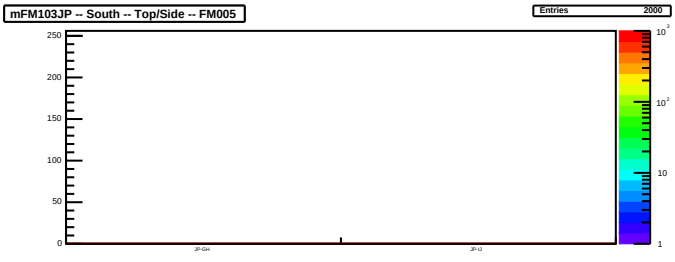

mFM102JPsim -- South -- Bottom -- FM004
Entries 0
JP-AB
JP-CD

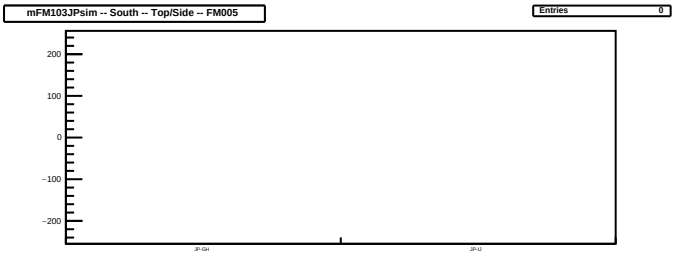

mFM103BS -- South -- Top/Side --FM005
Entries 4000
1000
1000
1000
1000
BS3
BS2
BS1
Unused

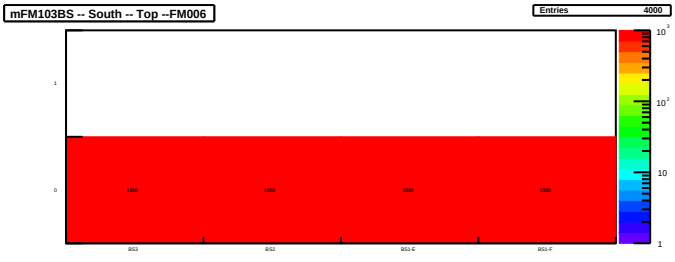

mFM103BSsim -- South -- Top/Side --FM0005
Entries 0
BS3
BS2
BS1
Unused

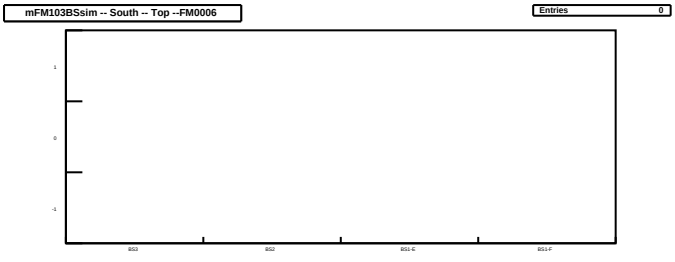

mFM103J -- South -- Top/Side -- FM005
Entries 1000
J(23)

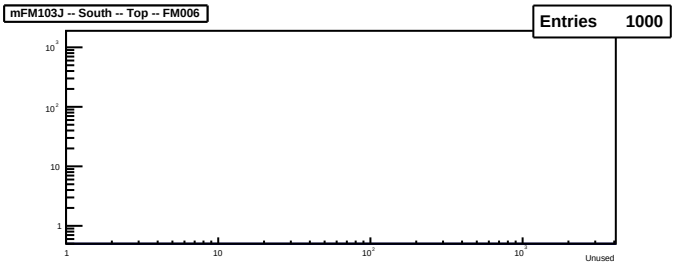

mFM103Jsim -- South -- Top/Side -- FM005
Entries 0
J(23)

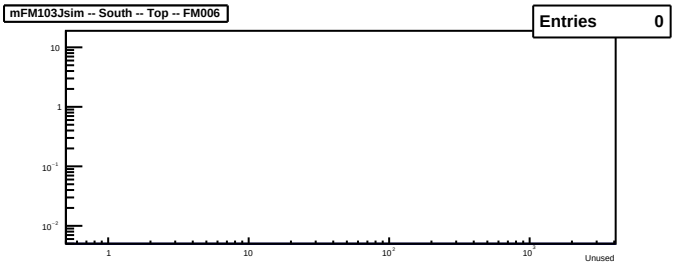

mFM103JP -- South -- Top/Side -- FM005
Entries 2000
JP-GH
JP-IJ

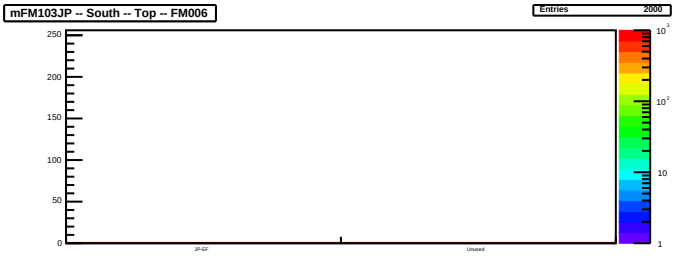

mFM103JPsim -- South -- Top/Side -- FM005
Entries 0
JP-GH
JP-IJ

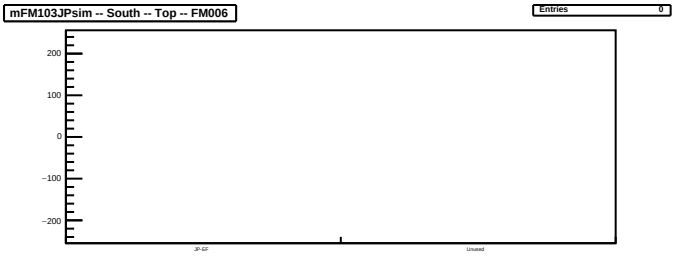

mFM103BS -- South -- Top --FM006
Entries 4000
1000
1000
1000
1000
BS3
BS2
BS1-E
BS1-F

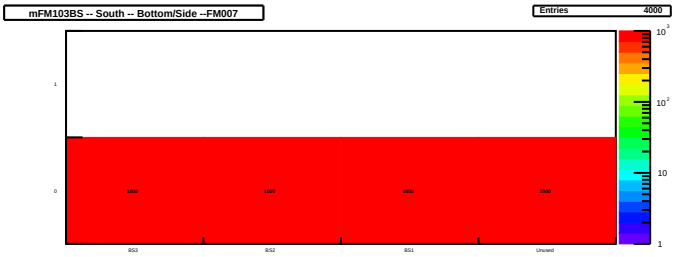

mFM103BSsim -- South -- Top --FM0006
Entries 0
BS3
BS2
BS1-E
BS1-F

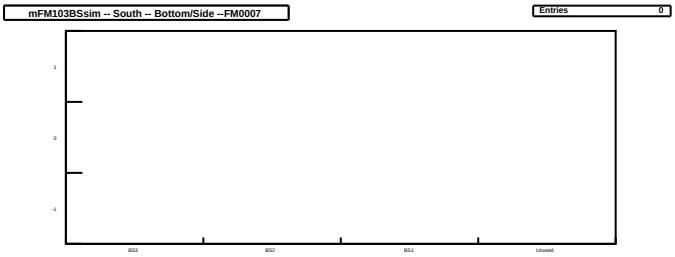

mFM103J -- South -- Top -- FM006
Entries 1000
Unused

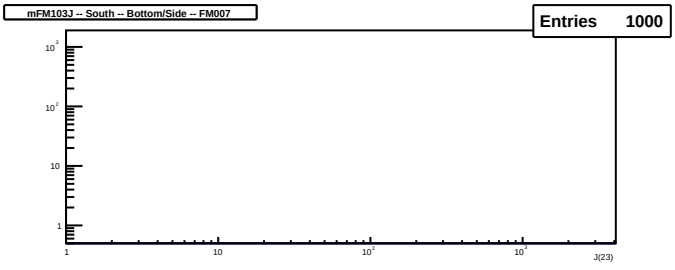

mFM103Jsim -- South -- Top -- FM006
Entries 0
Unused

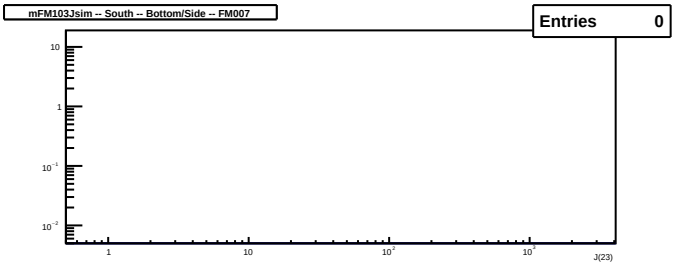

mFM103JP -- South -- Top -- FM006
Entries 2000
JP-EF
Unused

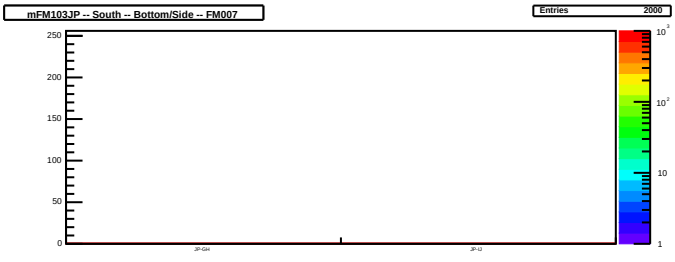

mFM103JPsim -- South -- Top -- FM006
Entries 0
JP-EF
Unused

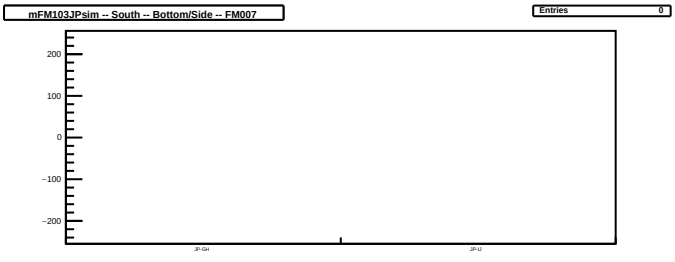

mFM103BS -- South -- Bottom/Side --FM007
Entries 4000
BS3
BS2
BS1
Unused

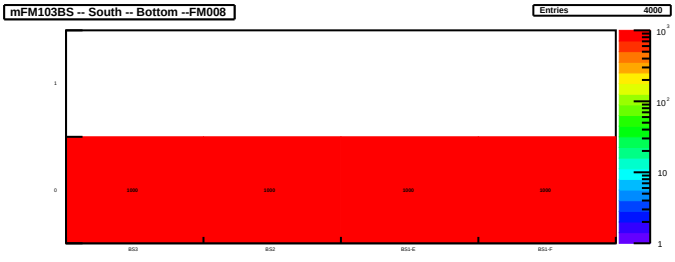

mFM103BSsim -- South -- Bottom/Side --FM0007
Entries 0
BS3
BS2
BS1
Unused

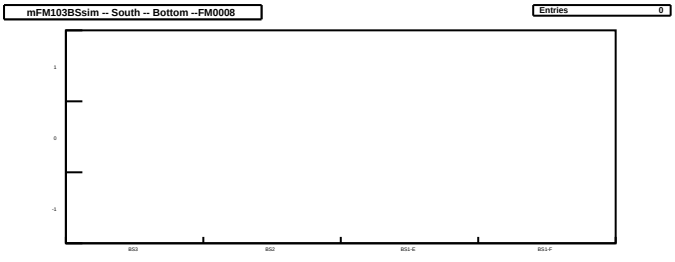

mFM103J -- South -- Bottom/Side -- FM007
Entries 1000
J(23)

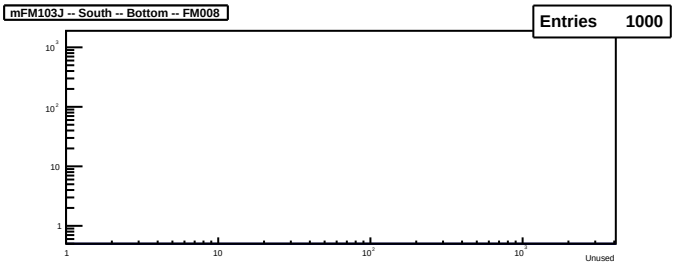

mFM103Jsim -- South -- Bottom/Side -- FM007
Entries 0
J(23)

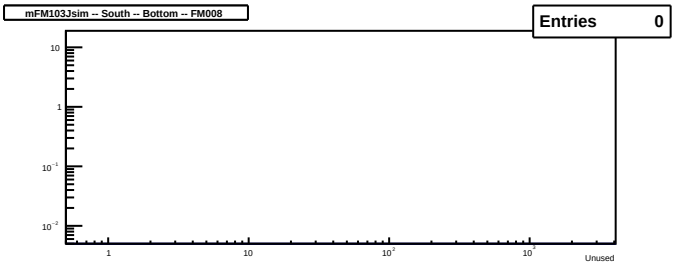

mFM103JP -- South -- Bottom/Side -- FM007
Entries 2000
JP-GH
JP-IJ


mFM103JPsim -- South -- Bottom/Side -- FM007
Entries 0
JP-GH
JP-IJ

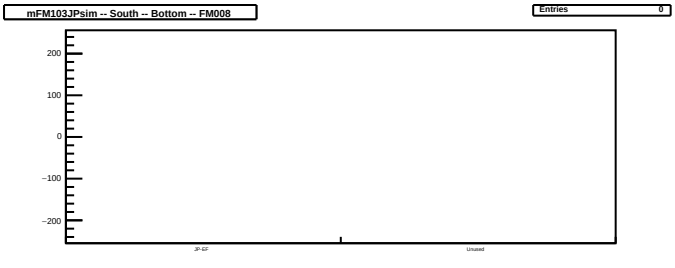

mFM103BS -- South -- Bottom --FM008
Entries 4000
BS3
BS2
BS1-E
BS1-F

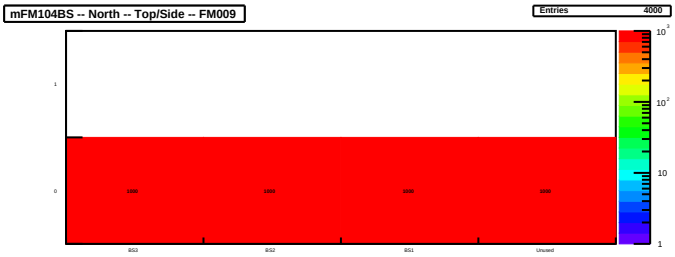

mFM103BSsim -- South -- Bottom --FM0008
Entries 0
BS3
BS2
BS1-E
BS1-F

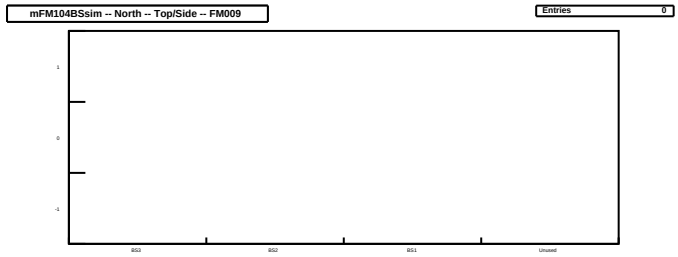

mFM103J -- South -- Bottom -- FM008
Entries 1000
Unused

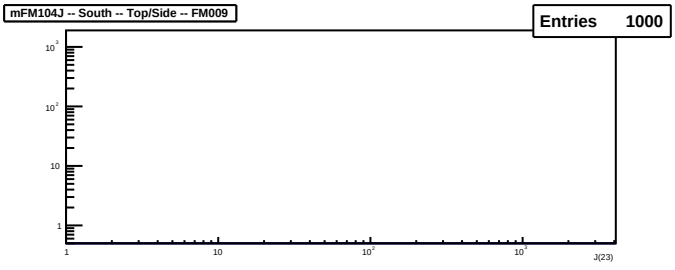

mFM103Jsim -- South -- Bottom -- FM008
Entries 0
Unused

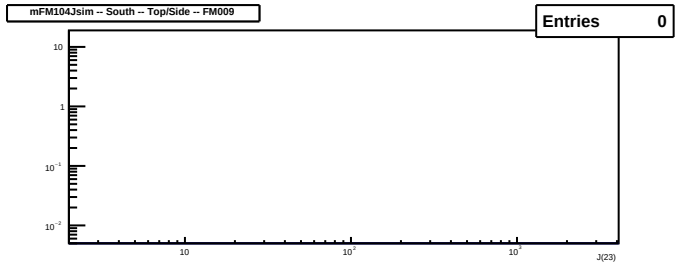
mFM103JP -- South -- Bottom -- FM008
Entries 2000
JP-EF
Unused

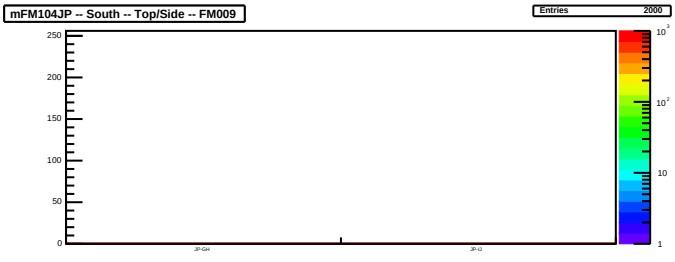

mFM103JPsim -- South -- Bottom -- FM008
Entries 0
JP-EF
Unused

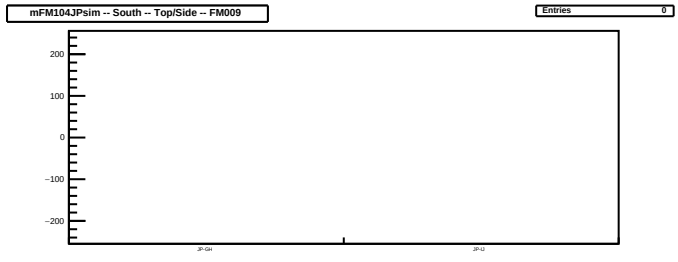

mFM104BS -- North -- Top/Side -- FM009
Entries 4000
BS3
BS2
BS1
Unused

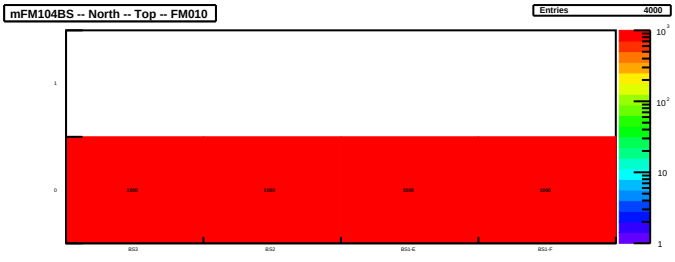

mFM104BSsim -- North -- Top/Side -- FM009
Entries 0
BS3
BS2
BS1
Unused

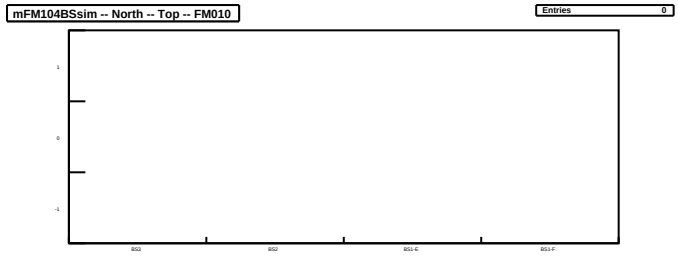

mFM104J -- South -- Top/Side -- FM009
Entries 1000
J(23)

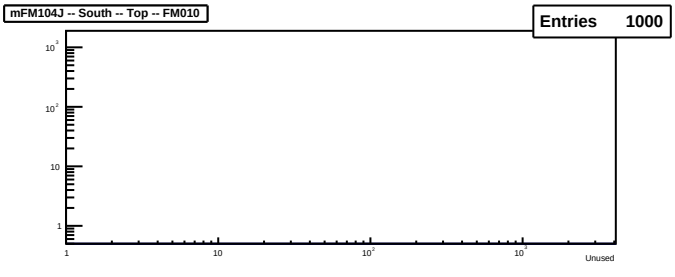

mFM104Jsim -- South -- Top/Side -- FM009
Entries 0
J(23)

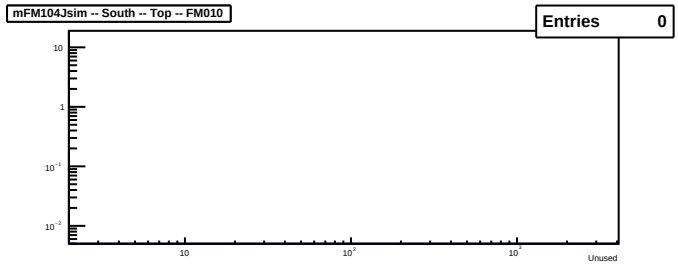

mFM104JP -- South -- Top/Side -- FM009
Entries 2000
JP-GH
JP-IJ

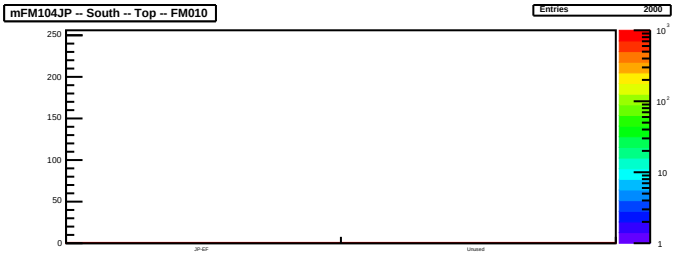

mFM104JPsim -- South -- Top/Side -- FM009
Entries 0
JP-GH
JP-IJ


mFM104BS -- North -- Top -- FM010
Entries 4000
BS3
BS2
BS1-E
BS1-F

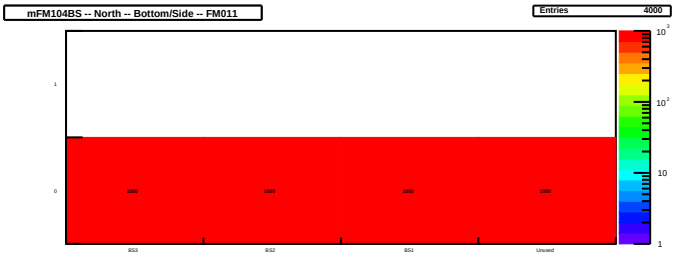

mFM104BSsim -- North -- Top -- FM010
Entries 0
BS3
BS2
BS1-E
BS1-F

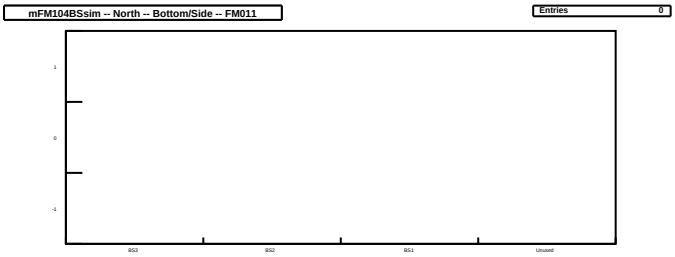

mFM104J -- South -- Top -- FM010
Entries 1000
Unused

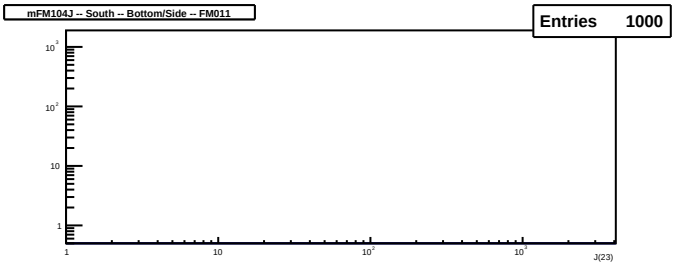

mFM104Jsim -- South -- Top -- FM010
Entries 0
Unused

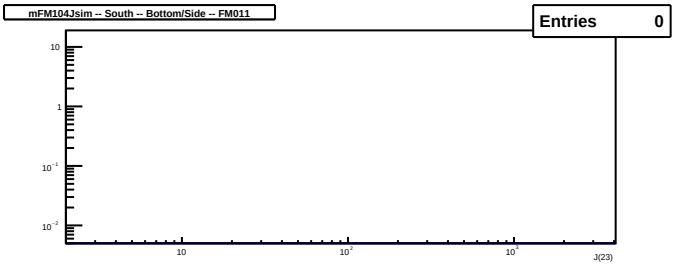

mFM104JP -- South -- Top -- FM010
Entries 2000
JP-EF
Unused

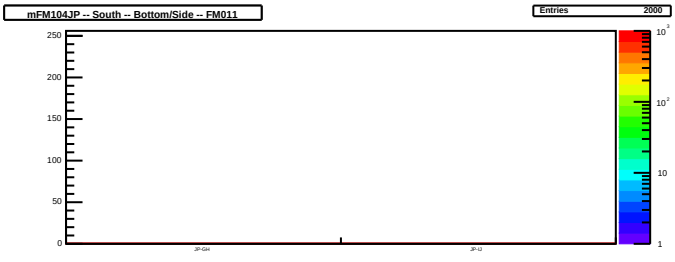
mFM104JPsim -- South -- Top -- FM010
Entries 0
JP-EF
Unused

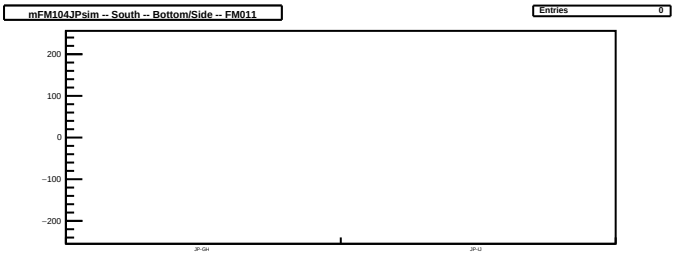

mFM104BS -- North -- Bottom/Side -- FM011
Entries 4000
1000
1000
1000
1000
BS3
BS2
BS1
Unused

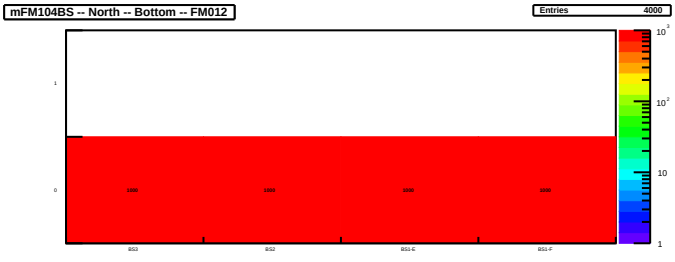

mFM104BSsim -- North -- Bottom/Side -- FM011
Entries 0
BS3
BS2
BS1
Unused

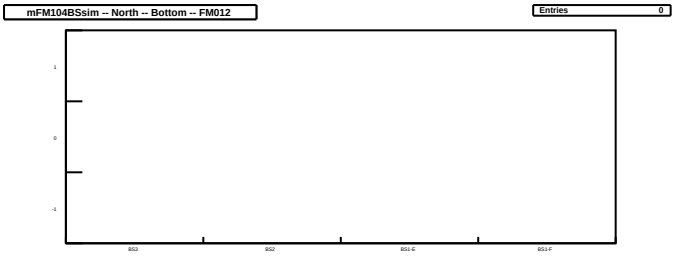

mFM104J -- South -- Bottom/Side -- FM011
Entries 1000
J(23)

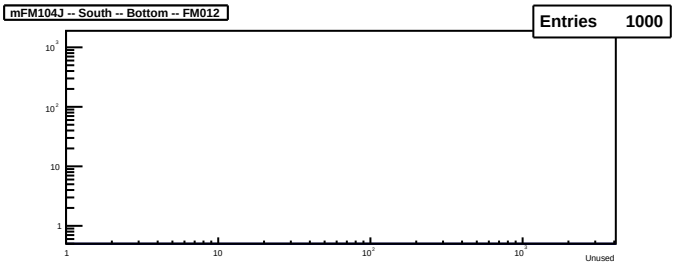

mFM104Jsim -- South -- Bottom/Side -- FM011
Entries 0
J(23)

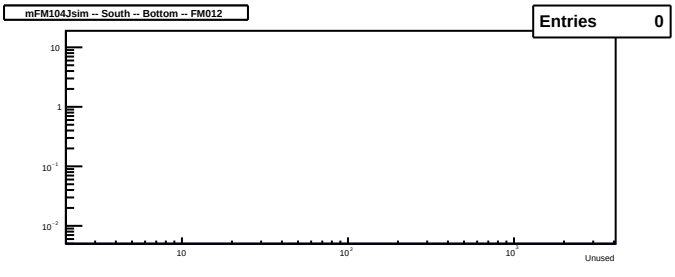

mFM104JP -- South -- Bottom/Side -- FM011
Entries 2000
JP-GH
JP-IJ


mFM104JPsim -- South -- Bottom/Side -- FM011
Entries 0
JP-GH
JP-IJ


mFM104BS -- North -- Bottom -- FM012
Entries 4000
1000
1000
1000
1000
BS3
BS2
BS1-E
BS1-F

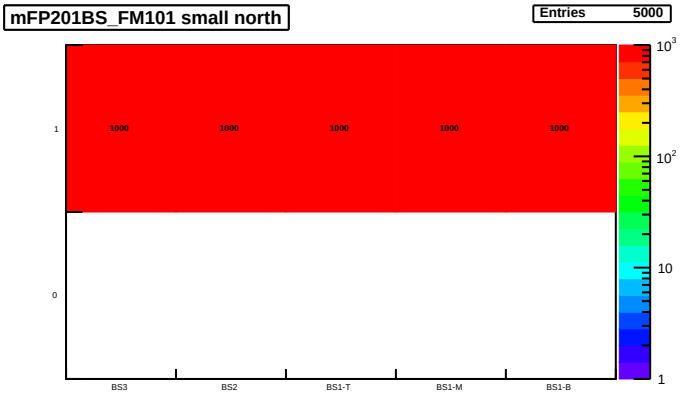

mFM104BSsim -- North -- Bottom -- FM012
Entries 0
BS3
BS2
BS1-E
BS1-F

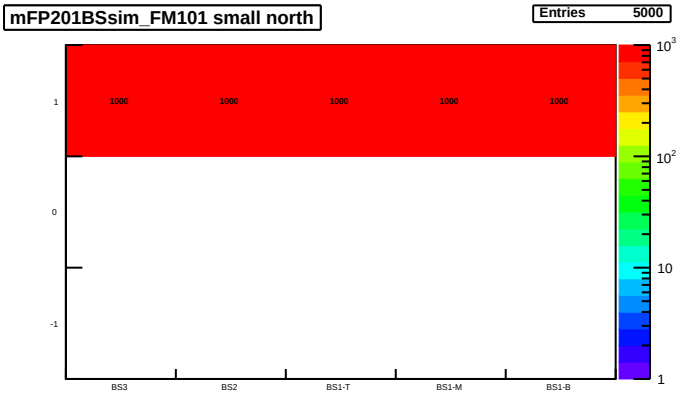

mFM104J -- South -- Bottom -- FM012
Entries 1000
Unused

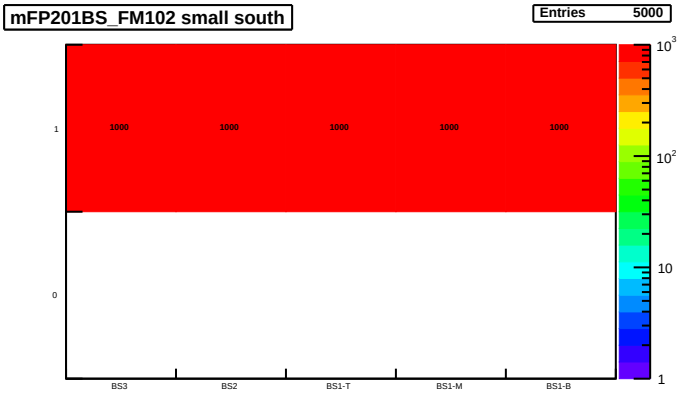

mFM104Jsim -- South -- Bottom -- FM012
Entries 0
Unused

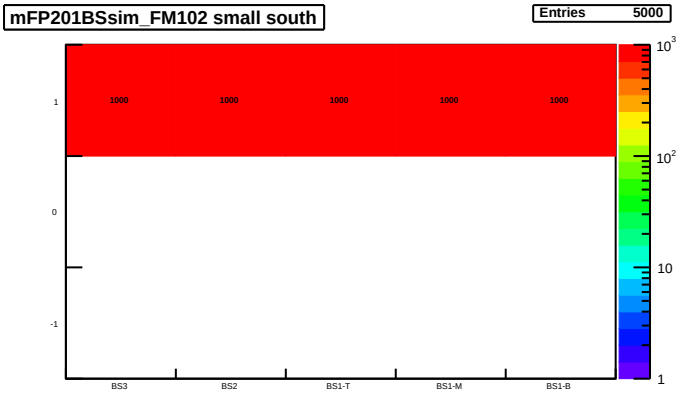
mFM104JP -- South -- Bottom -- FM012
Entries 2000
JP-EF
Unused

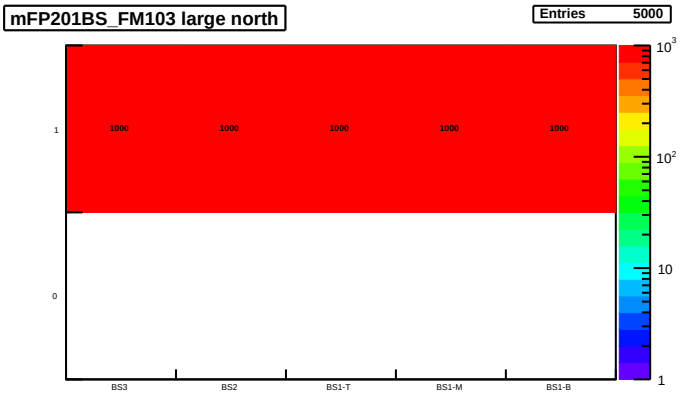
mFM104JPsim -- South -- Bottom -- FM012
Entries 0
JP-EF
Unused

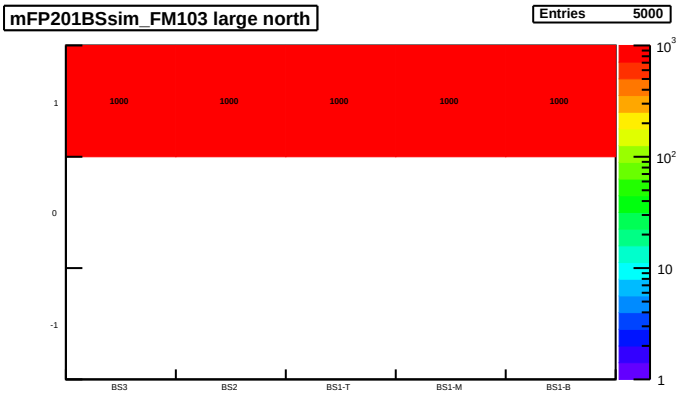

mFP201BS_FM101 small north
Entries 5000
1000 1000 1000 1000 1000
BS3 BS2 BS1-T BS1-M BS1-B

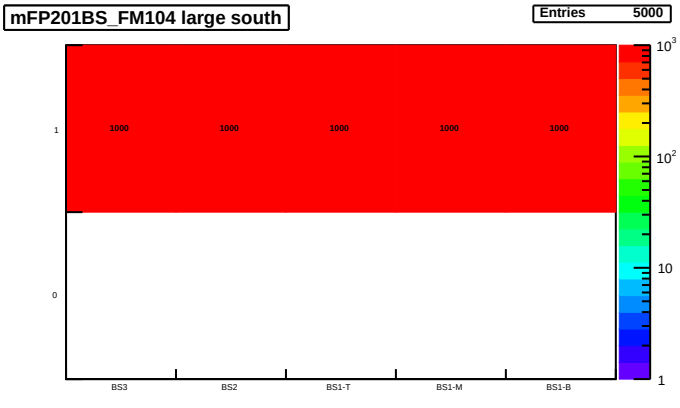

mFP201BSsim_FM101 small north
Entries 5000
1000 1000 1000 1000 1000
BS3 BS2 BS1-T BS1-M BS1-B

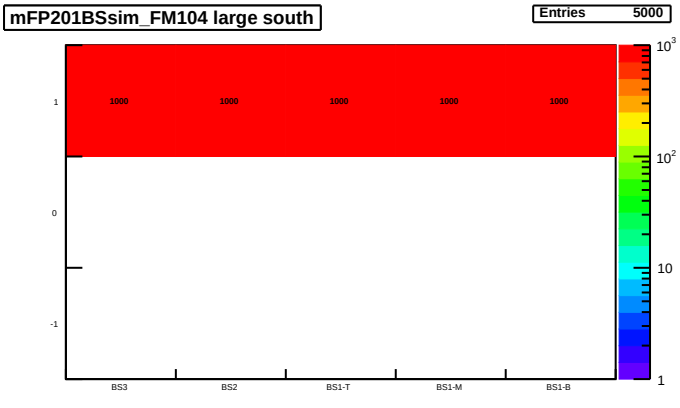

mFP201BS_FM102 small south
Entries 5000
1000 1000 1000 1000 1000
BS3 BS2 BS1-T BS1-M BS1-B

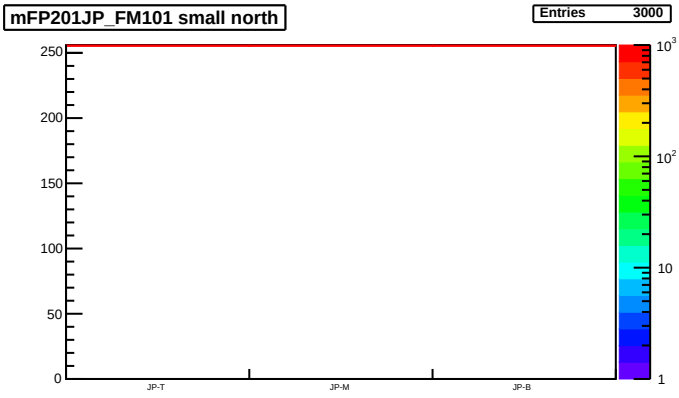

mFP201BSsim_FM102 small south
Entries 5000
1000 1000 1000 1000 1000
BS3 BS2 BS1-T BS1-M BS1-B

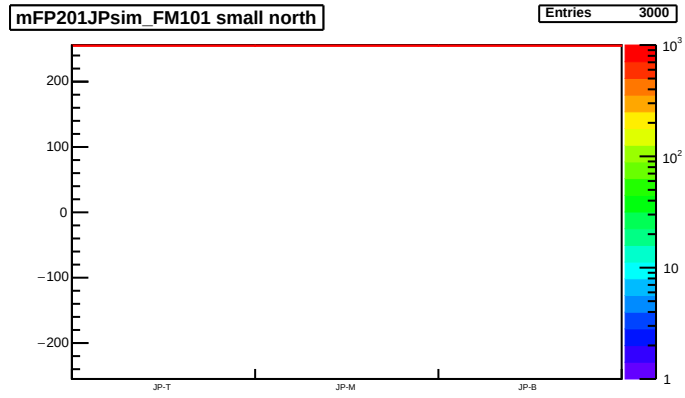

mFP201BS_FM103 large north
Entries 5000
1000 1000 1000 1000 1000
BS3 BS2 BS1-T BS1-M BS1-B

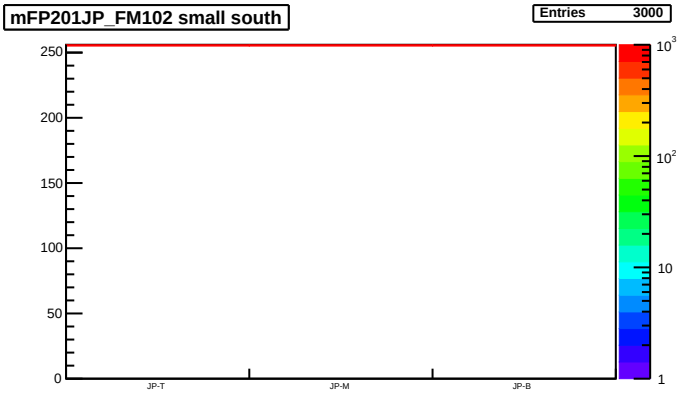

mFP201BSsim_FM103 large north
Entries 5000
1000 1000 1000 1000 1000
BS3 BS2 BS1-T BS1-M BS1-B

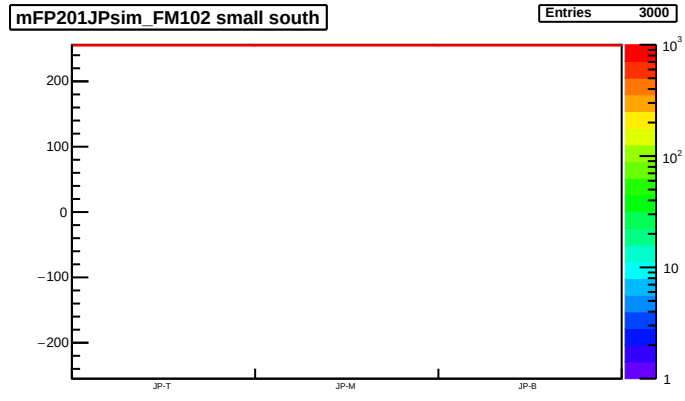

mFP201BS_FM104 large south
Entries 5000
1000 1000 1000 1000 1000
BS3 BS2 BS1-T BS1-M BS1-B

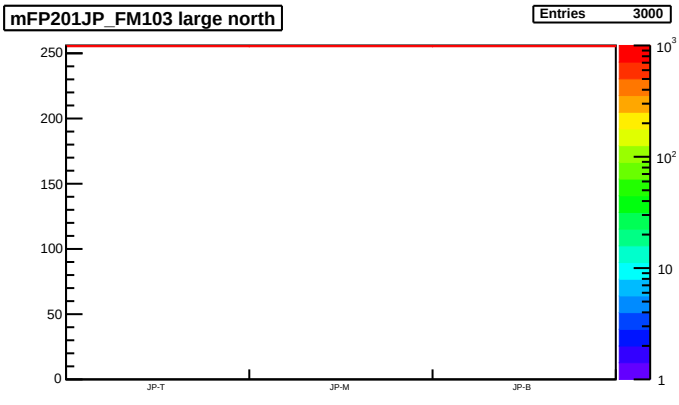

mFP201BSsim_FM104 large south
Entries 5000
1000 1000 1000 1000 1000
BS3 BS2 BS1-T BS1-M BS1-B

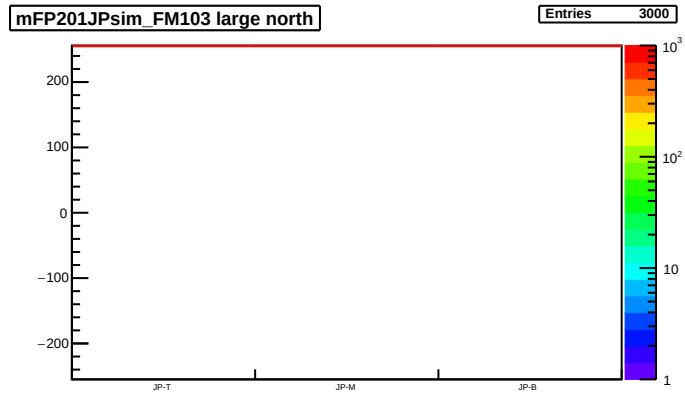

mFP201JP_FM101 small north
Entries 3000
JP-T
JP-M
JP-B

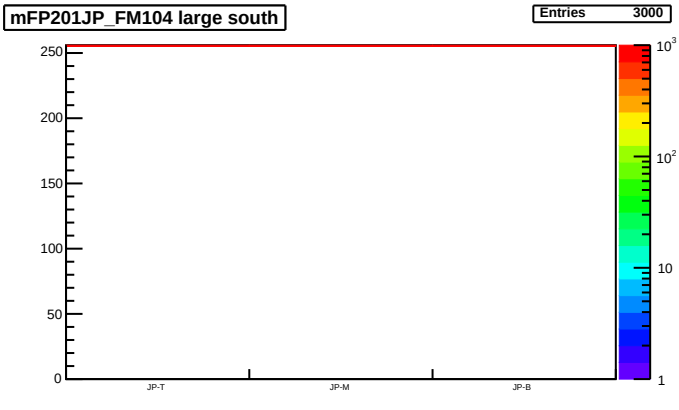

mFP201JPsim_FM101 small north
Entries 3000
JP-T
JP-M
JP-B

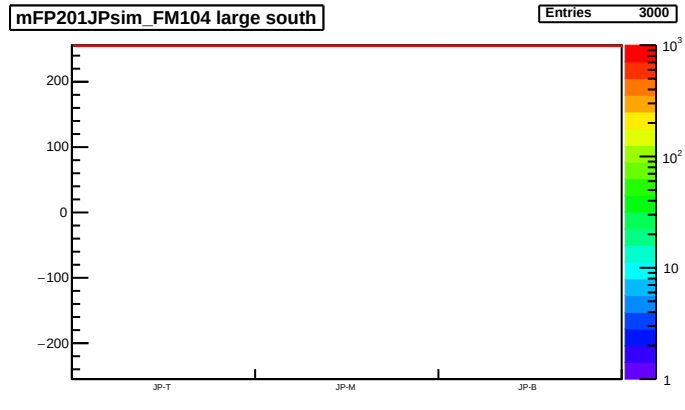

mFP201JP_FM102 small south
Entries 3000
JP-T
JP-M
JP-B

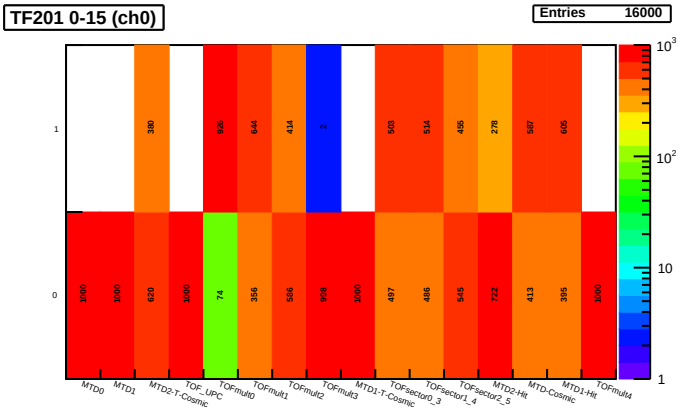

mFP201JPsim_FM102 small south
Entries 3000
JP-T
JP-M
JP-B

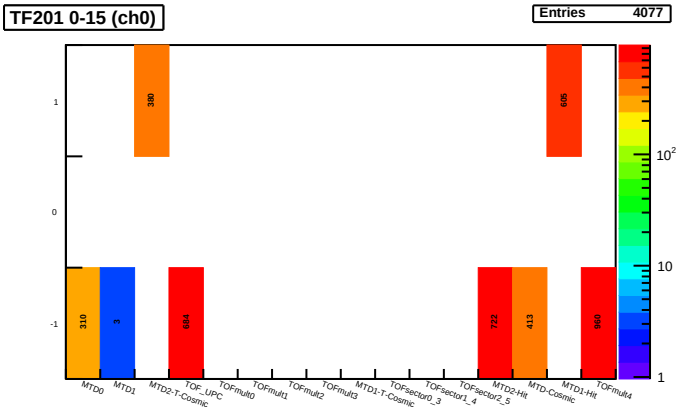

mFP201JP_FM103 large north
Entries 3000
JP-T
JP-M
JP-B

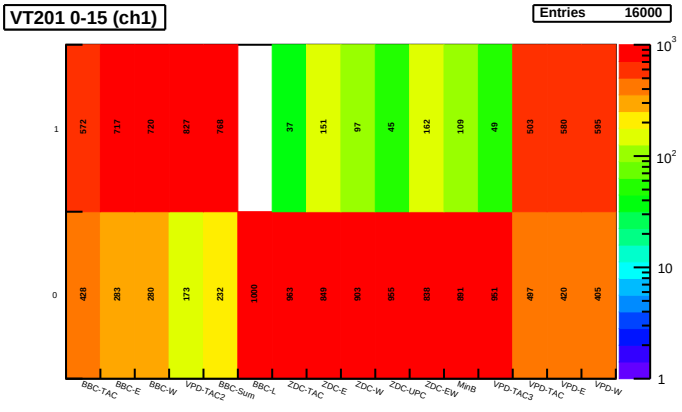

mFP201JPsim_FM103 large north
Entries 3000
JP-T
JP-M
JP-B

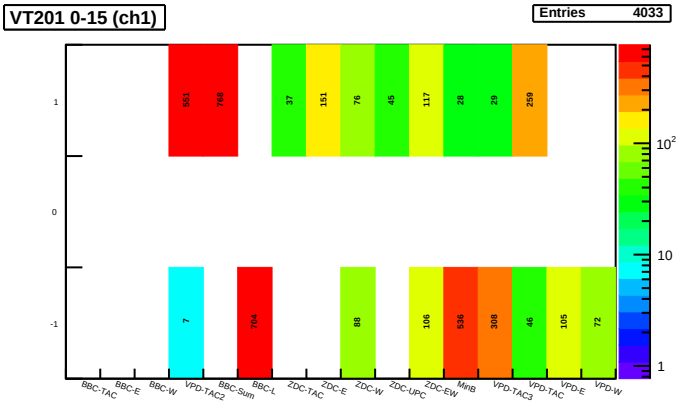

mFP201JP_FM104 large south
Entries 3000
JP-T
JP-M
JP-B

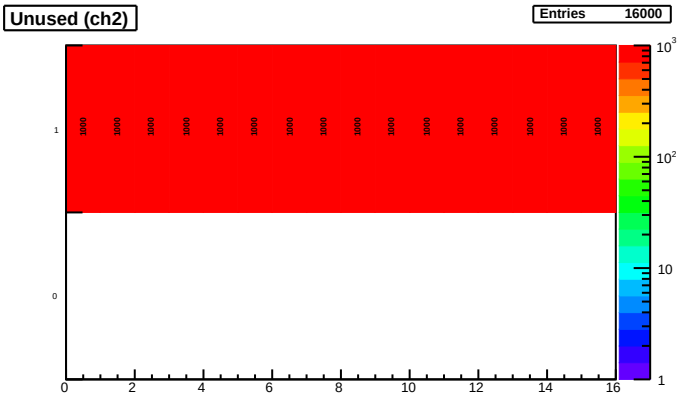

mFP201JPsim_FM104 large south
Entries 3000
JP-T
JP-M
JP-B

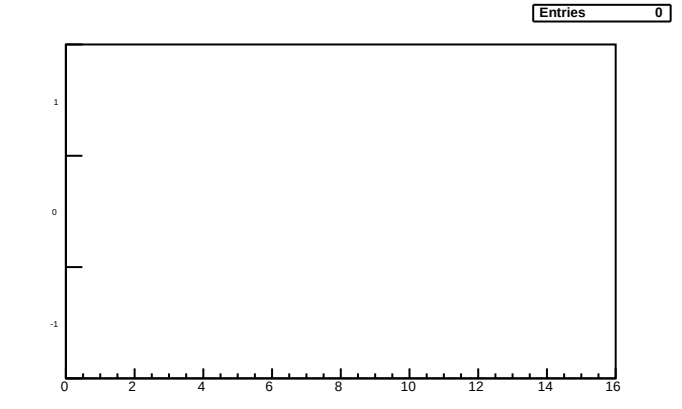

TF201 0-15 (ch0)
Entries 16000
MTD0 MTD1 MTD2-T-Cosmic TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 MTD1-T-Cosmic TOFsector0_3 TOFsector1_4 TOFsector2_5 MTD2-Hit MTD-Cosmic MTD1-Hit TOFmult4

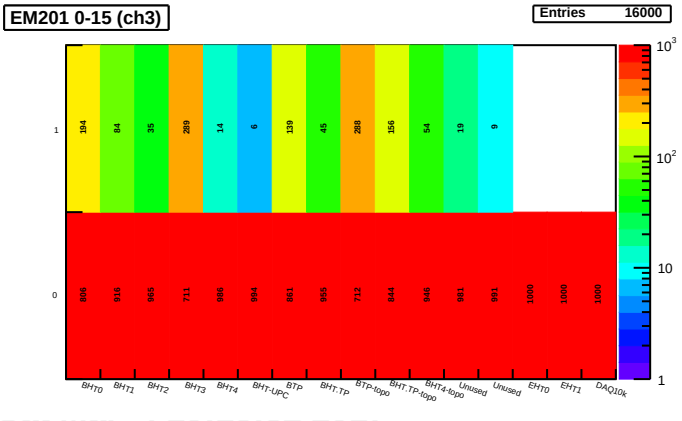

TF201 0-15 (ch0)
Entries 4077
MTD0 MTD1 MTD2-T-Cosmic TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 MTD1-T-Cosmic TOFsector0_3 TOFsector1_4 TOFsector2_5 MTD2-Hit MTD-Cosmic MTD1-Hit TOFmult4

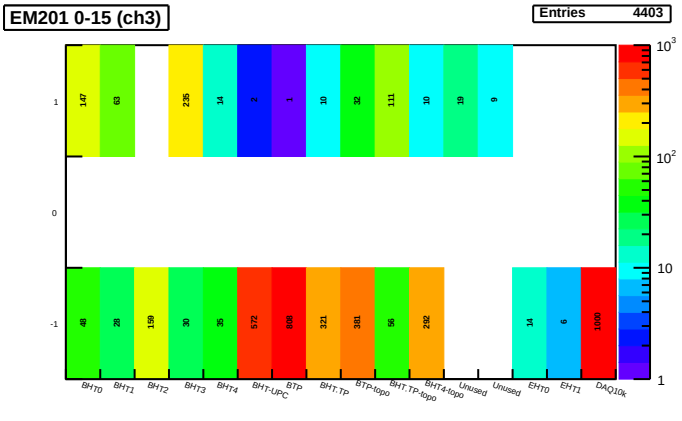

VT201 0-15 (ch1)
Entries 16000
BBC-TAC BBC-E BBC-W VPD-TAC2 BBC-Sum BBC-L ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW MinB VPD-TAC3 VPD-TAC VPD-E VPD-W

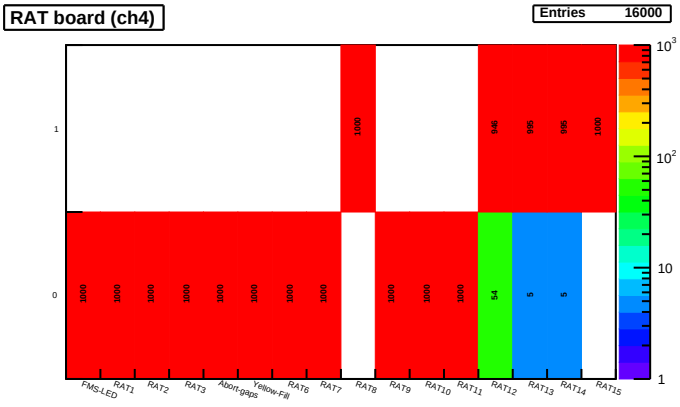

VT201 0-15 (ch1)
Entries 4033
BBC-TAC BBC-E BBC-W VPD-TAC2 BBC-Sum BBC-L ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW MinB VPD-TAC3 VPD-TAC VPD-E VPD-W

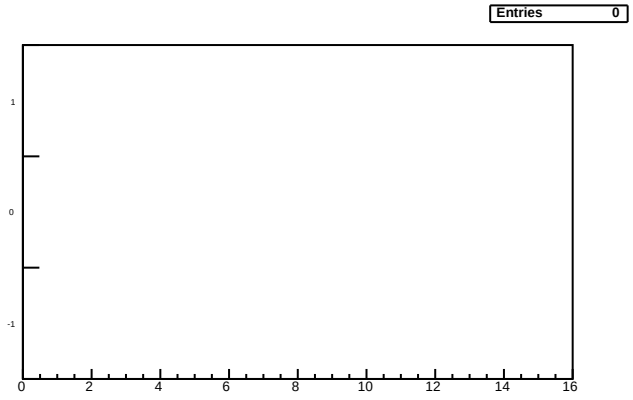

Unused (ch2)
Entries 16000

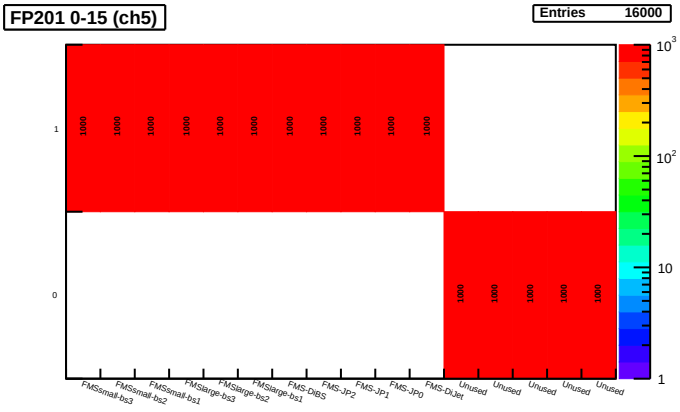

Entries 0

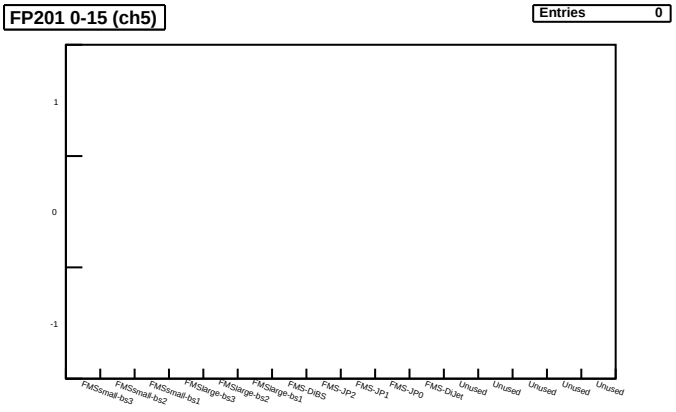

EM201 0-15 (ch3)
Entries 16000
BHT0 BHT1 BHT2 BHT3 BHT4 BHT-UPC BTP BHT-TP BTP-topo BHT-TP-topo BHT4-topo Unused Unused EHT0 EHT1 DAQ10k

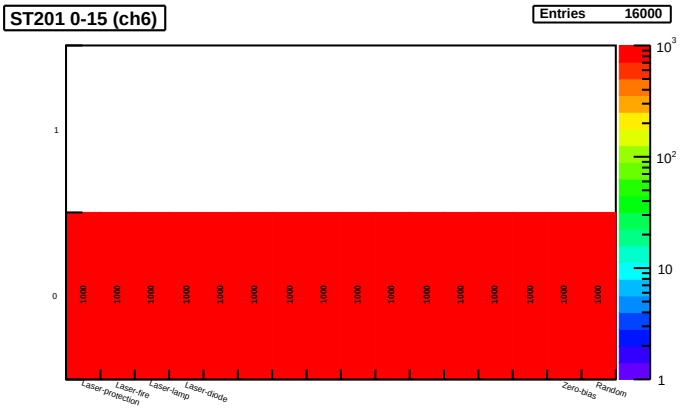

EM201 0-15 (ch3)
Entries 4403
BHT0 BHT1 BHT2 BHT3 BHT4 BHT-UPC BTP BHT-TP BTP-topo BHT-TP-topo BHT4-topo Unused Unused EHT0 EHT1 DAQ10k

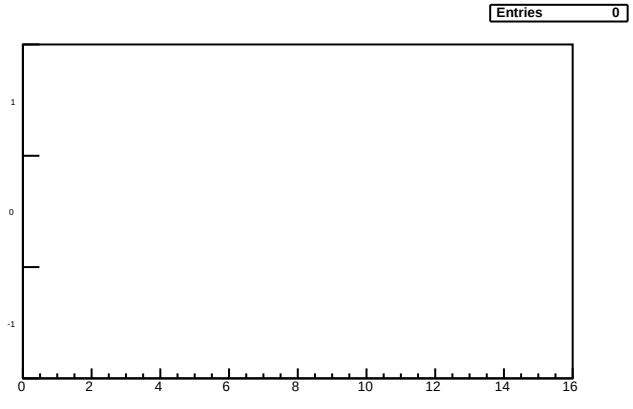

RAT board (ch4)
Entries 16000
FMS-LED
RAT1
RAT2
RAT3
Abort-gaps
Yellow-Fill
RAT6
RAT7
RAT8
RAT9
RAT10
RAT11
RAT12
RAT13
RAT14
RAT15

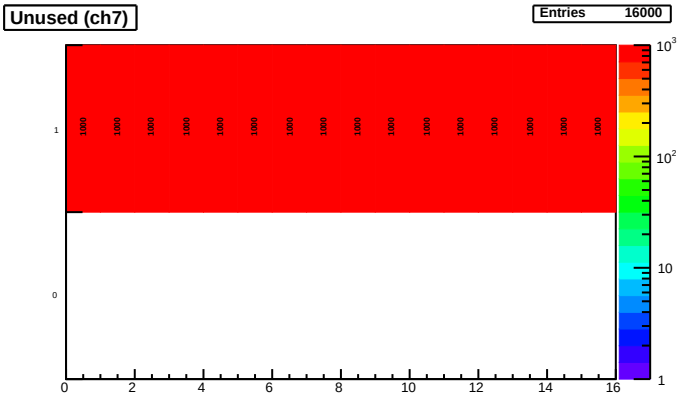

Entries 0

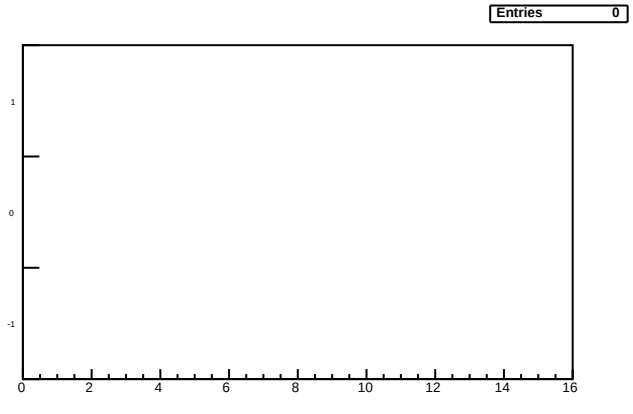

FP201 0-15 (ch5)
Entries 16000
FMSsmall-bs3
FMSsmall-bs2
FMSsmall-bs1
FMSlarge-bs3
FMSlarge-bs2
FMSlarge-bs1
FMS-DiBS
FMS-JP2
FMS-JP1
FMS-JP0
FMS-Dijet
Unused
Unused
Unused
Unused
Unused


FP201 0-15 (ch5)
Entries 0
FMSsmall-bs3
FMSsmall-bs2
FMSsmall-bs1
FMSlarge-bs3
FMSlarge-bs2
FMSlarge-bs1
FMS-DiBS
FMS-JP2
FMS-JP1
FMS-JP0
FMS-Dijet
Unused
Unused
Unused
Unused
Unused


ST201 0-15 (ch6)
Entries 16000
Laser-protection
Laser-fire
Laser-lamp
Laser-diode
Zero-bias
Random


Entries 0


Unused (ch7)
Entries 16000


Entries 0