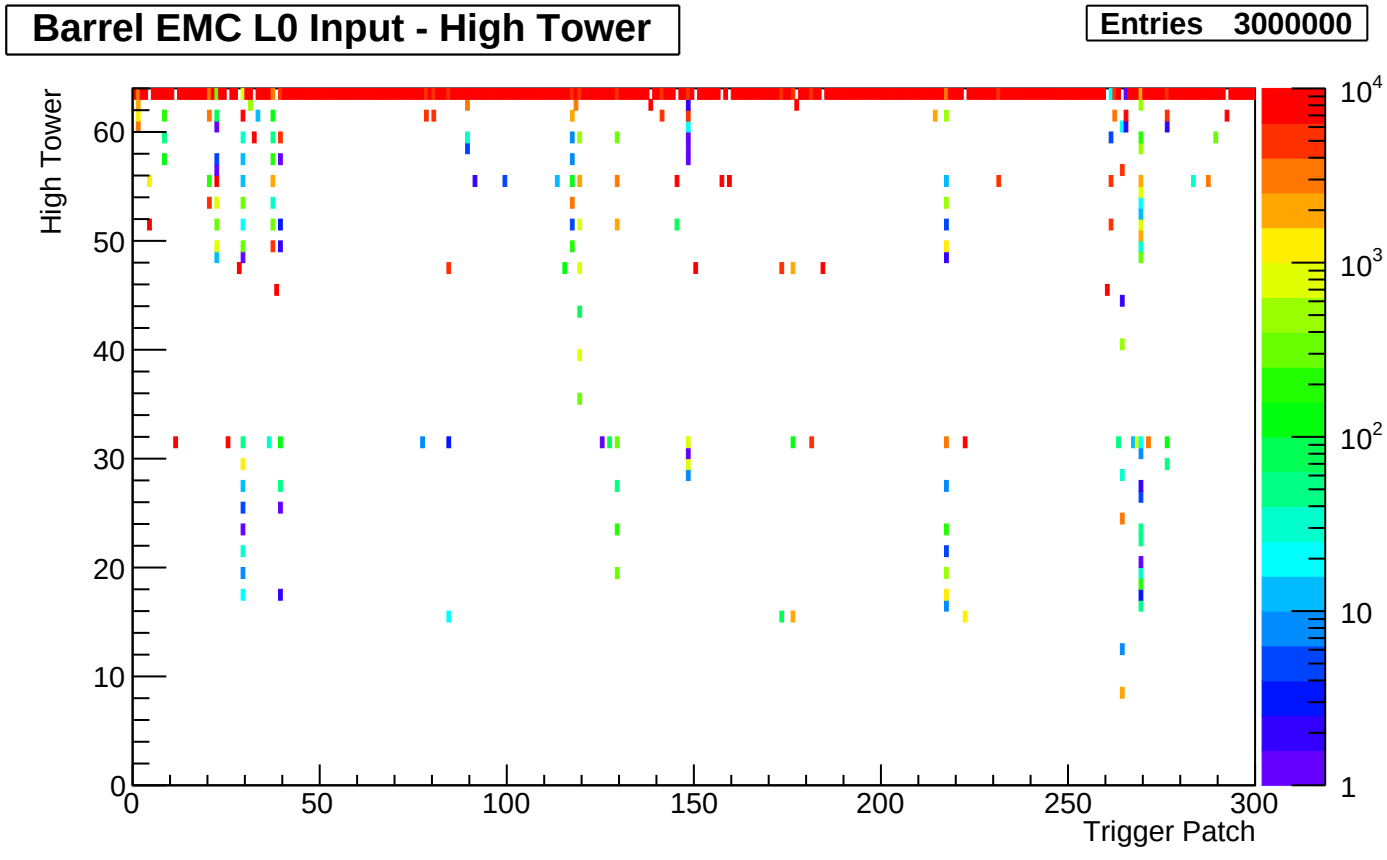
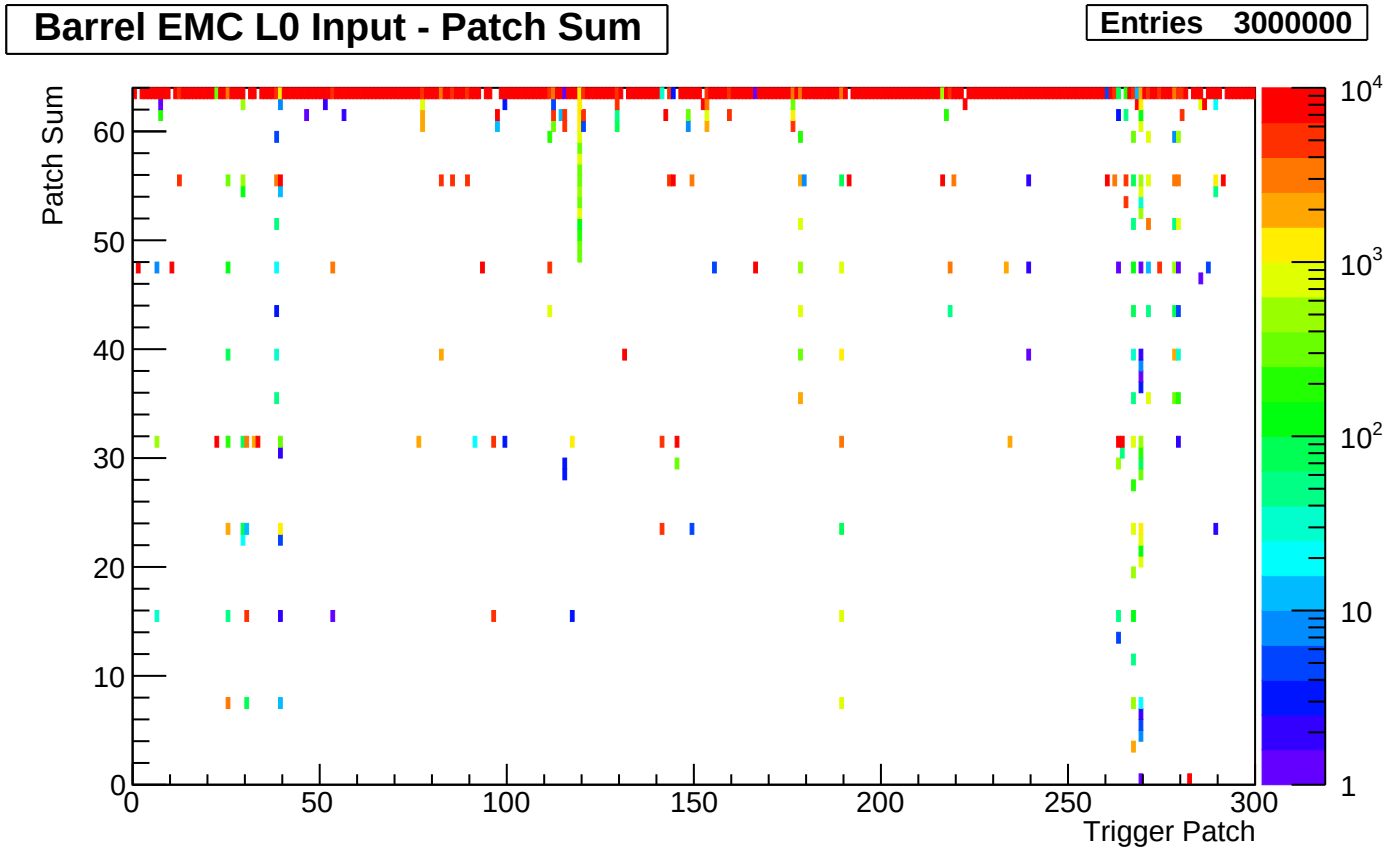
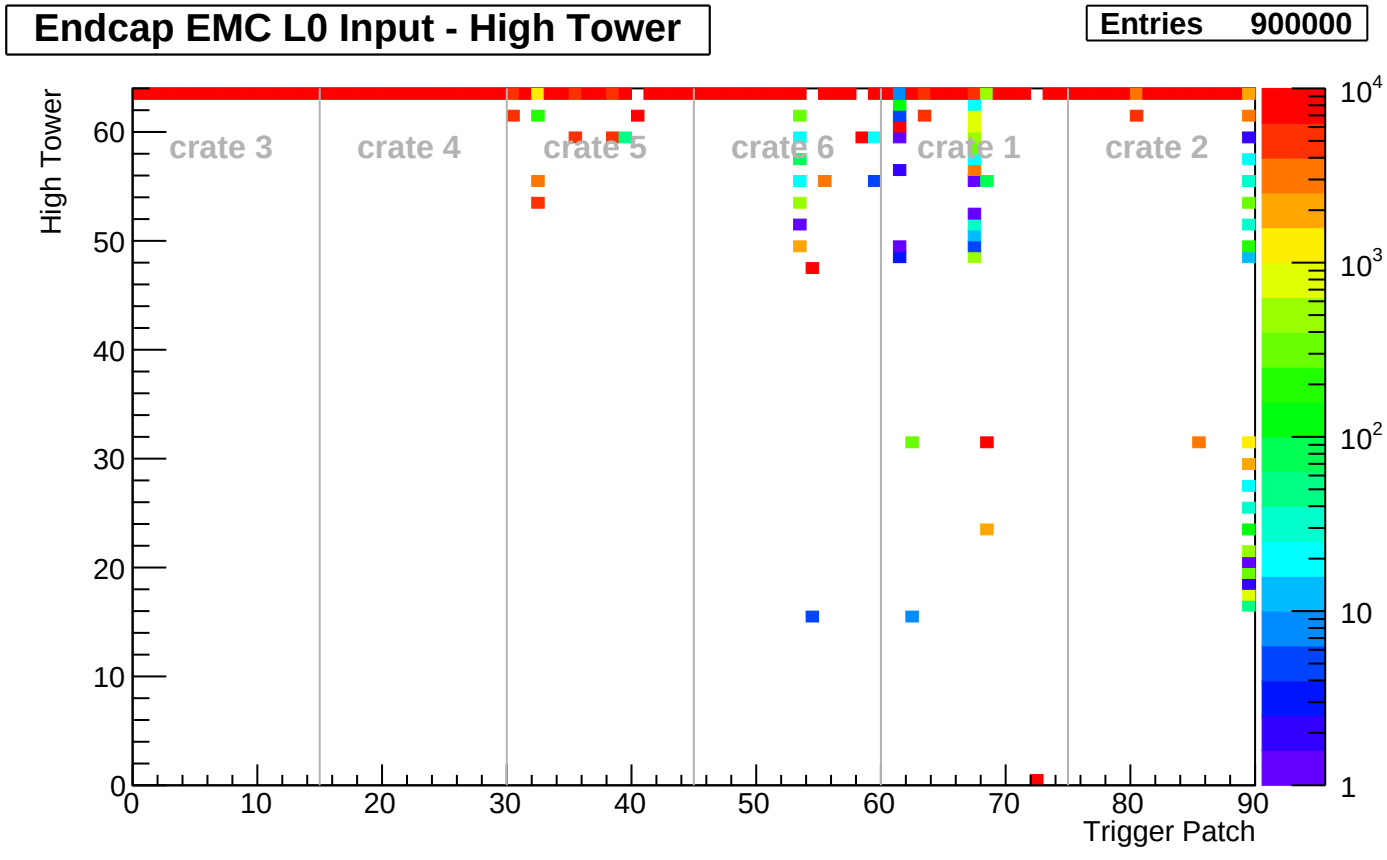
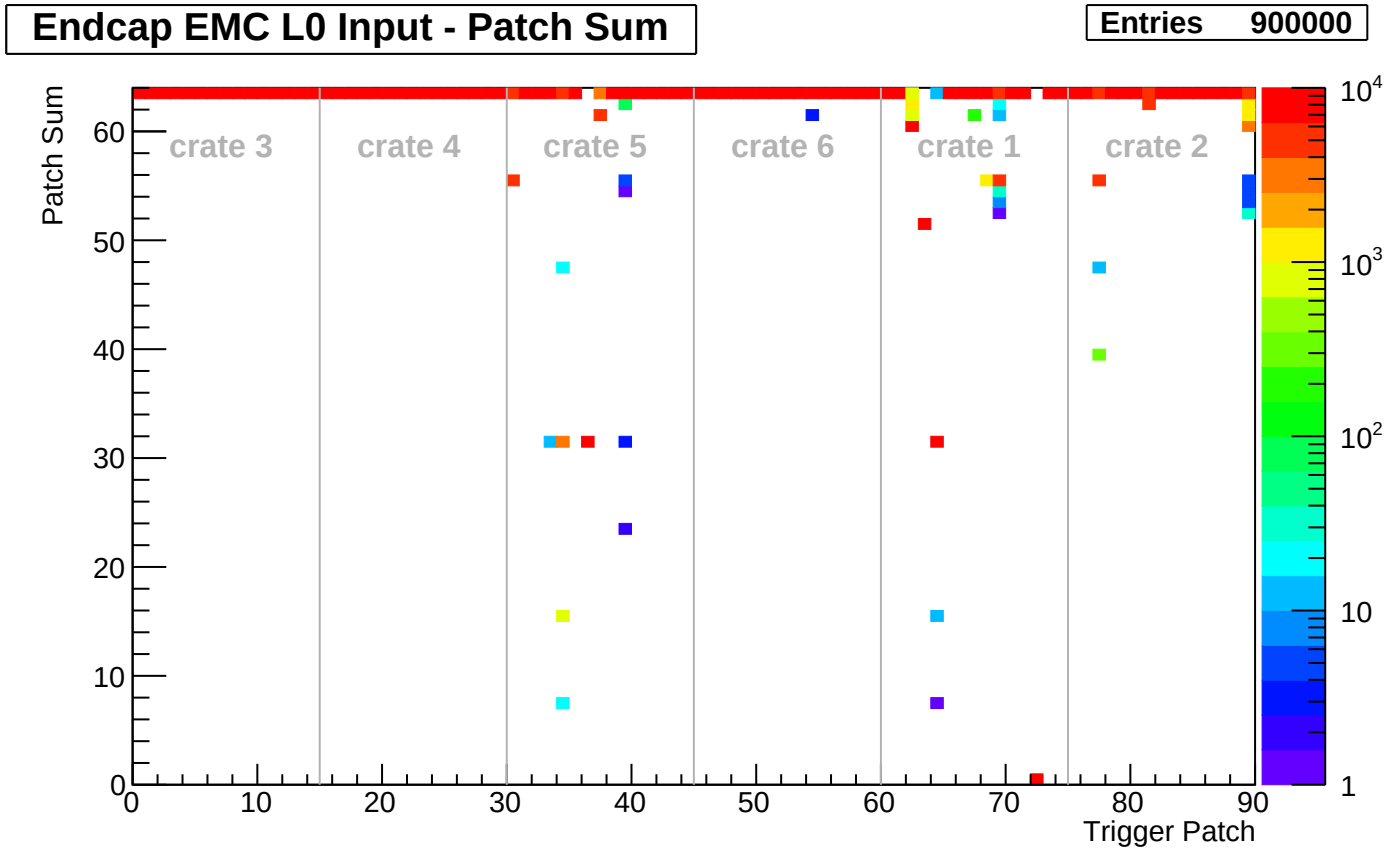
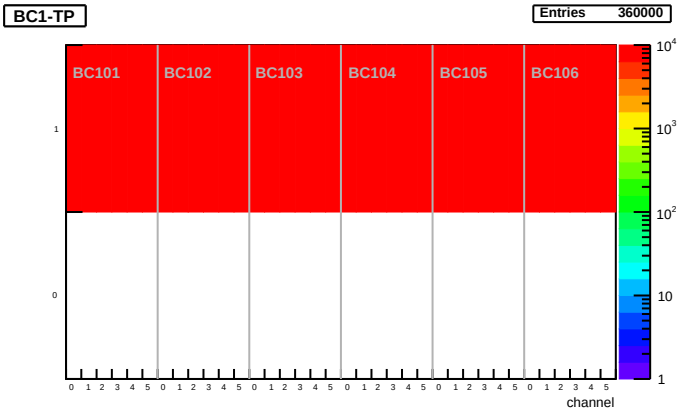
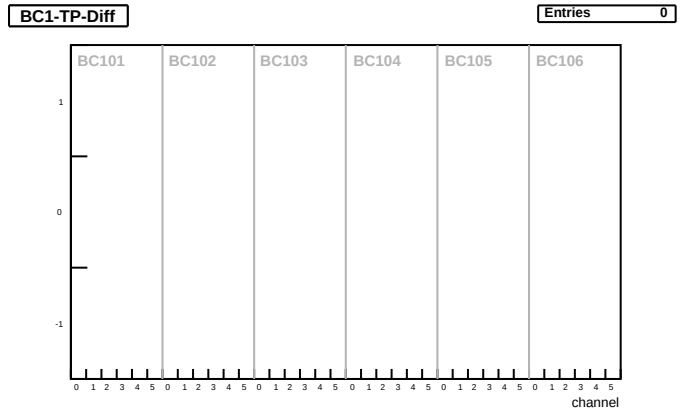

Barrel EMC L0 Input - High Tower
Entries 3000000
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

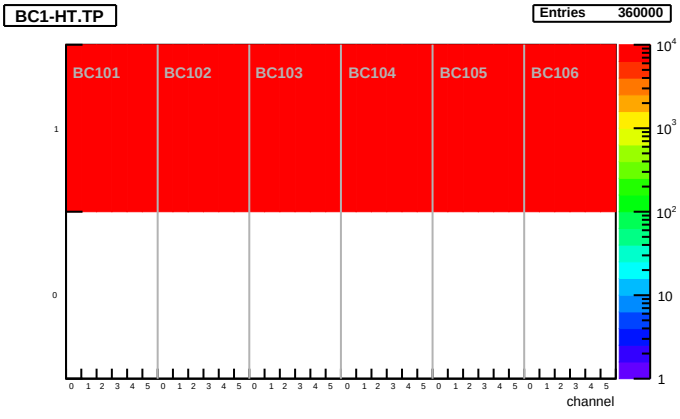

Barrel EMC L0 Input - Patch Sum
Entries 3000000
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

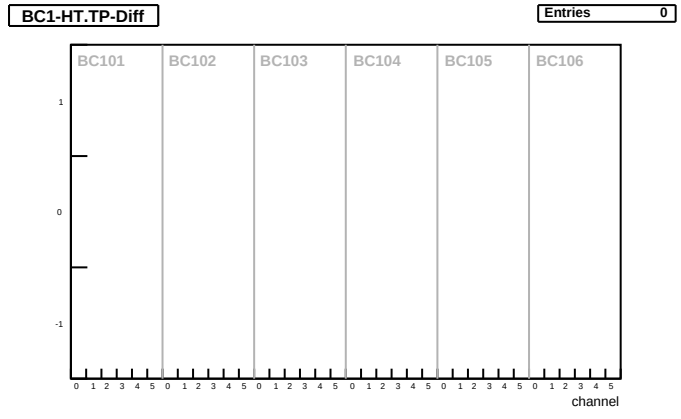

Endcap EMC L0 Input - High Tower
Entries 900000
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

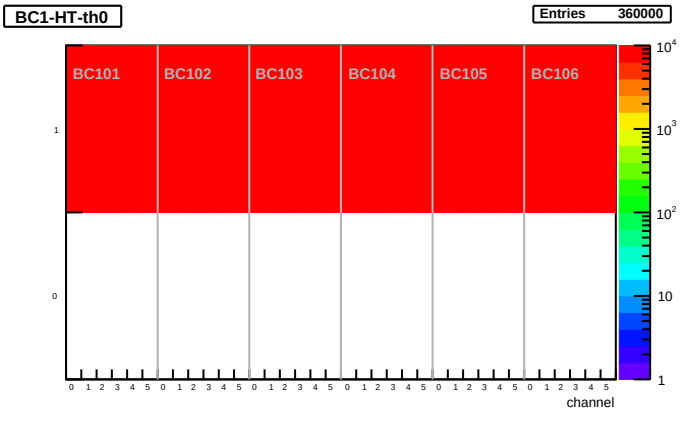

Endcap EMC L0 Input - Patch Sum
Entries 900000
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

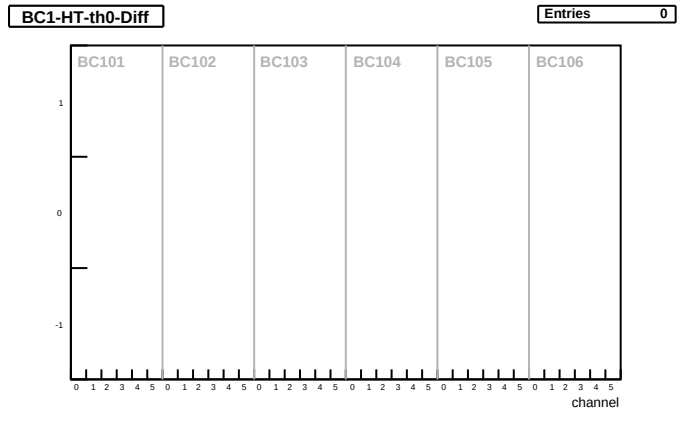

BC1-TP
Entries 360000
BC101
BC102
BC103
BC104
BC105
BC106
channel

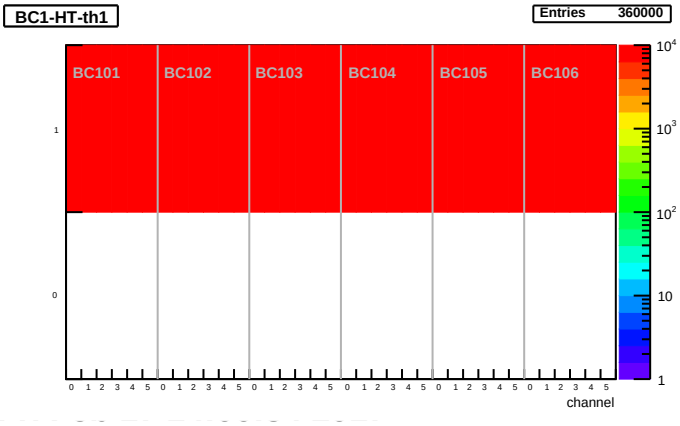

BC1-TP-Diff
Entries 0
BC101
BC102
BC103
BC104
BC105
BC106
channel

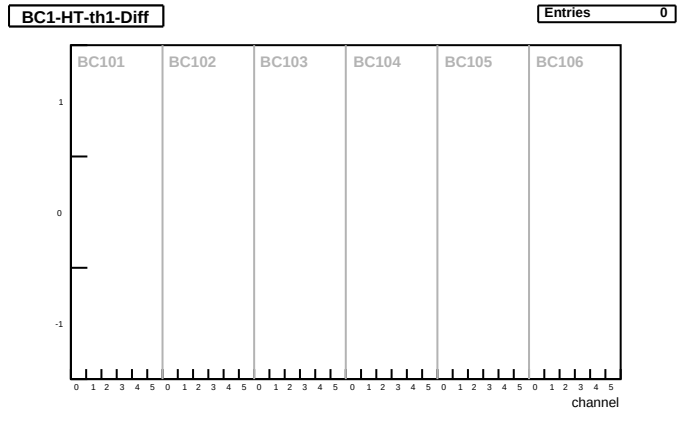

BC1-HT.TP
Entries 360000
BC101
BC102
BC103
BC104
BC105
BC106
channel

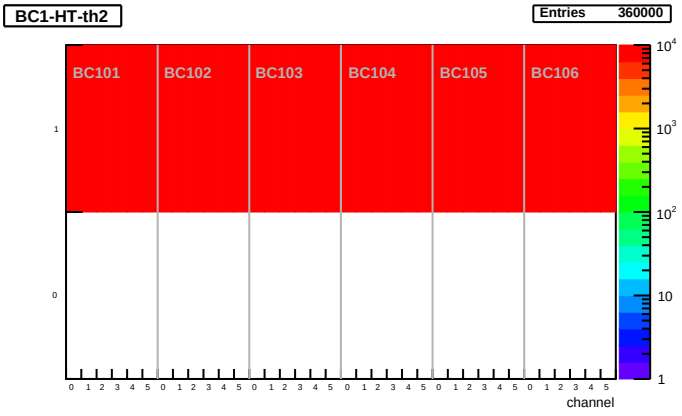

BC1-HT.TP-Diff
Entries 0
BC101
BC102
BC103
BC104
BC105
BC106
channel

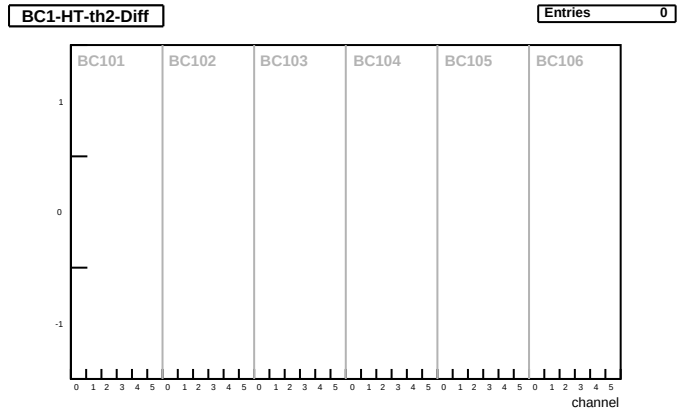

BC1-HT-th0
Entries 360000
BC101
BC102
BC103
BC104
BC105
BC106
channel

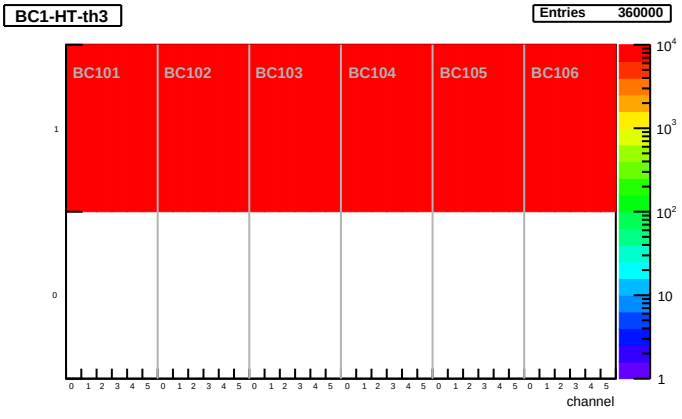

BC1-HT-th0-Diff
Entries 0
BC101
BC102
BC103
BC104
BC105
BC106
channel

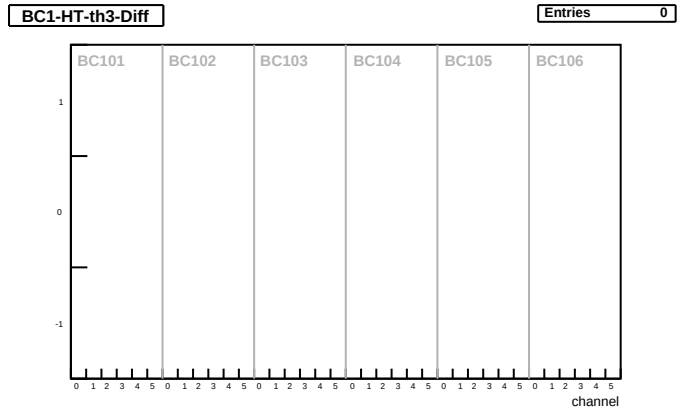

BC1-HT-th1
Entries 360000
BC101
BC102
BC103
BC104
BC105
BC106
channel

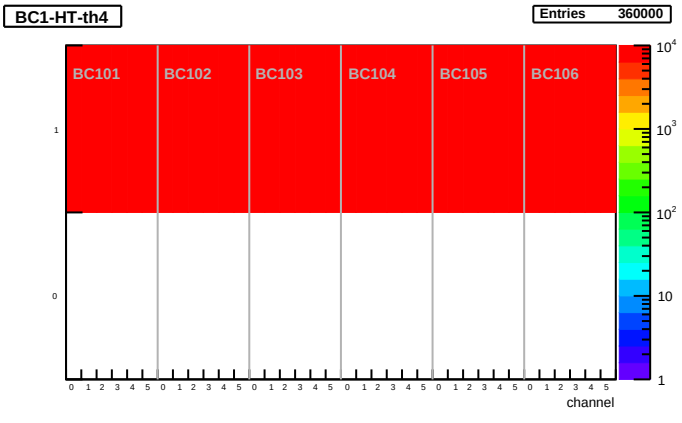

BC1-HT-th1-Diff
Entries 0
BC101
BC102
BC103
BC104
BC105
BC106
channel

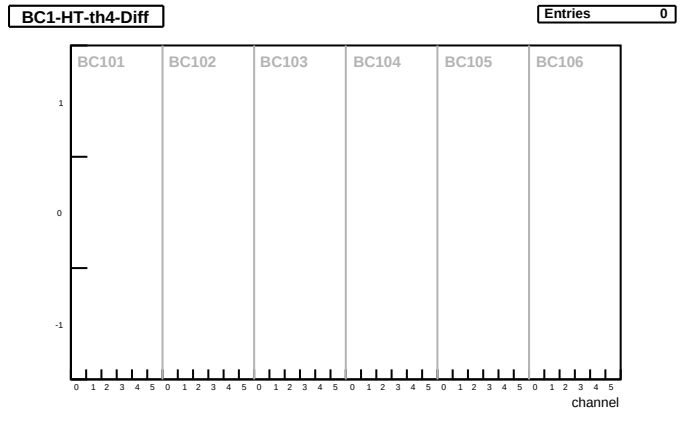

BC1-HT-th2
Entries 360000
BC101
BC102
BC103
BC104
BC105
BC106
channel

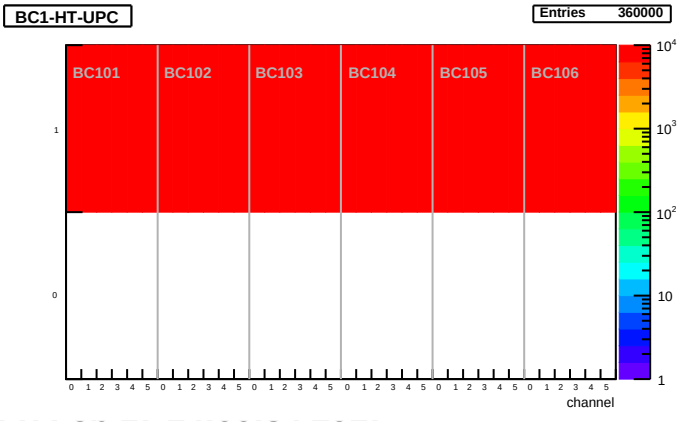

BC1-HT-th2-Diff
Entries 0
BC101
BC102
BC103
BC104
BC105
BC106
channel

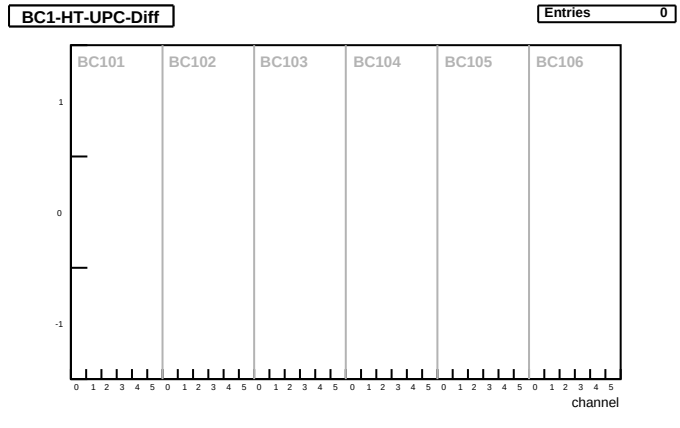

BC1-HT-th3
Entries 360000
BC101
BC102
BC103
BC104
BC105
BC106
channel

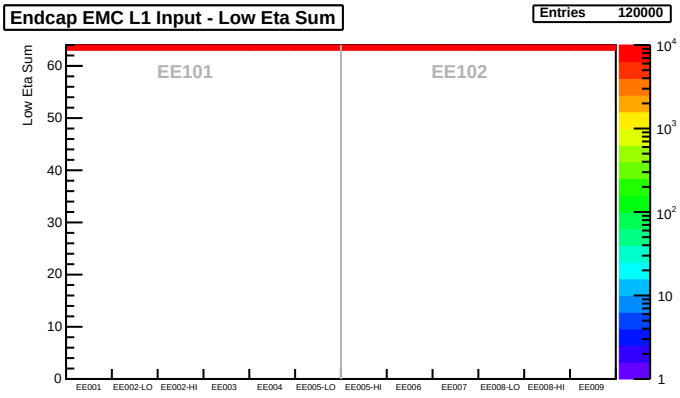

BC1-HT-th3-Diff
Entries 0
BC101
BC102
BC103
BC104
BC105
BC106
channel

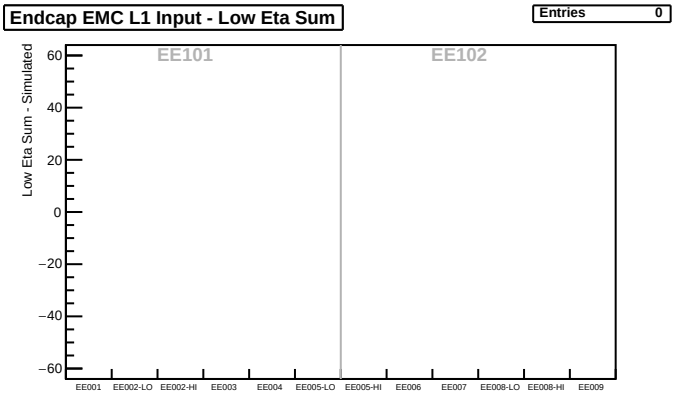

BC1-HT-th4
Entries 360000
BC101
BC102
BC103
BC104
BC105
BC106
channel

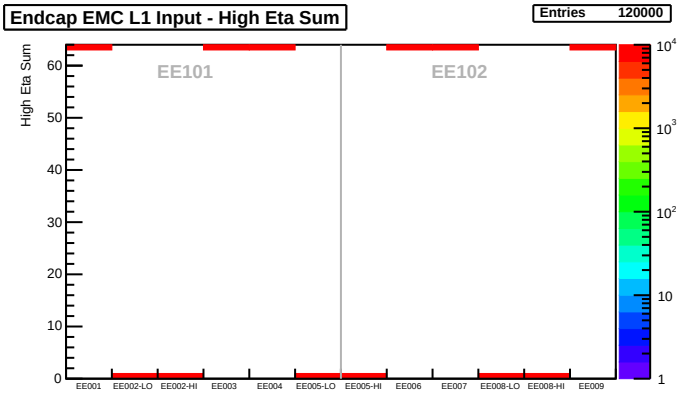

BC1-HT-th4-Diff
Entries 0
BC101
BC102
BC103
BC104
BC105
BC106
channel

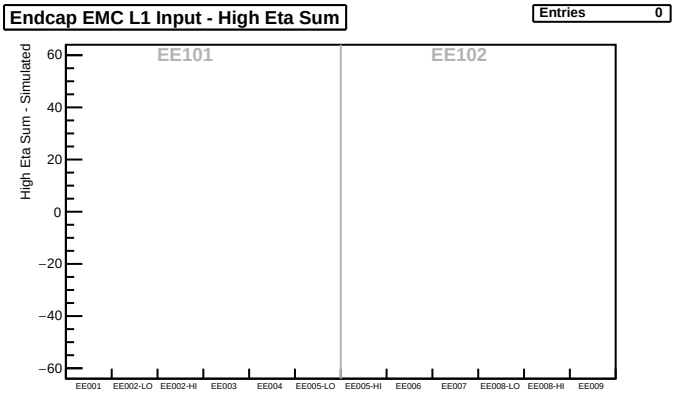

BC1-HT-UPC
Entries 360000
BC101
BC102
BC103
BC104
BC105
BC106
channel

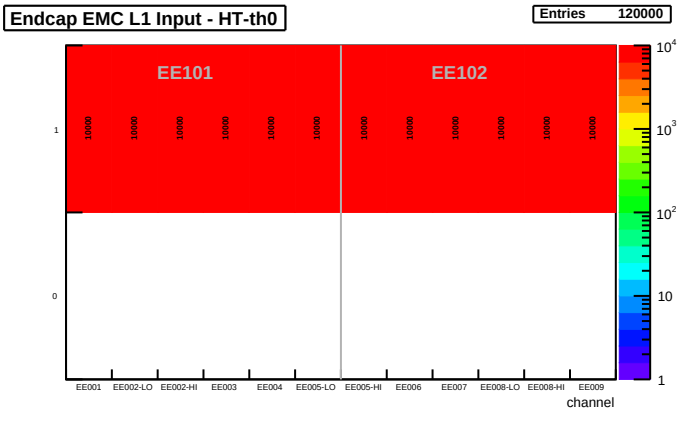

BC1-HT-UPC-Diff
Entries 0
BC101
BC102
BC103
BC104
BC105
BC106
channel

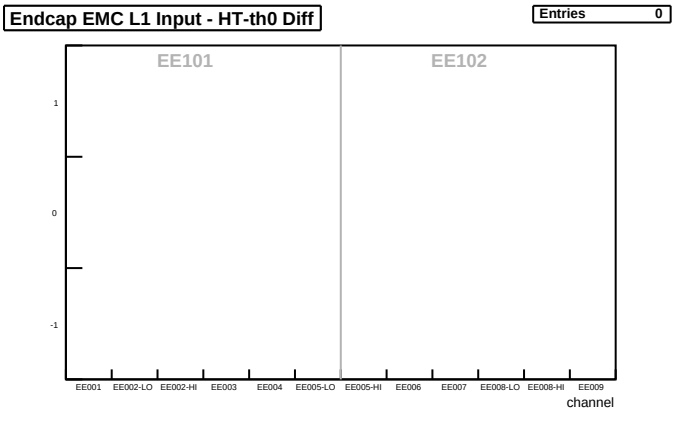

Endcap EMC L1 Input - Low Eta Sum
Entries 120000
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

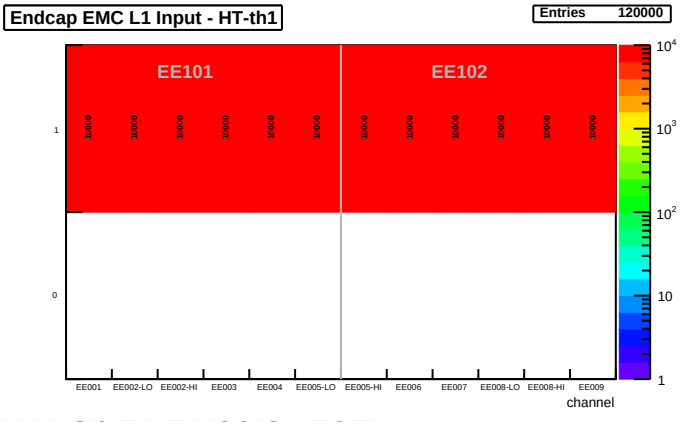

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

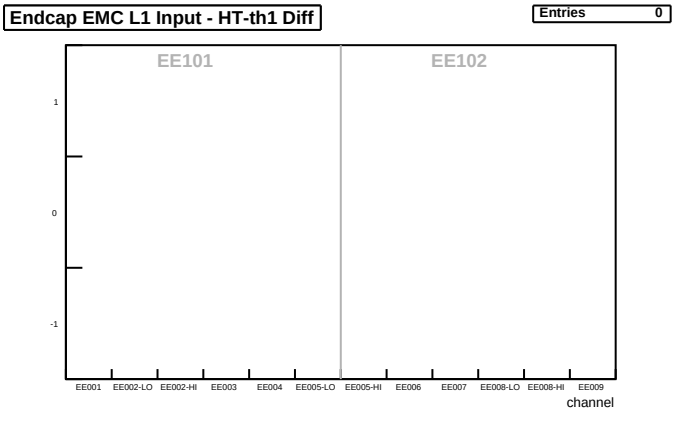

Endcap EMC L1 Input - High Eta Sum
Entries 120000
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

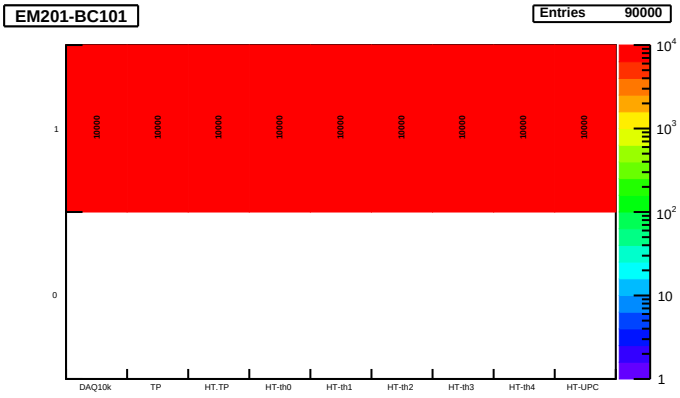

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

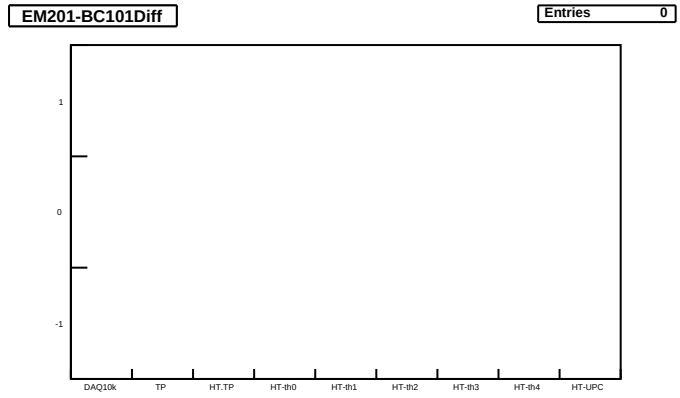

Endcap EMC L1 Input - HT-th0
Entries 120000
EE101
EE102
10000
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009
channel

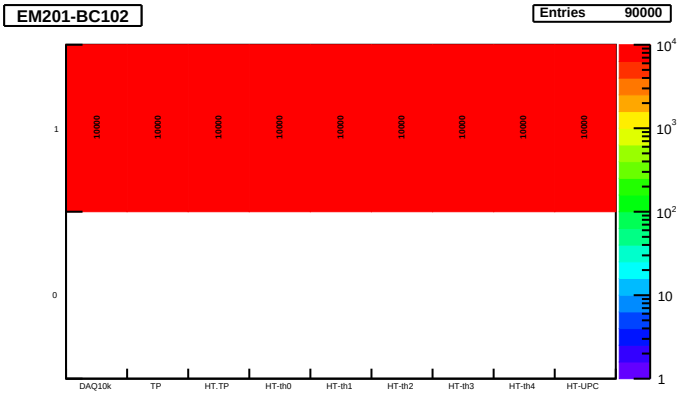

Endcap EMC L1 Input - HT-th0 Diff
Entries 0
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009
channel

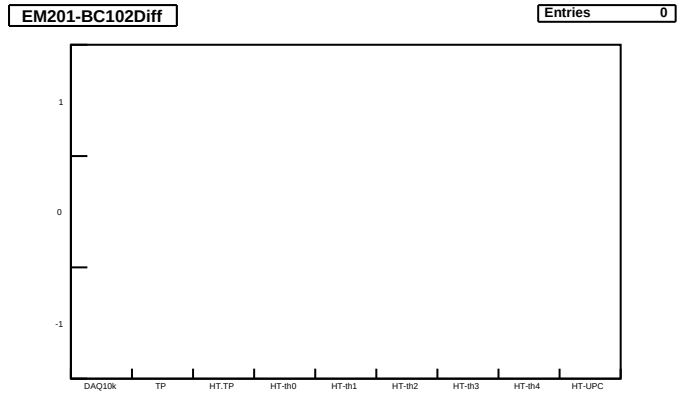

Endcap EMC L1 Input - HT-th1
Entries 120000
EE101
EE102
10000
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009
channel

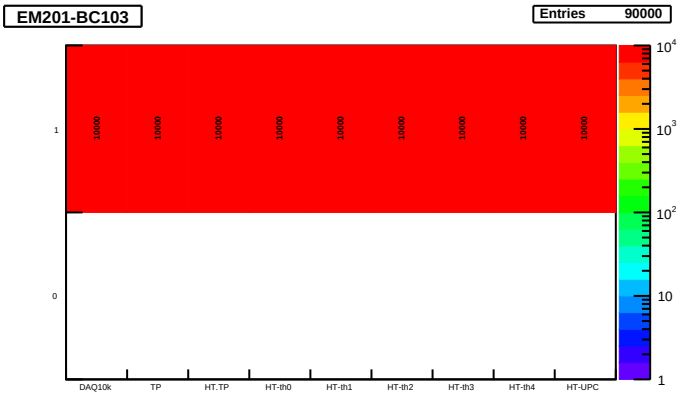

Endcap EMC L1 Input - HT-th1 Diff
Entries 0
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009
channel

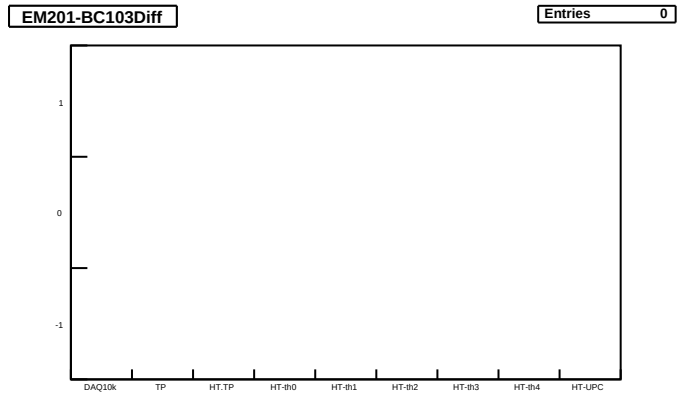

EM201-BC101
Entries 90000
10000
DAQ10k
TP
HT.TP
HT-th0
HT-th1
HT-th2
HT-th3
HT-th4
HT-UPC

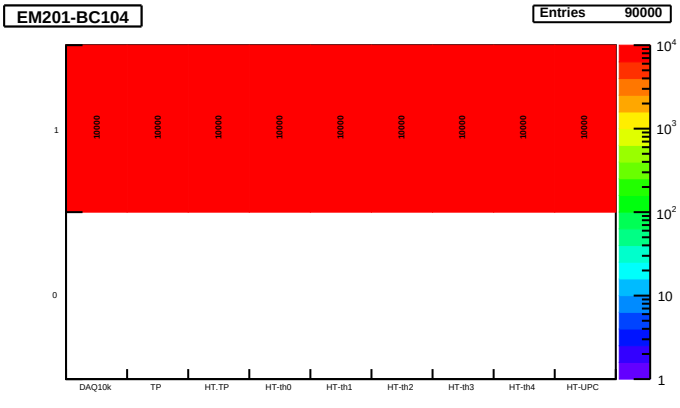

EM201-BC101Diff
Entries 0
DAQ10k
TP
HT.TP
HT-th0
HT-th1
HT-th2
HT-th3
HT-th4
HT-UPC

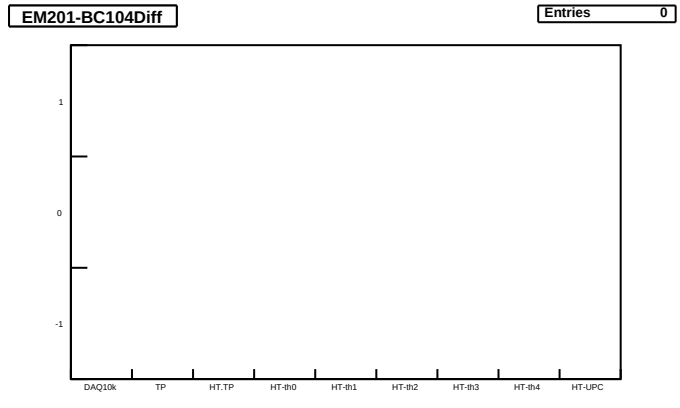

EM201-BC102
Entries 90000
10000
DAQ10k
TP
HT.TP
HT-th0
HT-th1
HT-th2
HT-th3
HT-th4
HT-UPC

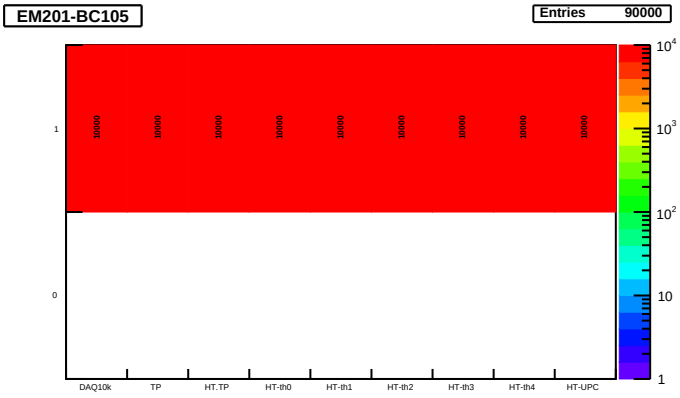

EM201-BC102Diff
Entries 0
DAQ10k
TP
HT.TP
HT-th0
HT-th1
HT-th2
HT-th3
HT-th4
HT-UPC

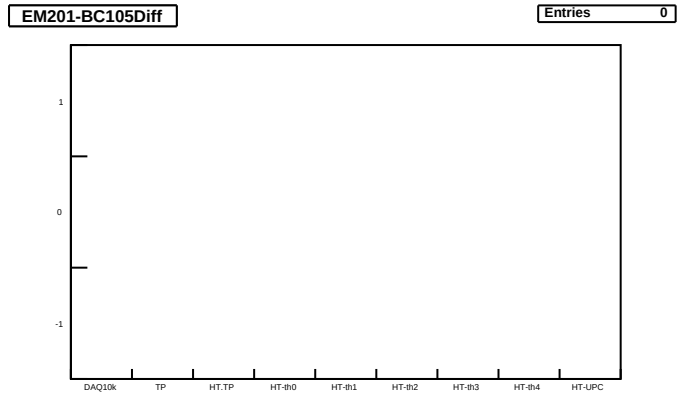

EM201-BC103
Entries 90000
10000
DAQ10k
TP
HT.TP
HT-th0
HT-th1
HT-th2
HT-th3
HT-th4
HT-UPC

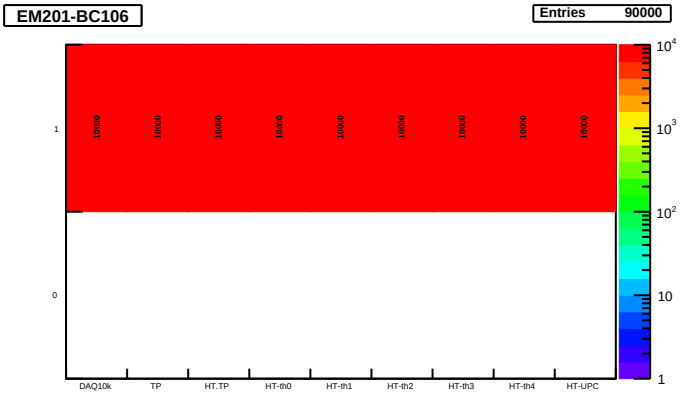

EM201-BC103Diff
Entries 0
DAQ10k
TP
HT.TP
HT-th0
HT-th1
HT-th2
HT-th3
HT-th4
HT-UPC

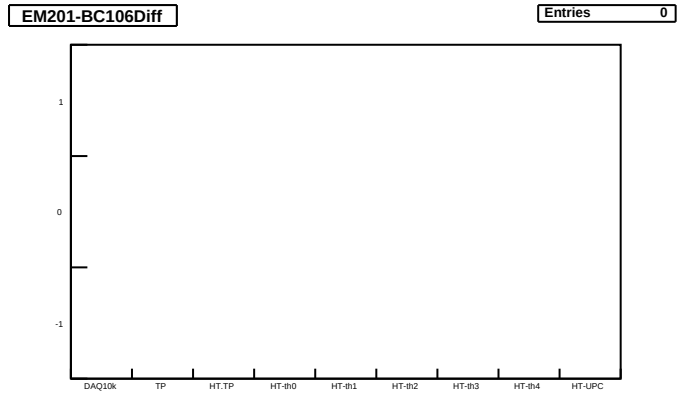

EM201-BC104
Entries 90000
10000
DAQ10k
TP
HT.TP
HT-th0
HT-th1
HT-th2
HT-th3
HT-th4
HT-UPC

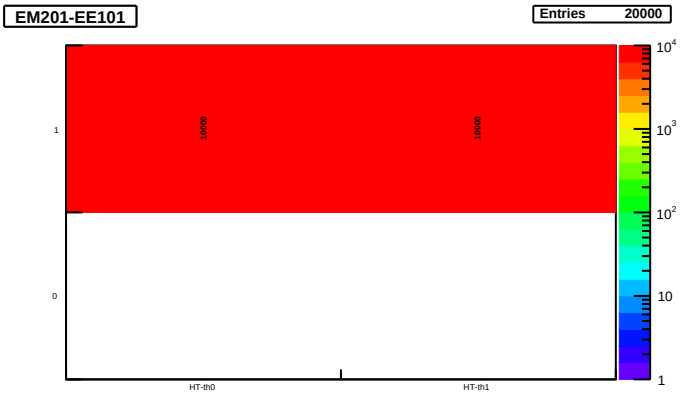

EM201-BC104Diff
Entries 0
DAQ10k
TP
HT.TP
HT-th0
HT-th1
HT-th2
HT-th3
HT-th4
HT-UPC

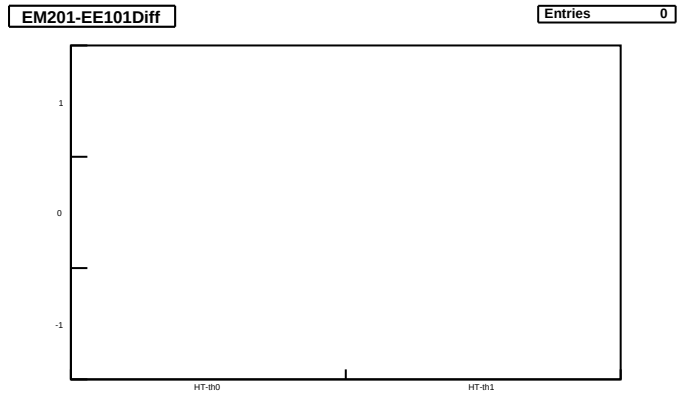

EM201-BC105
Entries 90000
10000
DAQ10k
TP
HT.TP
HT-th0
HT-th1
HT-th2
HT-th3
HT-th4
HT-UPC

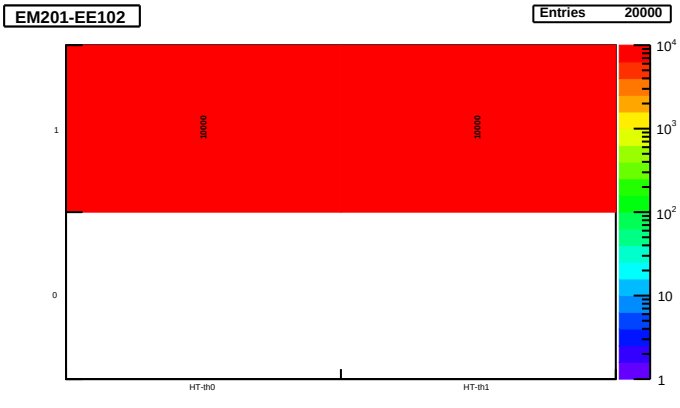

EM201-BC105Diff
Entries 0
DAQ10k
TP
HT.TP
HT-th0
HT-th1
HT-th2
HT-th3
HT-th4
HT-UPC

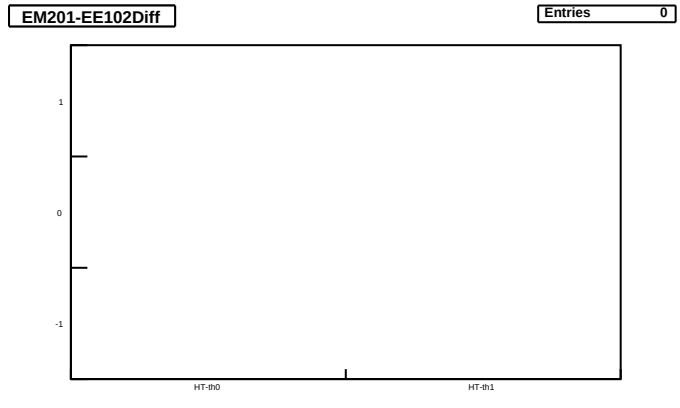

EM201-BC106
Entries 90000
10000
DAQ10k
TP
HT.TP
HT-th0
HT-th1
HT-th2
HT-th3
HT-th4
HT-UPC

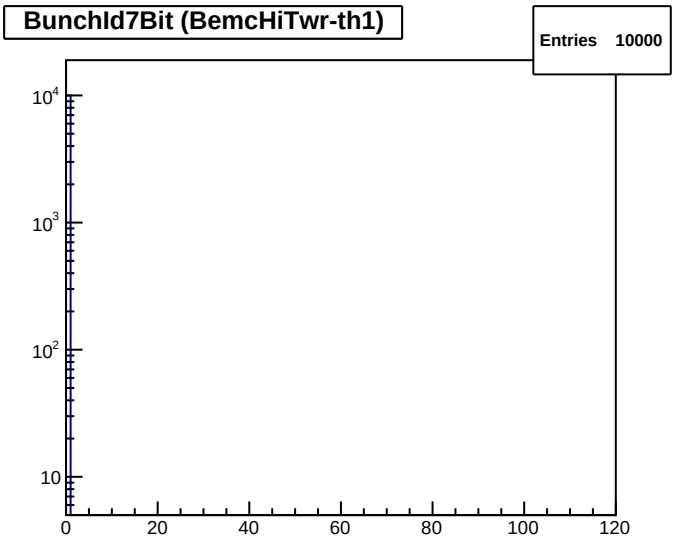

EM201-BC106Diff
Entries 0
DAQ10k
TP
HT.TP
HT-th0
HT-th1
HT-th2
HT-th3
HT-th4
HT-UPC

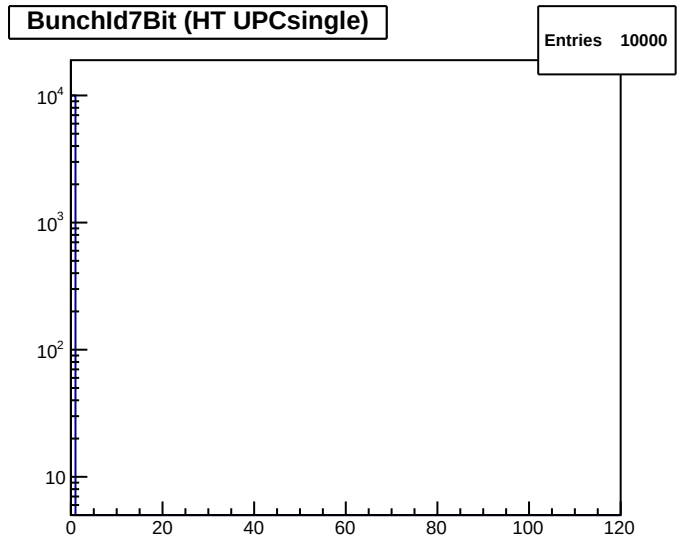

EM201-EE101
Entries 20000
10000
HT-th0
HT-th1

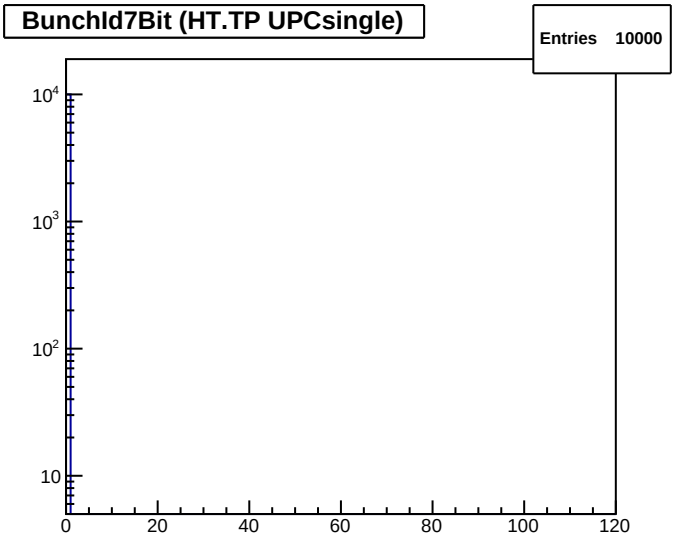

EM201-EE101Diff
Entries 0
HT-th0
HT-th1

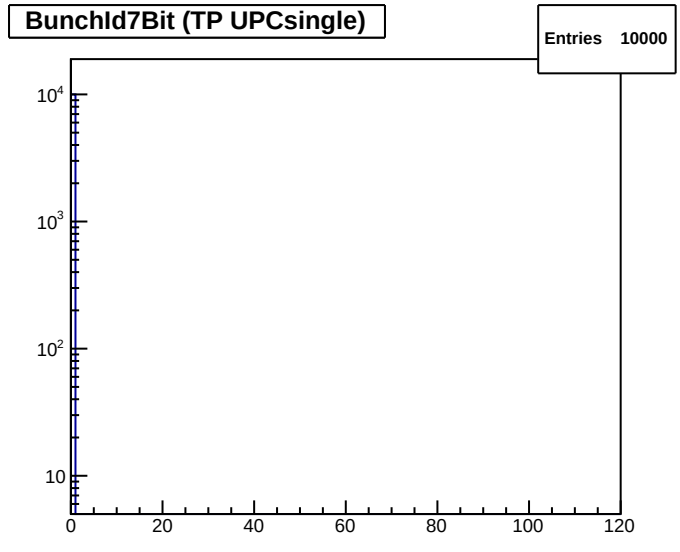

EM201-EE102
Entries 20000
10000
HT-th0
HT-th1

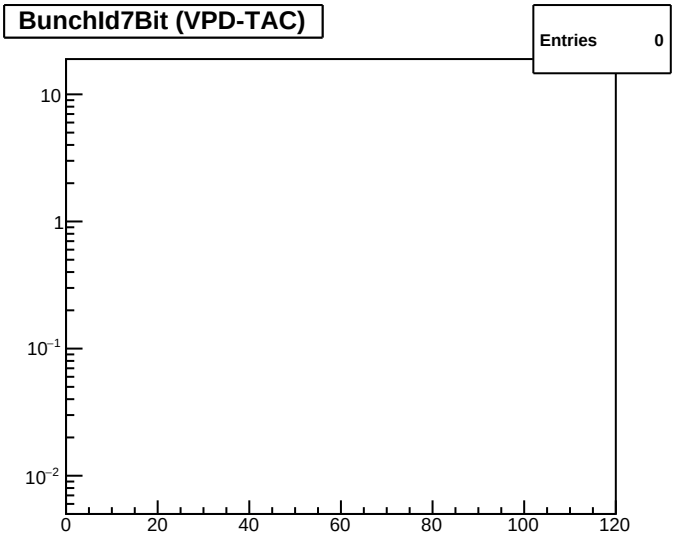

EM201-EE102Diff
Entries 0
HT-th0
HT-th1

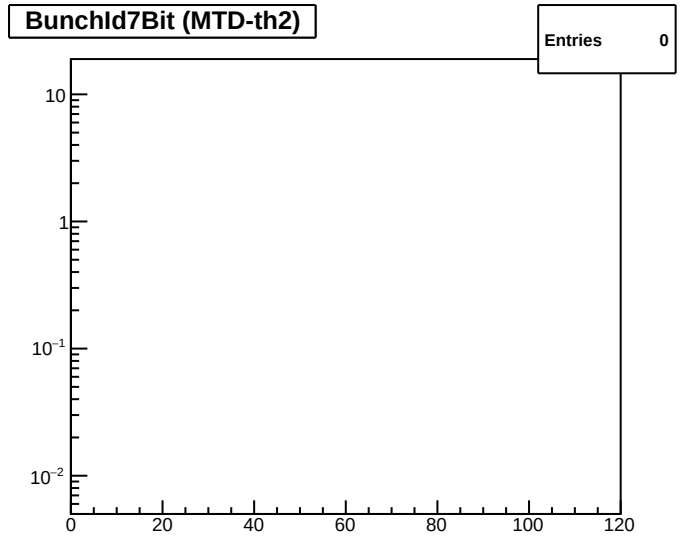

BunchId7Bit (BemcHiTwr-th1)
Entries 10000
$10^4$
$10^3$
$10^2$
10
0 20 40 60 80 100 120

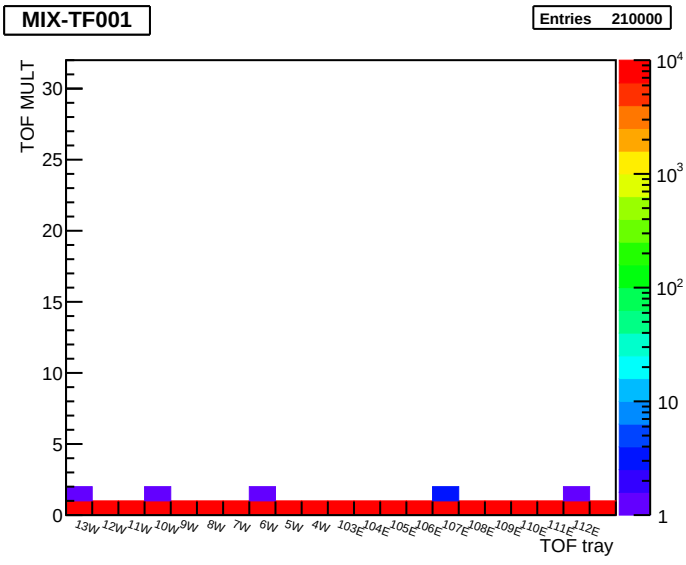

BunchId7Bit (HT UPCsingle)
Entries 10000
$10^4$
$10^3$
$10^2$
10
0 20 40 60 80 100 120

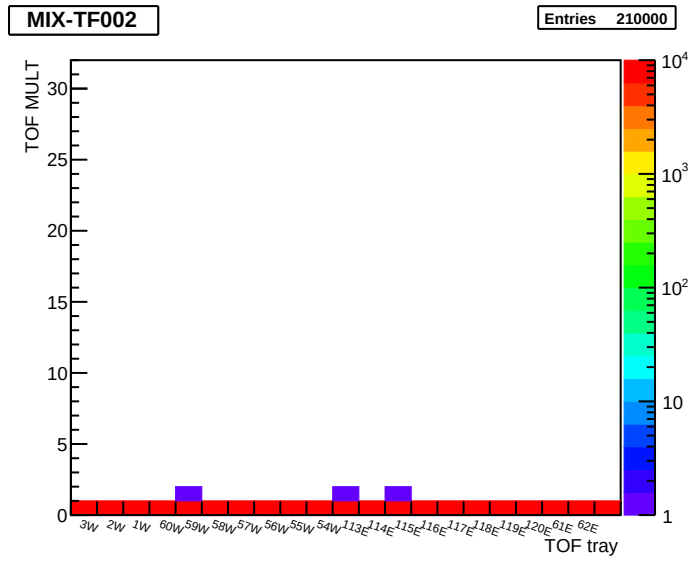

BunchId7Bit (HT.TP UPCsingle)
Entries 10000
$10^4$
$10^3$
$10^2$
10
0 20 40 60 80 100 120

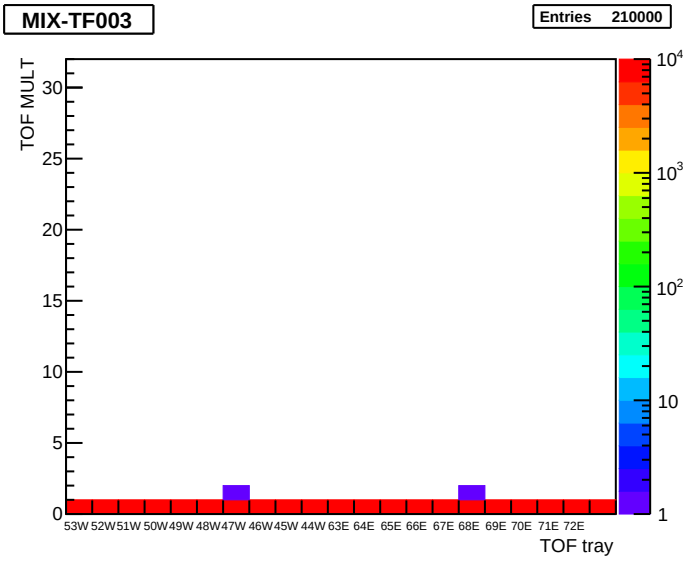

BunchId7Bit (TP UPCsingle)
Entries 10000
$10^4$
$10^3$
$10^2$
10
0 20 40 60 80 100 120

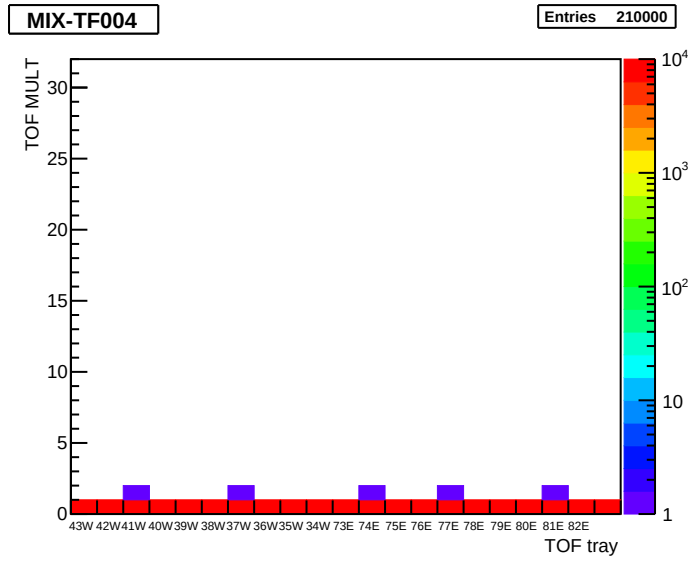

BunchId7Bit (VPD-TAC)
Entries 0
10
1
$10^{-1}$
$10^{-2}$
0 20 40 60 80 100 120

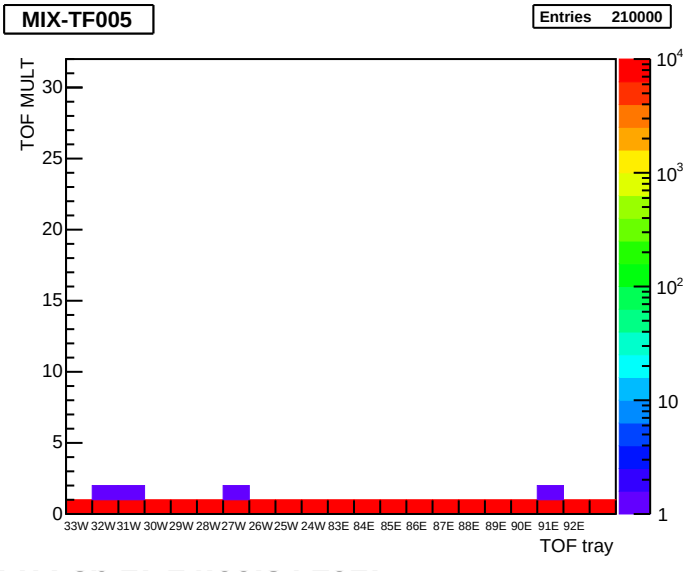

BunchId7Bit (MTD-th2)
Entries 0
10
1
$10^{-1}$
$10^{-2}$
0 20 40 60 80 100 120

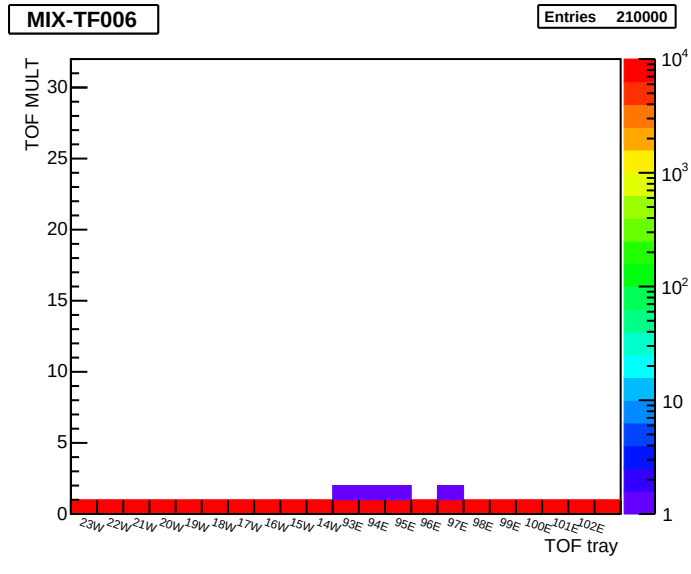

MIX-TF001
Entries 210000
TOF MULT
TOF tray
13W 12W 11W 10W 9W 8W 7W 6W 5W 4W 103E 104E 105E 106E 107E 108E 109E 110E 111E 112E

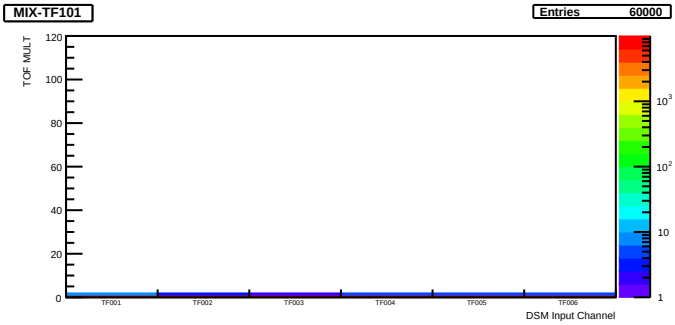

MIX-TF002
Entries 210000
TOF MULT
TOF tray
3W 2W 1W 60W 59W 58W 57W 56W 55W 54W 113E 114E 115E 116E 117E 118E 119E 120E 61E 62E

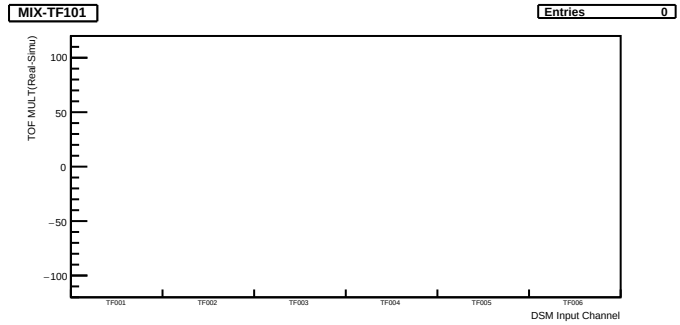

MIX-TF003
Entries 210000
TOF MULT
TOF tray
53W 52W 51W 50W 49W 48W 47W 46W 45W 44W 63E 64E 65E 66E 67E 68E 69E 70E 71E 72E

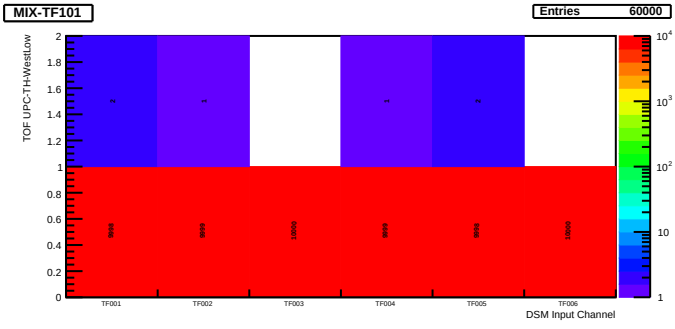

MIX-TF004
Entries 210000
TOF MULT
TOF tray
43W 42W 41W 40W 39W 38W 37W 36W 35W 34W 73E 74E 75E 76E 77E 78E 79E 80E 81E 82E

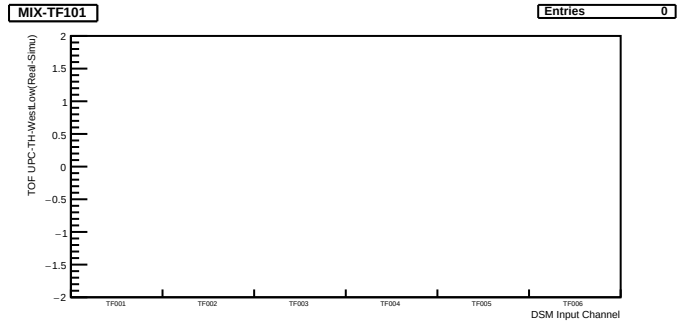

MIX-TF005
Entries 210000
TOF MULT
TOF tray
33W 32W 31W 30W 29W 28W 27W 26W 25W 24W 83E 84E 85E 86E 87E 88E 89E 90E 91E 92E

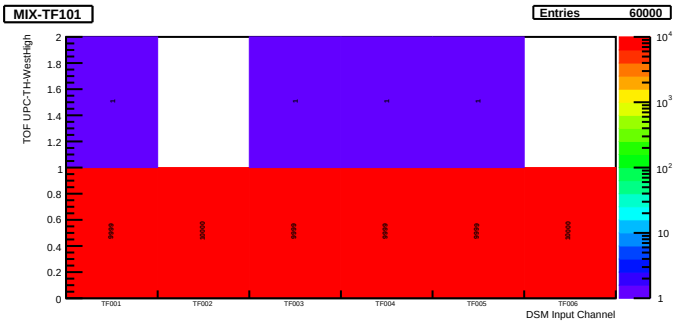

MIX-TF006
Entries 210000
TOF MULT
TOF tray
23W 22W 21W 20W 19W 18W 17W 16W 15W 14W 93E 94E 95E 96E 97E 98E 99E 100E 101E 102E

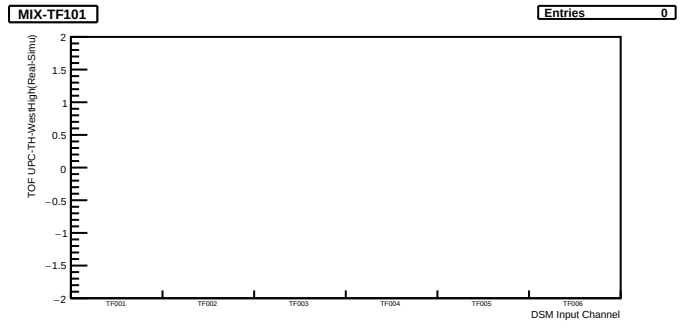

MIX-TF101
Entries 60000
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

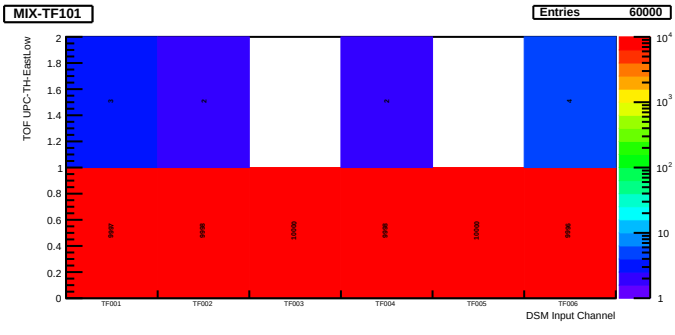

MIX-TF101
Entries 0
TOF MULT(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

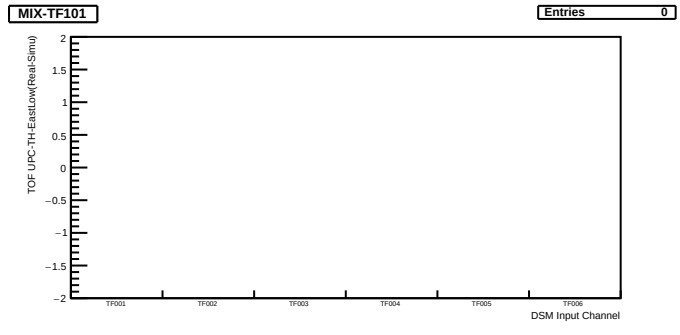

MIX-TF101
Entries 60000
TOF UPC-TH-WestLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

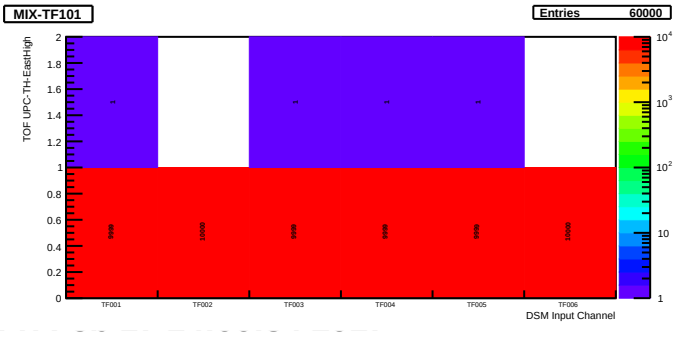

MIX-TF101
Entries 0
TOF UPC-TH-WestLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

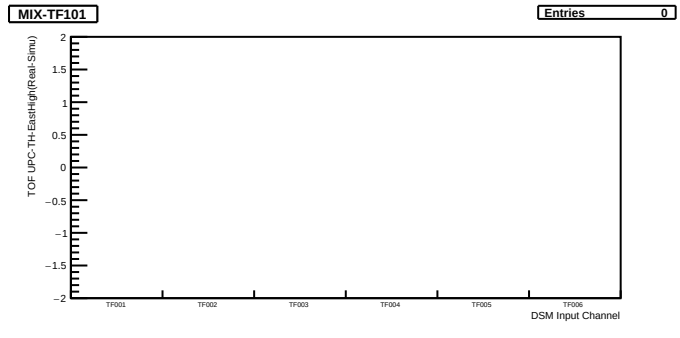

MIX-TF101
Entries 60000
TOF UPC-TH-WestHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

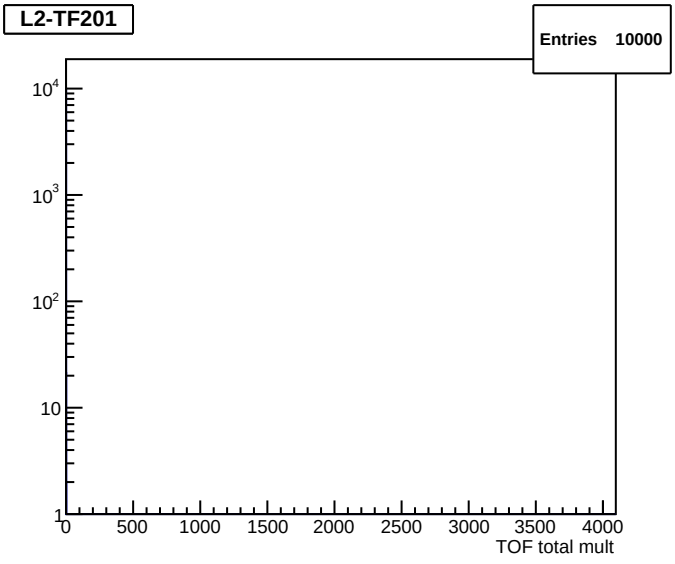

MIX-TF101
Entries 0
TOF UPC-TH-WestHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

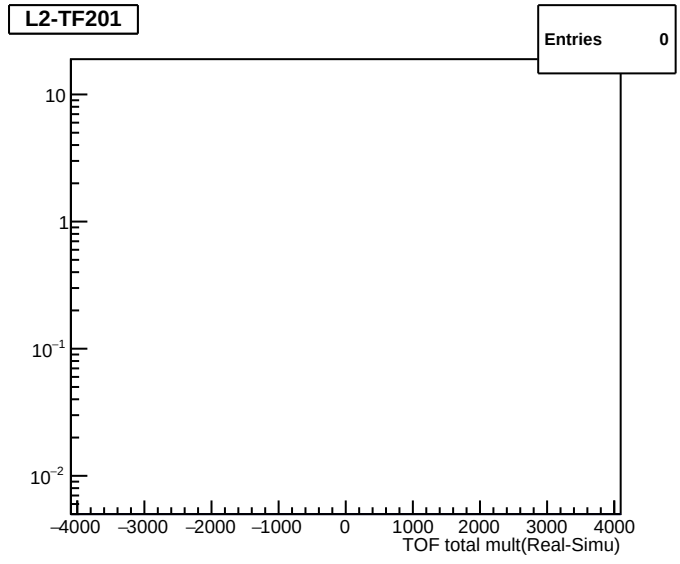

MIX-TF101
Entries 60000
TOF UPC-TH-EastLow
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

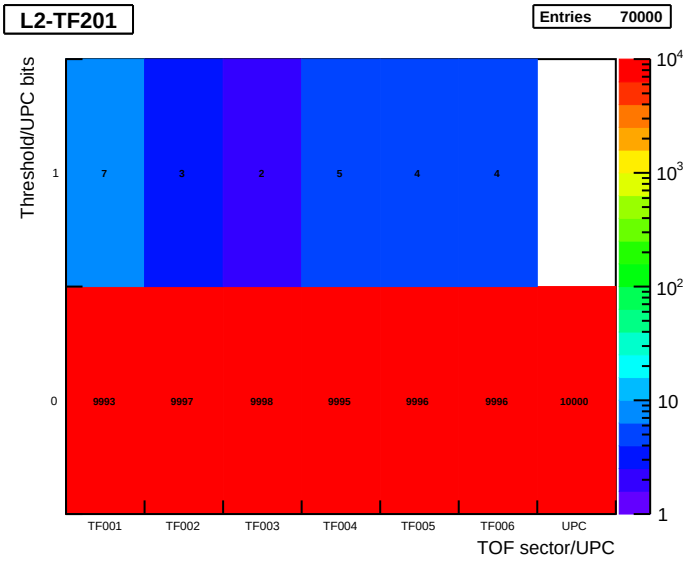

MIX-TF101
Entries 0
TOF UPC-TH-EastLow(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

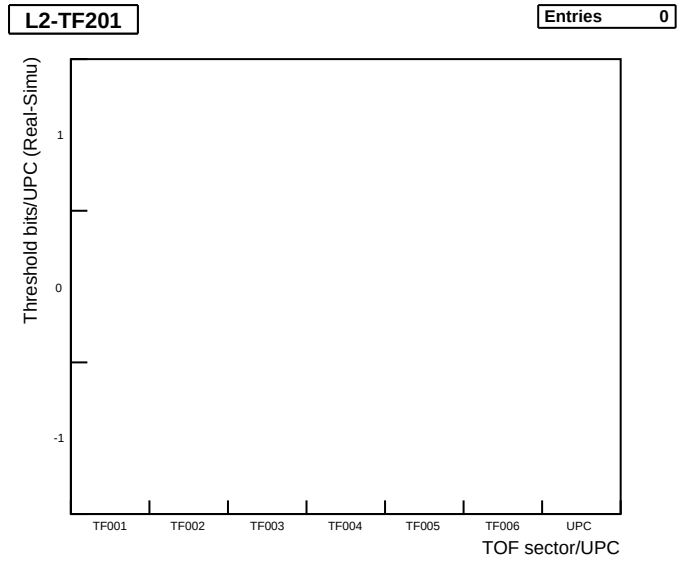

MIX-TF101
Entries 60000
TOF UPC-TH-EastHigh
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

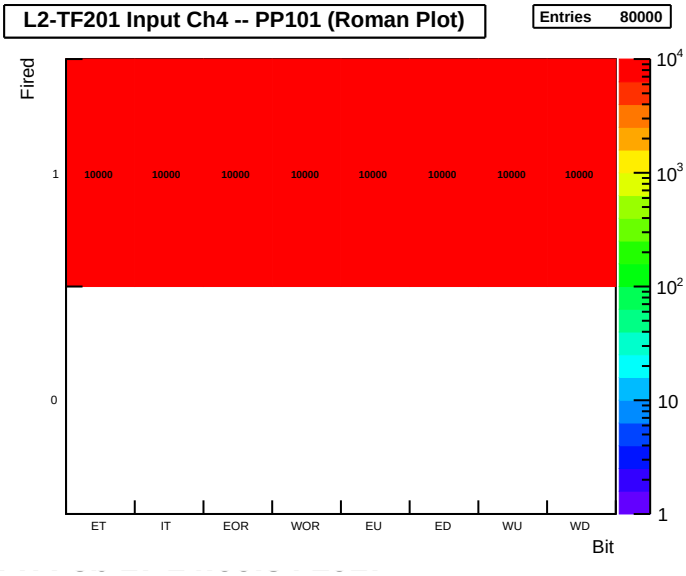

MIX-TF101
Entries 0
TOF UPC-TH-EastHigh(Real-Simu)
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

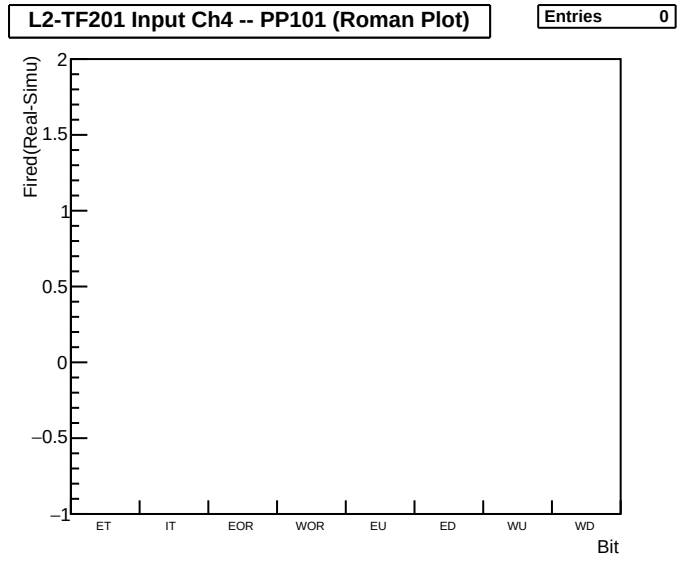

L2-TF201
Entries 10000
TOF total mult

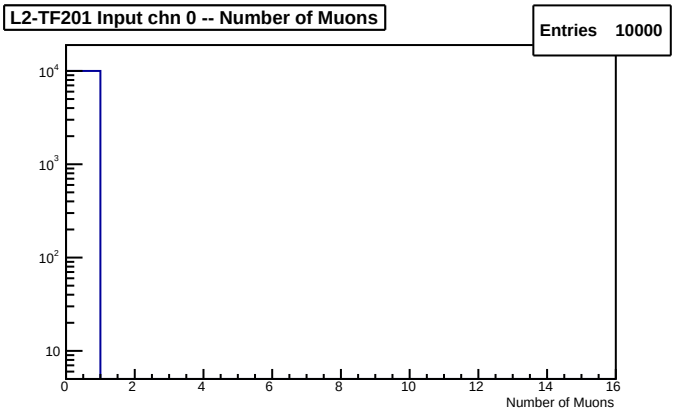

L2-TF201
Entries 0
TOF total mult(Real-Simu)

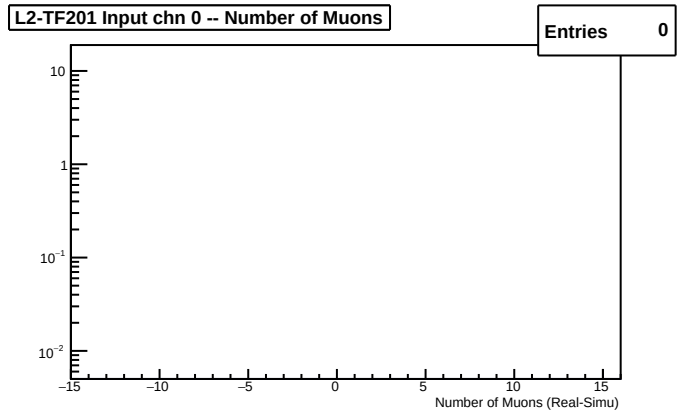

L2-TF201
Entries 70000
Threshold/UPC bits
7 3 2 5 4 4
9993 9997 9998 9995 9996 9996 10000
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

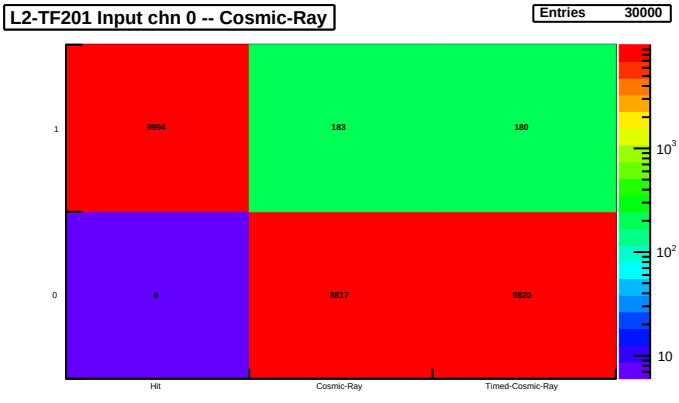

L2-TF201
Entries 0
Threshold bits/UPC (Real-Simu)
TF001 TF002 TF003 TF004 TF005 TF006 UPC
TOF sector/UPC

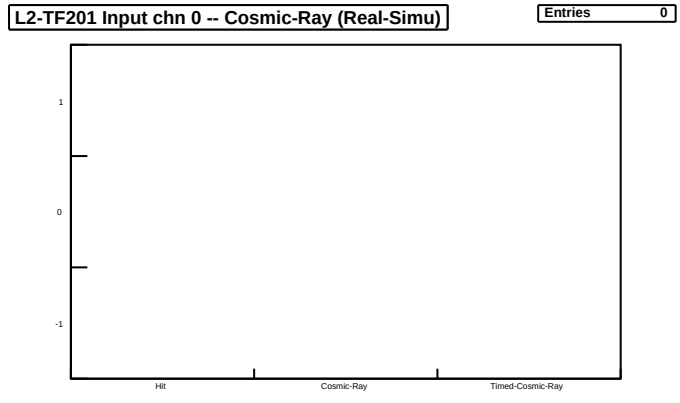

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 80000
Fired
10000 10000 10000 10000 10000 10000 10000 10000
ET IT EOR WOR EU ED WU WD
Bit

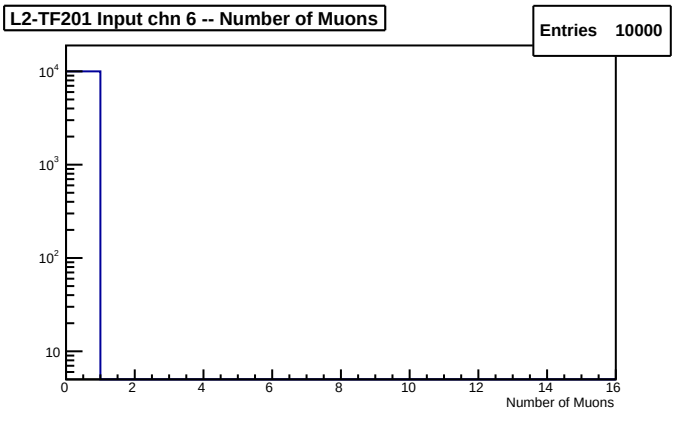

L2-TF201 Input Ch4 -- PP101 (Roman Plot)
Entries 0
Fired(Real-Simu)
ET IT EOR WOR EU ED WU WD
Bit

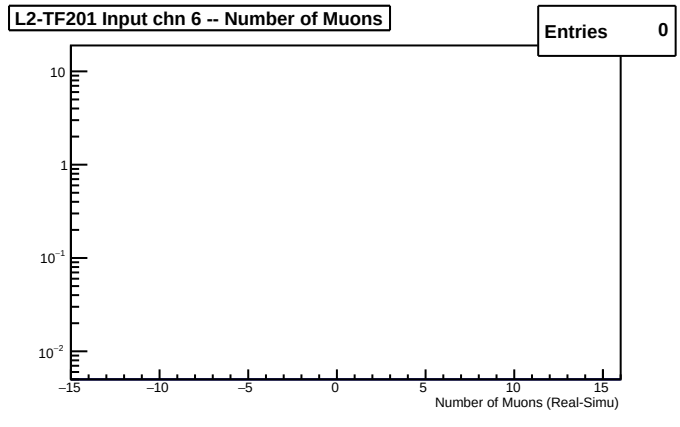

L2-TF201 Input chn 0 -- Number of Muons
Entries 10000
Number of Muons

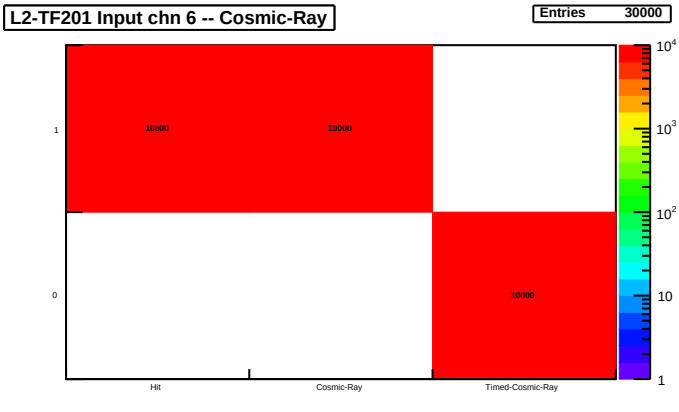

L2-TF201 Input chn 0 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

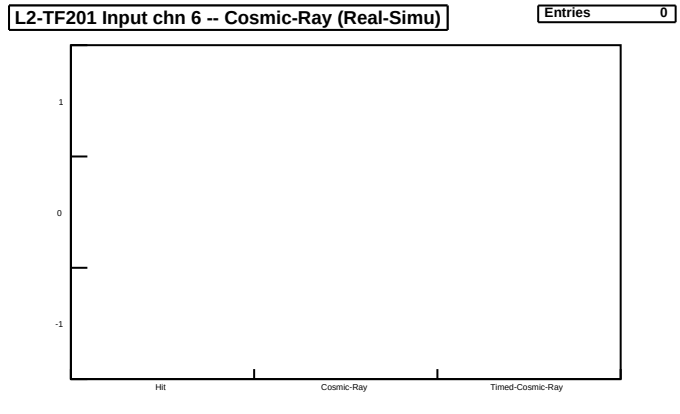

L2-TF201 Input chn 0 -- Cosmic-Ray
Entries 30000
9994
183
180
6
9817
9820
Hit
Cosmic-Ray
Timed-Cosmic-Ray

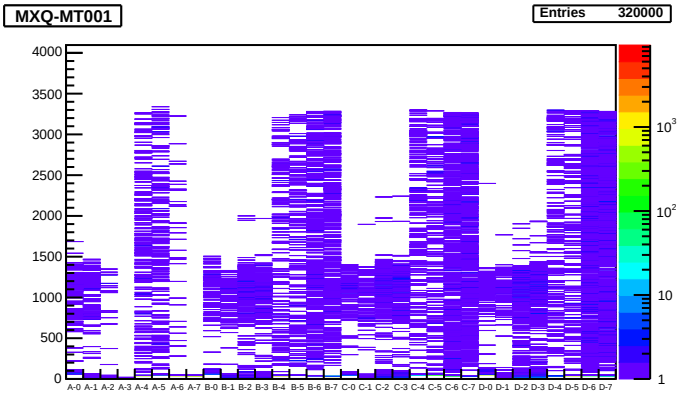

L2-TF201 Input chn 0 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

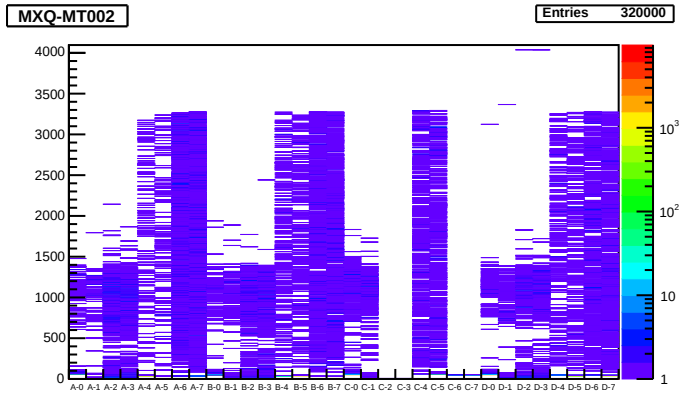

L2-TF201 Input chn 6 -- Number of Muons
Entries 10000
Number of Muons

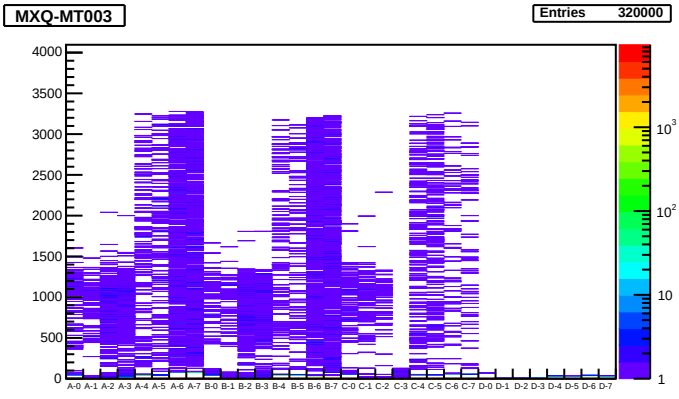

L2-TF201 Input chn 6 -- Number of Muons
Entries 0
Number of Muons (Real-Simu)

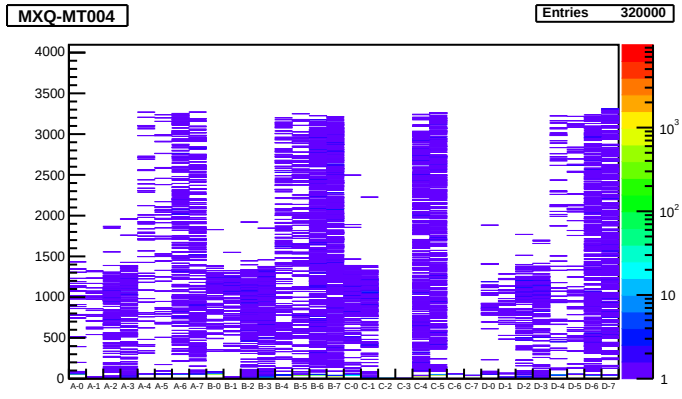

L2-TF201 Input chn 6 -- Cosmic-Ray
Entries 30000
10000
10000
10000
Hit
Cosmic-Ray
Timed-Cosmic-Ray

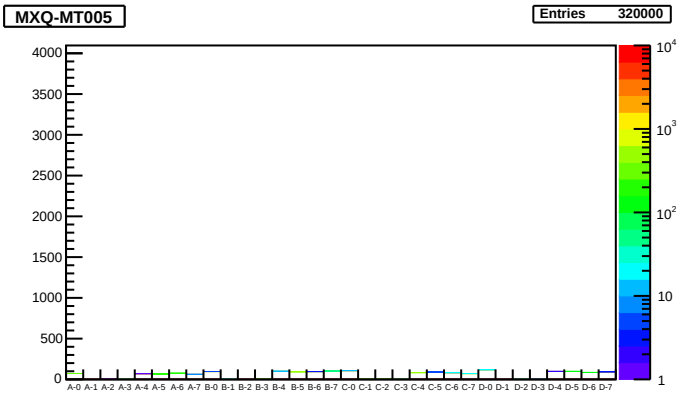

L2-TF201 Input chn 6 -- Cosmic-Ray (Real-Simu)
Entries 0
Hit
Cosmic-Ray
Timed-Cosmic-Ray

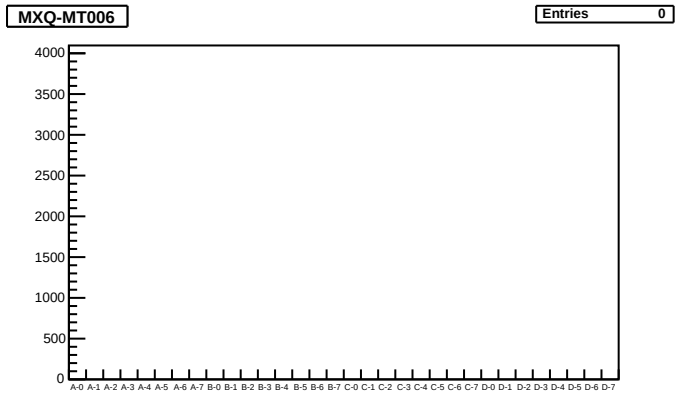

MXQ-MT001
Entries 320000

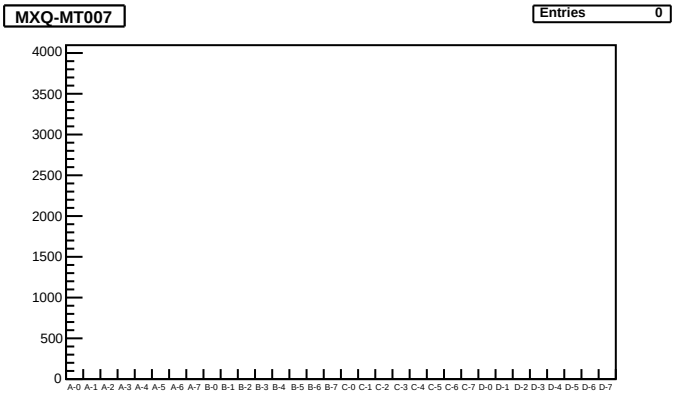

MXQ-MT002
Entries 320000

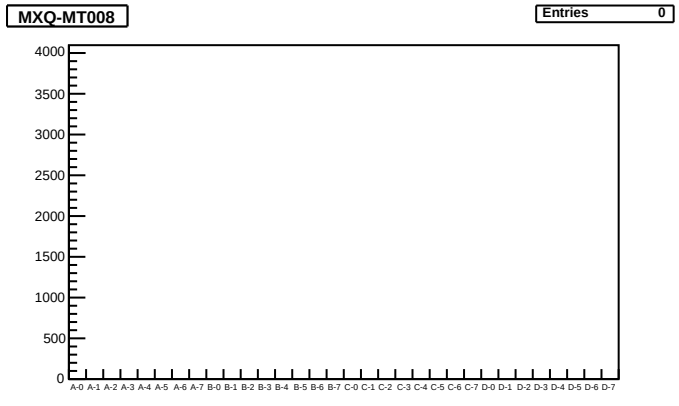

MXQ-MT003
Entries 320000

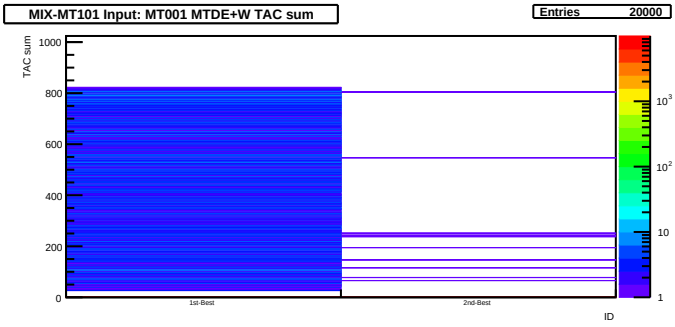

MXQ-MT004
Entries 320000

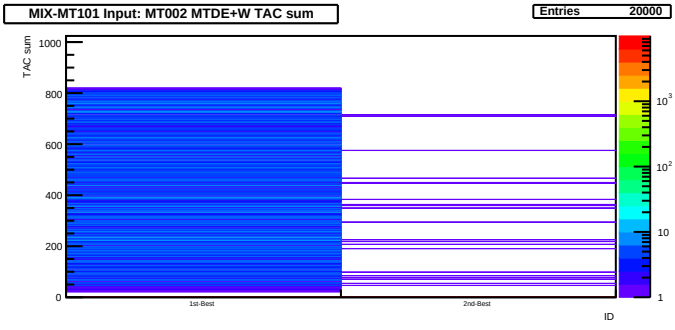

MXQ-MT005
Entries 320000

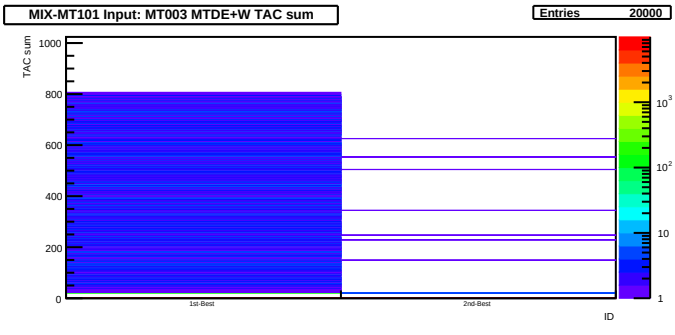

MXQ-MT006
Entries 0

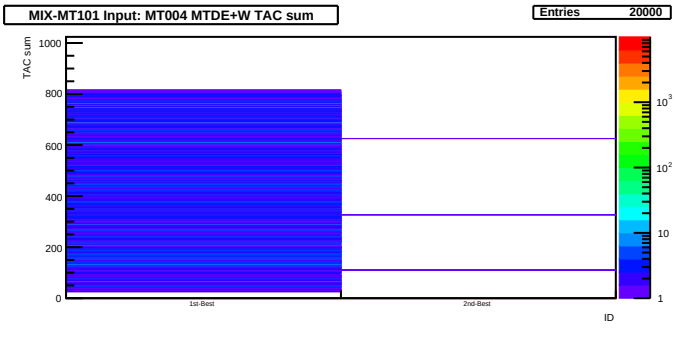

MXQ-MT007
Entries 0

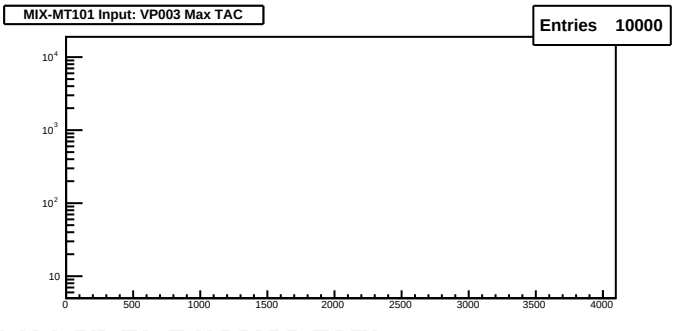

MXQ-MT008
Entries 0

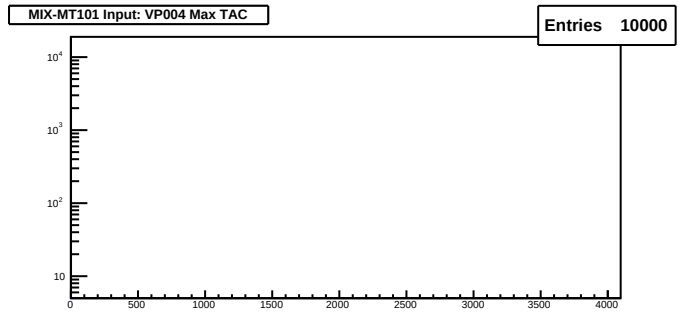

MIX-MT101 Input: MT001 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

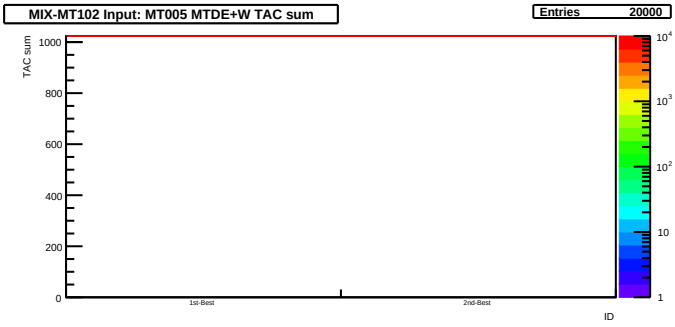

MIX-MT101 Input: MT002 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

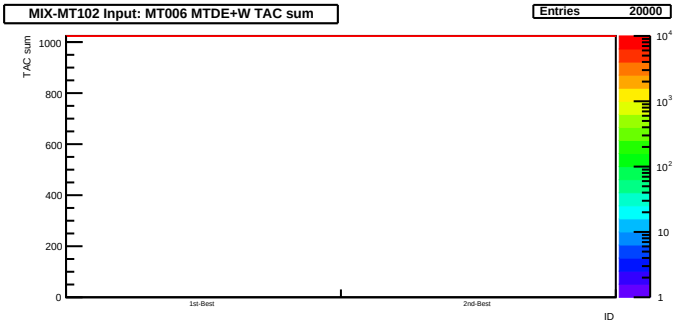

MIX-MT101 Input: MT003 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

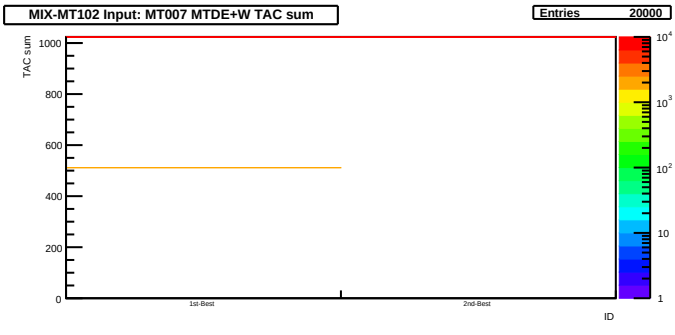

MIX-MT101 Input: MT004 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

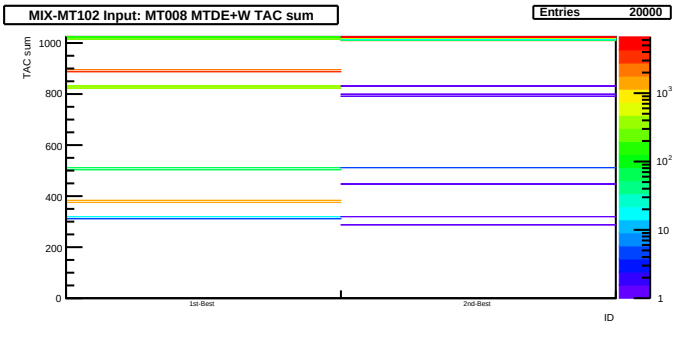

MIX-MT101 Input: VP003 Max TAC
Entries 10000

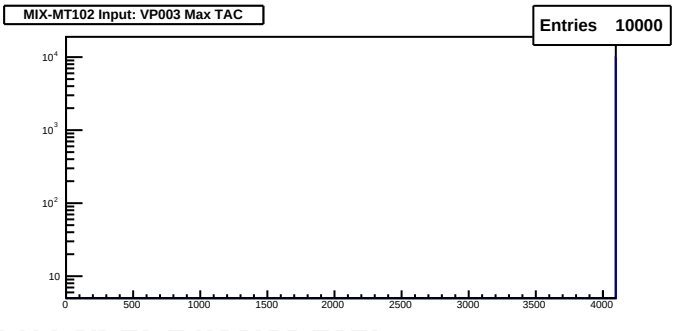

MIX-MT101 Input: VP004 Max TAC
Entries 10000

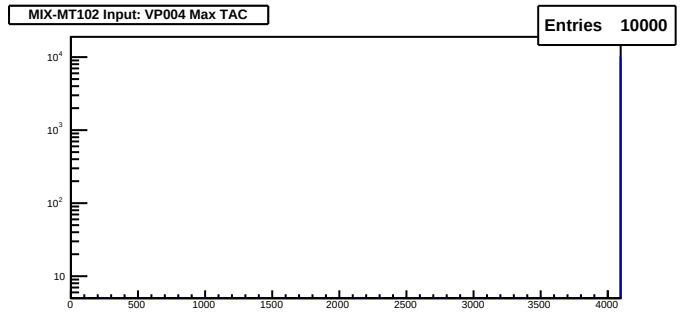

MIX-MT102 Input: MT005 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

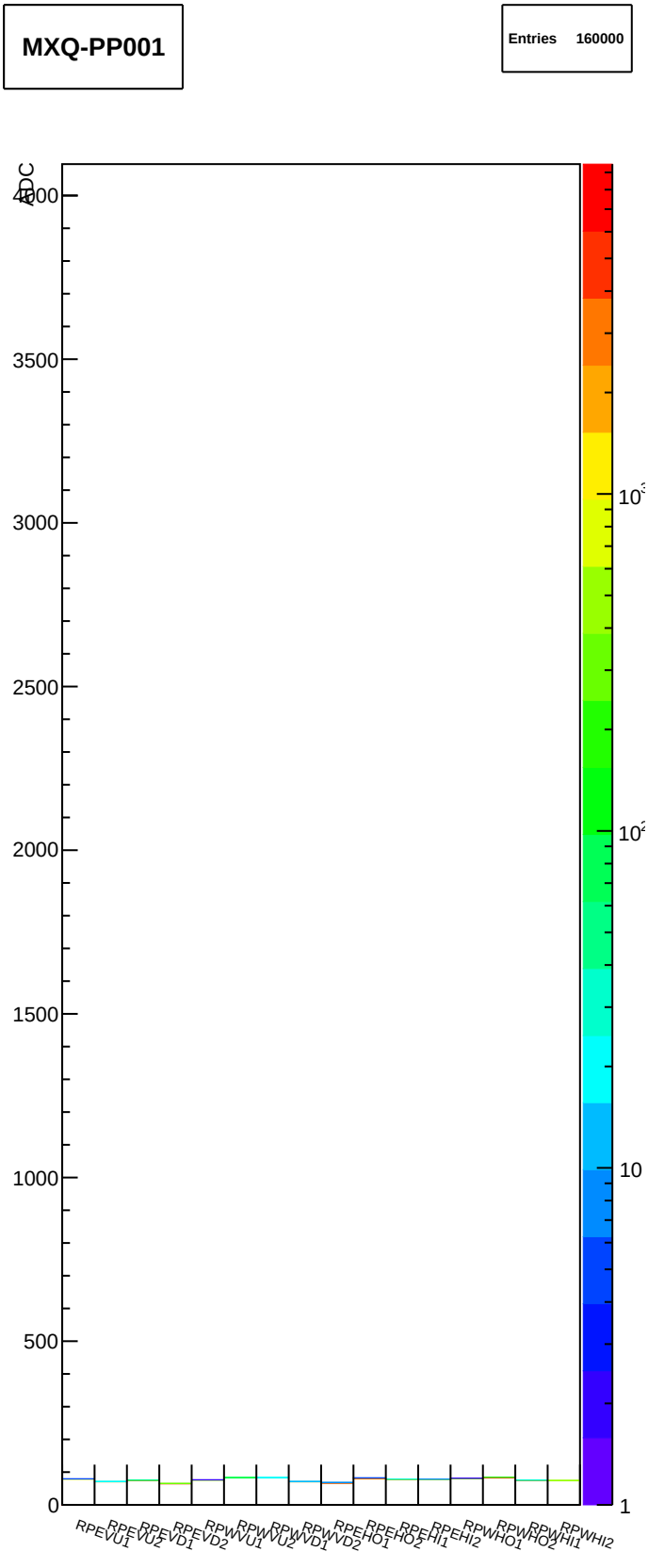

MIX-MT102 Input: MT006 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

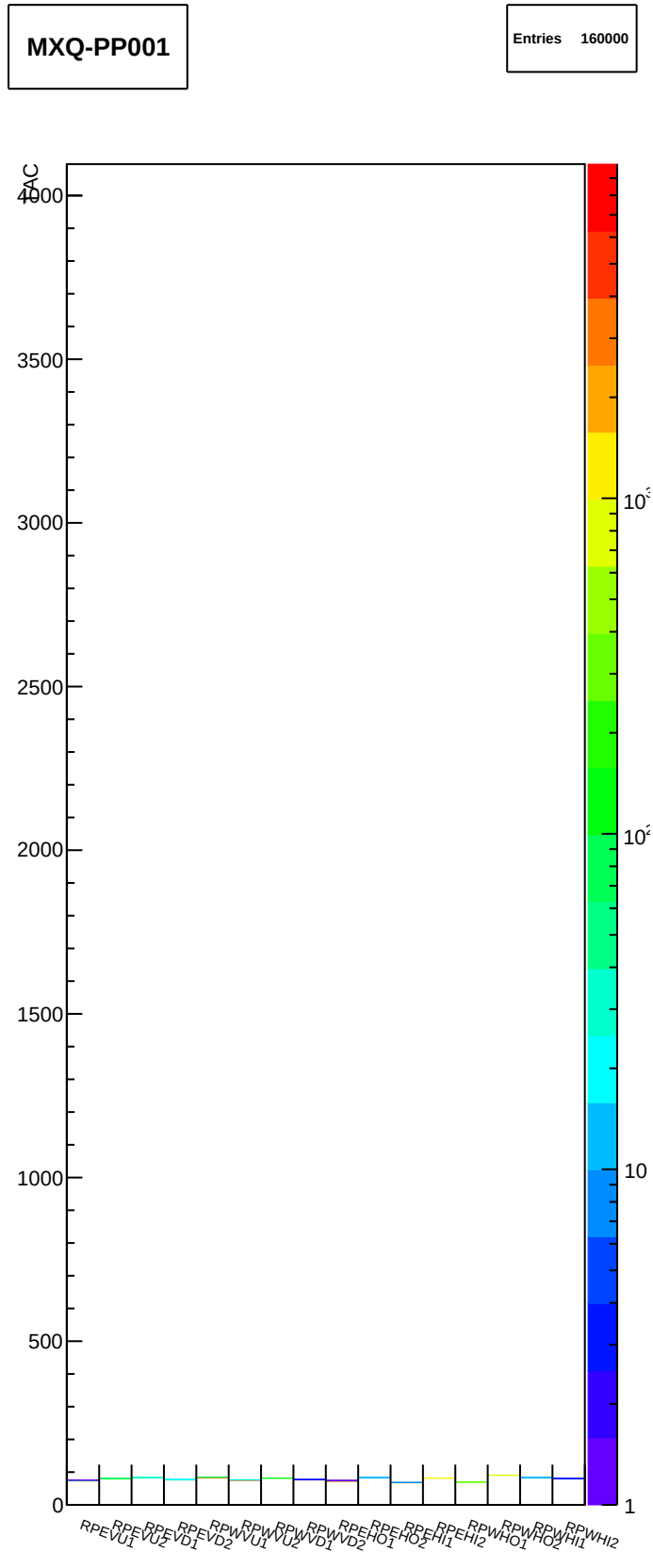

MIX-MT102 Input: MT007 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

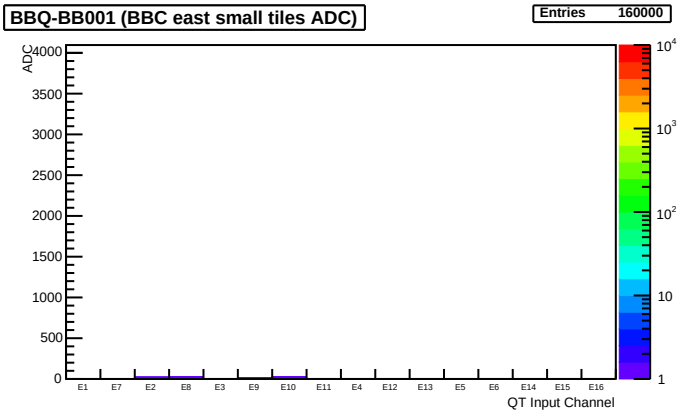

MIX-MT102 Input: MT008 MTDE+W TAC sum
Entries 20000
TAC sum
1st-Best
2nd-Best
ID

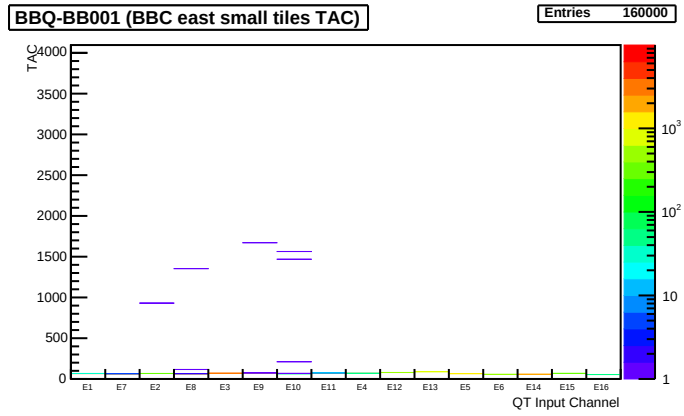

MIX-MT102 Input: VP003 Max TAC
Entries 10000

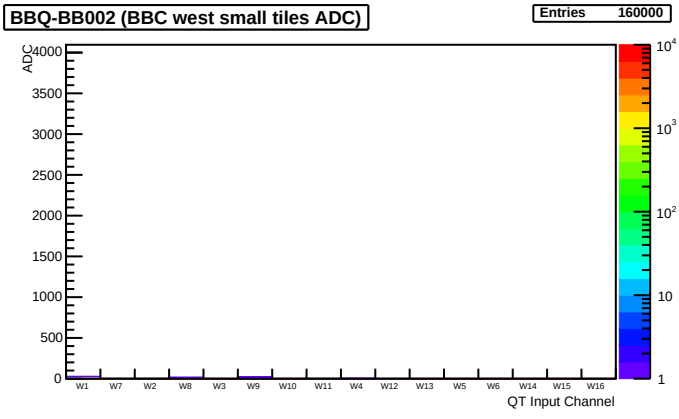

MIX-MT102 Input: VP004 Max TAC
Entries 10000

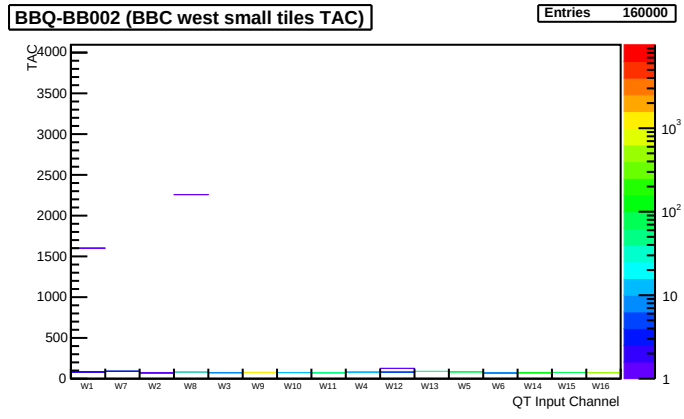

MXQ-PP001
Entries 160000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

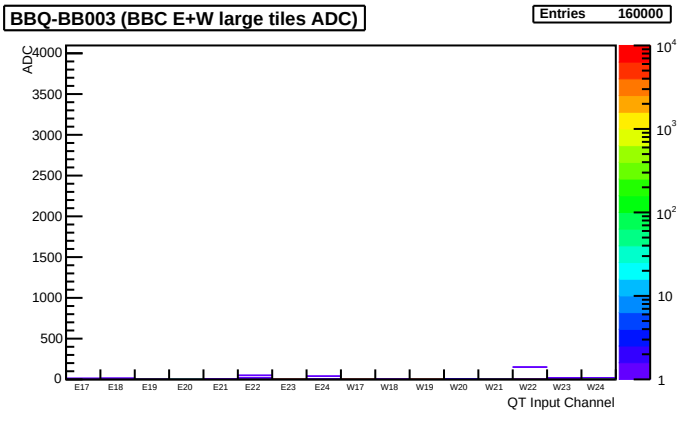

MXQ-PP001
Entries 160000
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
RPEVU1
RPEVU2
RPEVD1
RPEVD2
RPWVU1
RPWVU2
RPWVD1
RPWVD2
RPEHO1
RPEHO2
RPEHI1
RPEHI2
RPWHO1
RPWHO2
RPWHI1
RPWHI2
10
1

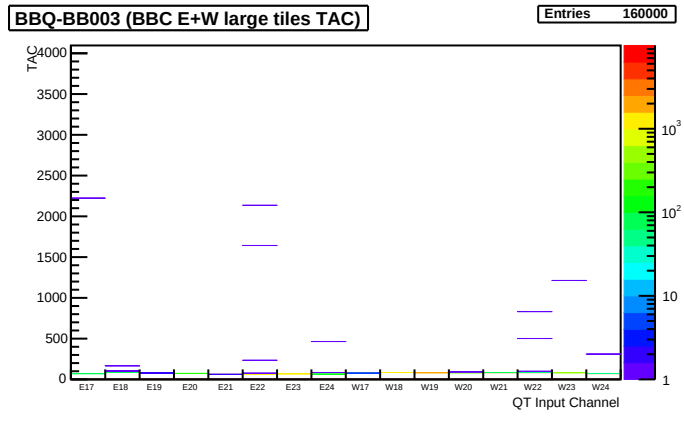

BBQ-BB001 (BBC east small tiles ADC)
Entries 160000
ADC
QT Input Channel

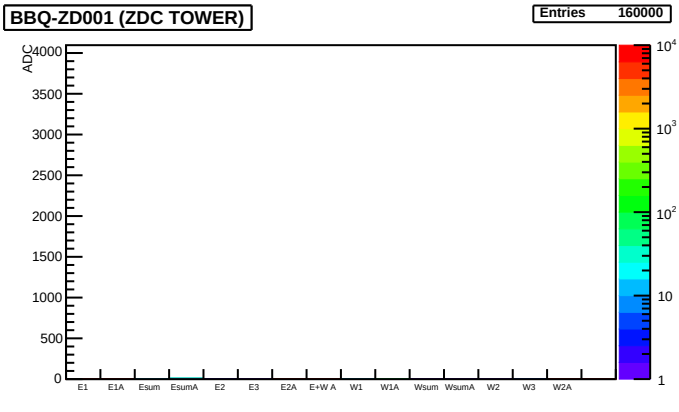

BBQ-BB001 (BBC east small tiles TAC)
Entries 160000
TAC
QT Input Channel

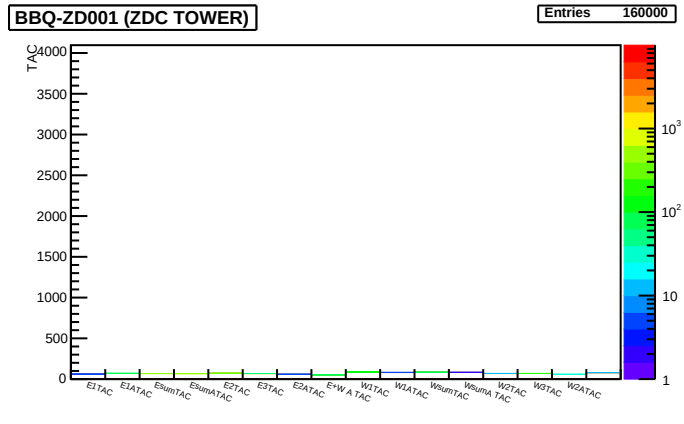

BBQ-BB002 (BBC west small tiles ADC)
Entries 160000
ADC
QT Input Channel

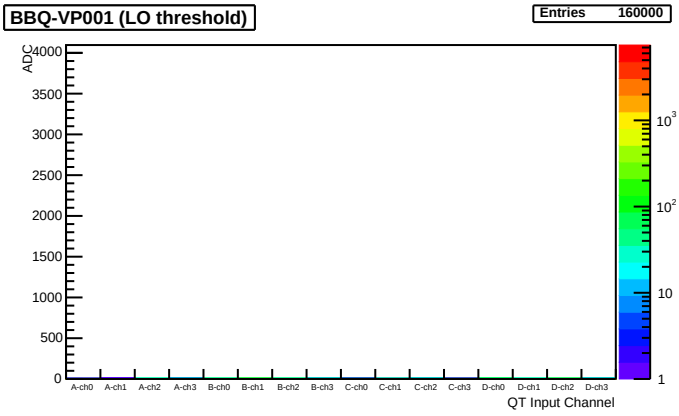

BBQ-BB002 (BBC west small tiles TAC)
Entries 160000
TAC
QT Input Channel

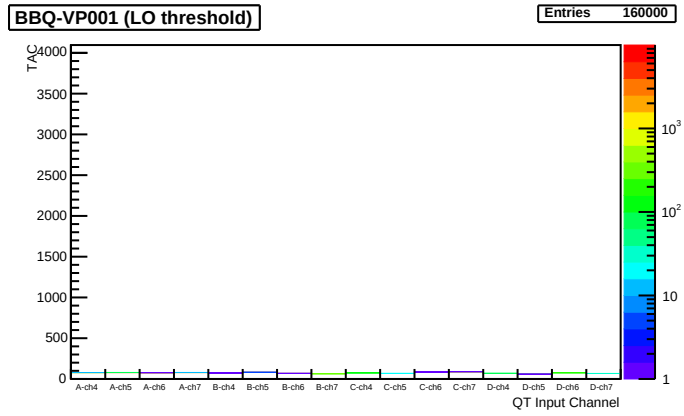

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 160000
ADC
QT Input Channel

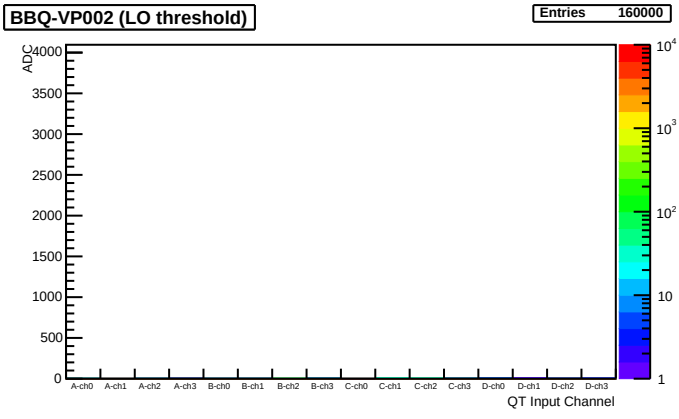

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 160000
TAC
QT Input Channel

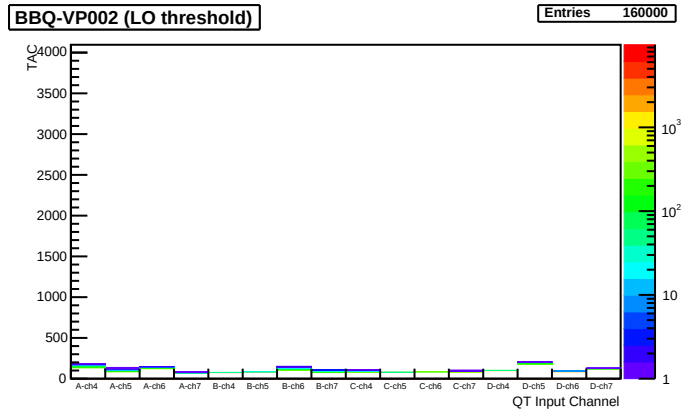

BBQ-ZD001 (ZDC TOWER)
Entries 160000
ADC

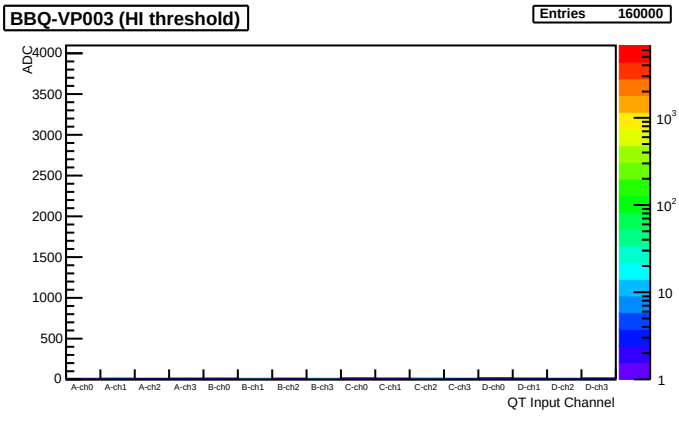

BBQ-ZD001 (ZDC TOWER)
Entries 160000
TAC

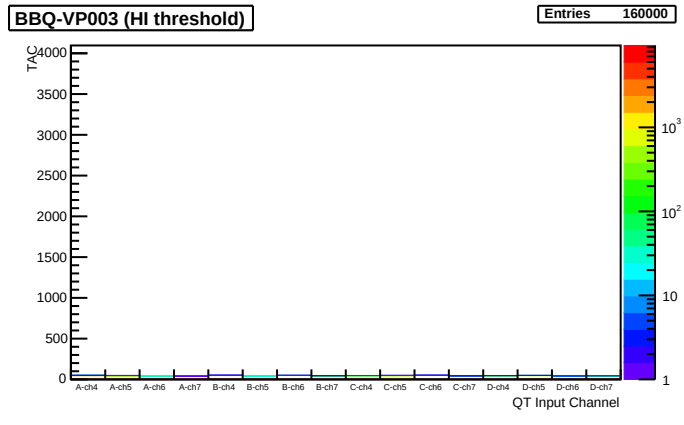

BBQ-VP001 (LO threshold)
Entries 160000
ADC
QT Input Channel

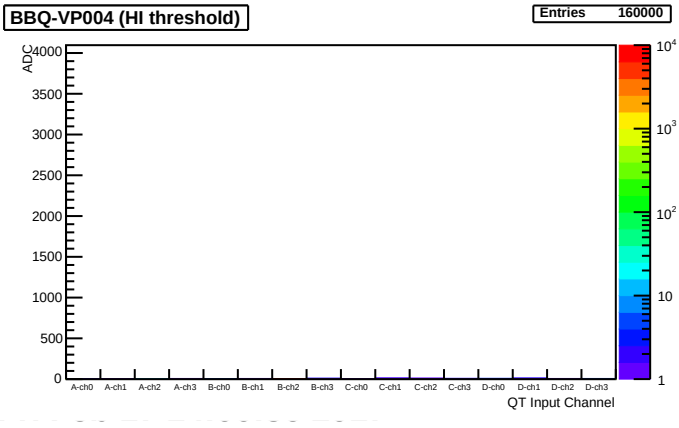

BBQ-VP001 (LO threshold)
Entries 160000
TAC
QT Input Channel

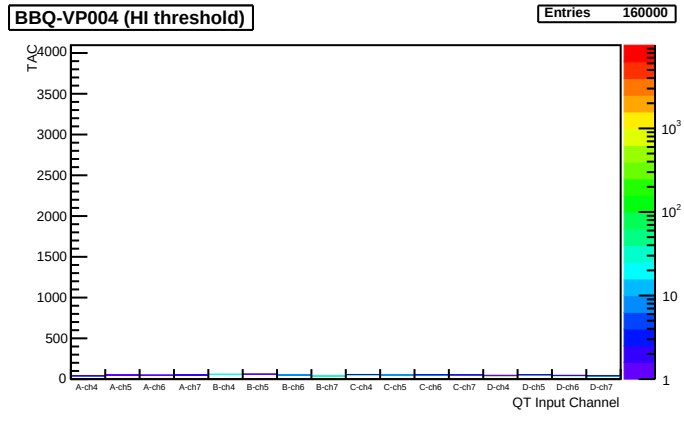

BBQ-VP002 (LO threshold)
Entries 160000
ADC
QT Input Channel

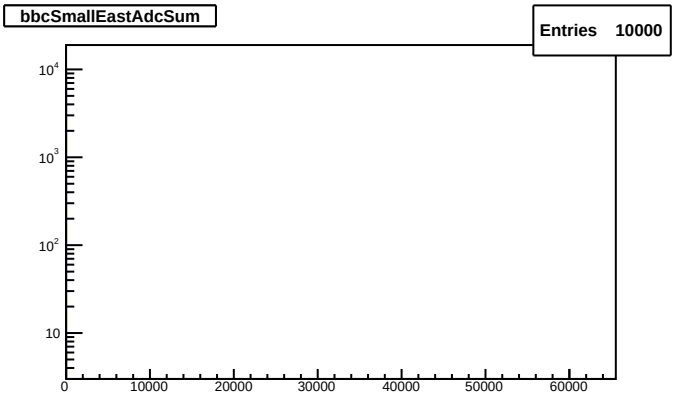

BBQ-VP002 (LO threshold)
Entries 160000
TAC
QT Input Channel

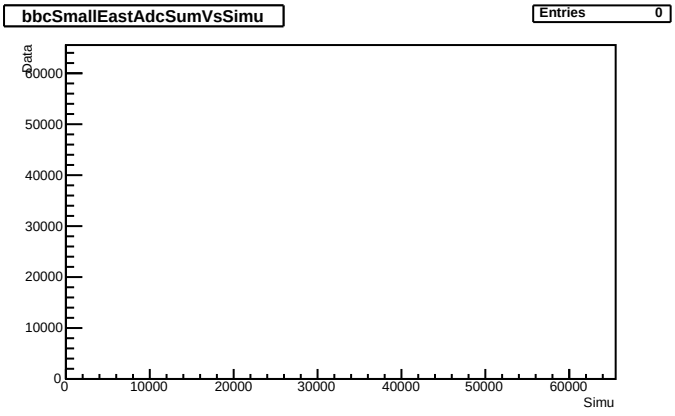

BBQ-VP003 (HI threshold)
Entries 160000
ADC
QT Input Channel

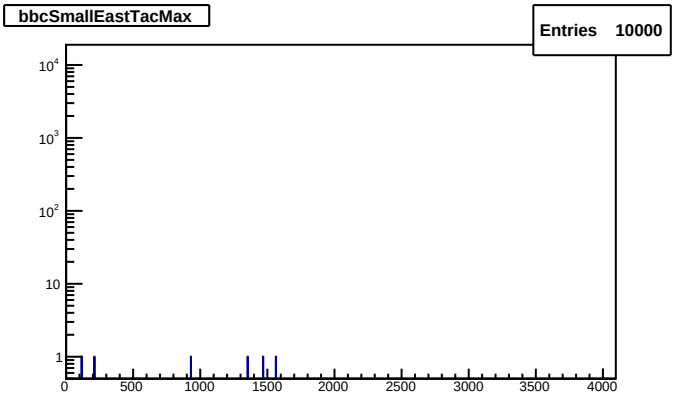

BBQ-VP003 (HI threshold)
Entries 160000
TAC
QT Input Channel

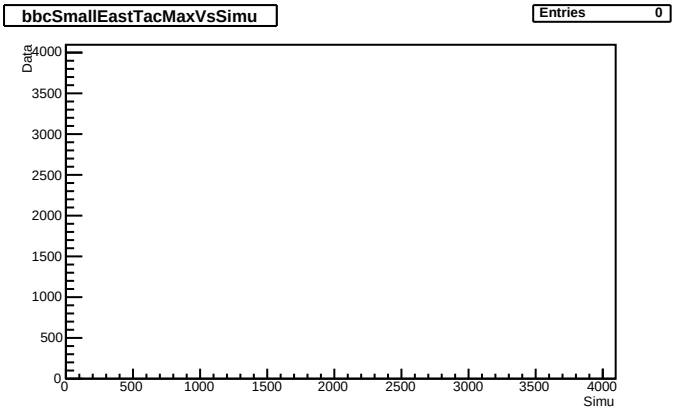

BBQ-VP004 (HI threshold)
Entries 160000
ADC
QT Input Channel

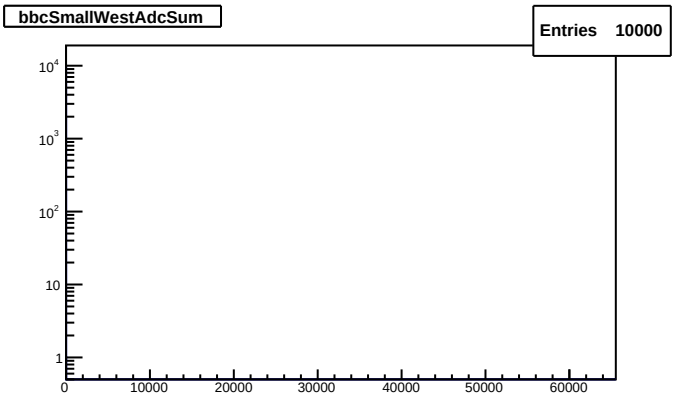

BBQ-VP004 (HI threshold)
Entries 160000
TAC
QT Input Channel

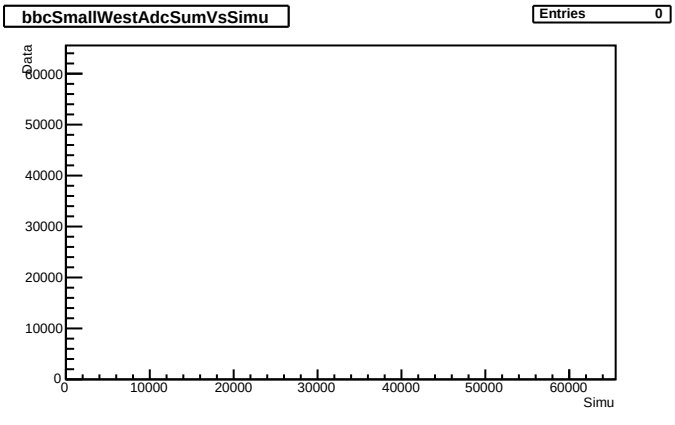

bbcSmallEastAdcSum
Entries 10000

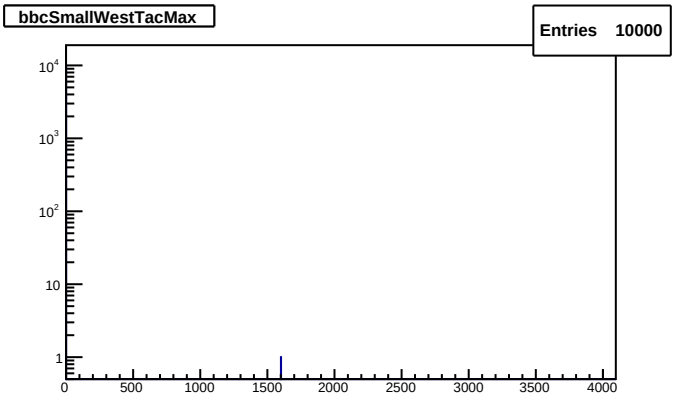

bbcSmallEastAdcSumVsSimu
Entries 0
Data
Simu

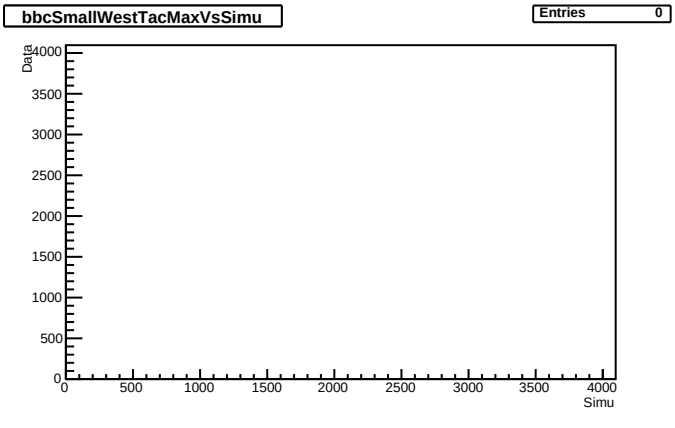

bbcSmallEastTacMax
Entries 10000

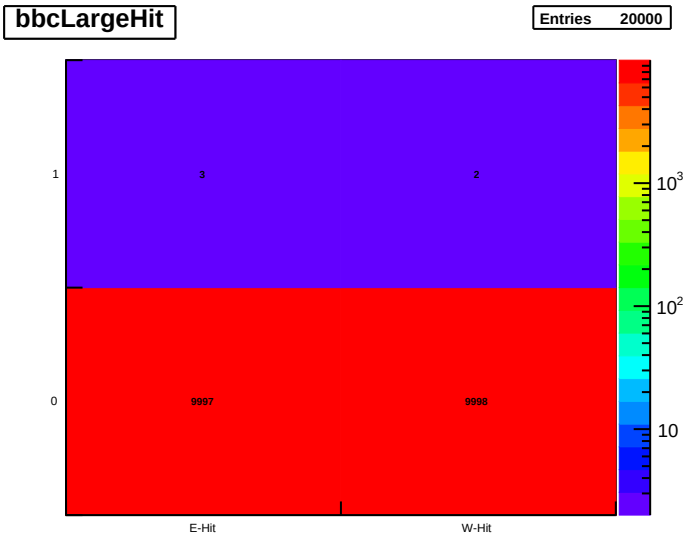

bbcSmallEastTacMaxVsSimu
Entries 0
Data
Simu

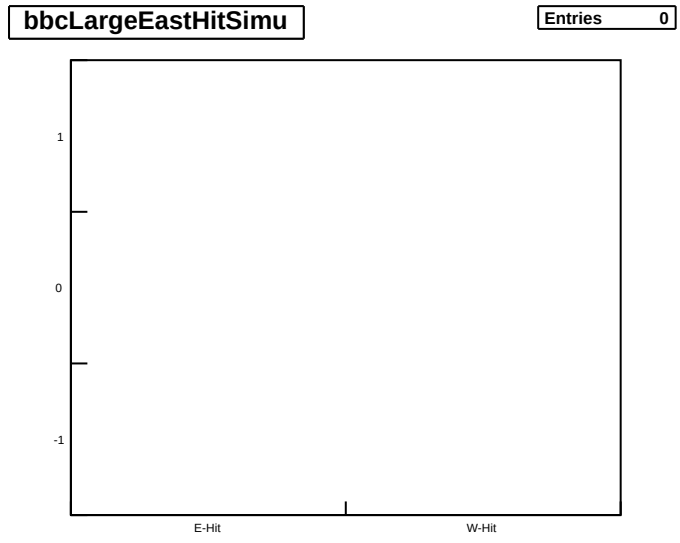

bbcSmallWestAdcSum
Entries 10000

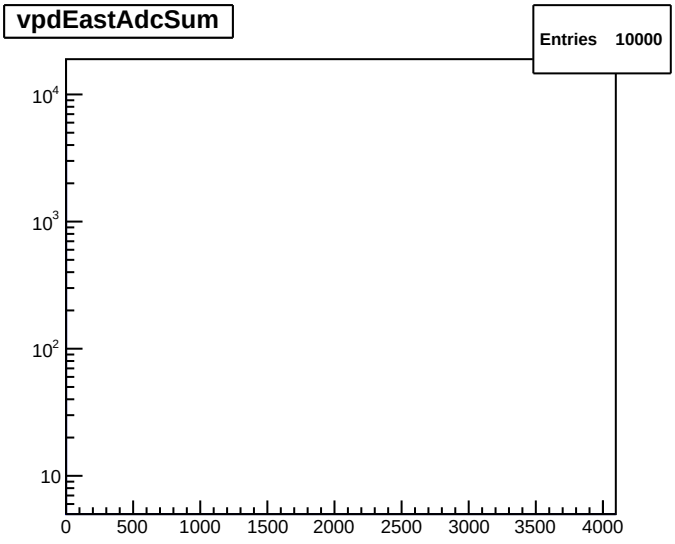

bbcSmallWestAdcSumVsSimu
Entries 0
Data
Simu

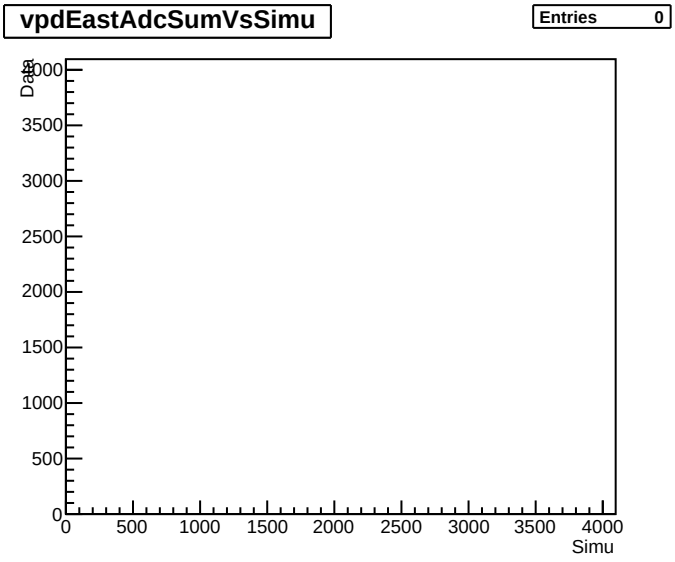

bbcSmallWestTacMax
Entries 10000

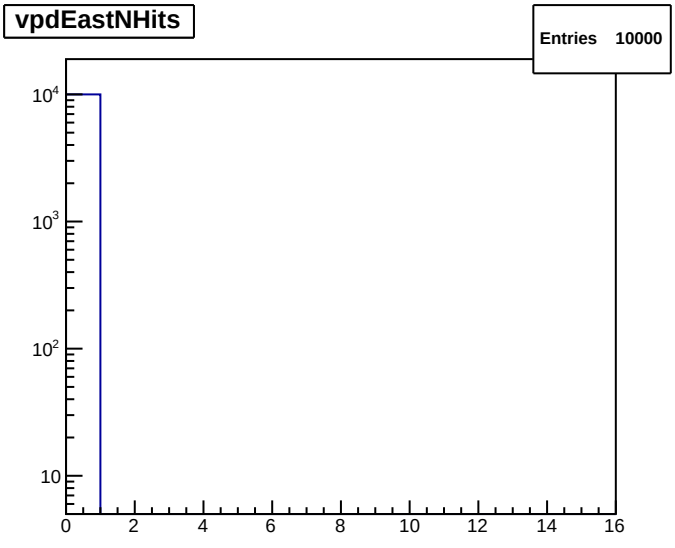

bbcSmallWestTacMaxVsSimu
Entries 0
Data
Simu

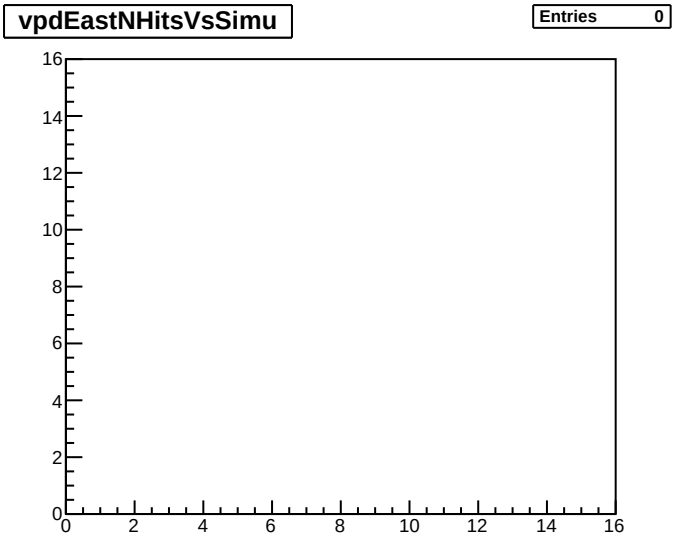

bbcLargeHit
Entries 20000
1
0
3
2
9997
9998
E-Hit
W-Hit
$10^3$
$10^2$
10

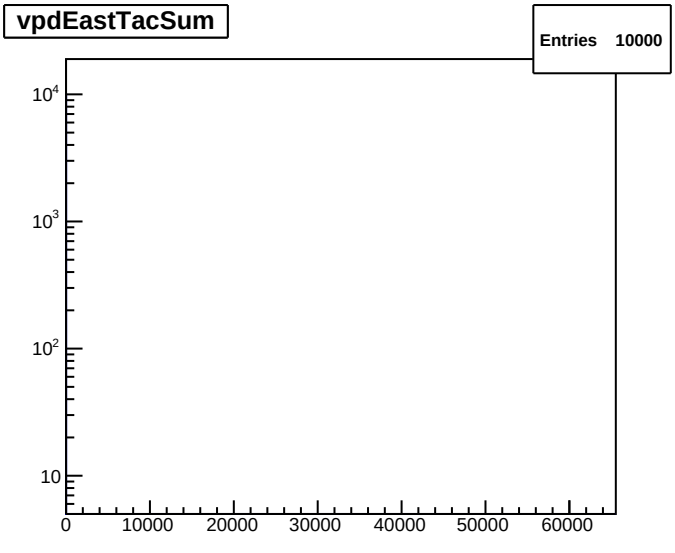

bbcLargeEastHitSimu
Entries 0
1
0
-1
E-Hit
W-Hit

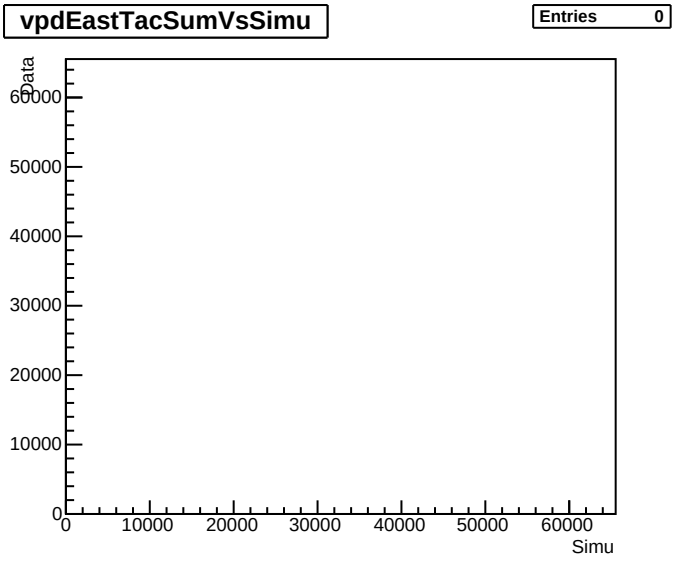

vpdEastAdcSum
Entries 10000
$10^4$
$10^3$
$10^2$
10
0 500 1000 1500 2000 2500 3000 3500 4000

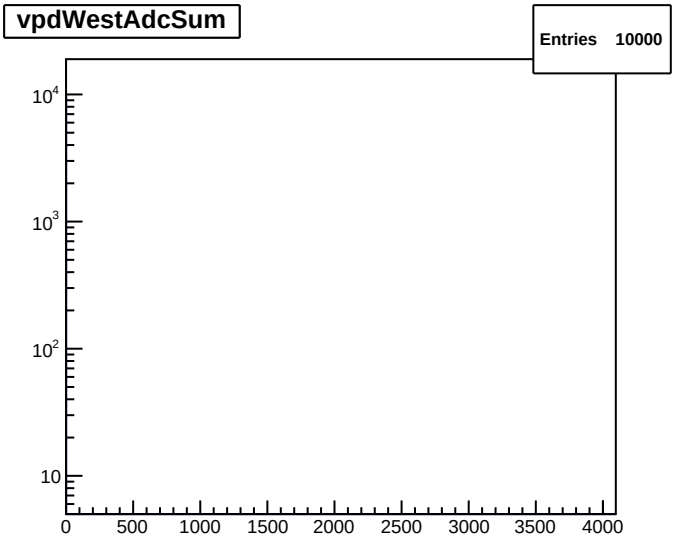

vpdEastAdcSumVsSimu
Entries 0
Data
4000 3500 3000 2500 2000 1500 1000 500 0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

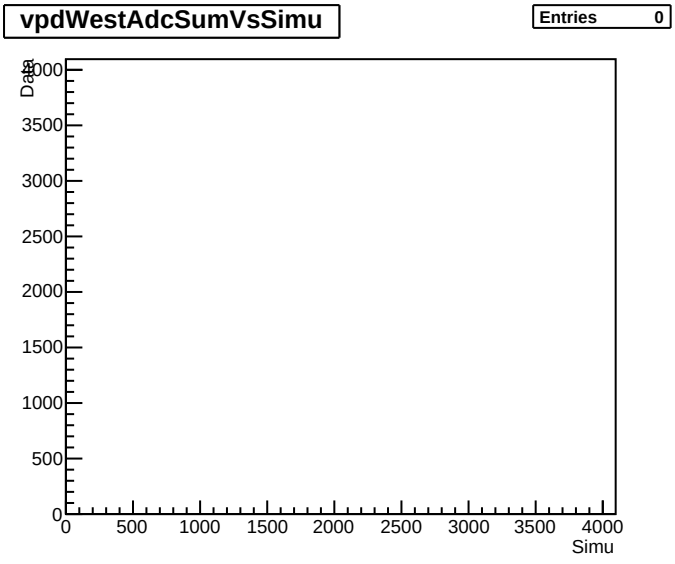

vpdEastNHits
Entries 10000
$10^4$
$10^3$
$10^2$
10
0 2 4 6 8 10 12 14 16

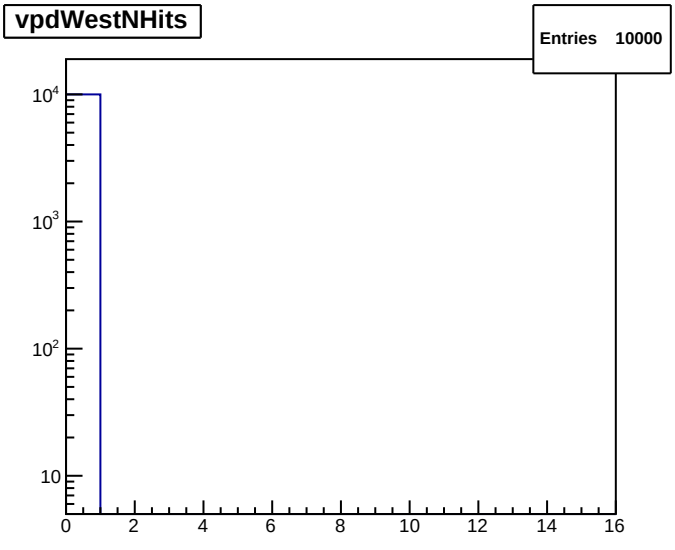

vpdEastNHitsVsSimu
Entries 0
16 14 12 10 8 6 4 2 0
0 2 4 6 8 10 12 14 16

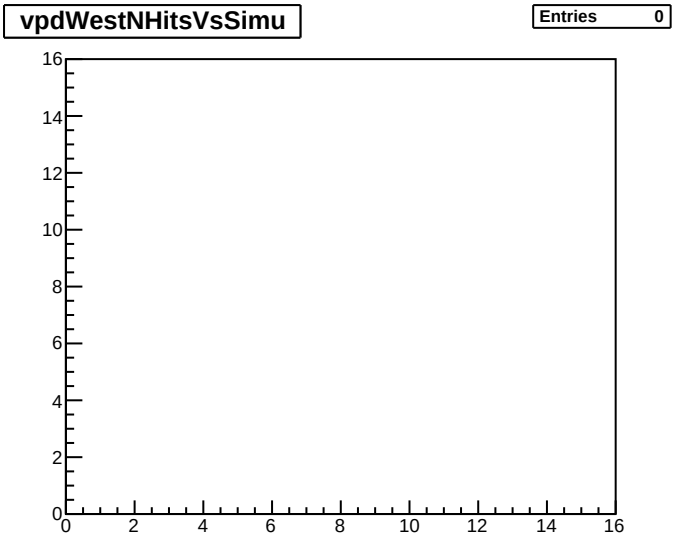

vpdEastTacSum
Entries 10000
$10^4$
$10^3$
$10^2$
10
0 10000 20000 30000 40000 50000 60000

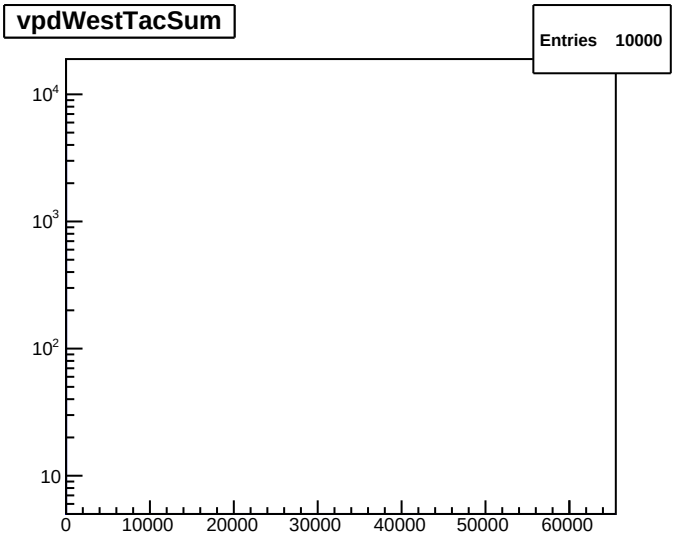

vpdEastTacSumVsSimu
Entries 0
Data
60000 50000 40000 30000 20000 10000 0
0 10000 20000 30000 40000 50000 60000
Simu

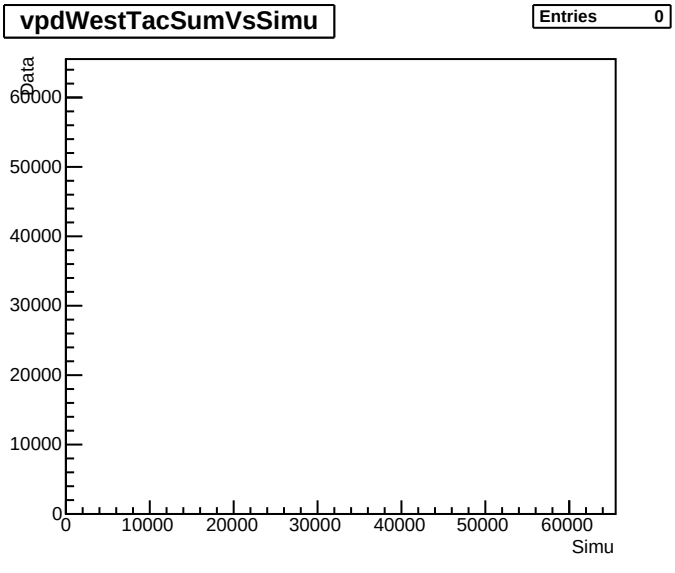

vpdWestAdcSum
Entries 10000
$10^4$
$10^3$
$10^2$
10
0 500 1000 1500 2000 2500 3000 3500 4000

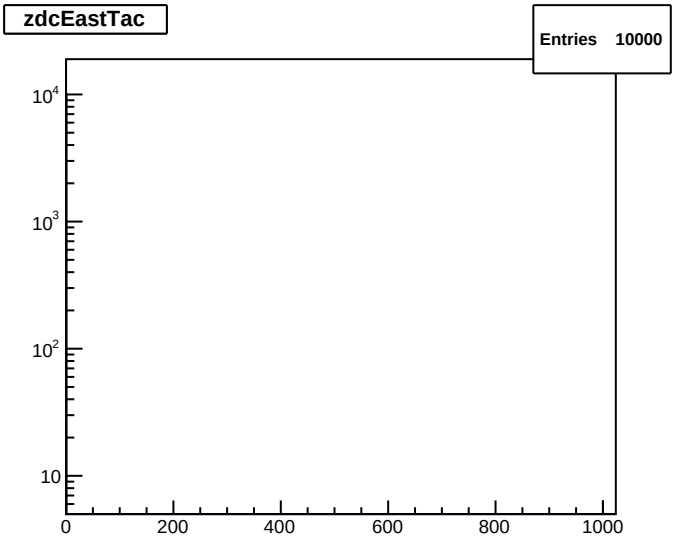

vpdWestAdcSumVsSimu
Entries 0
Data
4000
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000
Simu

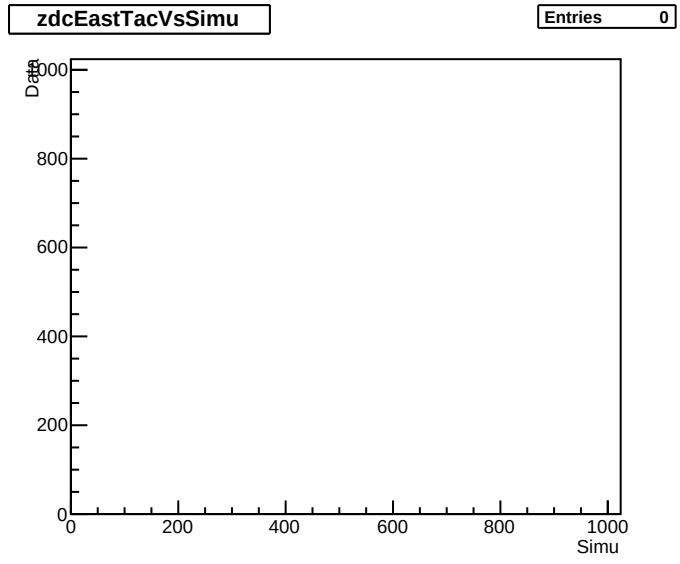

vpdWestNHits
Entries 10000
$10^4$
$10^3$
$10^2$
10
0 2 4 6 8 10 12 14 16

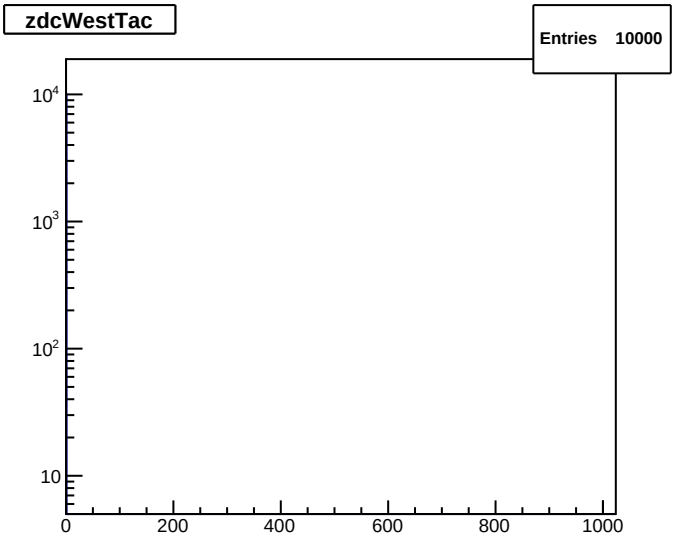

vpdWestNHitsVsSimu
Entries 0
16
14
12
10
8
6
4
2
0
0 2 4 6 8 10 12 14 16

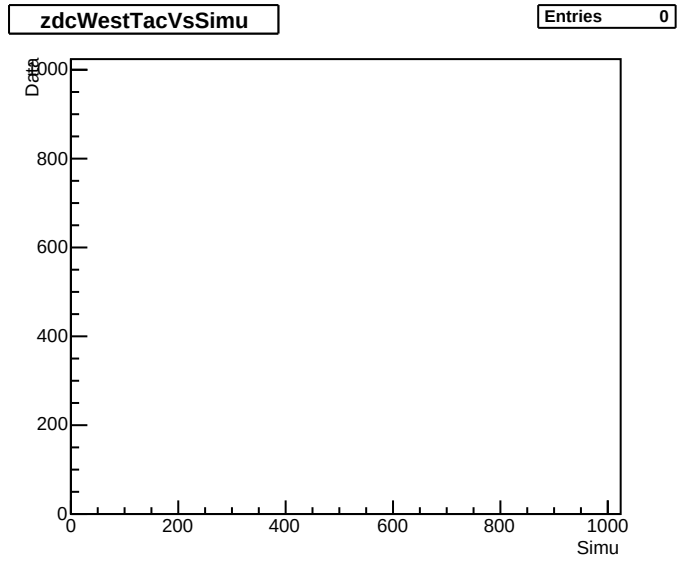

vpdWestTacSum
Entries 10000
$10^4$
$10^3$
$10^2$
10
0 10000 20000 30000 40000 50000 60000

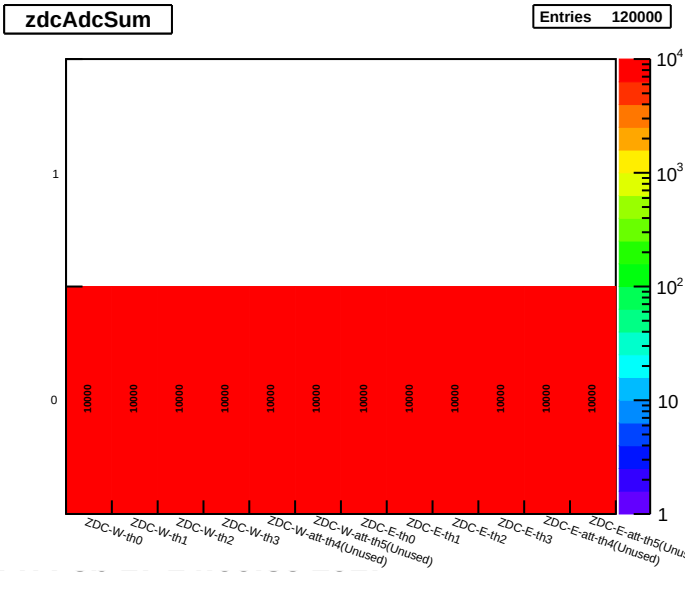

vpdWestTacSumVsSimu
Entries 0
Data
60000
50000
40000
30000
20000
10000
0
0 10000 20000 30000 40000 50000 60000
Simu

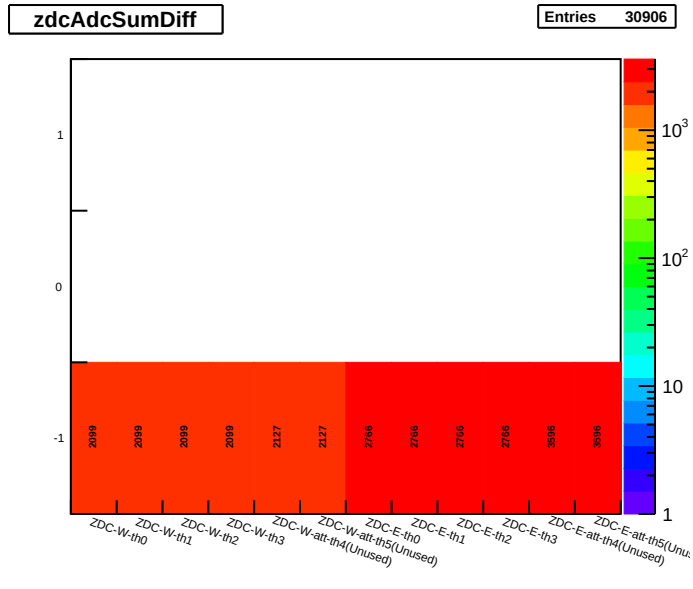

zdcEastTac
Entries 10000
$10^4$
$10^3$
$10^2$
10
0
200
400
600
800
1000

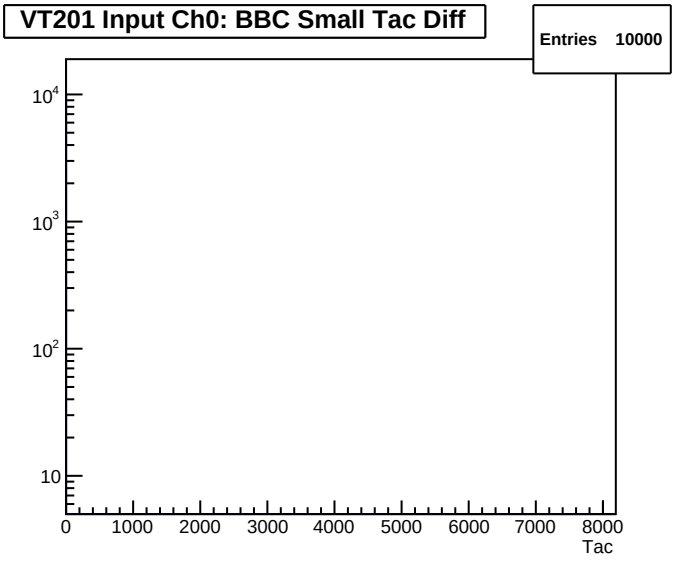

zdcEastTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

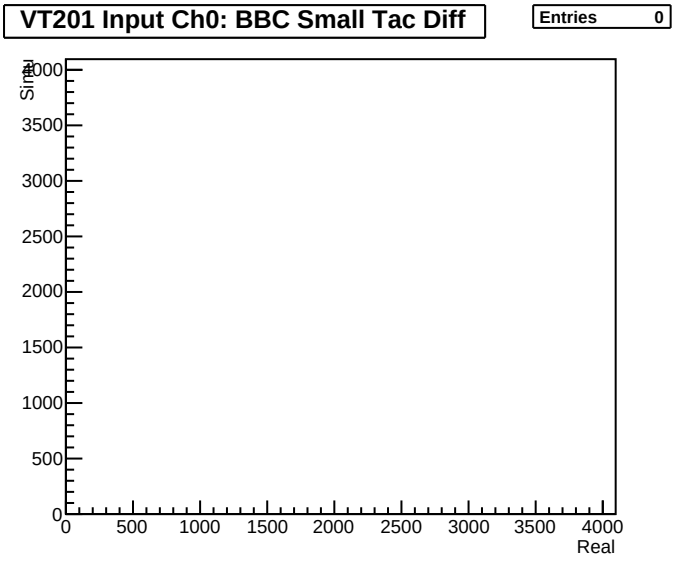

zdcWestTac
Entries 10000
$10^4$
$10^3$
$10^2$
10
0
200
400
600
800
1000

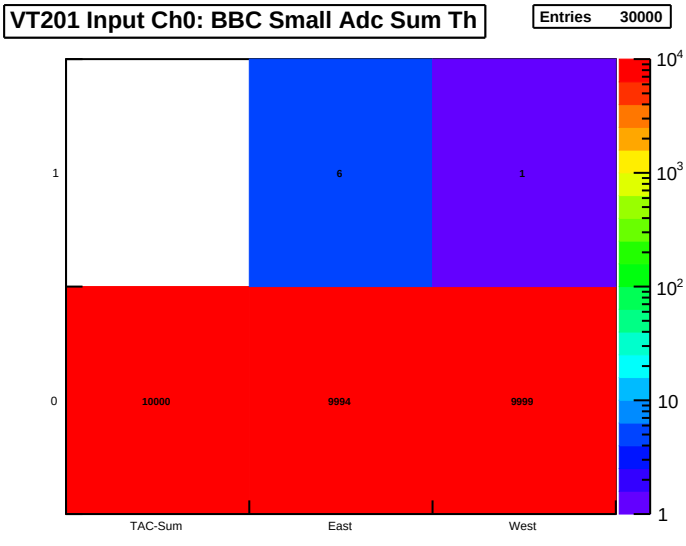

zdcWestTacVsSimu
Entries 0
Data
1000
800
600
400
200
0
0
200
400
600
800
1000
Simu

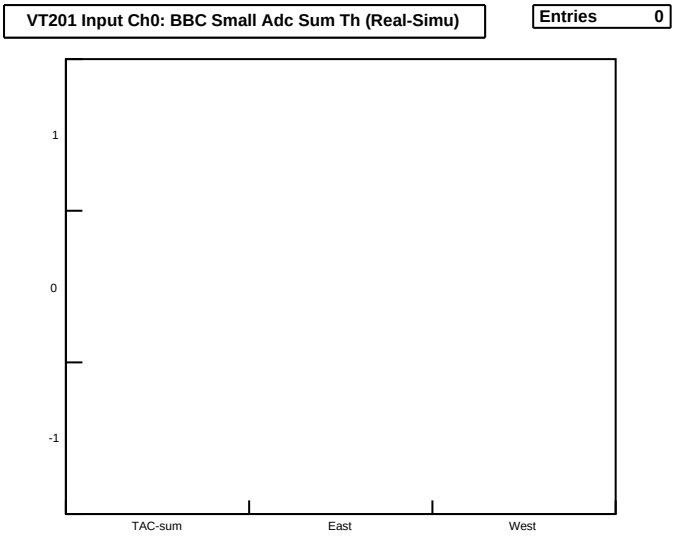

zdcAdcSum
Entries 120000
1
0
10000
$10^4$
$10^3$
$10^2$
10
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

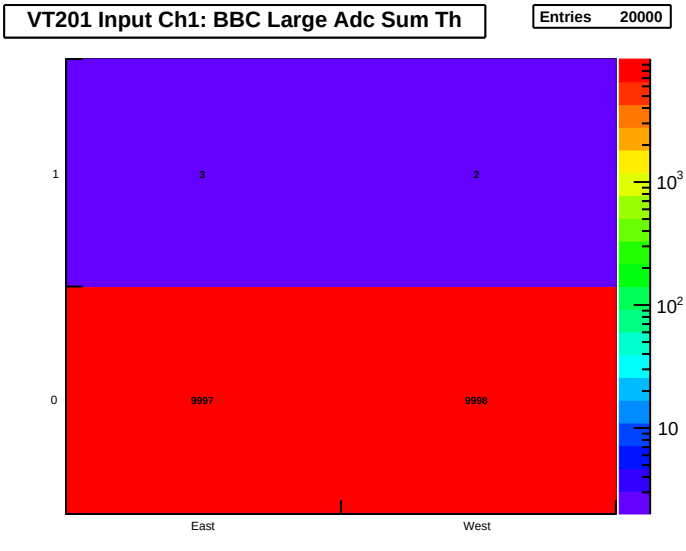

zdcAdcSumDiff
Entries 30906
1
0
-1
2099
2127
2766
3596
$10^3$
$10^2$
10
1
ZDC-W-th0
ZDC-W-th1
ZDC-W-th2
ZDC-W-th3
ZDC-W-att-th4(Unused)
ZDC-W-att-th5(Unused)
ZDC-E-th0
ZDC-E-th1
ZDC-E-th2
ZDC-E-th3
ZDC-E-att-th4(Unused)

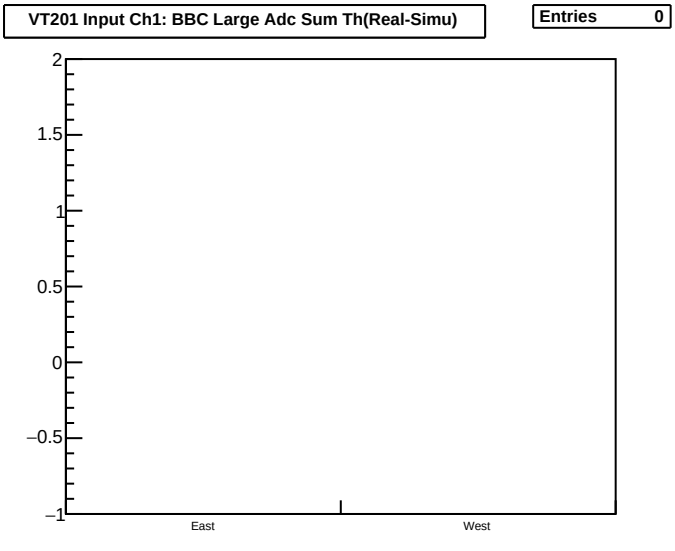

VT201 Input Ch0: BBC Small Tac Diff
Entries 10000
Tac


VT201 Input Ch0: BBC Small Tac Diff
Entries 0
Simu
Real

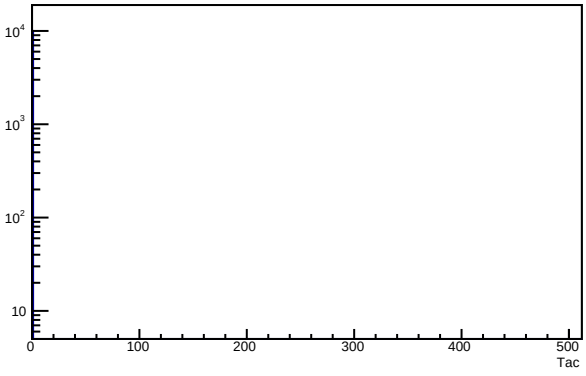

VT201 Input Ch0: BBC Small Adc Sum Th
Entries 30000
6
1
10000
9994
9999
TAC-Sum
East
West


VT201 Input Ch0: BBC Small Adc Sum Th (Real-Simu)
Entries 0
TAC-sum
East
West

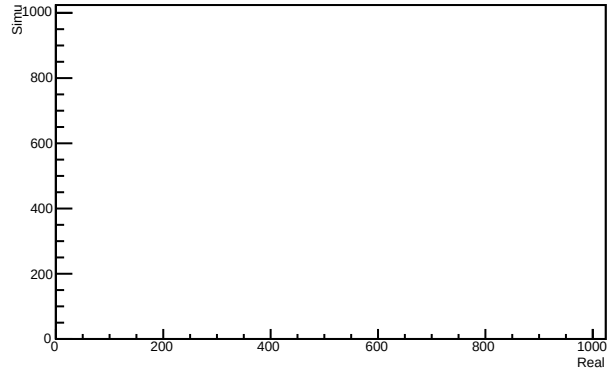

VT201 Input Ch1: BBC Large Adc Sum Th
Entries 20000
3
2
9997
9998
East
West


VT201 Input Ch1: BBC Large Adc Sum Th(Real-Simu)
Entries 0
East
West

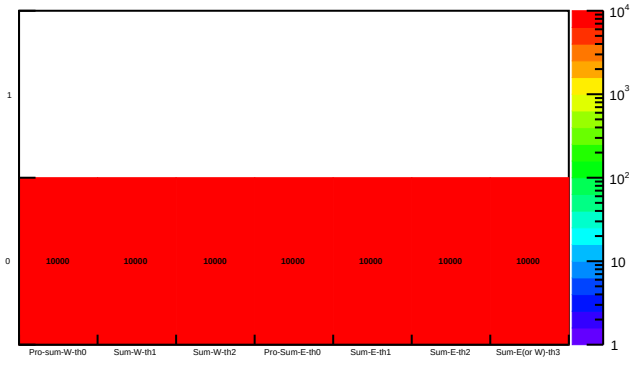
**VT201 Input Ch2: ZDC Tac Diff**



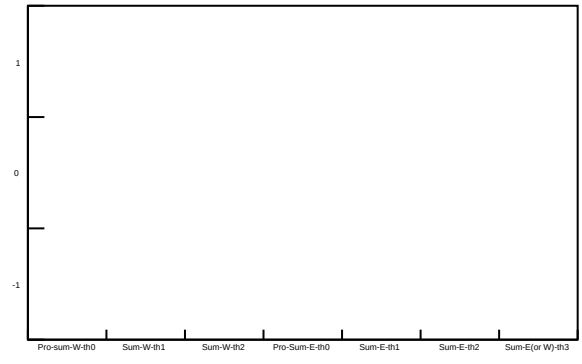

**VT201 Input Ch2: ZDC Tac Diff**



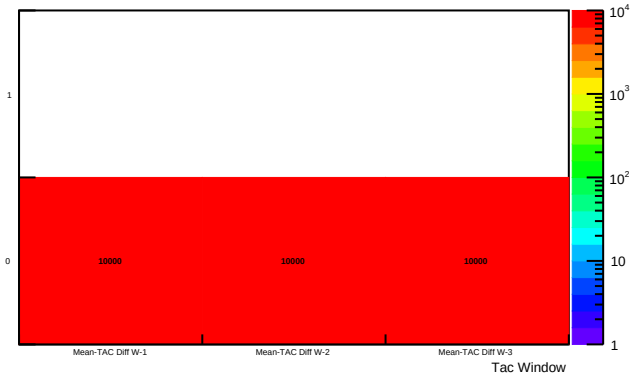

**VT201 Input Ch2: ZDC Adc Sum Th**



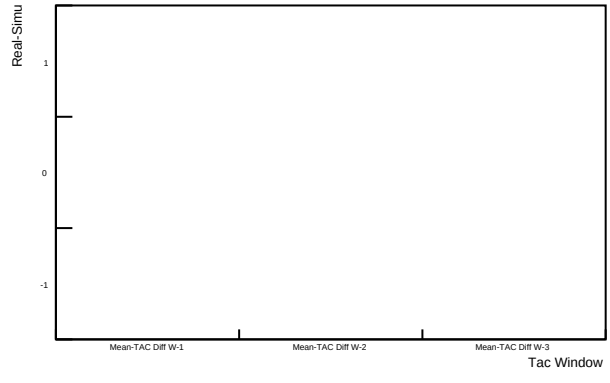

**VT201 Input Ch2: ZDC Adc Sum Th (Real-Simu)**



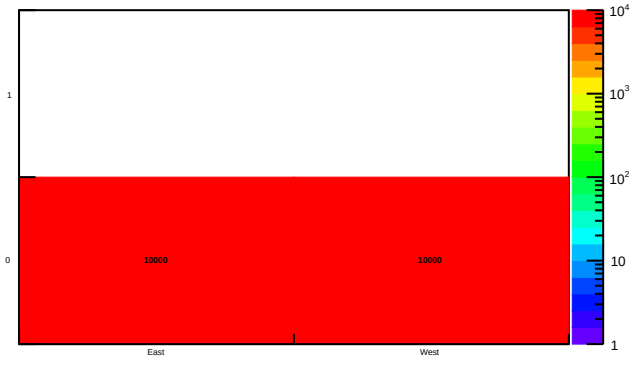

**VT201 Input Ch4: VPD Tac Diff**



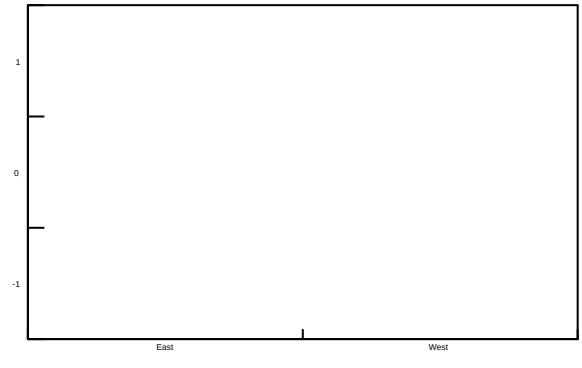

**VT201 Input Ch4: VPD Tac Diff**

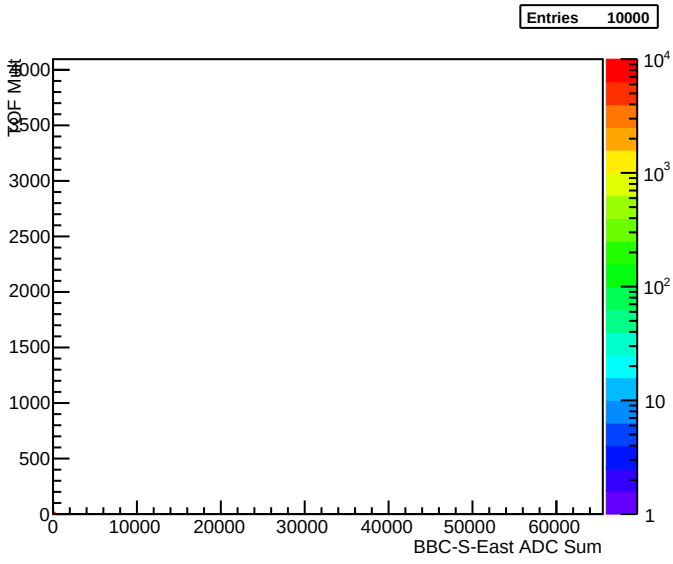


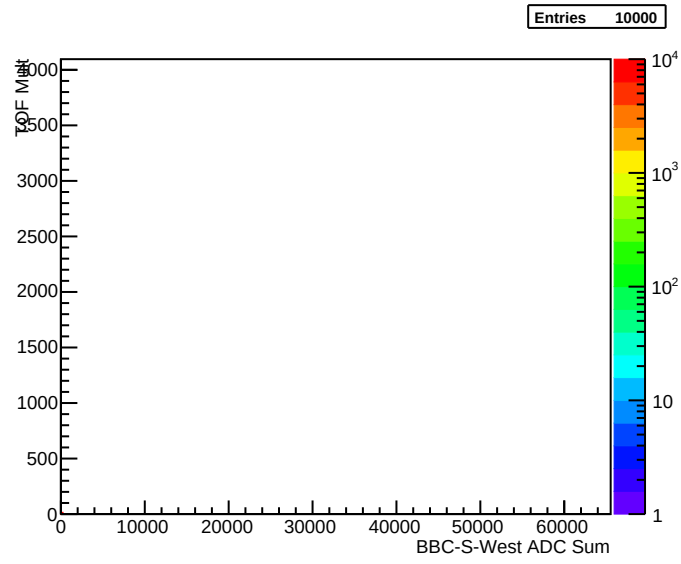

**VT201 Input Ch4: VPD Adc Sum Th**

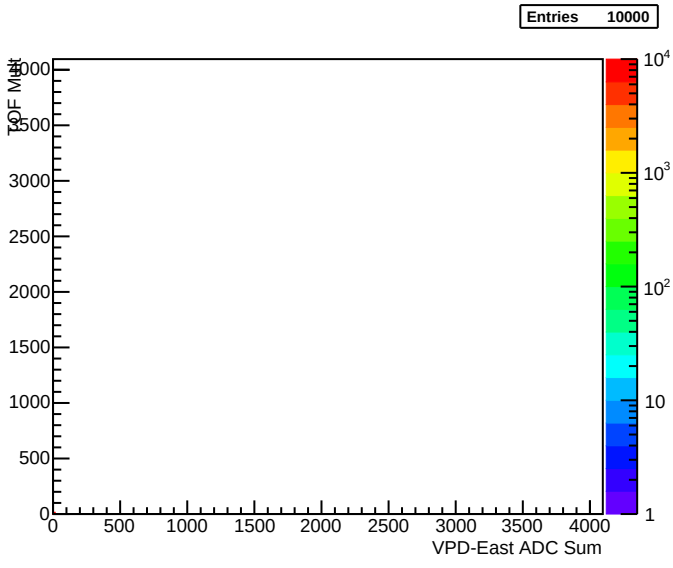


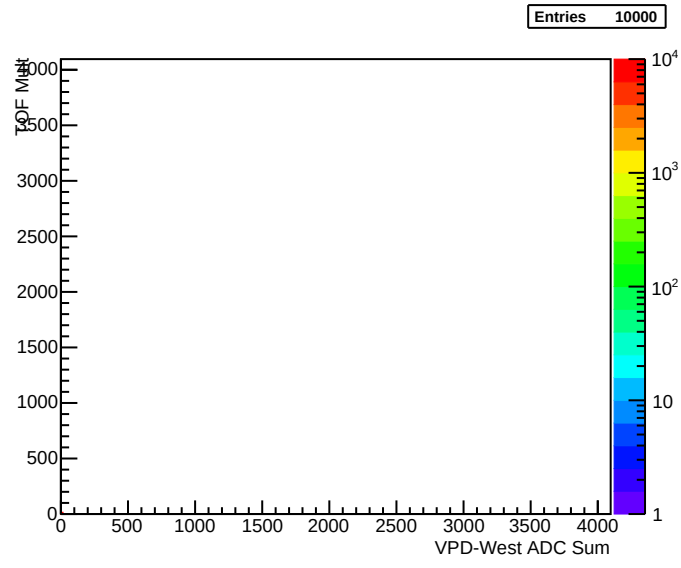

**VT201 Input Ch4: VPD Adc Sum Th(Real-Simu)**

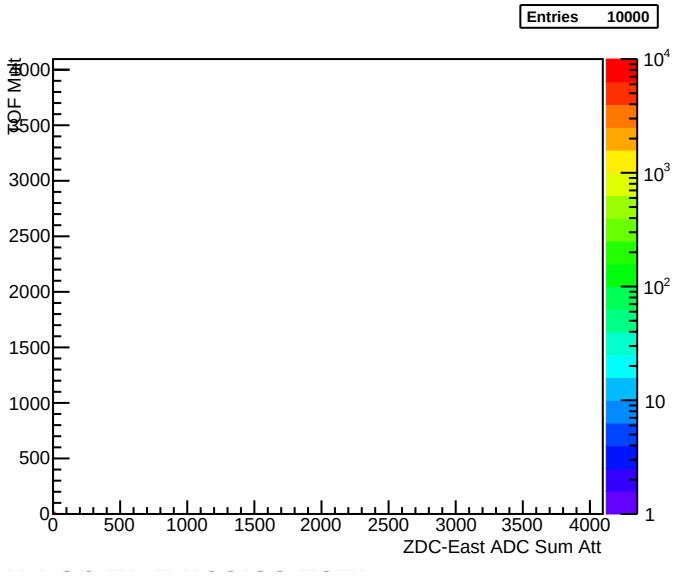


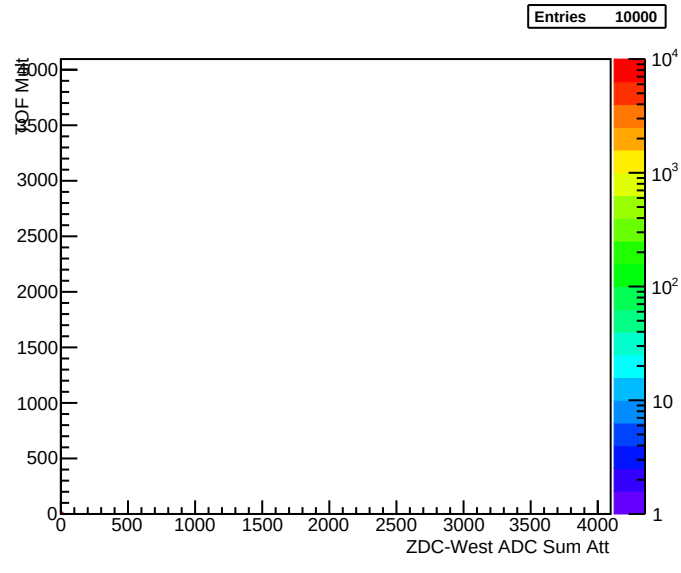

Entries 10000
TOF Mult
BBC-S-East ADC Sum

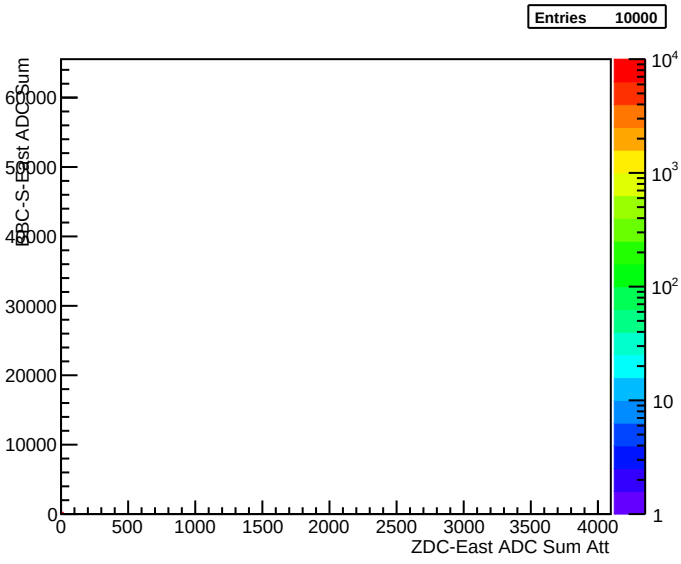

Entries 10000
TOF Mult
BBC-S-West ADC Sum

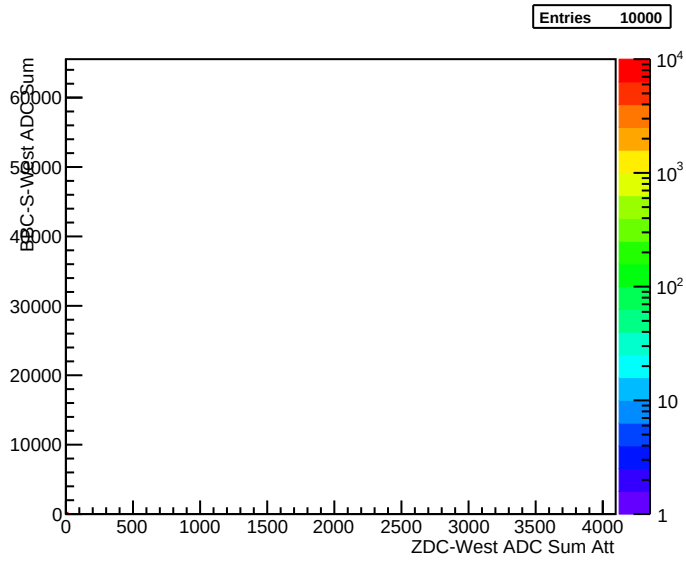

Entries 10000
TOF Mult
VPD-East ADC Sum

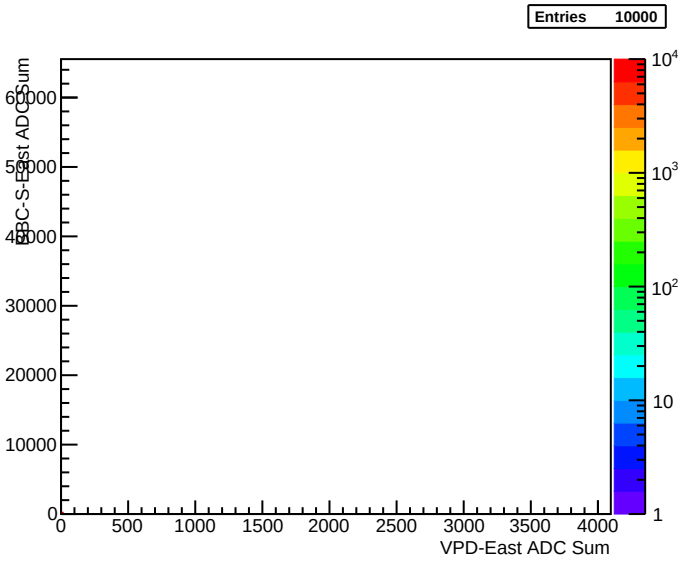

Entries 10000
TOF Mult
VPD-West ADC Sum

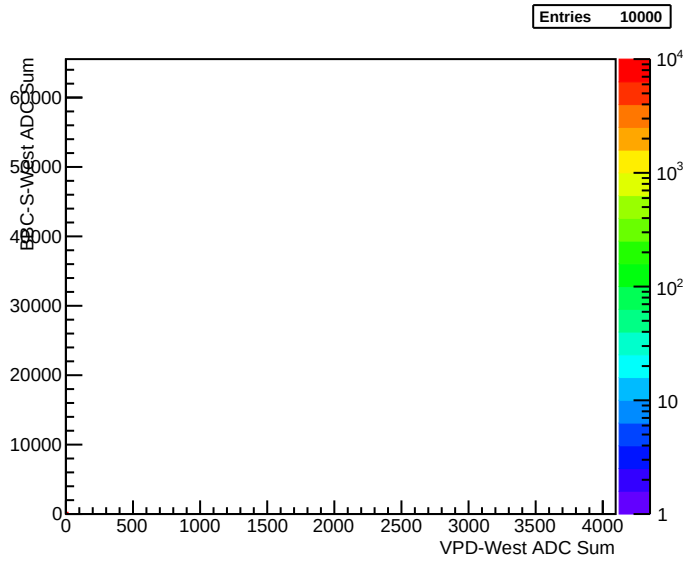

Entries 10000
TOF Mult
ZDC-East ADC Sum Att

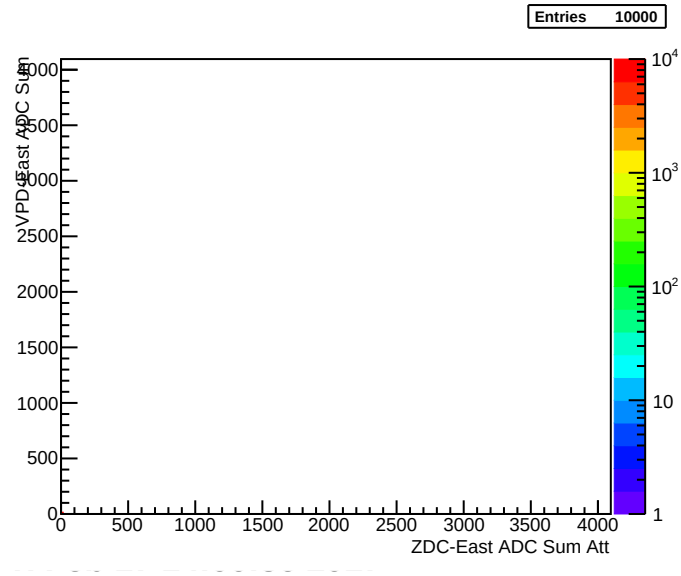

Entries 10000
TOF Mult
ZDC-West ADC Sum Att

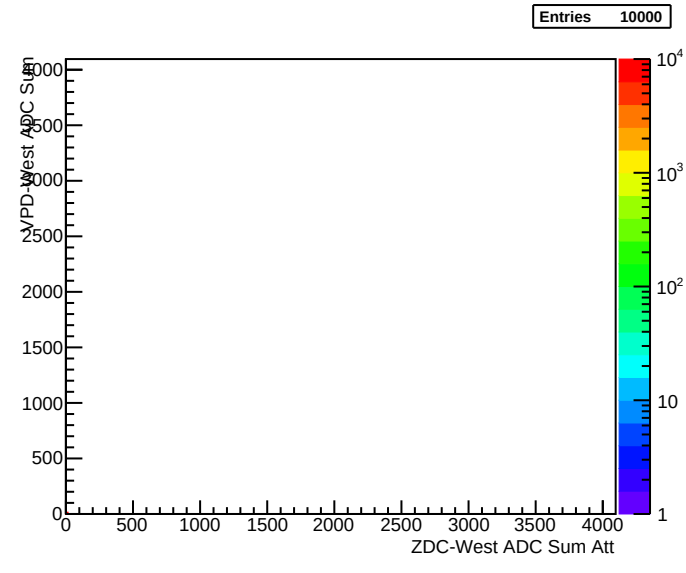

Entries 10000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

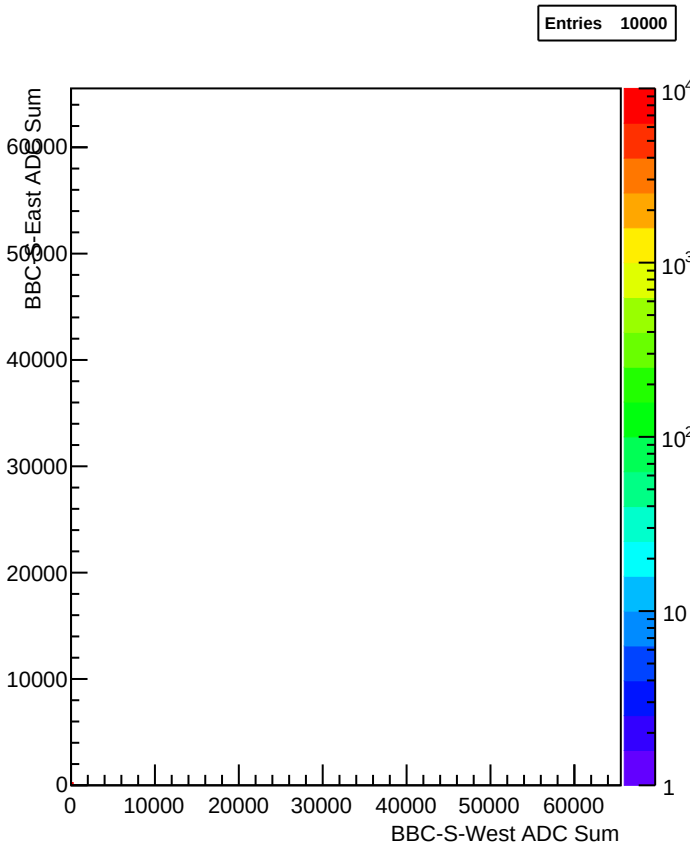

Entries 10000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

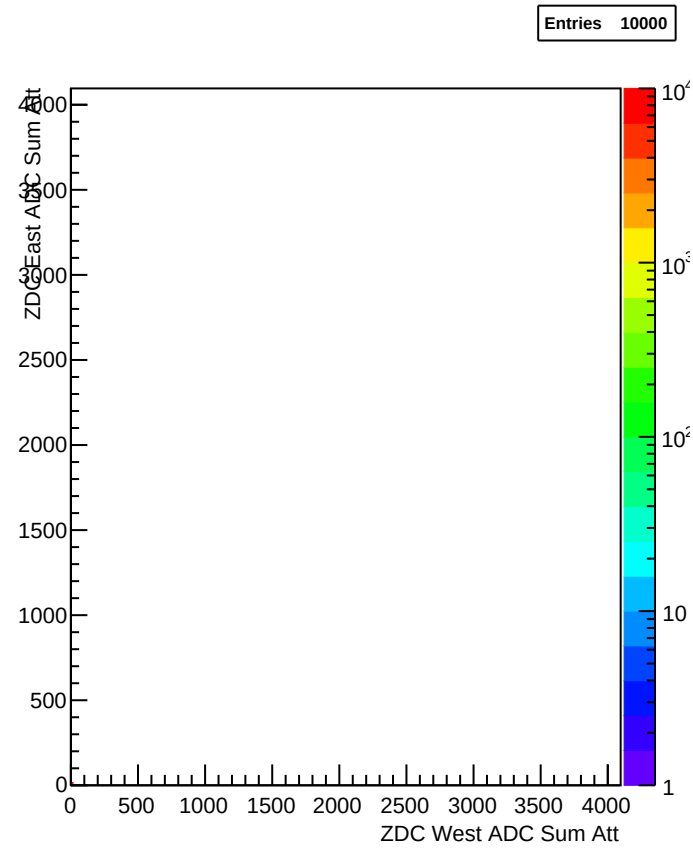

Entries 10000
BBC-S-East ADC Sum
VPD-East ADC Sum

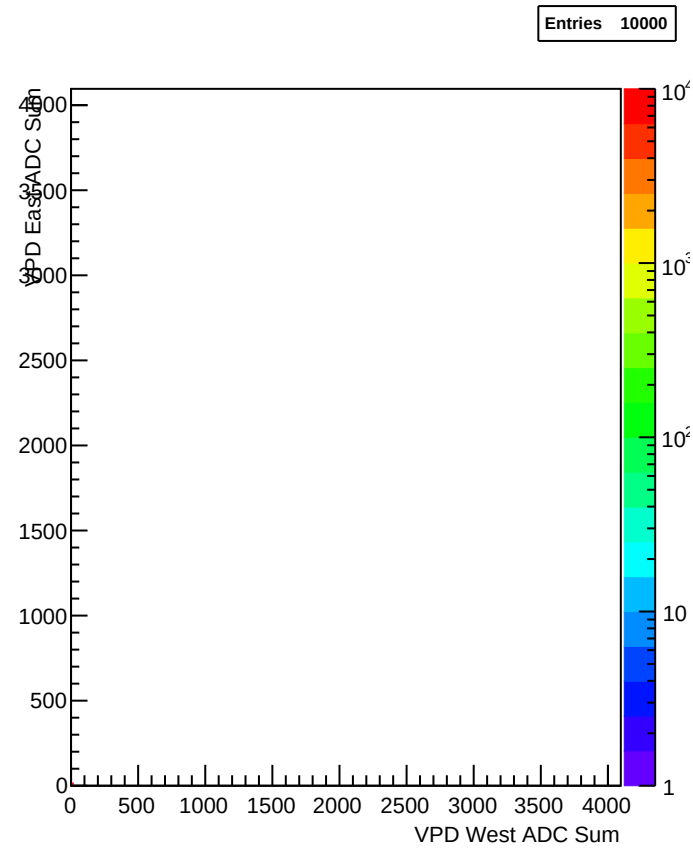

Entries 10000
BBC-S-West ADC Sum
VPD-West ADC Sum

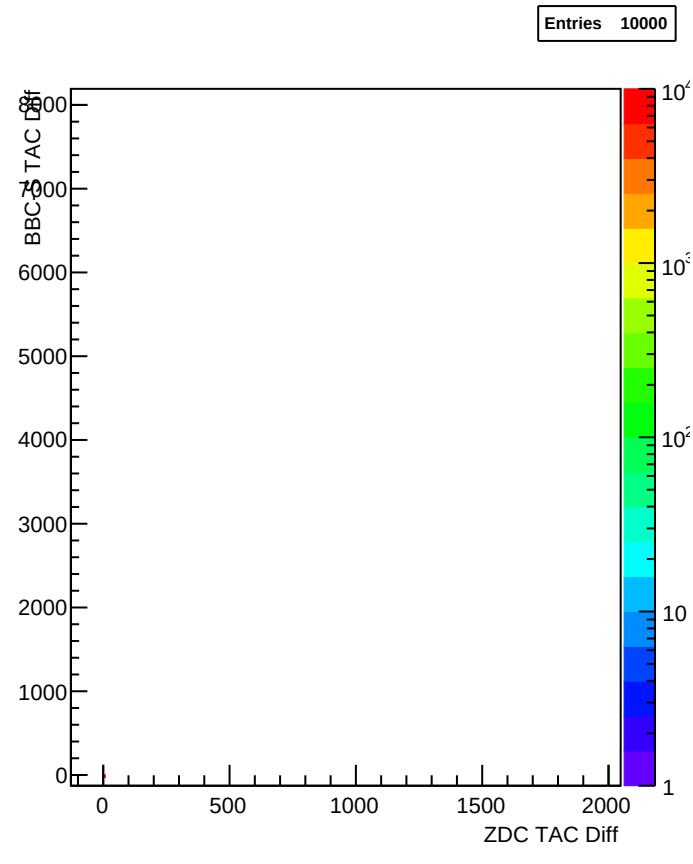

Entries 10000
VPD-East ADC Sum
ZDC-East ADC Sum Att

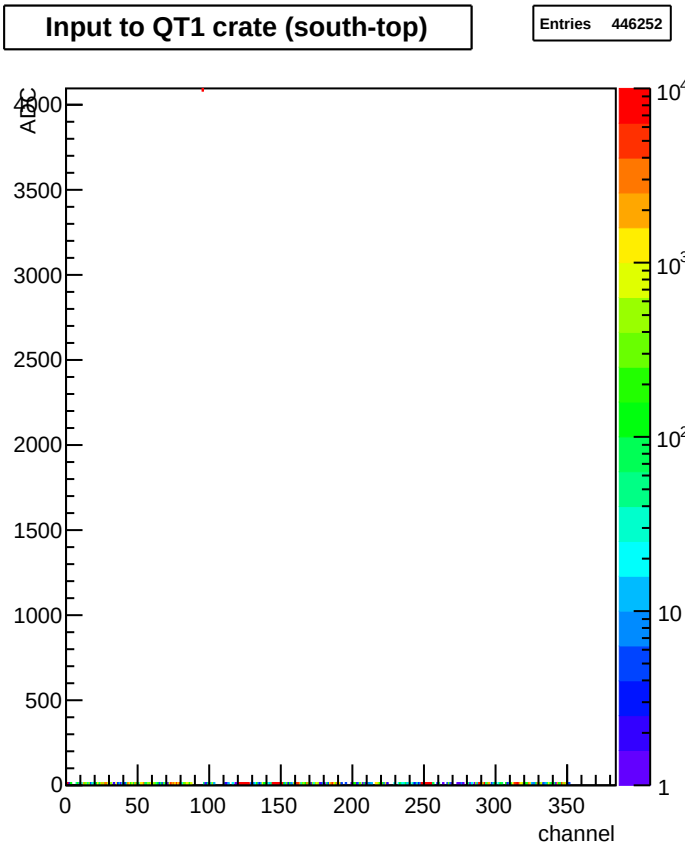

Entries 10000
VPD-West ADC Sum
ZDC-West ADC Sum Att

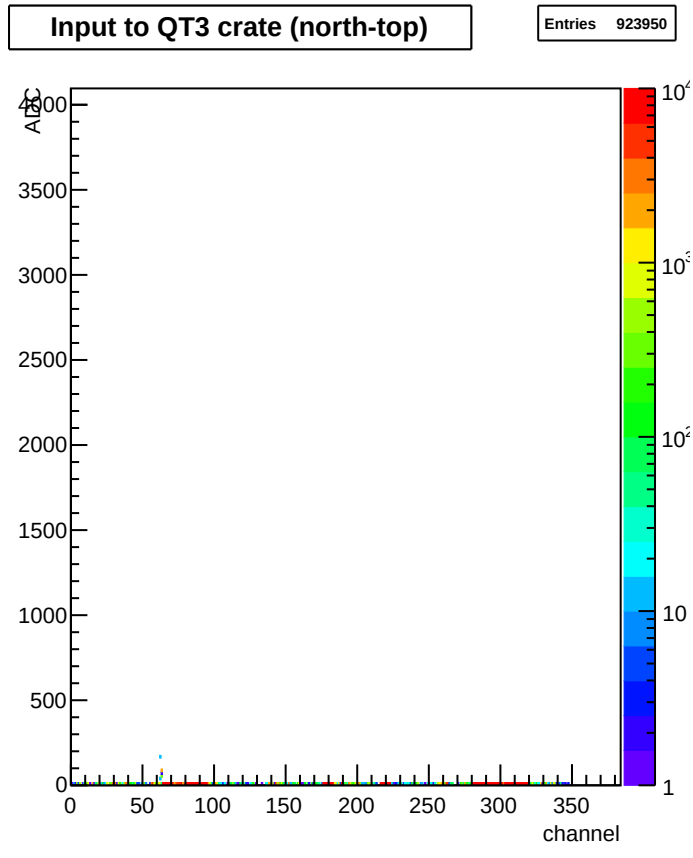

Entries 10000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

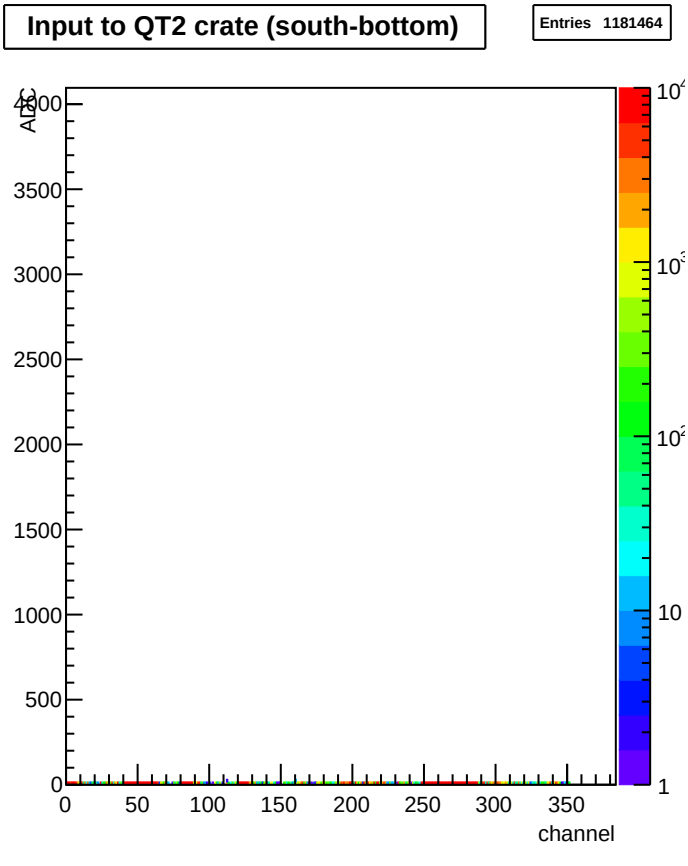

Entries 10000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

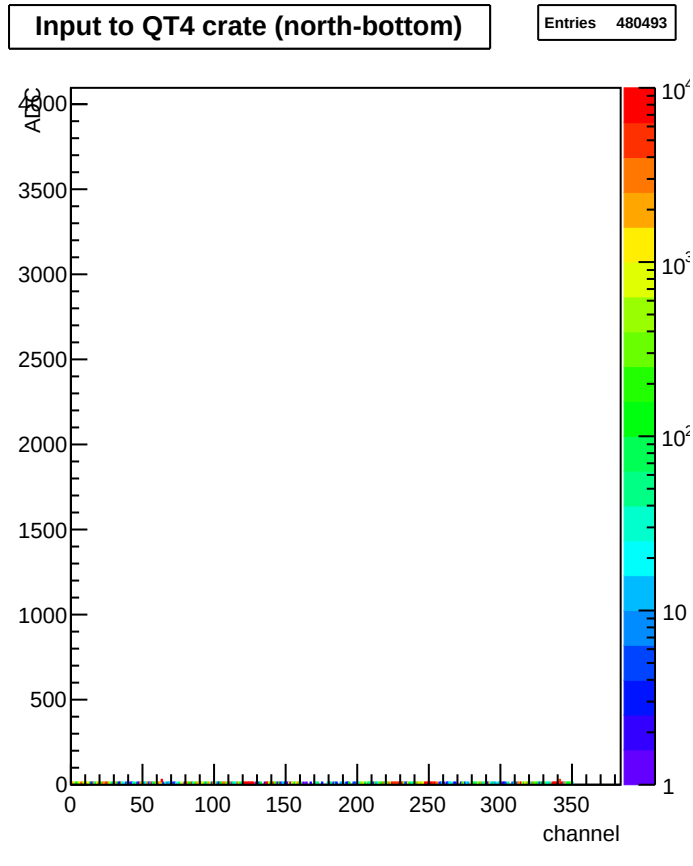

Entries 10000
VPD East ADC Sum
VPD West ADC Sum

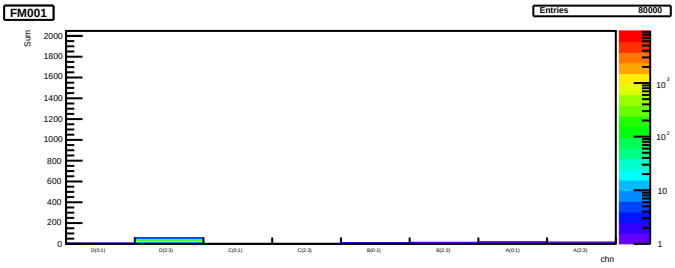

Entries 10000
BBC-S TAC Diff
ZDC TAC Diff

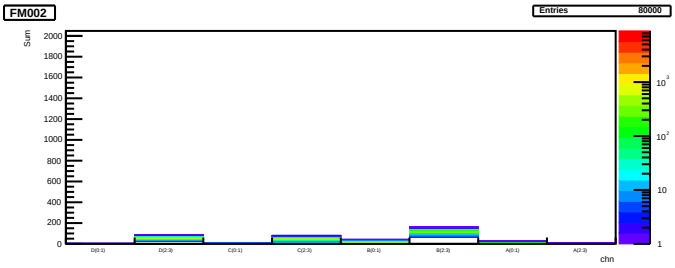

Input to QT1 crate (south-top)
Entries 446252
ADC
channel

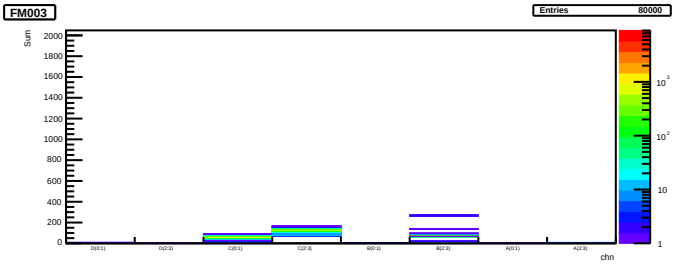

Input to QT3 crate (north-top)
Entries 923950
ADC
channel

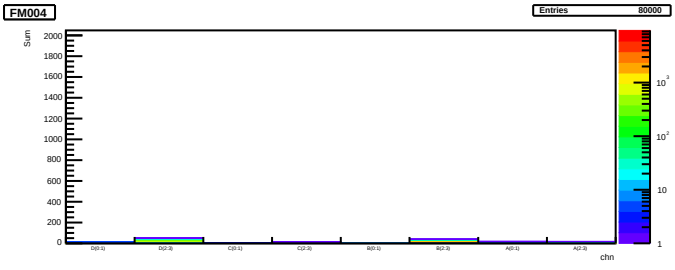

Input to QT2 crate (south-bottom)
Entries 1181464
ADC
channel

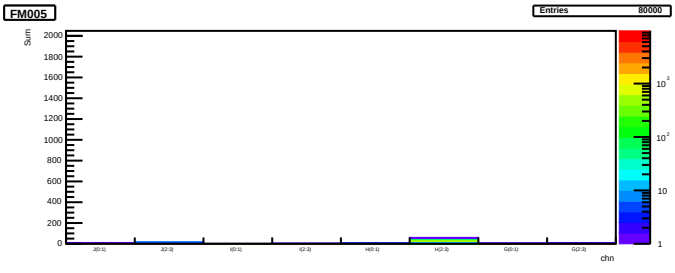

Input to QT4 crate (north-bottom)
Entries 480493
ADC
channel

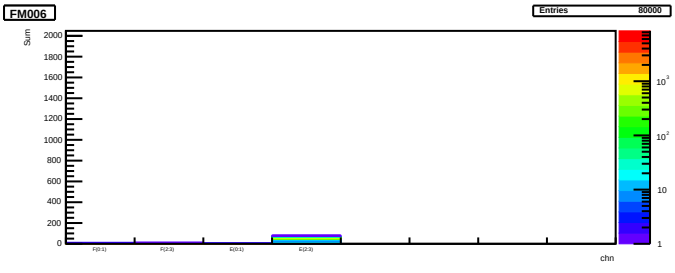

FM001
Entries 80000
Sum
chn

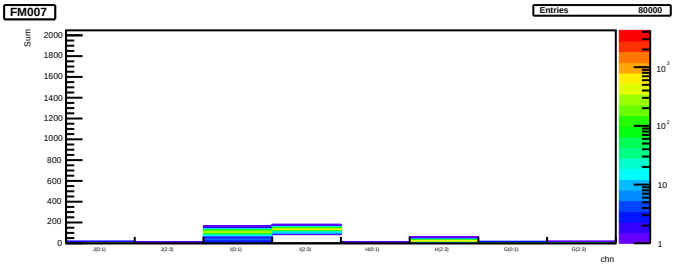

FM002
Entries 80000
Sum
chn

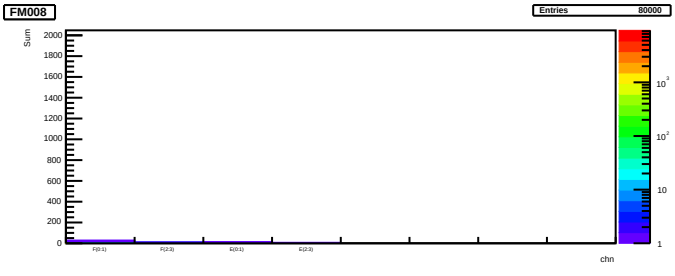

FM003
Entries 80000
Sum
chn

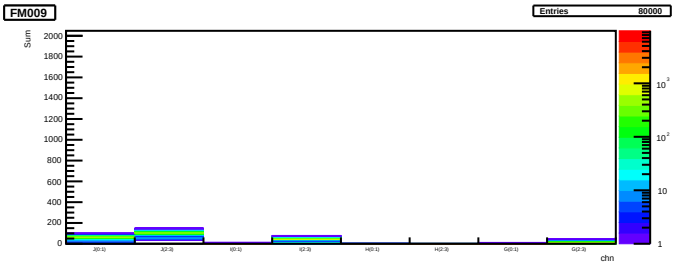

FM004
Entries 80000
Sum
chn

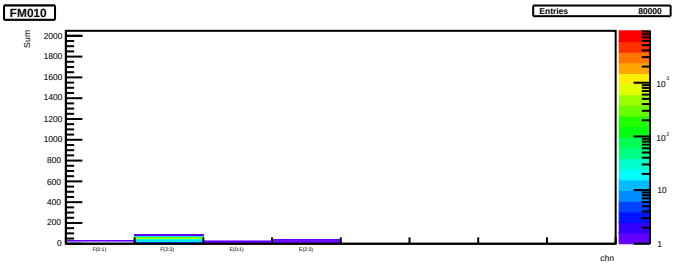

FM005
Entries 80000
Sum
chn

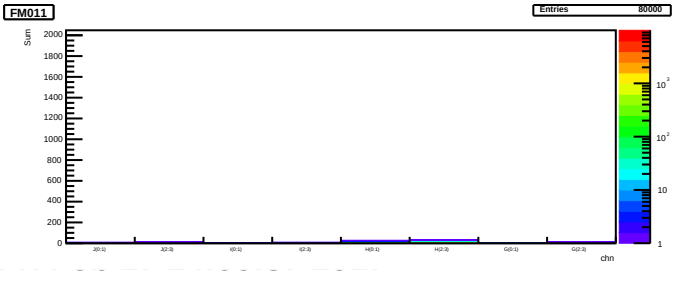

FM006
Entries 80000
Sum
chn

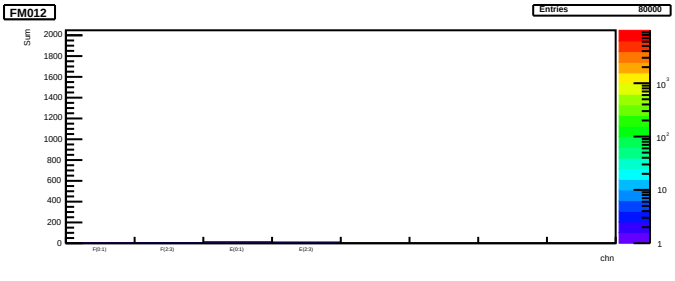

FM007
Entries 80000
Sum
chn

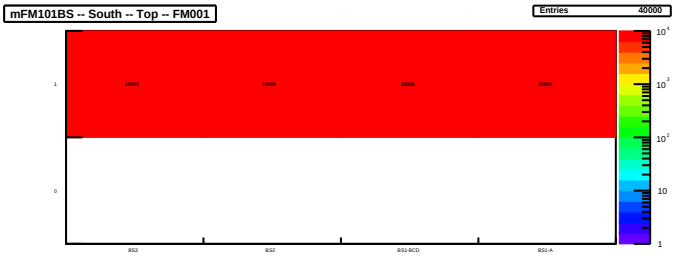

FM008
Entries 80000
Sum
chn


FM009
Entries 80000
Sum
chn

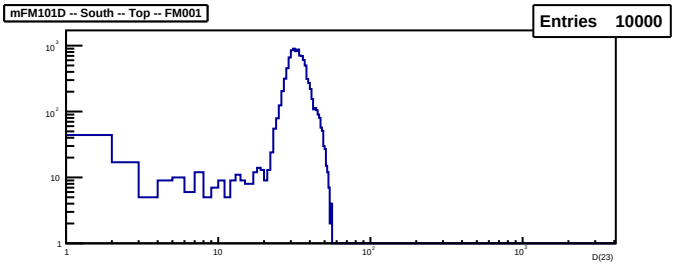

FM010
Entries 80000
Sum
chn

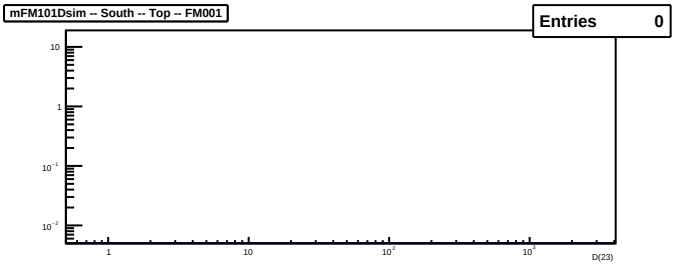

FM011
Entries 80000
Sum
chn

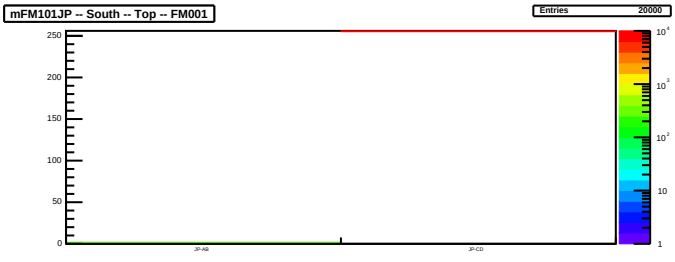

FM012
Entries 80000
Sum
chn


mFM101BS -- South -- Top -- FM001
Entries 40000
BS3
BS2
BS1-BCD
BS1-A

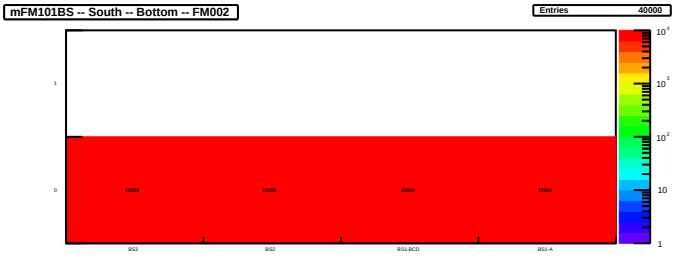
mFM101BSsim -- South -- Top -- FM001
Entries 10000
BS3
BS2
BS1-BCD
BS1-A

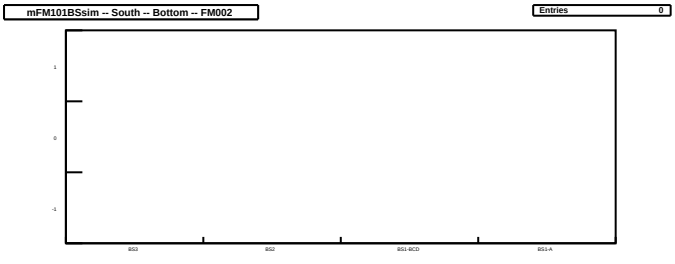

mFM101D -- South -- Top -- FM001
Entries 10000
D(23)

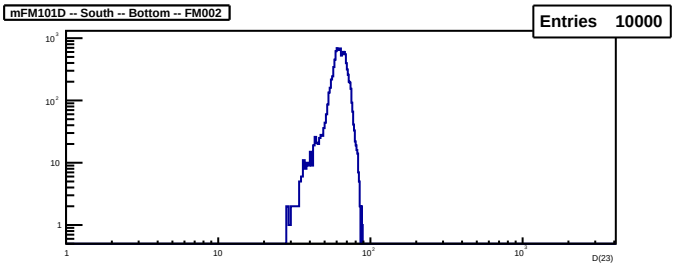

mFM101Dsim -- South -- Top -- FM001
Entries 0
D(23)

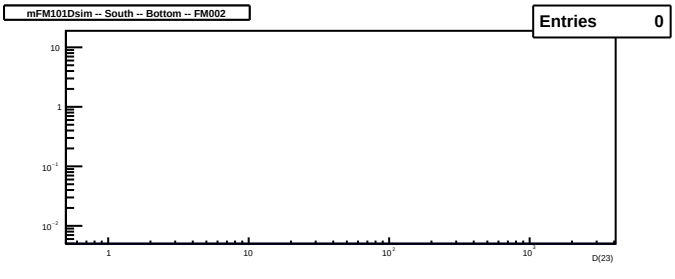

mFM101JP -- South -- Top -- FM001
Entries 20000
JP-AB
JP-CD

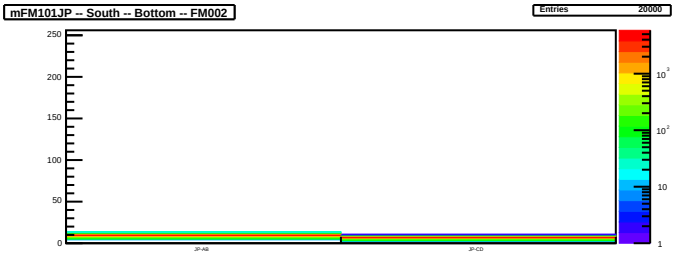
mFM101JPsim -- South -- Top -- FM001
Entries 0
JP-AB
JP-CD

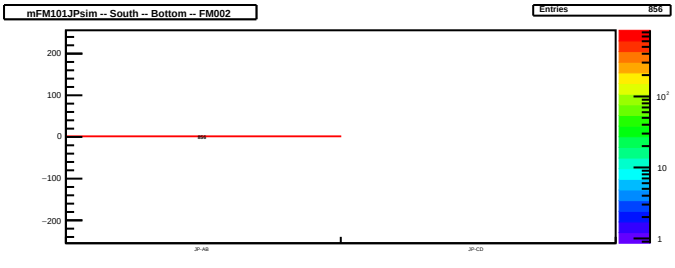

mFM101BS -- South -- Bottom -- FM002
Entries 40000
BS3
BS2
BS1-BCD
BS1-A

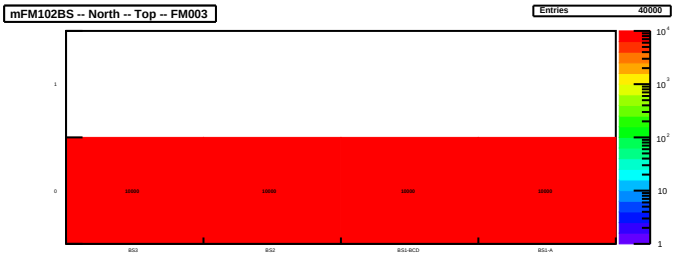

mFM101BSsim -- South -- Bottom -- FM002
Entries 0
BS3
BS2
BS1-BCD
BS1-A


mFM101D -- South -- Bottom -- FM002
Entries 10000
D(23)

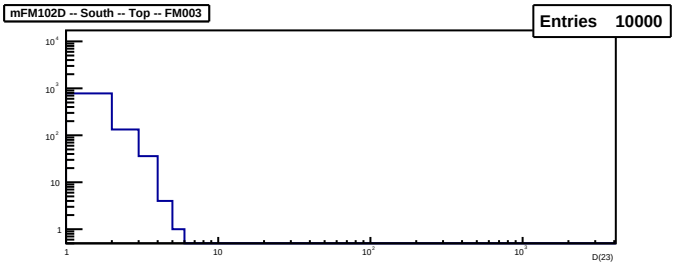

mFM101Dsim -- South -- Bottom -- FM002
Entries 0
D(23)

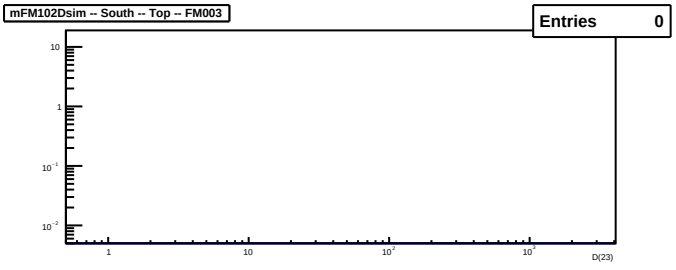

mFM101JP -- South -- Bottom -- FM002
Entries 20000
JP-AB
JP-CD

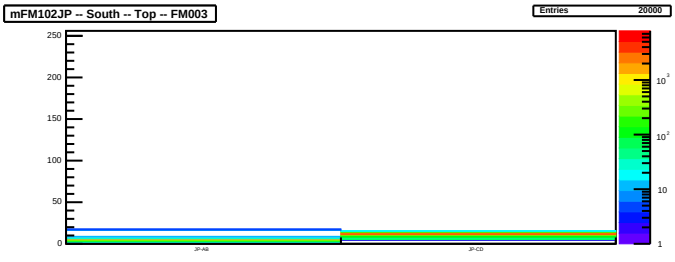

mFM101JPsim -- South -- Bottom -- FM002
Entries 856
JP-AB
JP-CD


mFM102BS -- North -- Top -- FM003
Entries 40000
10000
10000
10000
10000
BS3
BS2
BS1-BCD
BS1-A

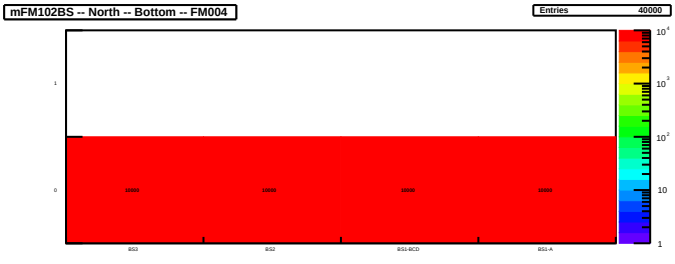
mFM102BSsim -- North -- Top -- FM003
Entries 0
BS3
BS2
BS1-BCD
BS1-A

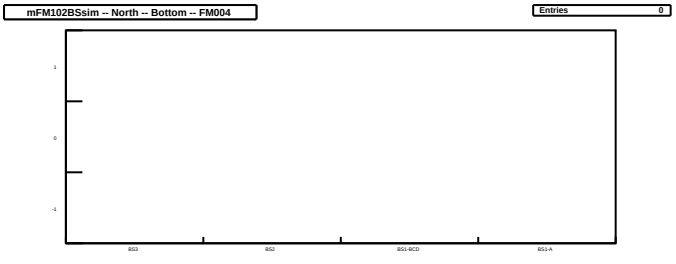

mFM102D -- South -- Top -- FM003
Entries 10000
D(23)

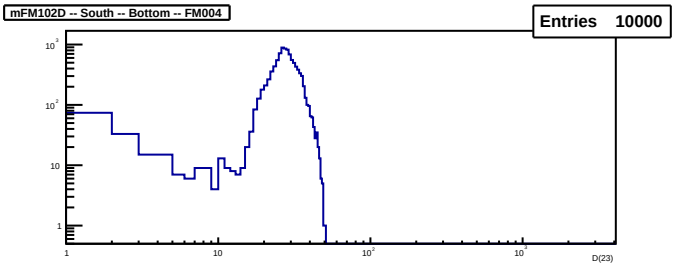

mFM102Dsim -- South -- Top -- FM003
Entries 0
D(23)

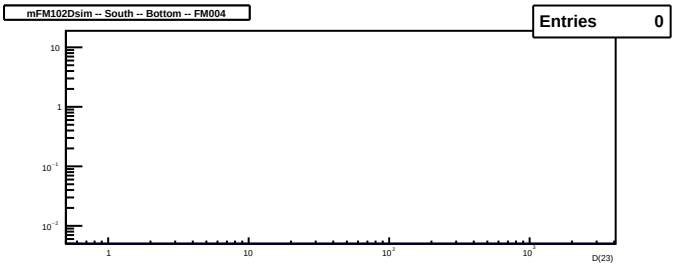

mFM102JP -- South -- Top -- FM003
Entries 20000
JP-AB
JP-CD

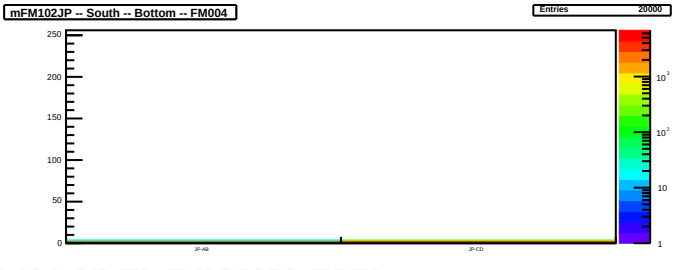
mFM102JPsim -- South -- Top -- FM003
Entries 0
JP-AB
JP-CD

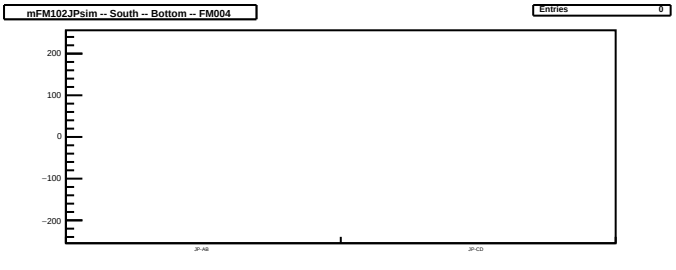

mFM102BS -- North -- Bottom -- FM004
Entries 40000
10000
10000
10000
10000
BS3
BS2
BS1-BCD
BS1-A

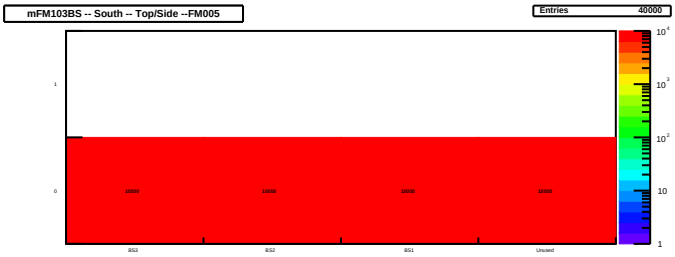

mFM102BSsim -- North -- Bottom -- FM004
Entries 0
BS3
BS2
BS1-BCD
BS1-A

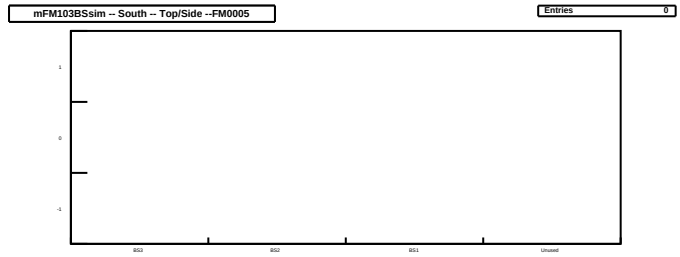

mFM102D -- South -- Bottom -- FM004
Entries 10000
D(23)

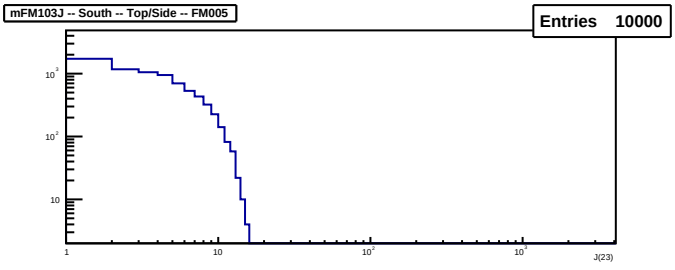

mFM102Dsim -- South -- Bottom -- FM004
Entries 0
D(23)

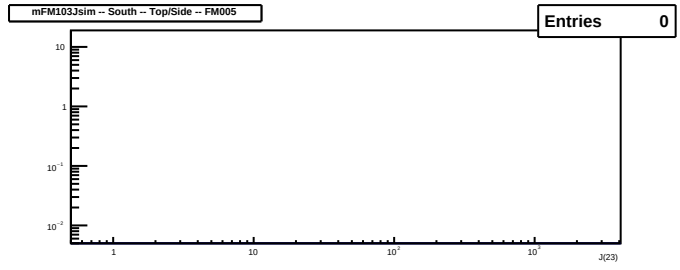

mFM102JP -- South -- Bottom -- FM004
Entries 20000
JP-AB
JP-CD

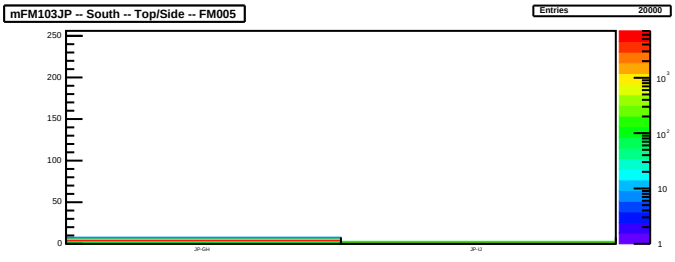

mFM102JPsim -- South -- Bottom -- FM004
Entries 0
JP-AB
JP-CD


mFM103BS -- South -- Top/Side --FM005
Entries 40000
10000
10000
10000
10000
BS3
BS2
BS1
Unused

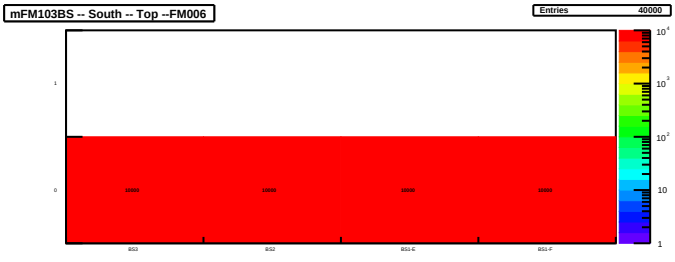

mFM103BSsim -- South -- Top/Side --FM0005
Entries 0
BS3
BS2
BS1
Unused

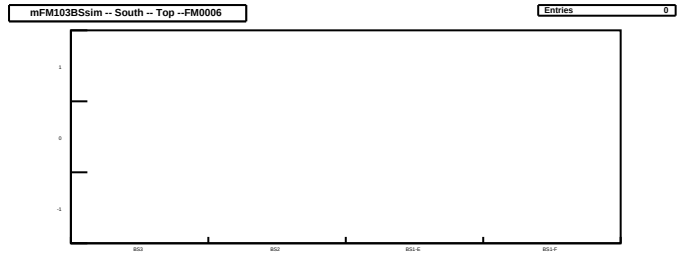

mFM103J -- South -- Top/Side -- FM005
Entries 10000
J(23)

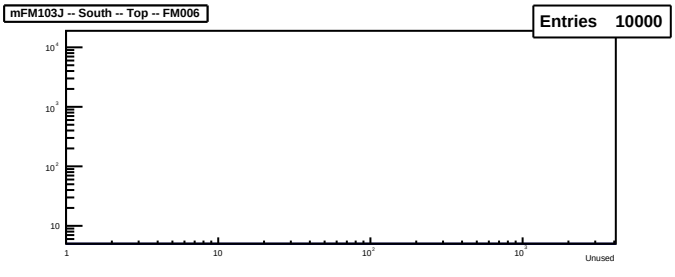

mFM103Jsim -- South -- Top/Side -- FM005
Entries 0
J(23)

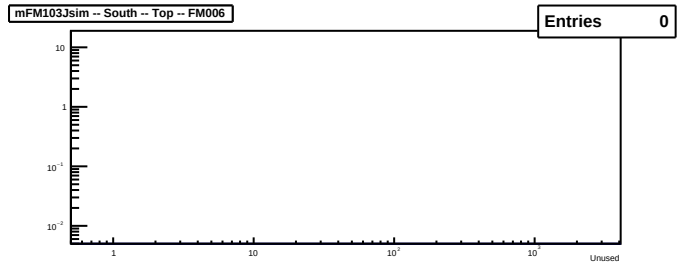

mFM103JP -- South -- Top/Side -- FM005
Entries 20000
JP-GH
JP-U

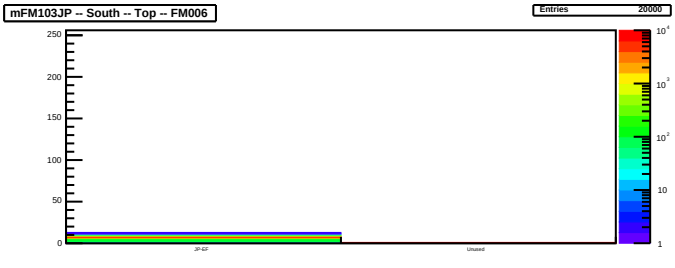
mFM103JPsim -- South -- Top/Side -- FM005
Entries 0
JP-GH
JP-U

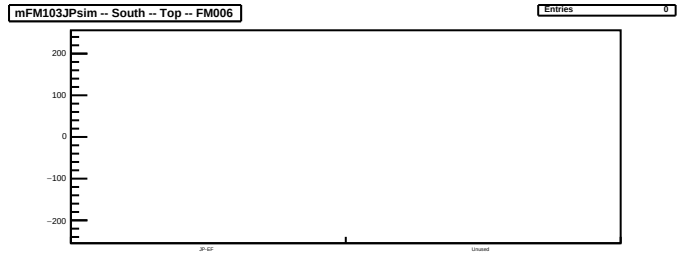

mFM103BS -- South -- Top --FM006
Entries 40000
10000
10000
10000
10000
BS3
BS2
BS1-E
BS1-F

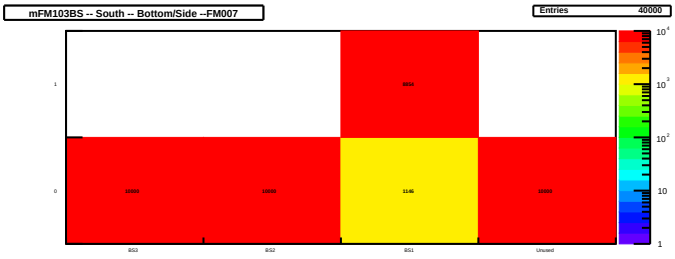

mFM103BSsim -- South -- Top --FM0006
Entries 0
BS3
BS2
BS1-E
BS1-F

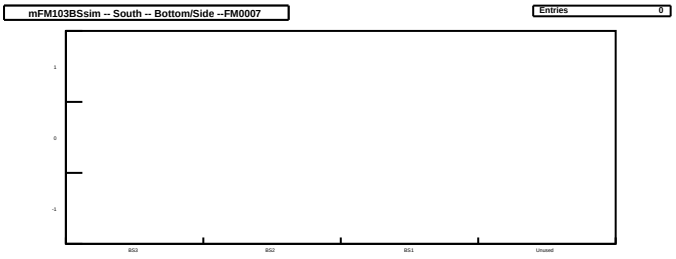

mFM103J -- South -- Top -- FM006
Entries 10000
Unused

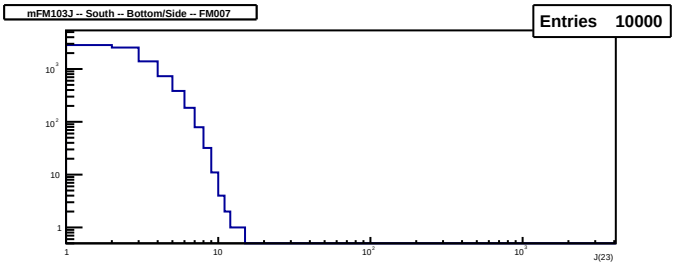

mFM103Jsim -- South -- Top -- FM006
Entries 0
Unused

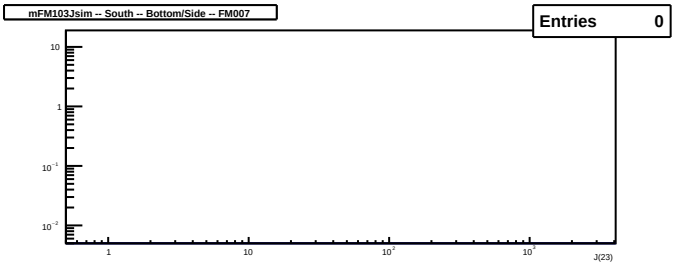

mFM103JP -- South -- Top -- FM006
Entries 20000
JP-EF
Unused

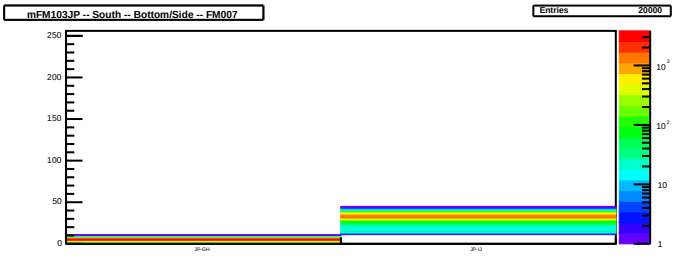

mFM103JPsim -- South -- Top -- FM006
Entries 0
JP-EF
Unused

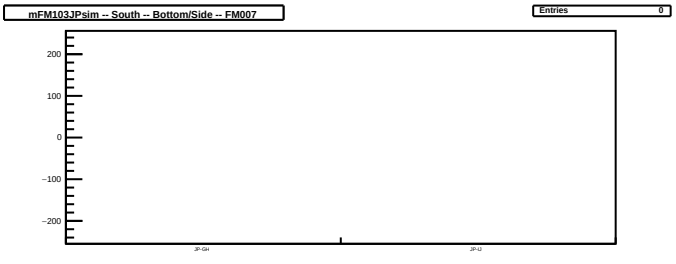

mFM103BS -- South -- Bottom/Side --FM007
Entries 40000
10000
10000
1146
10000
BS3
BS2
BS1
Unused

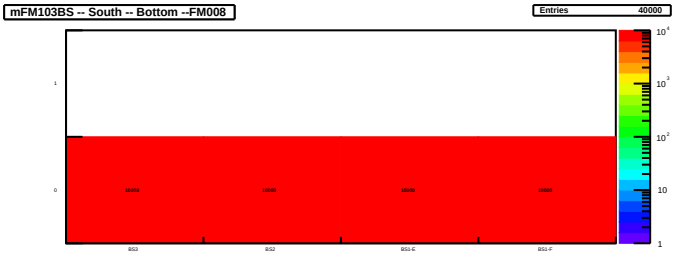

mFM103BSsim -- South -- Bottom/Side --FM0007
Entries 0
BS3
BS2
BS1
Unused

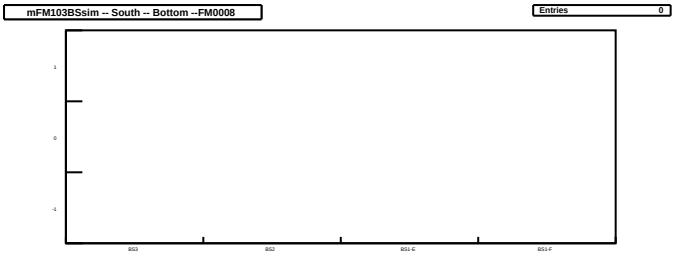

mFM103J -- South -- Bottom/Side -- FM007
Entries 10000
J(23)

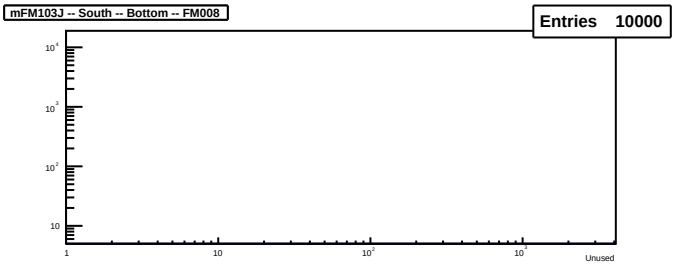

mFM103Jsim -- South -- Bottom/Side -- FM007
Entries 0
J(23)

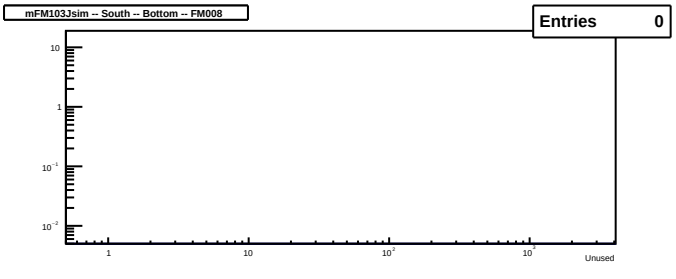

mFM103JP -- South -- Bottom/Side -- FM007
Entries 20000
JP-G4
JP-U

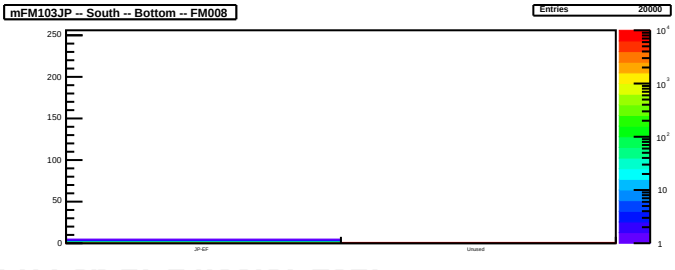

mFM103JPsim -- South -- Bottom/Side -- FM007
Entries 0
JP-G4
JP-U

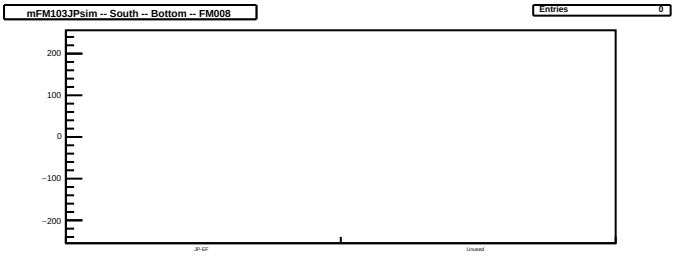

mFM103BS -- South -- Bottom --FM008
Entries 40000
10000
10000
10000
10000
BS3
BS2
BS1-E
BS1-F

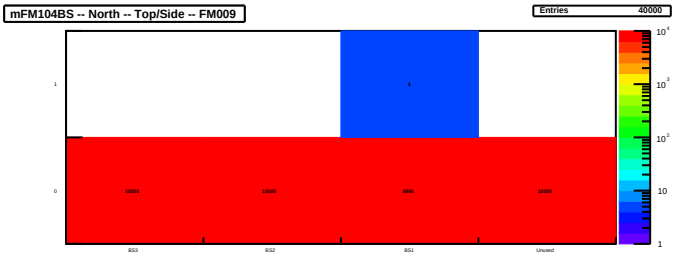

mFM103BSsim -- South -- Bottom --FM0008
Entries 0
BS3
BS2
BS1-E
BS1-F

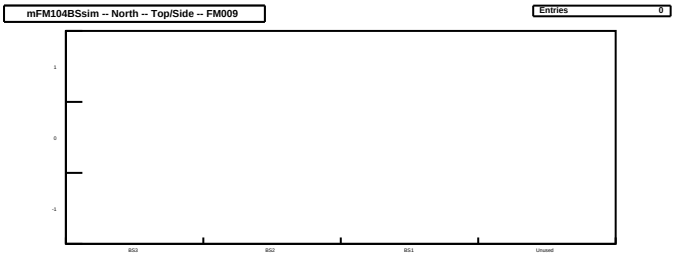

mFM103J -- South -- Bottom -- FM008
Entries 10000
Unused

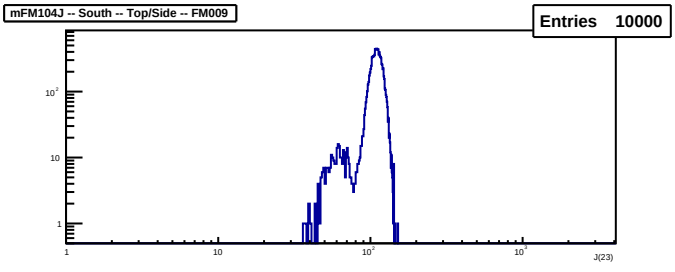

mFM103Jsim -- South -- Bottom -- FM008
Entries 0
Unused

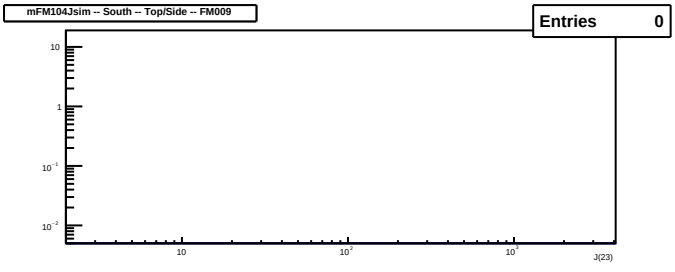

mFM103JP -- South -- Bottom -- FM008
Entries 20000
JP-EF
Unused

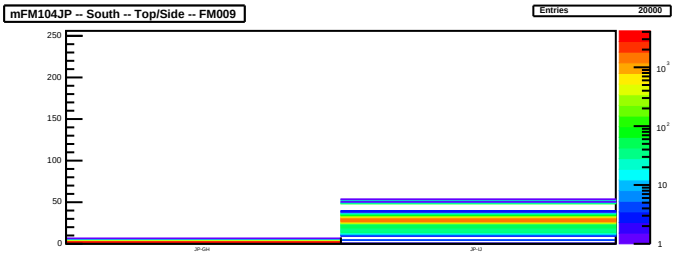

mFM103JPsim -- South -- Bottom -- FM008
Entries 0
JP-EF
Unused

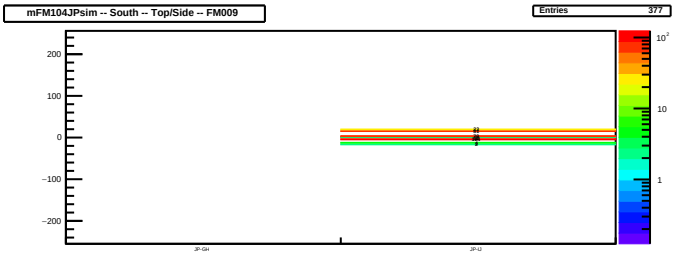

mFM104BS -- North -- Top/Side -- FM009
Entries 40000
BS3
BS2
BS1
Unused

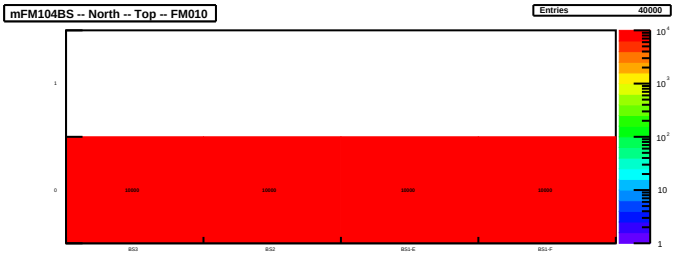

mFM104BSsim -- North -- Top/Side -- FM009
Entries 0
BS3
BS2
BS1
Unused

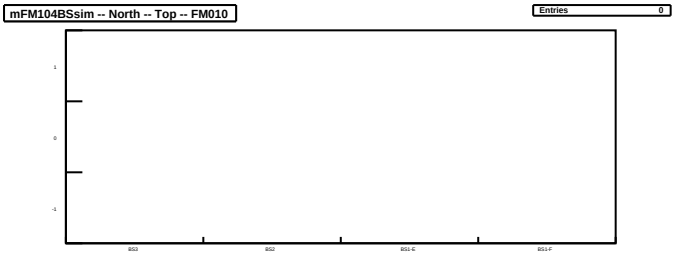

mFM104J -- South -- Top/Side -- FM009
Entries 10000
J(23)

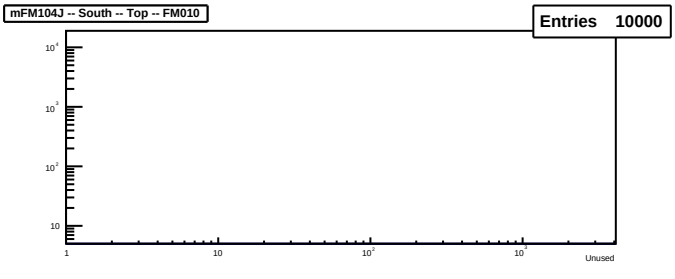

mFM104Jsim -- South -- Top/Side -- FM009
Entries 0
J(23)

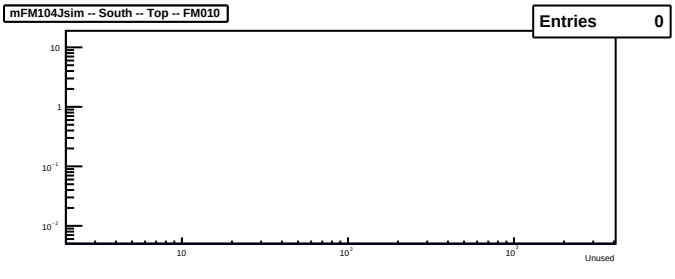

mFM104JP -- South -- Top/Side -- FM009
Entries 20000
JP-GH
JP-IJ

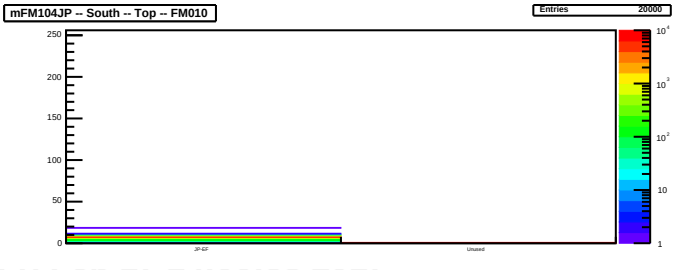

mFM104JPsim -- South -- Top/Side -- FM009
Entries 377
JP-GH
JP-IJ

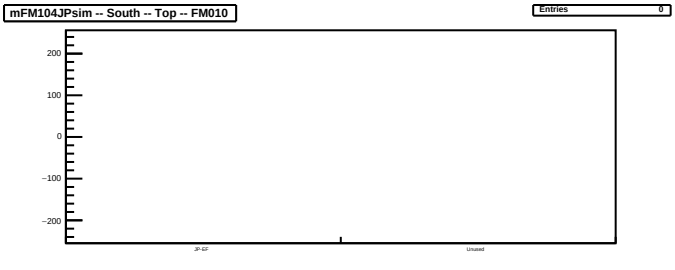

mFM104BS -- North -- Top -- FM010
Entries 40000
BS3
BS2
BS1-E
BS1-F

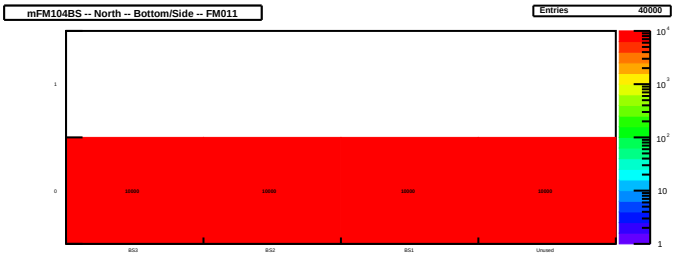

mFM104BSsim -- North -- Top -- FM010
Entries 0
BS3
BS2
BS1-E
BS1-F

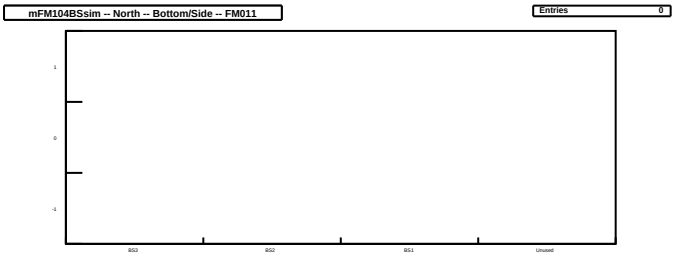

mFM104J -- South -- Top -- FM010
Entries 10000
Unused

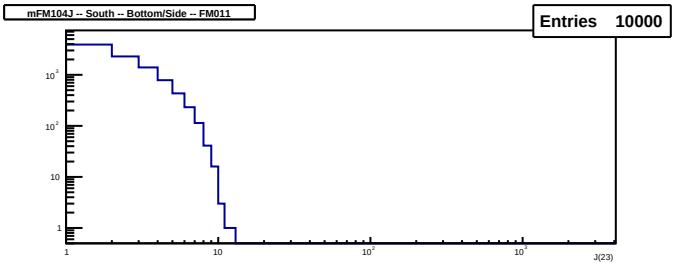

mFM104Jsim -- South -- Top -- FM010
Entries 0
Unused

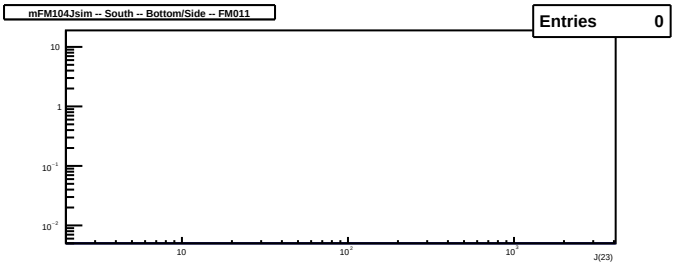

mFM104JP -- South -- Top -- FM010
Entries 20000
JP-EF
Unused

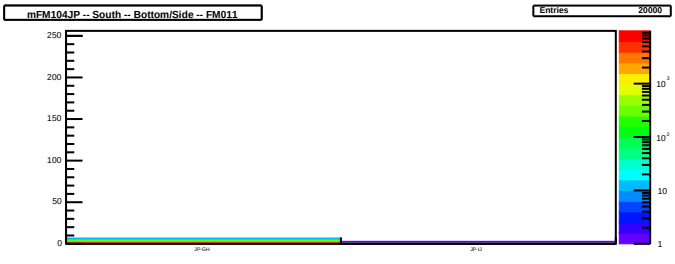

mFM104JPsim -- South -- Top -- FM010
Entries 0
JP-EF
Unused

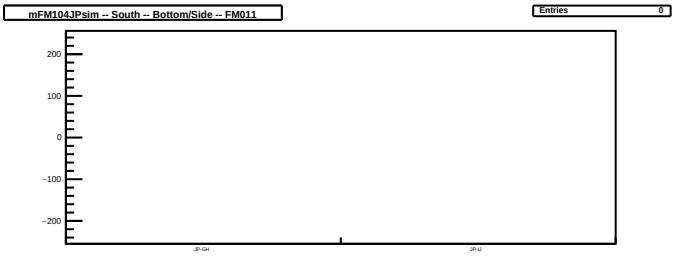

mFM104BS -- North -- Bottom/Side -- FM011
Entries 40000
10000
10000
10000
10000
BS3
BS2
BS1
Unused

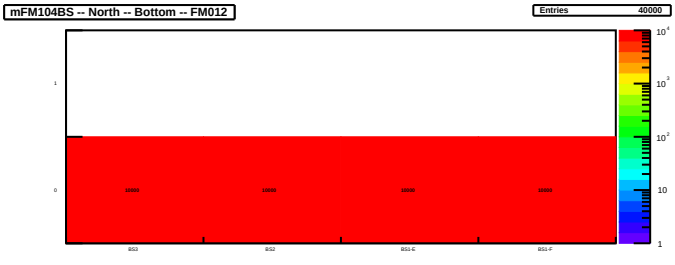

mFM104BSsim -- North -- Bottom/Side -- FM011
Entries 0
BS3
BS2
BS1
Unused

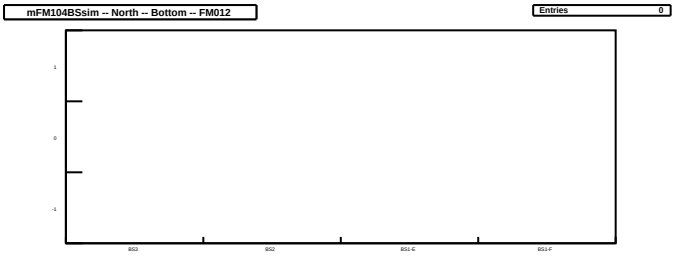

mFM104J -- South -- Bottom/Side -- FM011
Entries 10000
J(23)

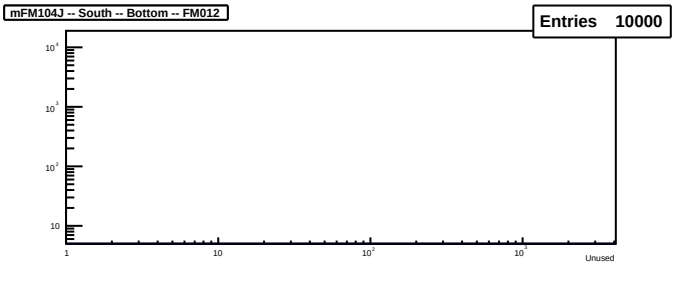

mFM104Jsim -- South -- Bottom/Side -- FM011
Entries 0
J(23)

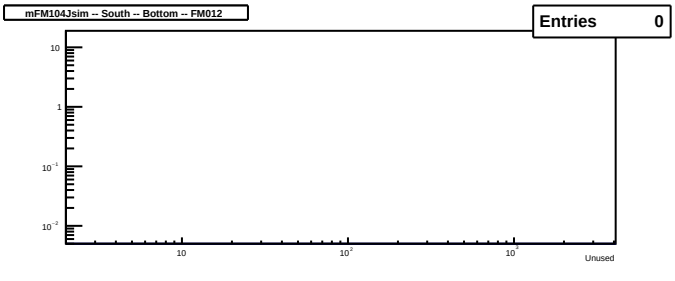

mFM104JP -- South -- Bottom/Side -- FM011
Entries 20000
JP-GH
JP-IJ

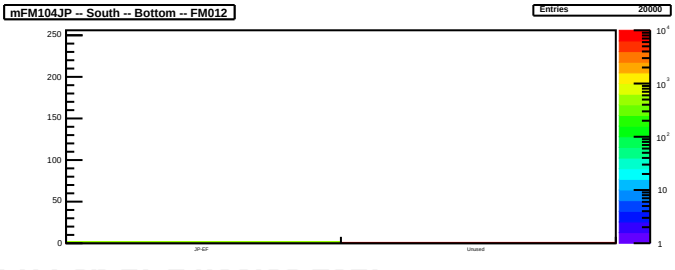

mFM104JPsim -- South -- Bottom/Side -- FM011
Entries 0
JP-GH
JP-IJ

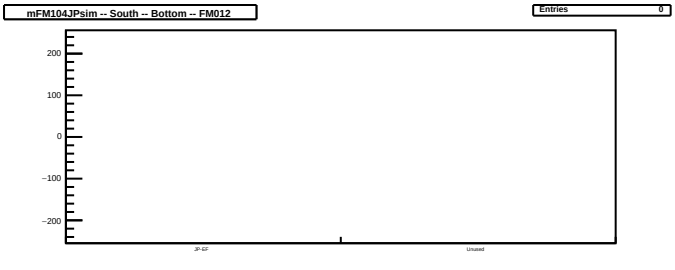

mFM104BS -- North -- Bottom -- FM012
Entries 40000
10000
10000
10000
10000
BS3
BS2
BS1-E
BS1-F

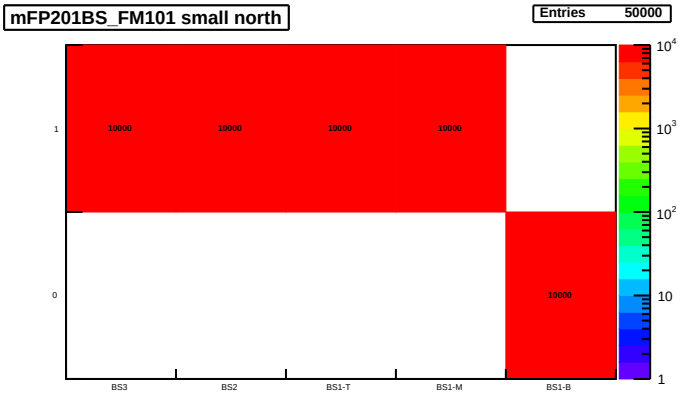

mFM104BSsim -- North -- Bottom -- FM012
Entries 0
BS3
BS2
BS1-E
BS1-F

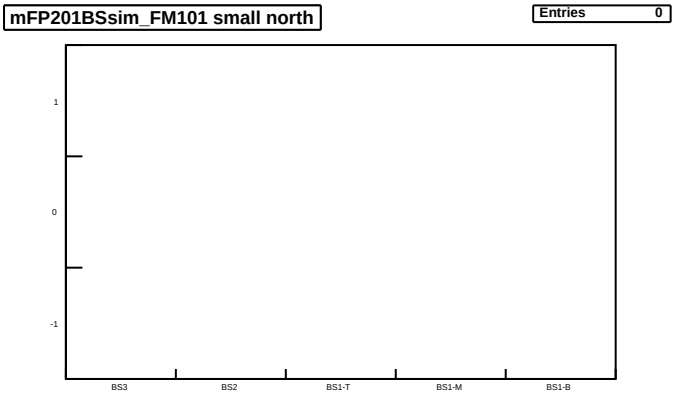

mFM104J -- South -- Bottom -- FM012
Entries 10000
Unused

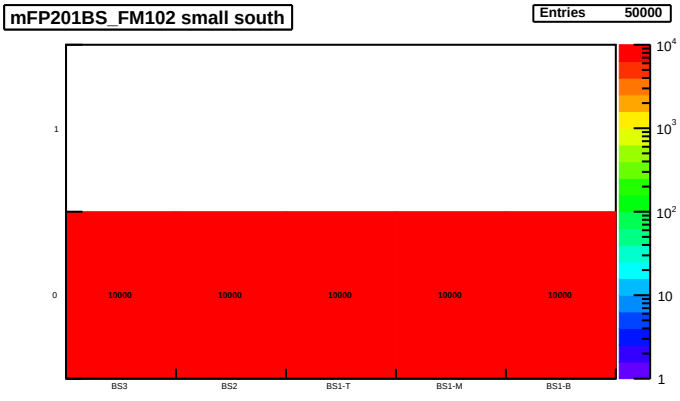

mFM104Jsim -- South -- Bottom -- FM012
Entries 0
Unused

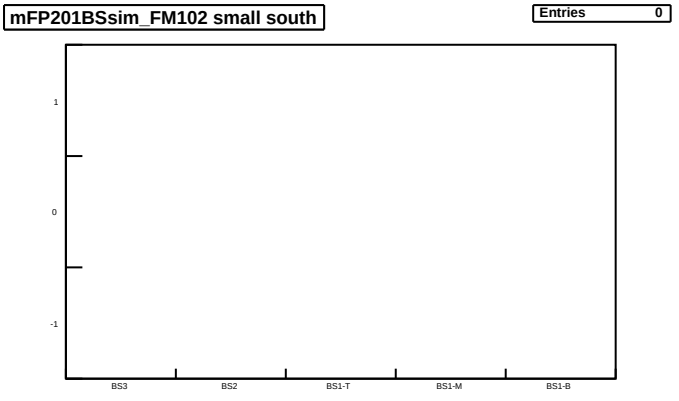

mFM104JP -- South -- Bottom -- FM012
Entries 20000
JP-EF
Unused

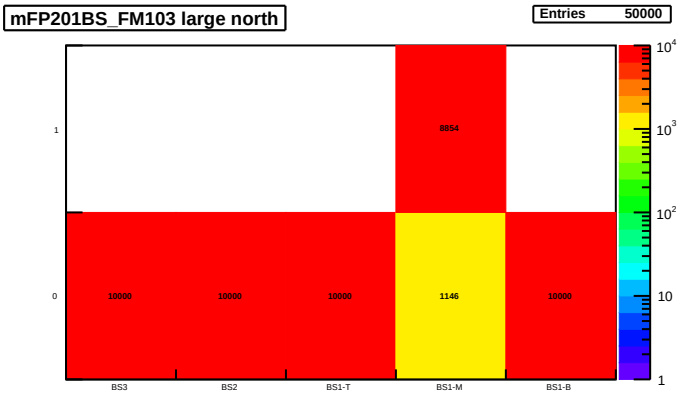

mFM104JPsim -- South -- Bottom -- FM012
Entries 0
JP-EF
Unused

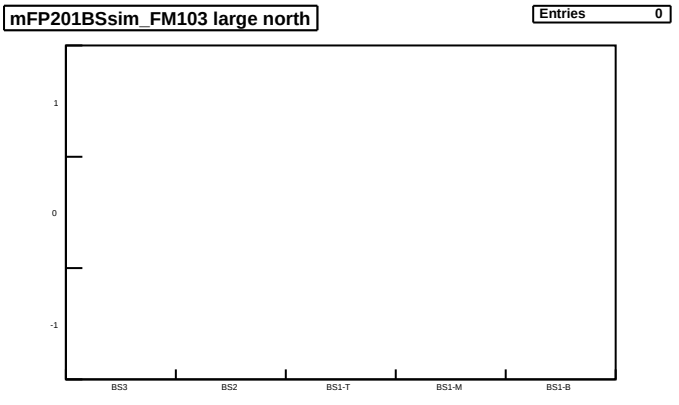

mFP201BS_FM101 small north
Entries 50000
10000
10000
10000
10000
10000
BS3
BS2
BS1-T
BS1-M
BS1-B

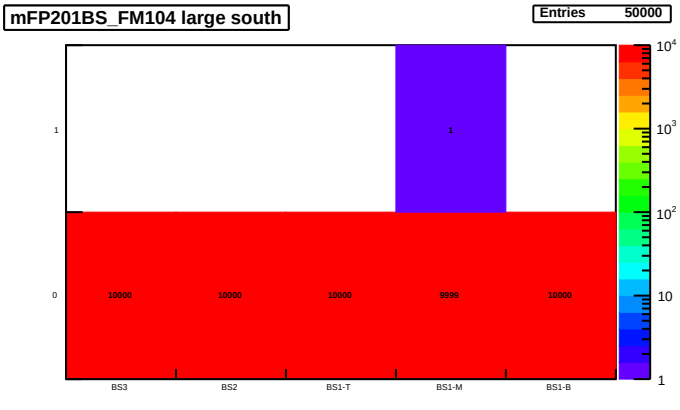

mFP201BSsim_FM101 small north
Entries 0
BS3
BS2
BS1-T
BS1-M
BS1-B

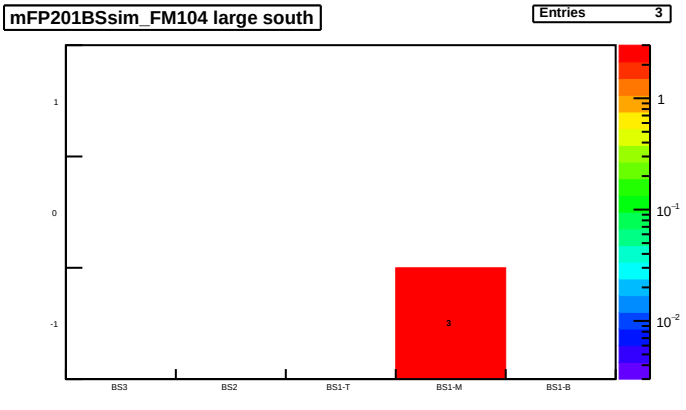

mFP201BS_FM102 small south
Entries 50000
10000
10000
10000
10000
10000
BS3
BS2
BS1-T
BS1-M
BS1-B

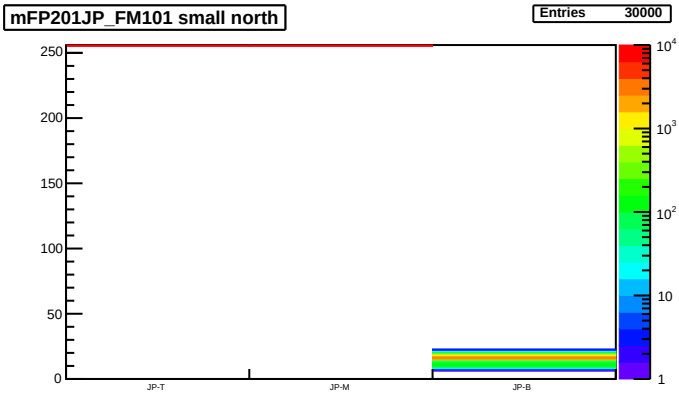

mFP201BSsim_FM102 small south
Entries 0
BS3
BS2
BS1-T
BS1-M
BS1-B

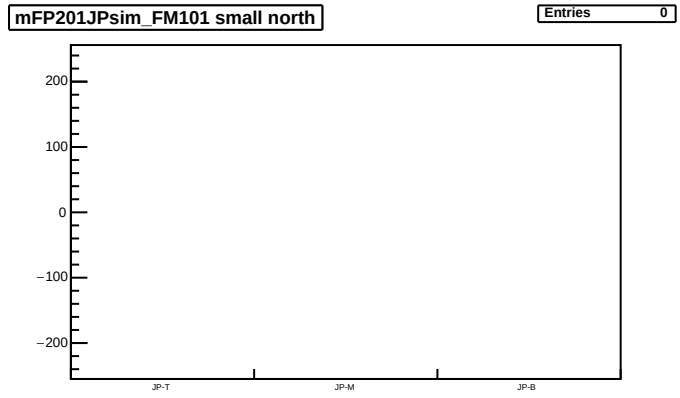

mFP201BS_FM103 large north
Entries 50000
8854
10000
10000
10000
1146
10000
BS3
BS2
BS1-T
BS1-M
BS1-B

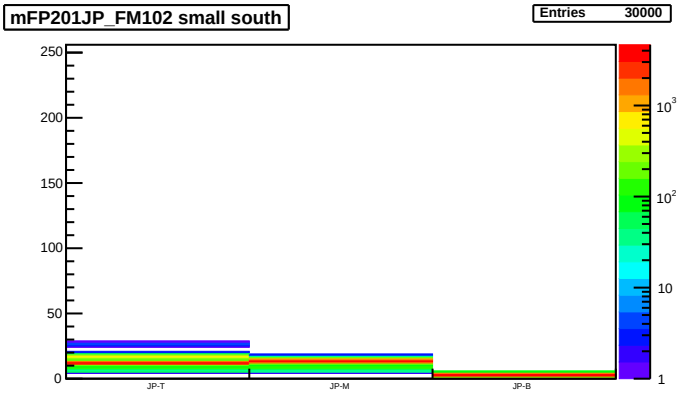

mFP201BSsim_FM103 large north
Entries 0
BS3
BS2
BS1-T
BS1-M
BS1-B

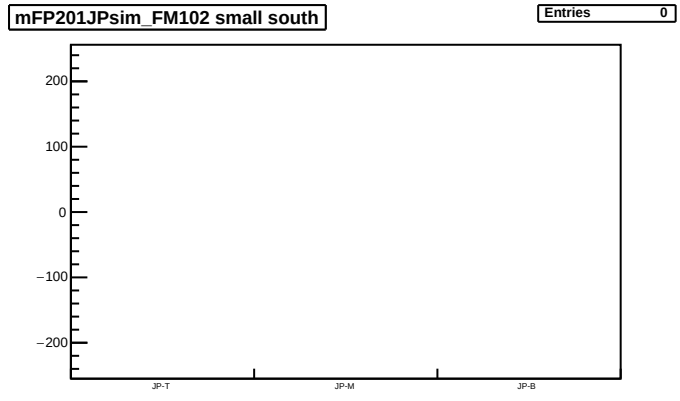

mFP201BS_FM104 large south
Entries 50000
1
10000
10000
10000
9999
10000
BS3
BS2
BS1-T
BS1-M
BS1-B

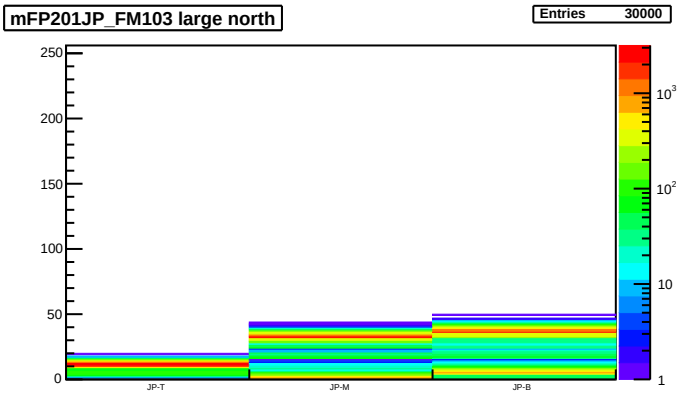

mFP201BSsim_FM104 large south
Entries 3
3
BS3
BS2
BS1-T
BS1-M
BS1-B

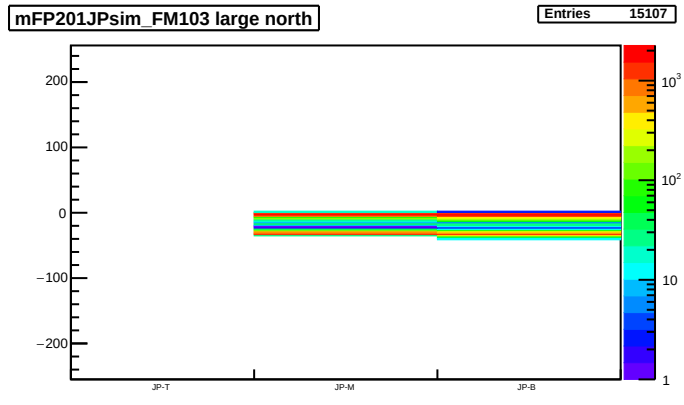

mFP201JP_FM101 small north
Entries 30000
JP-T
JP-M
JP-B

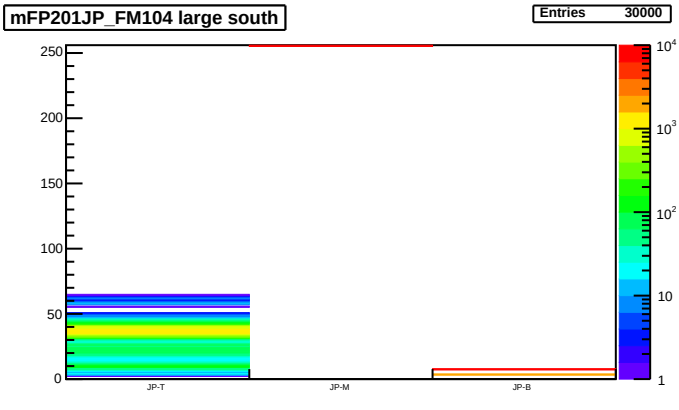

mFP201JPsim_FM101 small north
Entries 0
JP-T
JP-M
JP-B

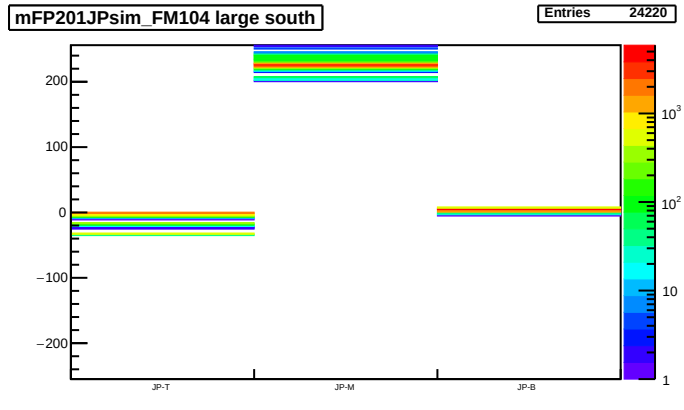

mFP201JP_FM102 small south
Entries 30000
JP-T
JP-M
JP-B

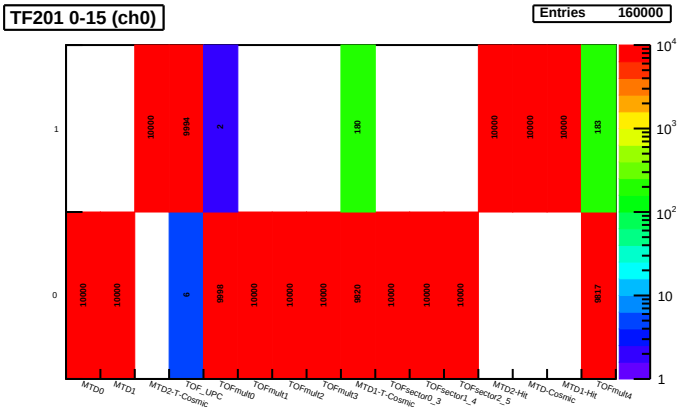

mFP201JPsim_FM102 small south
Entries 0
JP-T
JP-M
JP-B

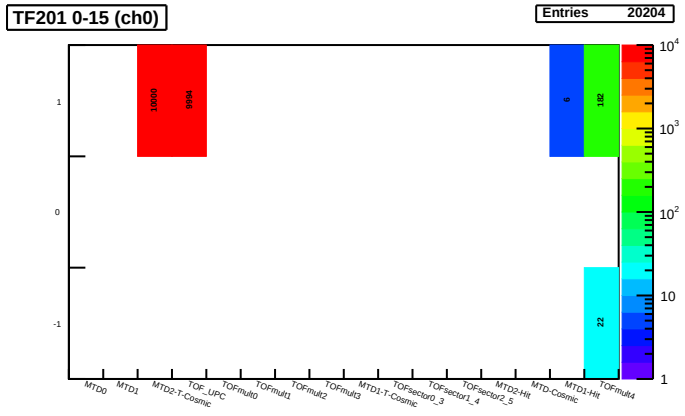

mFP201JP_FM103 large north
Entries 30000
JP-T
JP-M
JP-B

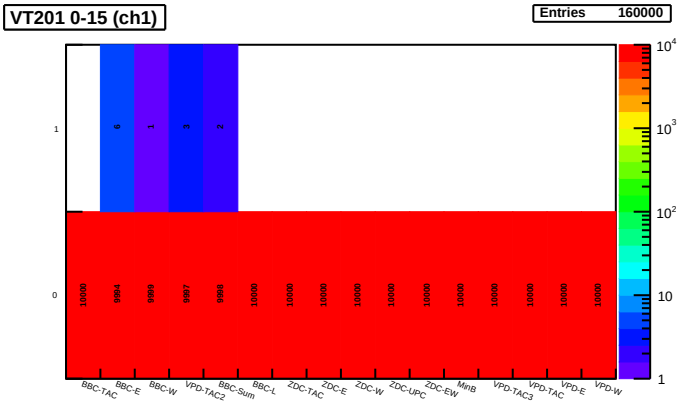

mFP201JPsim_FM103 large north
Entries 15107
JP-T
JP-M
JP-B

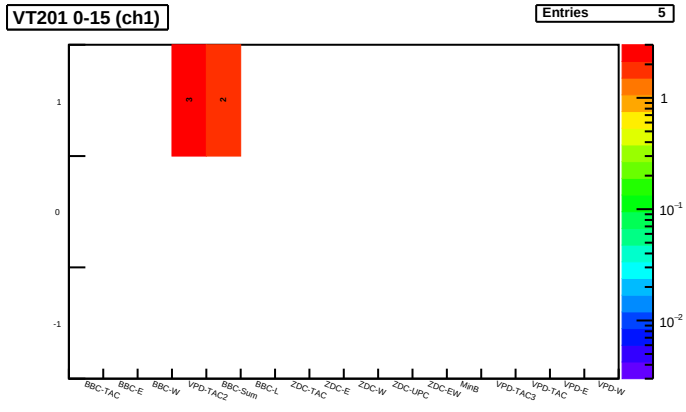

mFP201JP_FM104 large south
Entries 30000
JP-T
JP-M
JP-B

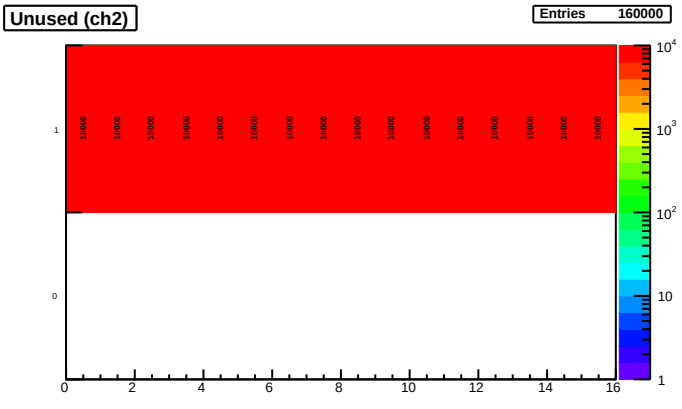

mFP201JPsim_FM104 large south
Entries 24220
JP-T
JP-M
JP-B

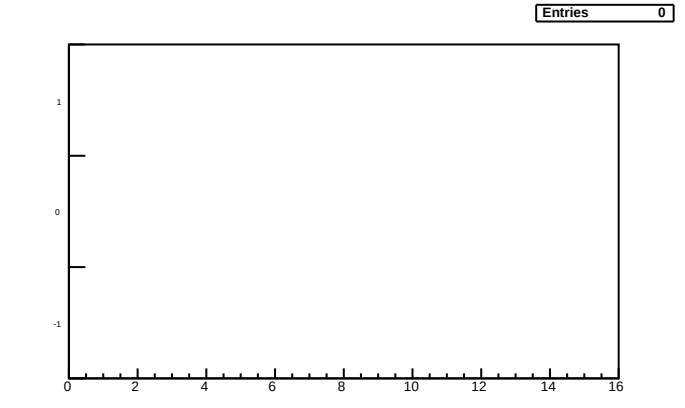

TF201 0-15 (ch0)
Entries 160000
MTD0 MTD1 MTD2-T-Cosmic TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 MTD1-T-Cosmic TOFsector0_3 TOFsector1_4 TOFsector2_5 MTD2-Hit MTD-Cosmic MTD1-Hit TOFmult4

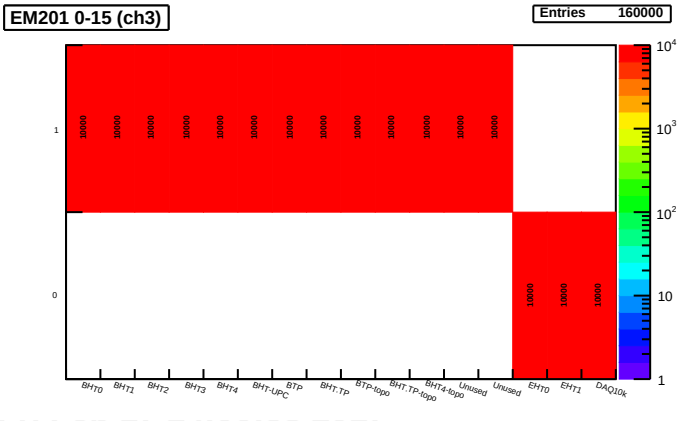

TF201 0-15 (ch0)
Entries 20204
MTD0 MTD1 MTD2-T-Cosmic TOF_UPC TOFmult0 TOFmult1 TOFmult2 TOFmult3 MTD1-T-Cosmic TOFsector0_3 TOFsector1_4 TOFsector2_5 MTD2-Hit MTD-Cosmic MTD1-Hit TOFmult4

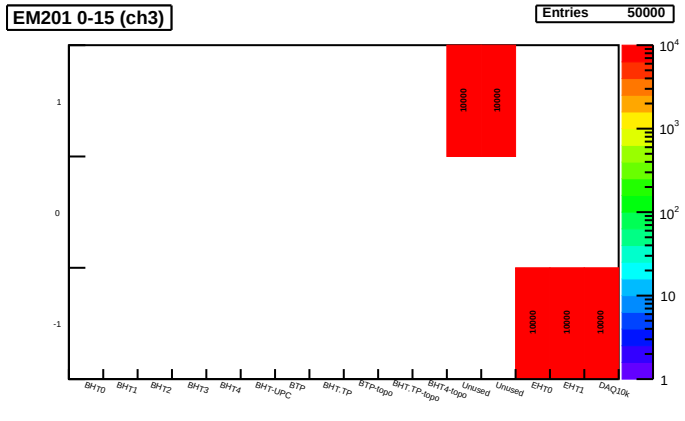

VT201 0-15 (ch1)
Entries 160000
BBC-TAC BBC-E BBC-W VPD-TAC2 BBC-Sum BBC-L ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW MinB VPD-TAC3 VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 5
BBC-TAC BBC-E BBC-W VPD-TAC2 BBC-Sum BBC-L ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW MinB VPD-TAC3 VPD-TAC VPD-E VPD-W

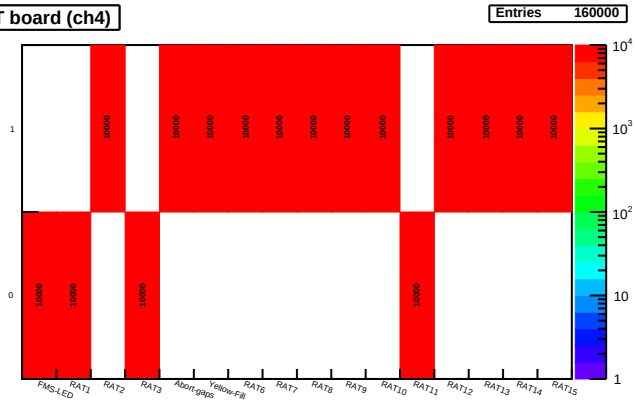

Unused (ch2)
Entries 160000

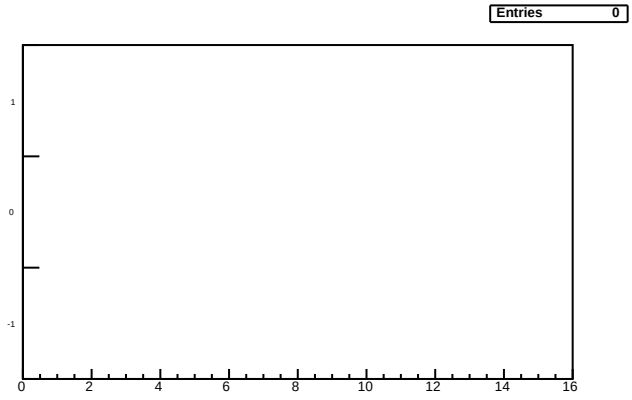

Entries 0


EM201 0-15 (ch3)
Entries 160000
BH10 BH11 BH12 BH13 BH14 BH1-UPC BTp BH1:TP BTp-topo BH1:TP-topo BH14-topo Unused Unused EH10 EH11 DAQ10k

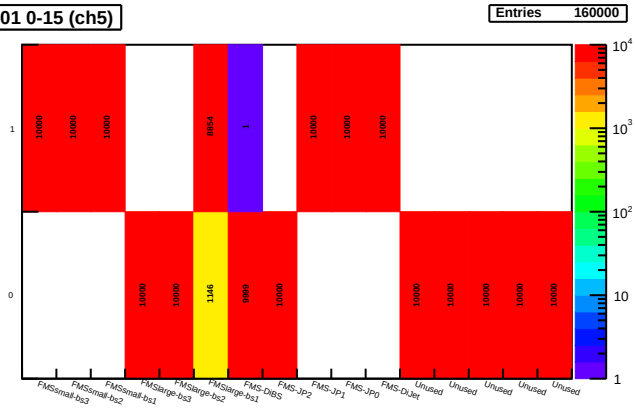

EM201 0-15 (ch3)
Entries 50000
BH10 BH11 BH12 BH13 BH14 BH1-UPC BTp BH1:TP BTp-topo BH1:TP-topo BH14-topo Unused Unused EH10 EH11 DAQ10k

**RAT board (ch4)**

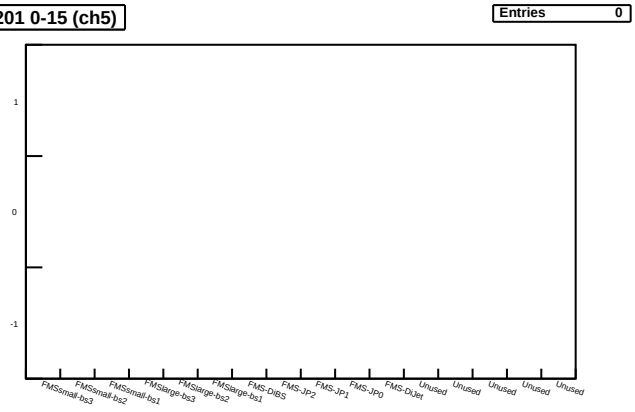


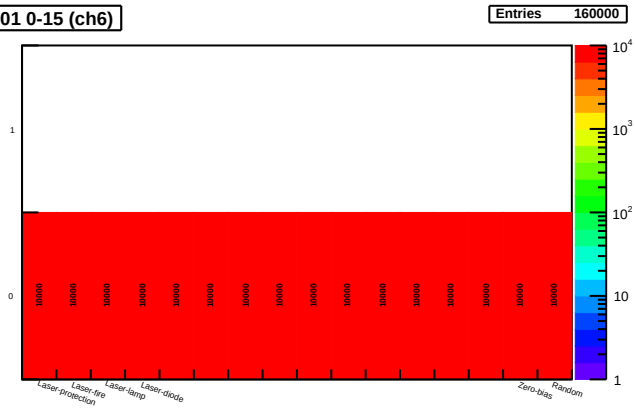

**FP201 0-15 (ch5)**

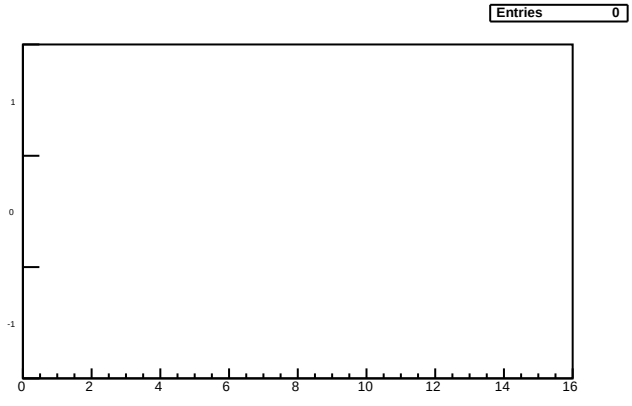


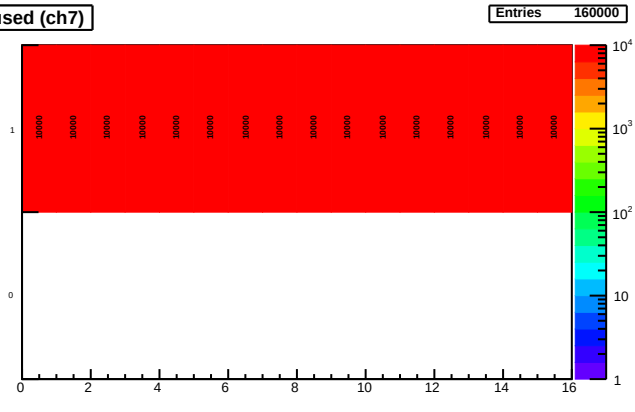
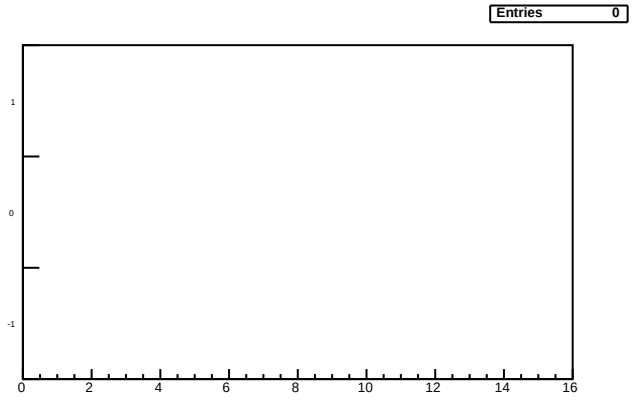

**FP201 0-15 (ch5)**




**ST201 0-15 (ch6)**




**Unused (ch7)**