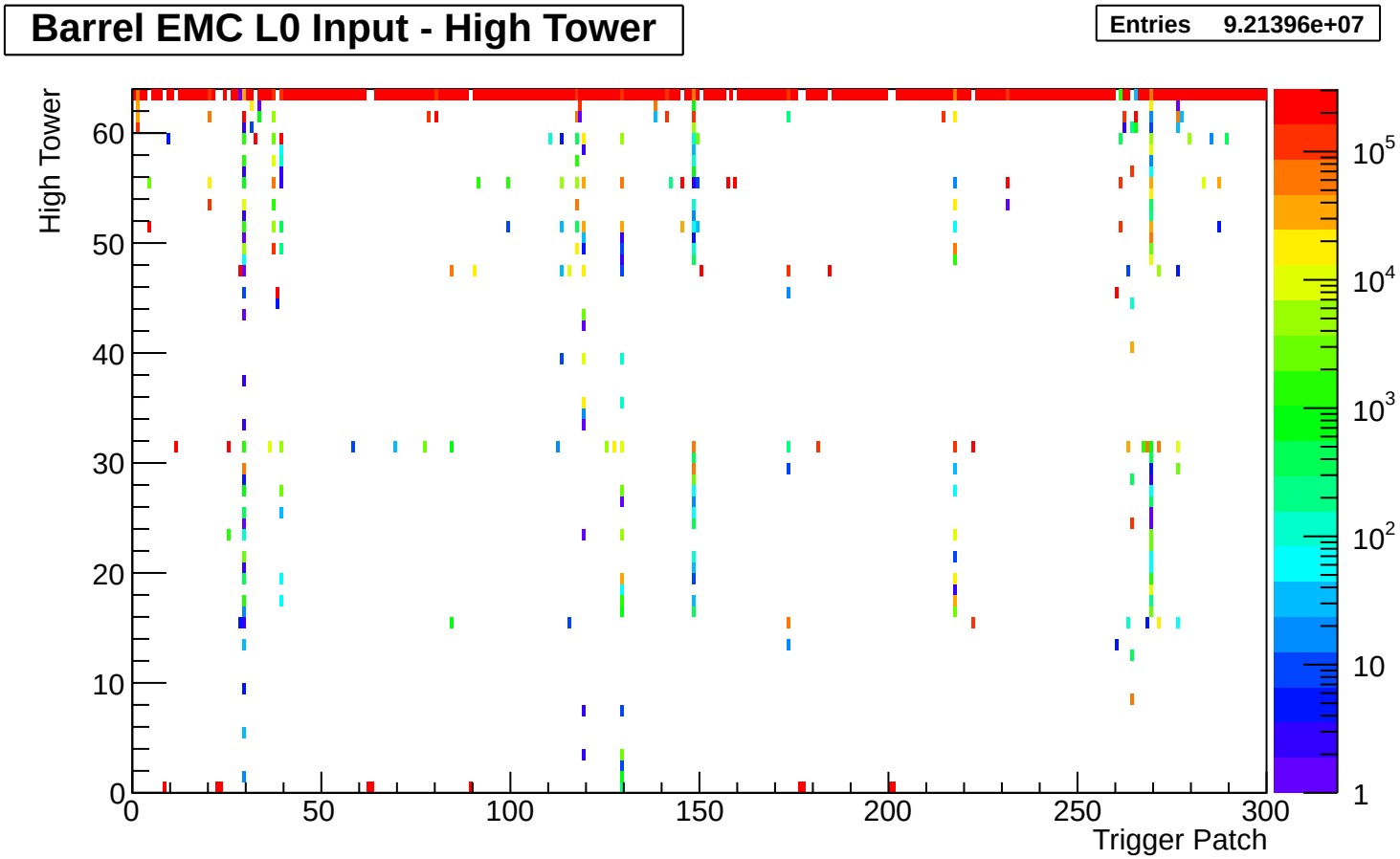
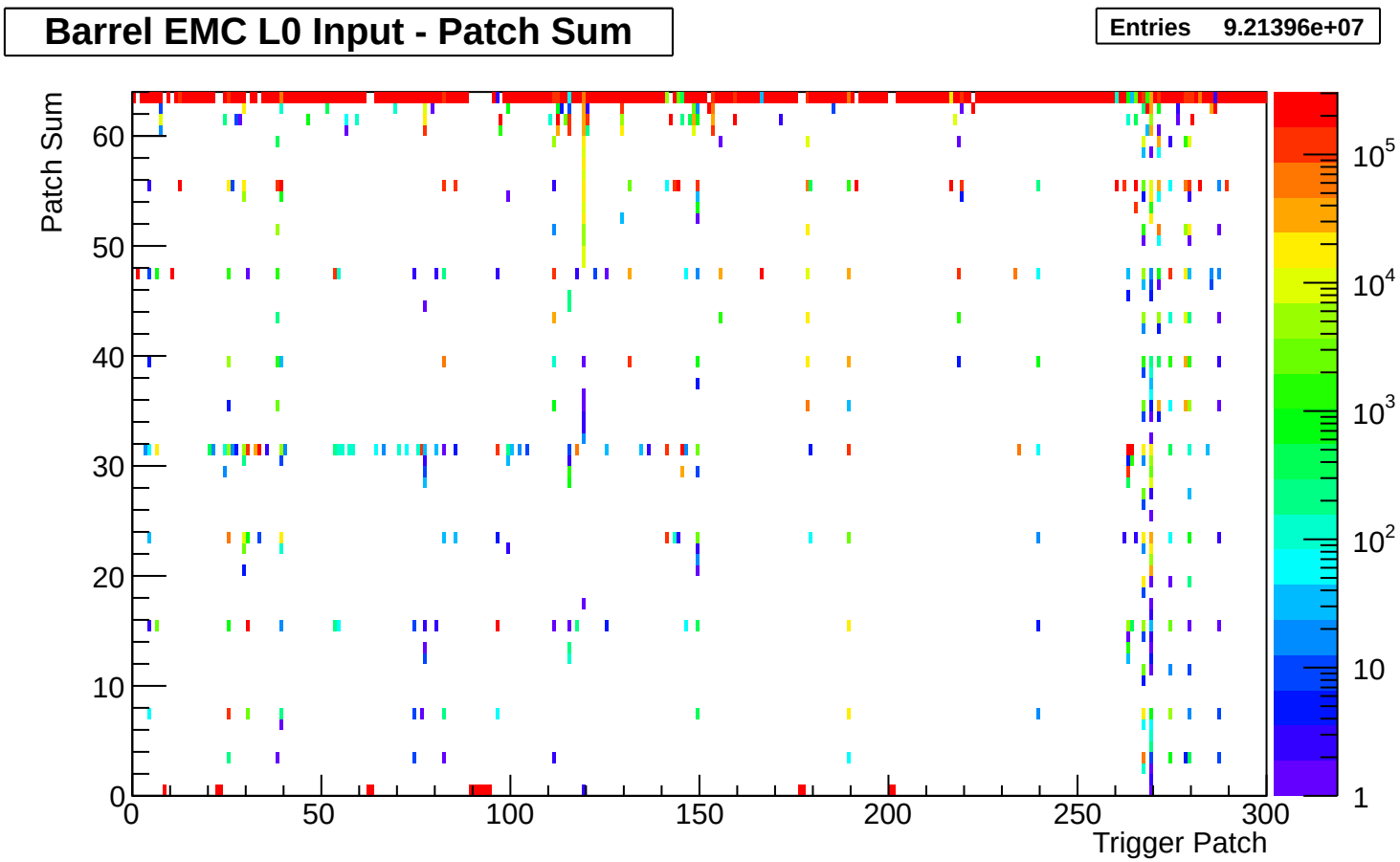
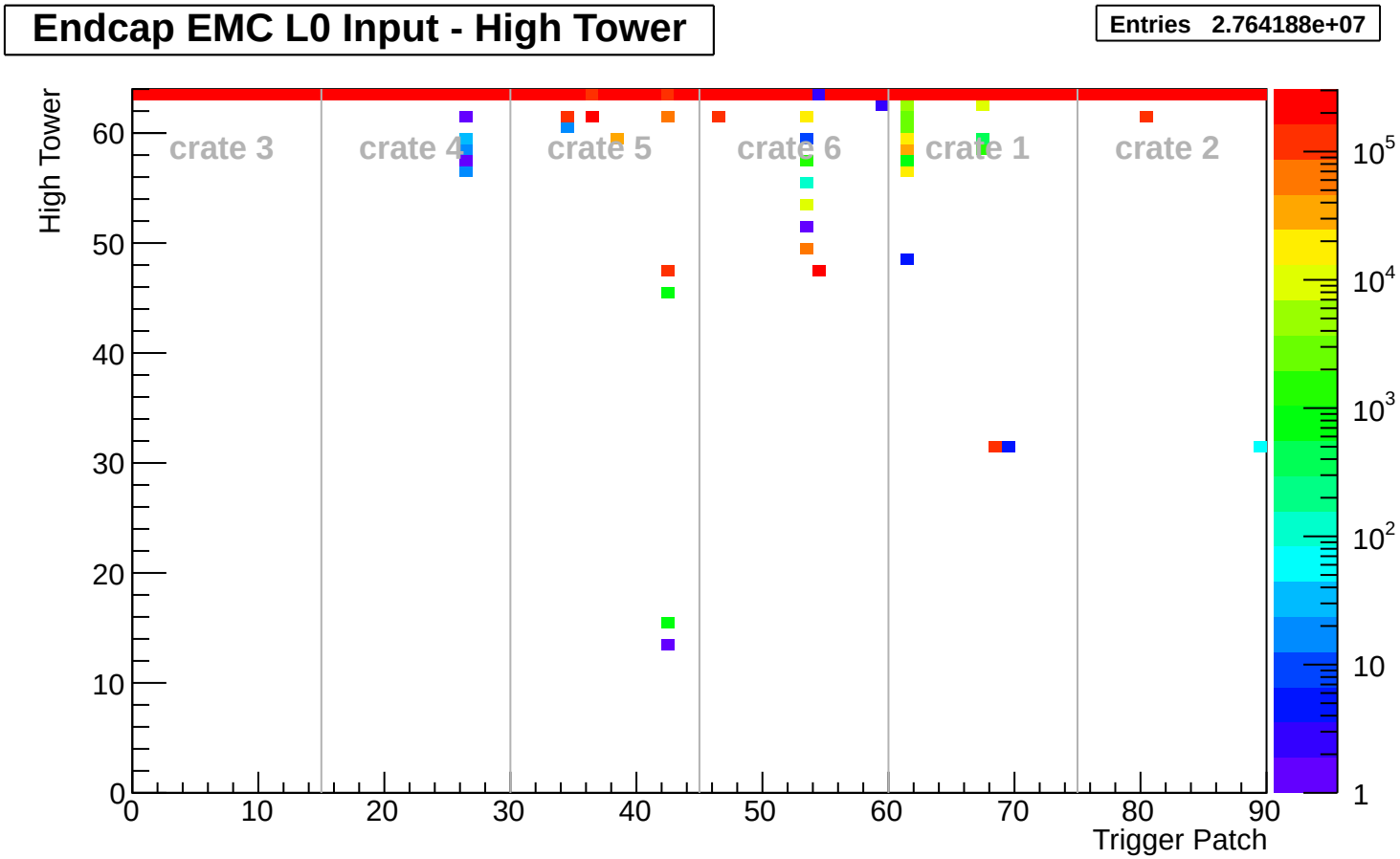
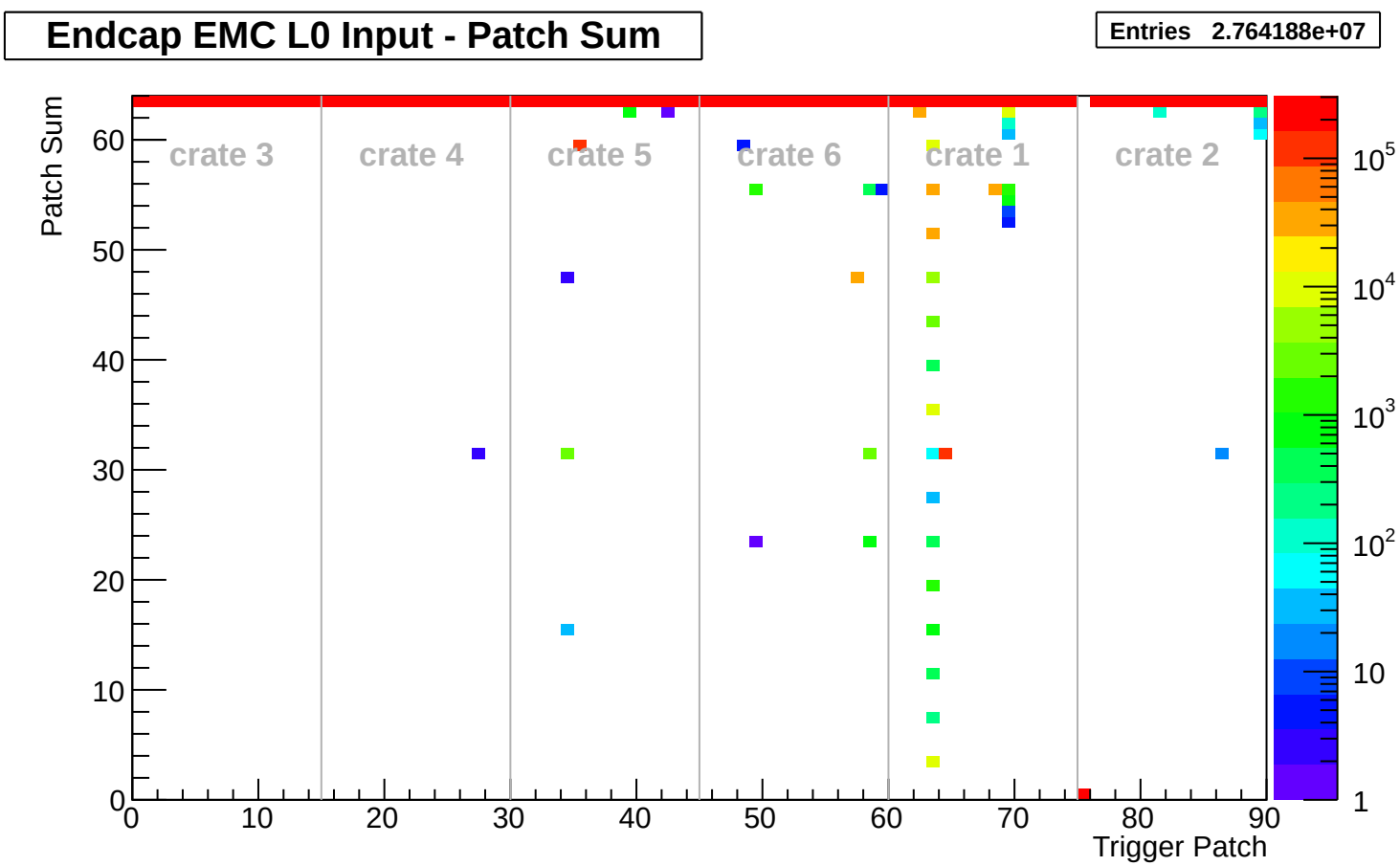
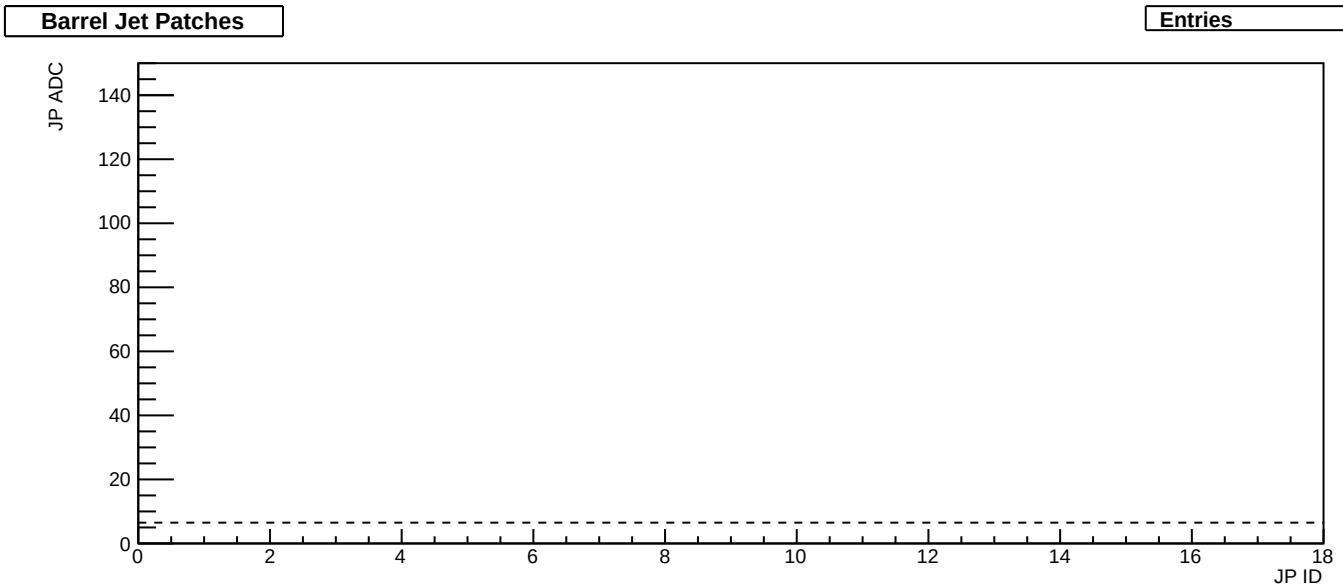
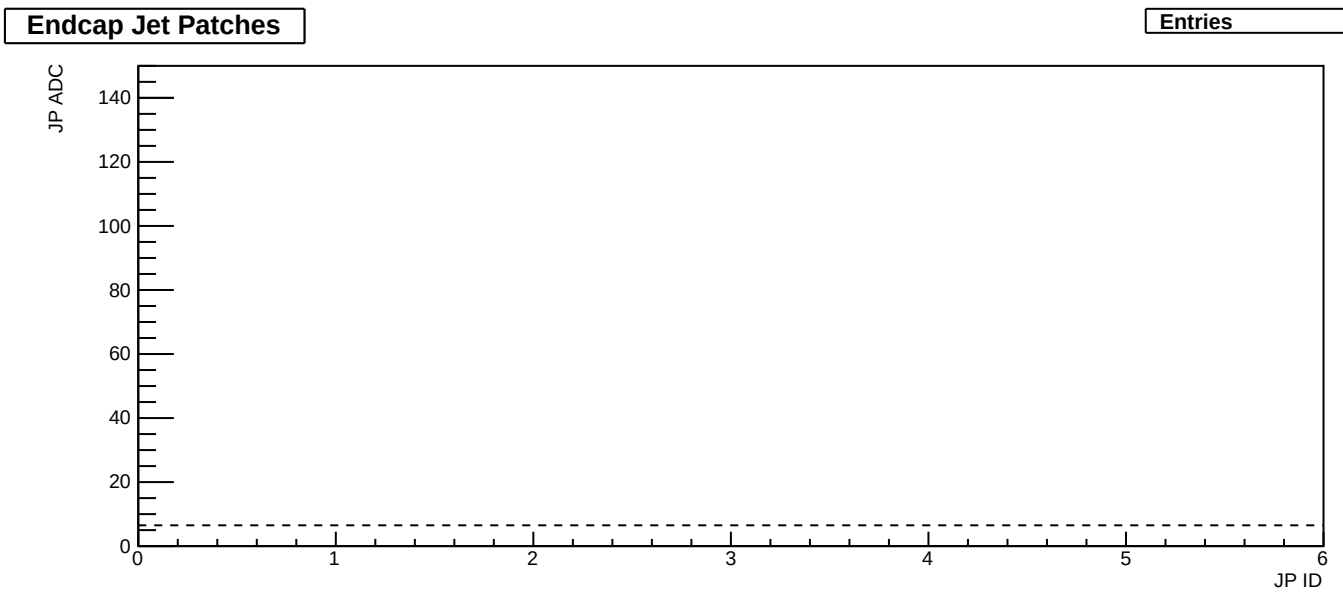

Barrel EMC L0 Input - High Tower
Entries 9.21396e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

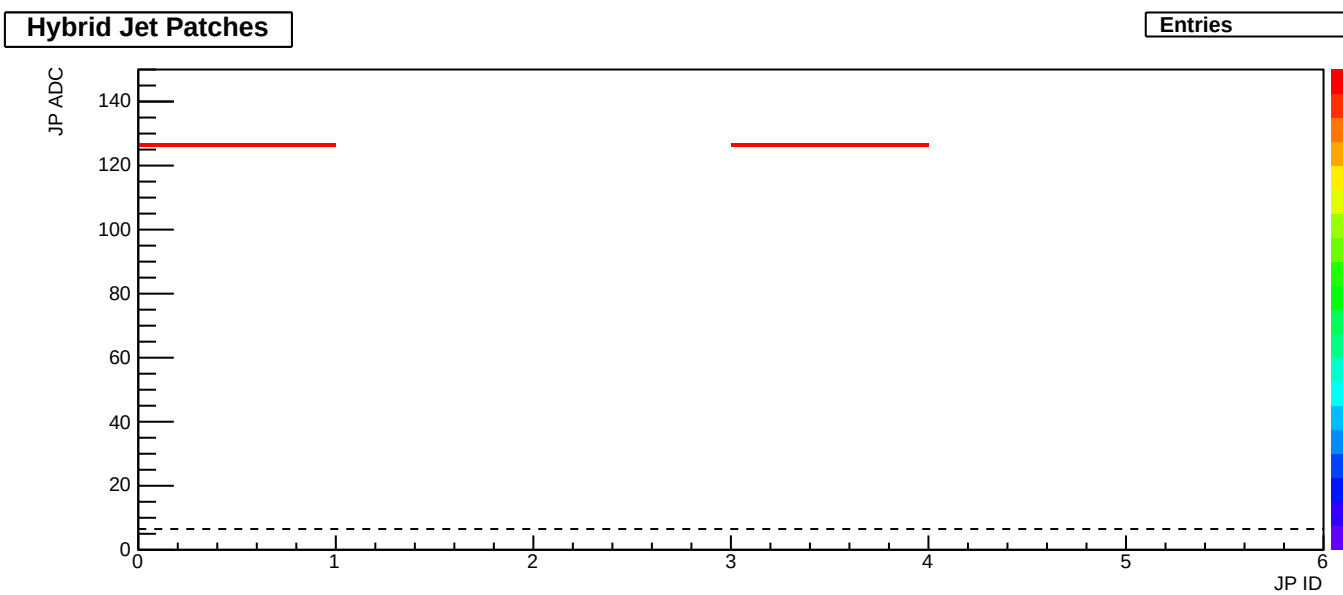

Barrel EMC L0 Input - Patch Sum
Entries 9.21396e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

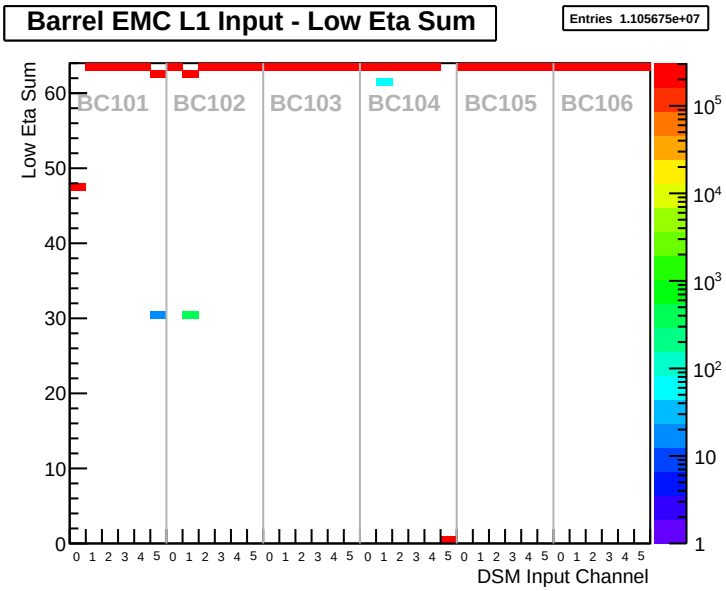

Endcap EMC L0 Input - High Tower
Entries 2.764188e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

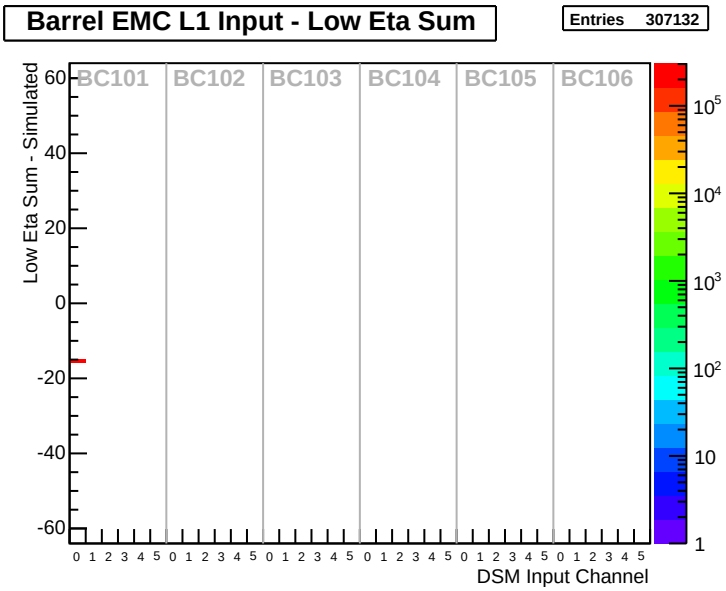

Endcap EMC L0 Input - Patch Sum
Entries 2.764188e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

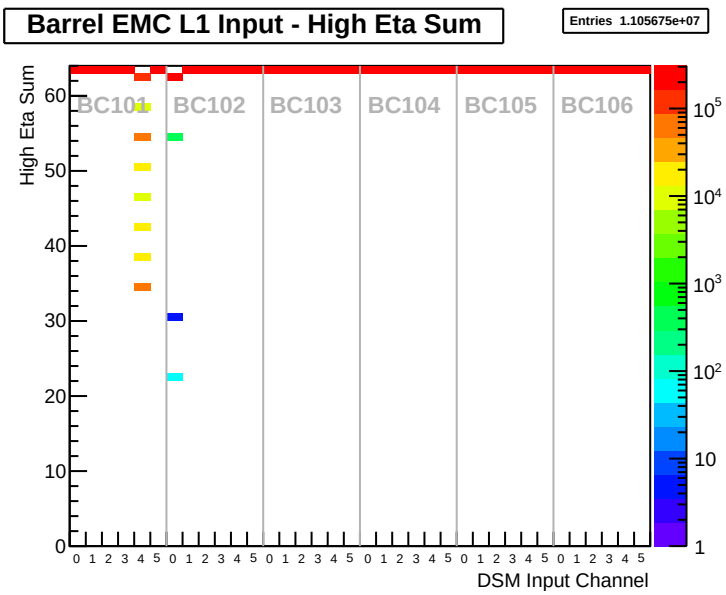

Barrel Jet Patches
Entries 5528376
JP ADC
JP ID

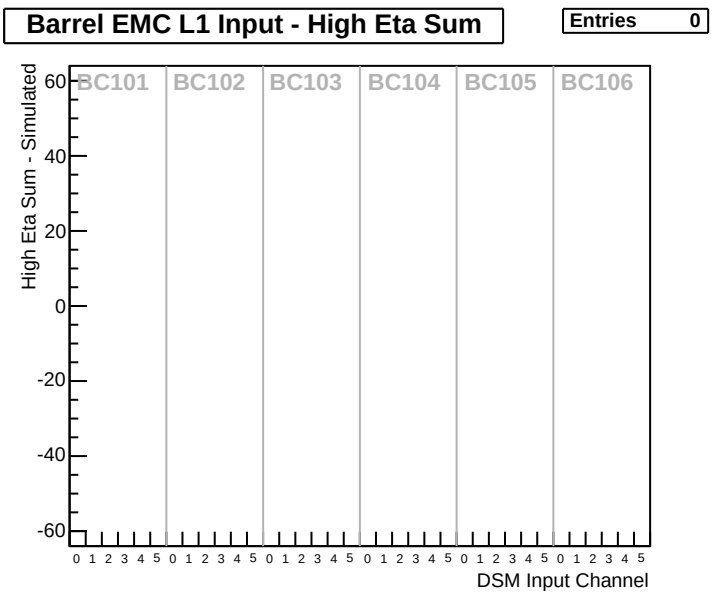

Endcap Jet Patches
Entries 1842792
JP ADC
JP ID

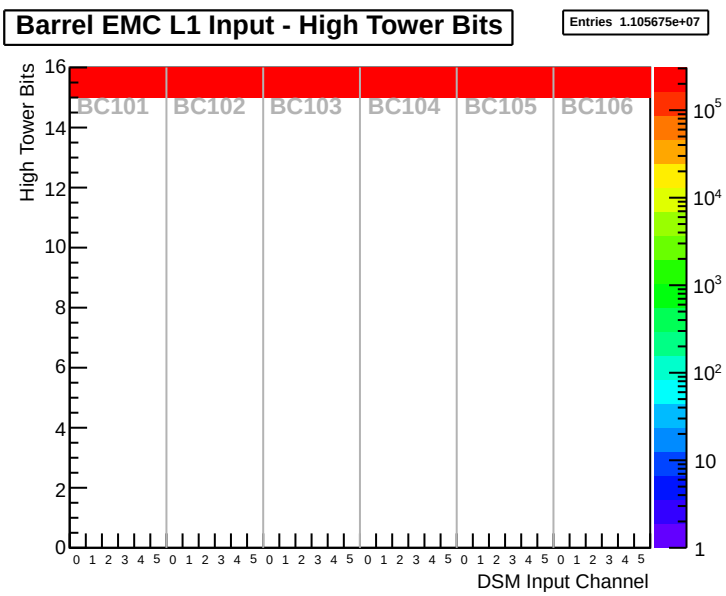

Hybrid Jet Patches
Entries 614264
JP ADC
JP ID

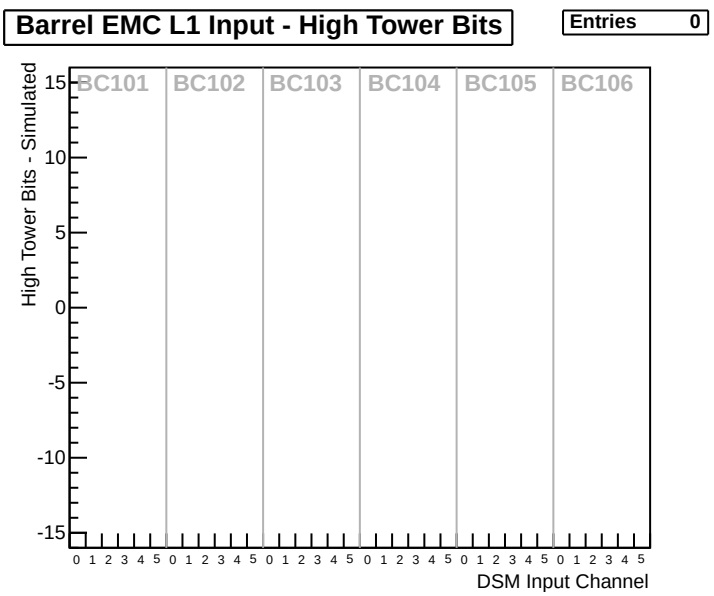

Barrel EMC L1 Input - Low Eta Sum
Entries 1.105675e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

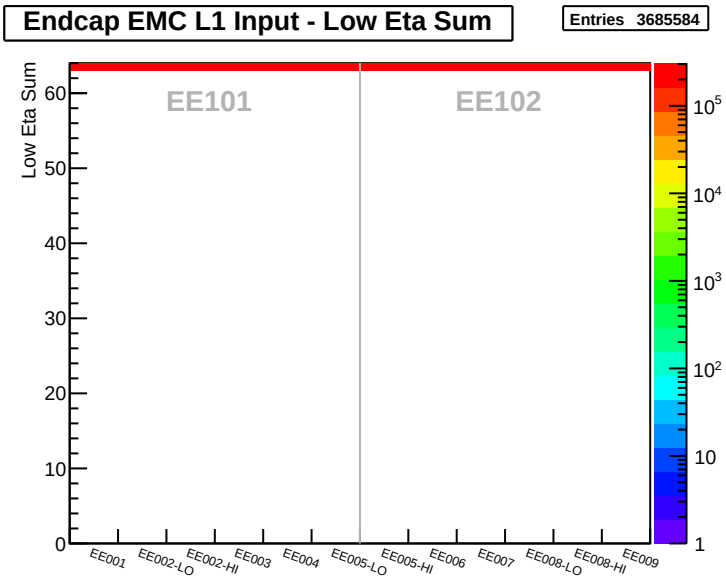

Barrel EMC L1 Input - Low Eta Sum
Entries 307132
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

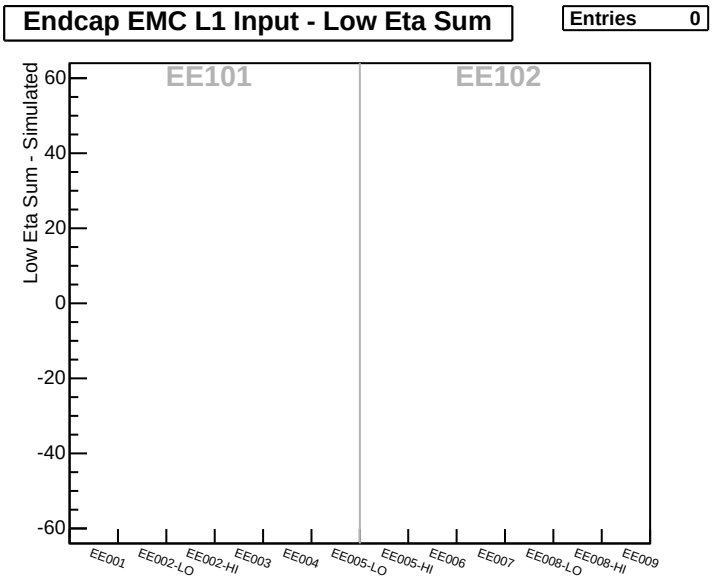

Barrel EMC L1 Input - High Eta Sum
Entries 1.105675e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

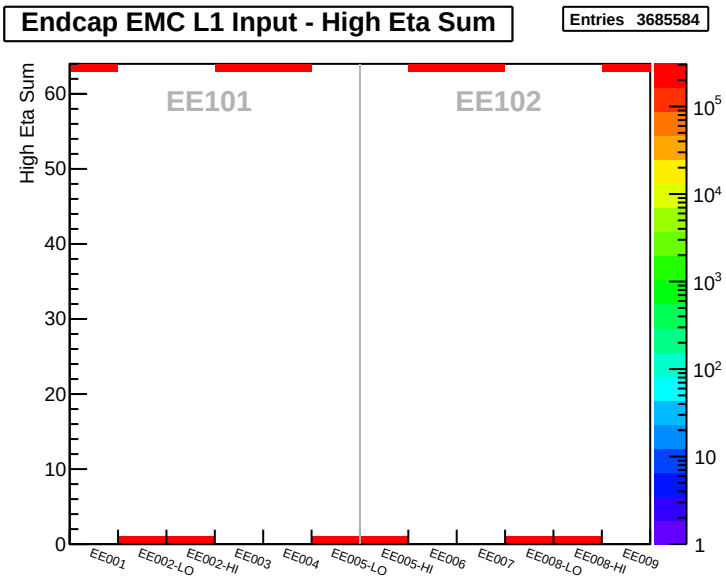

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

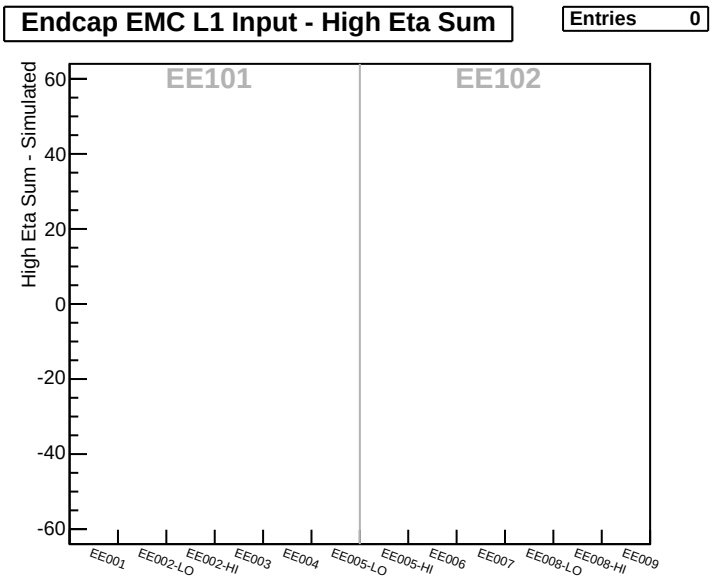

Barrel EMC L1 Input - High Tower Bits
Entries 1.105675e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

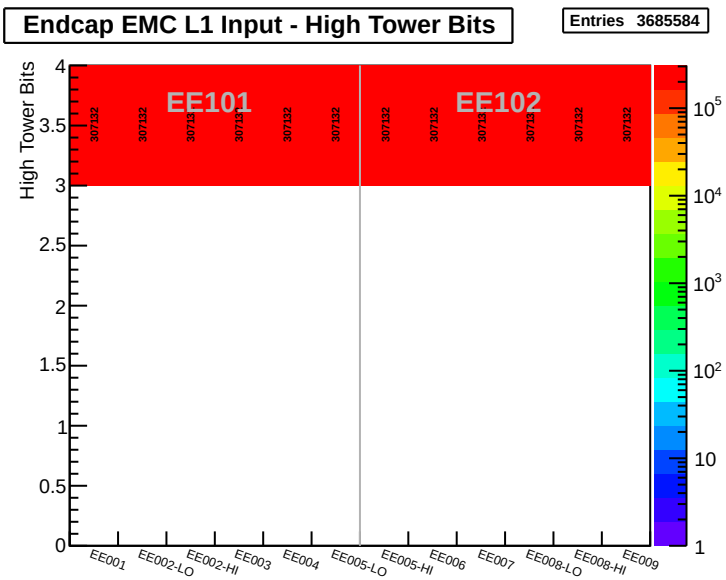

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

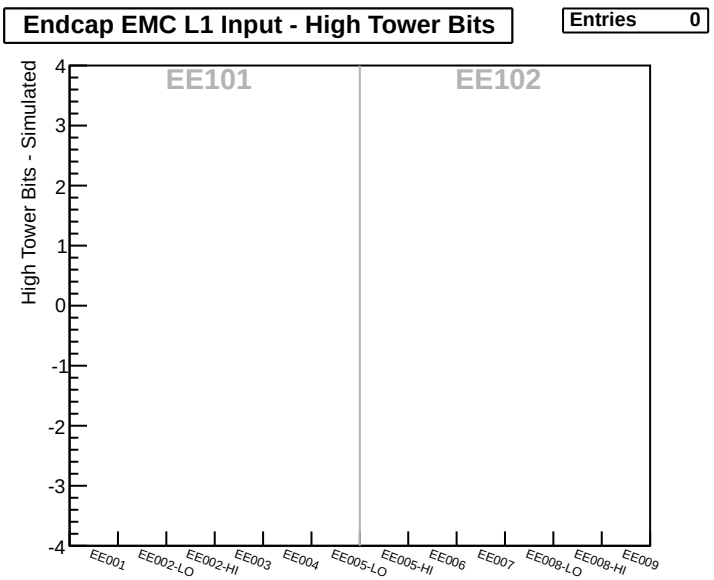

Endcap EMC L1 Input - Low Eta Sum
Entries 3685584
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

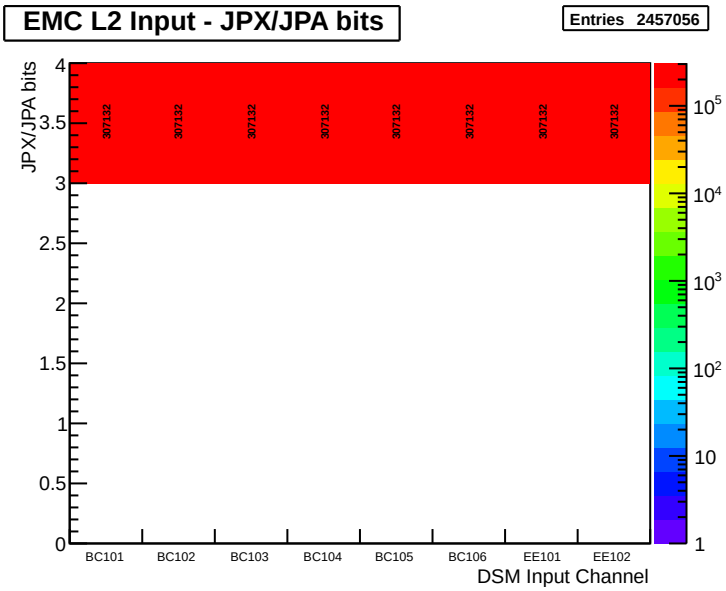

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

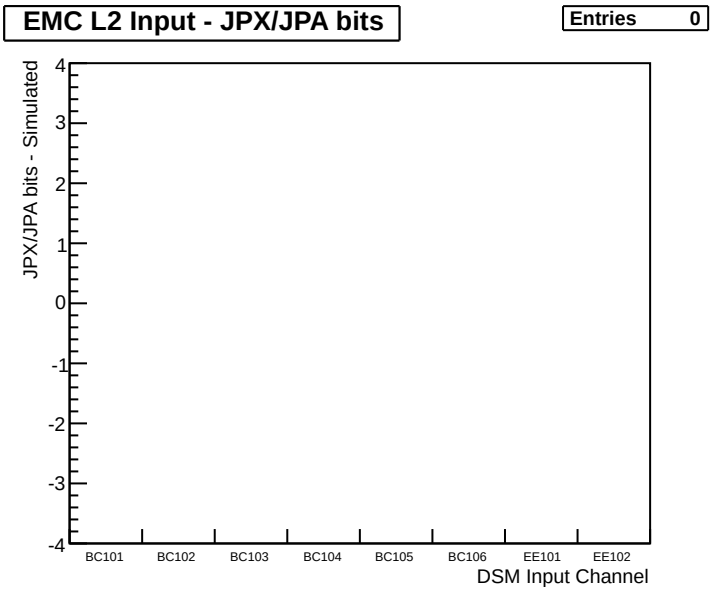

Endcap EMC L1 Input - High Eta Sum
Entries 3685584
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

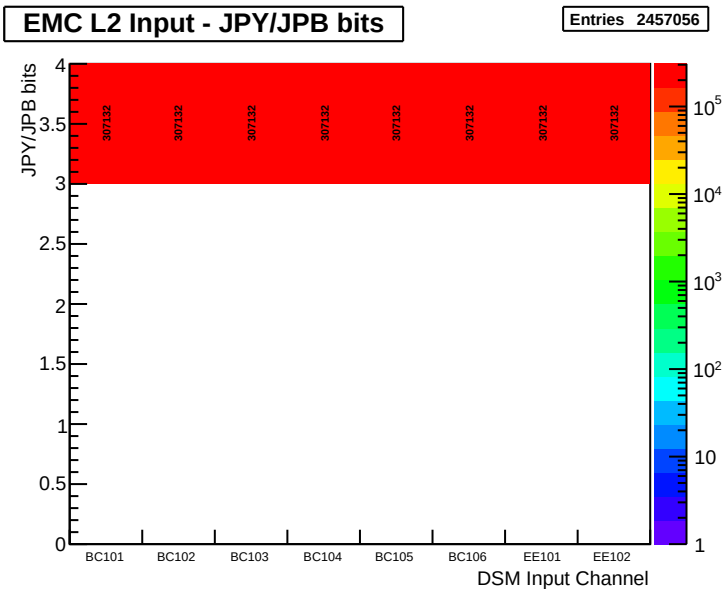

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

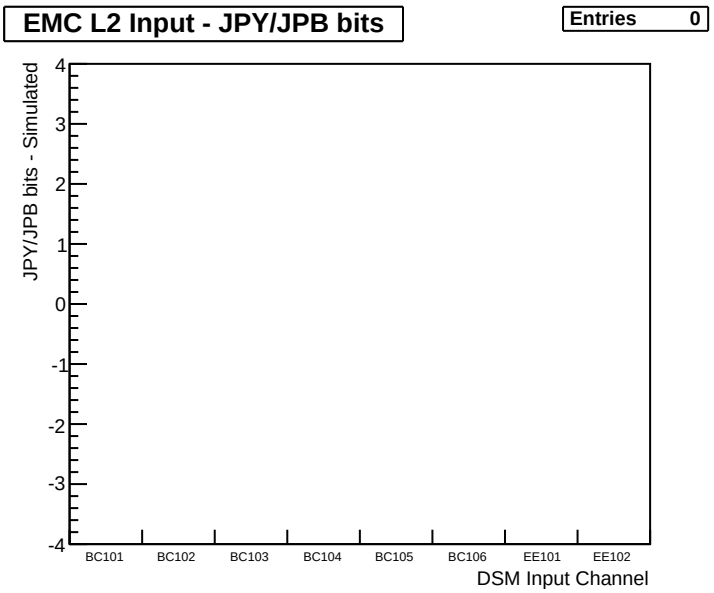

Endcap EMC L1 Input - High Tower Bits
Entries 3685584
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

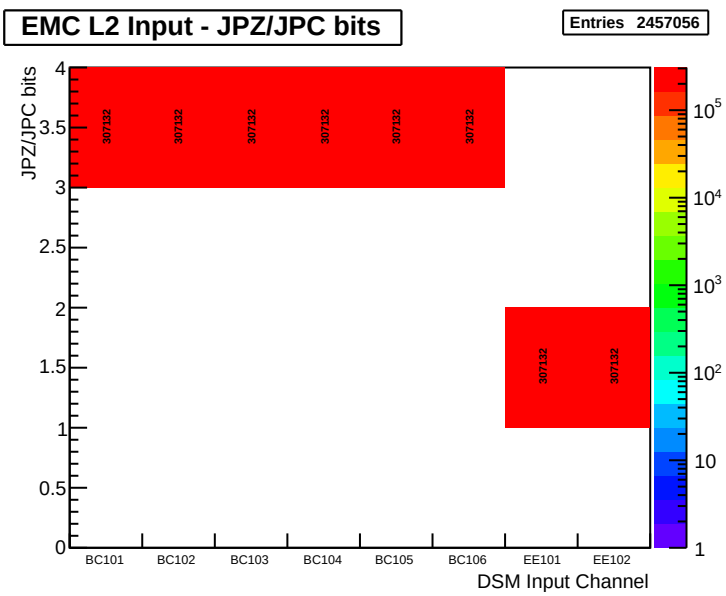

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

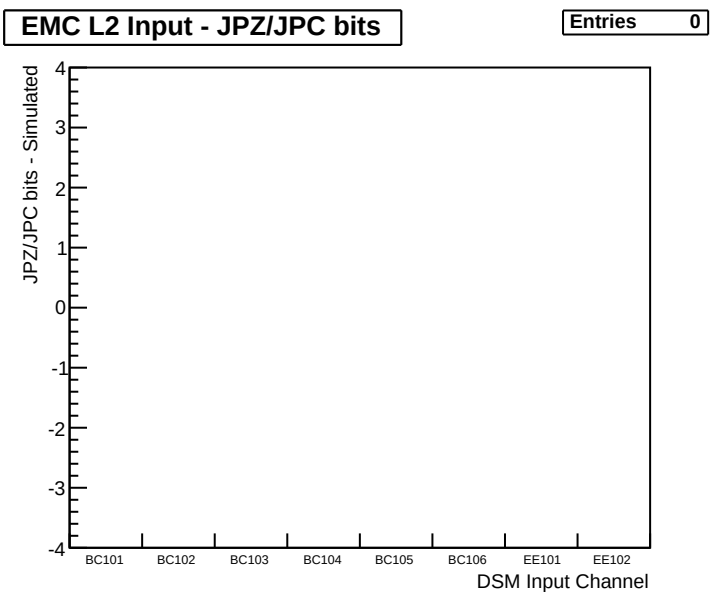

EMC L2 Input - JPX/JPA bits
Entries 2457056
JPX/JPA bits
307132
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102
DSM Input Channel

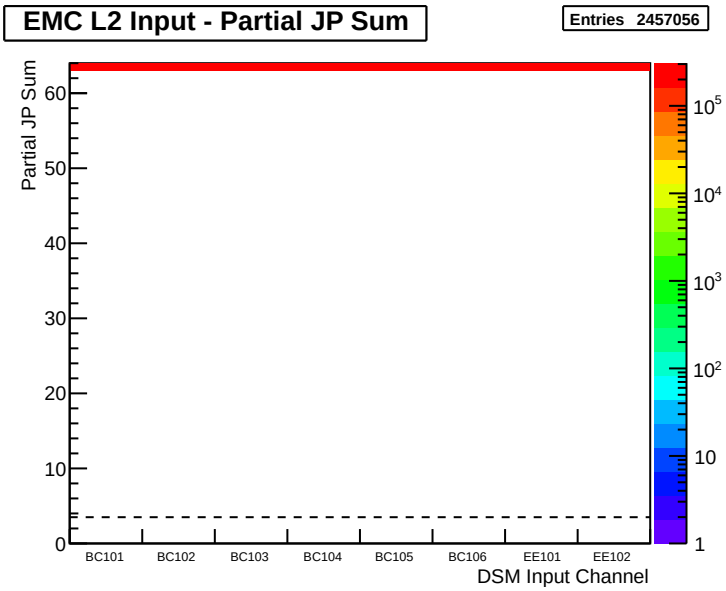

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102
DSM Input Channel

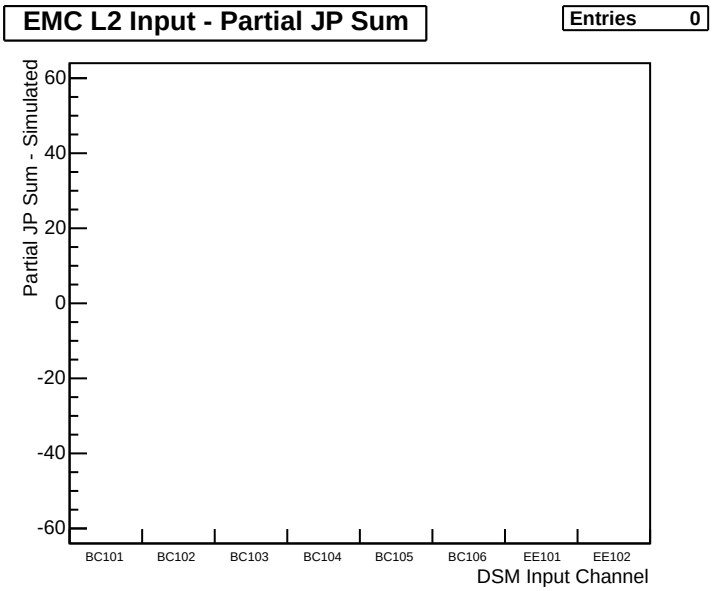

EMC L2 Input - JPY/JPB bits
Entries 2457056
JPY/JPB bits
307132
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102
DSM Input Channel

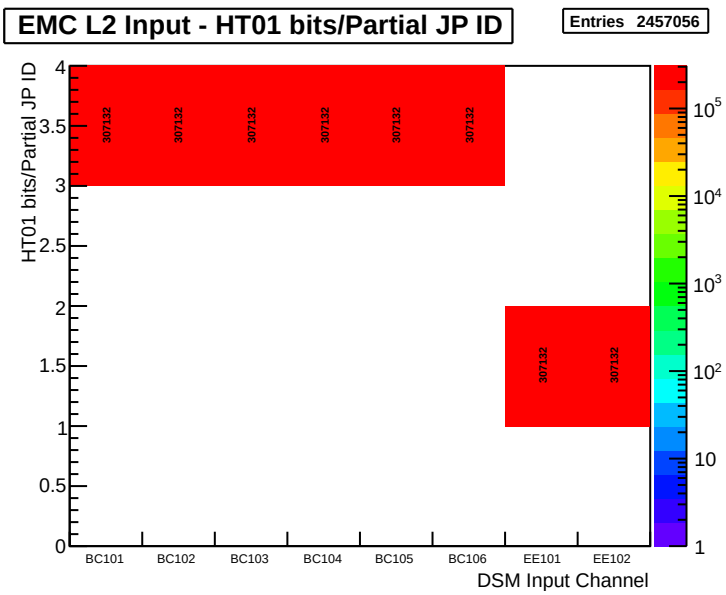

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102
DSM Input Channel

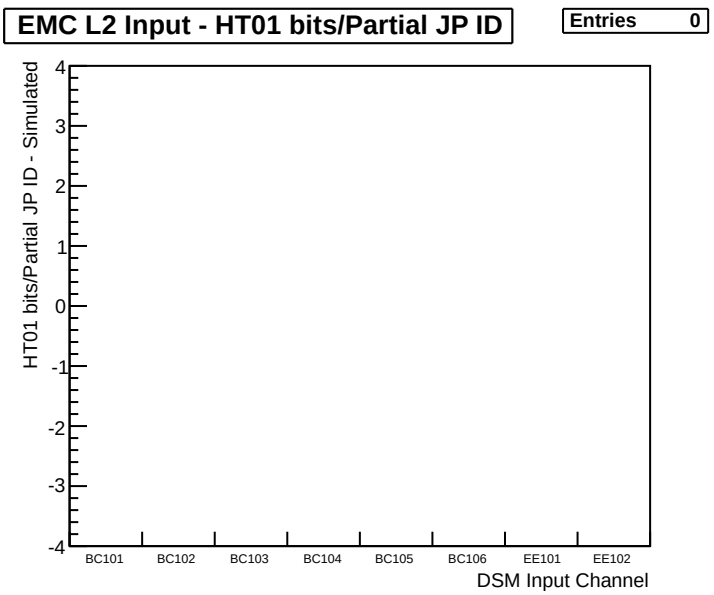

EMC L2 Input - JPZ/JPC bits
Entries 2457056
JPZ/JPC bits
307132
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102
DSM Input Channel

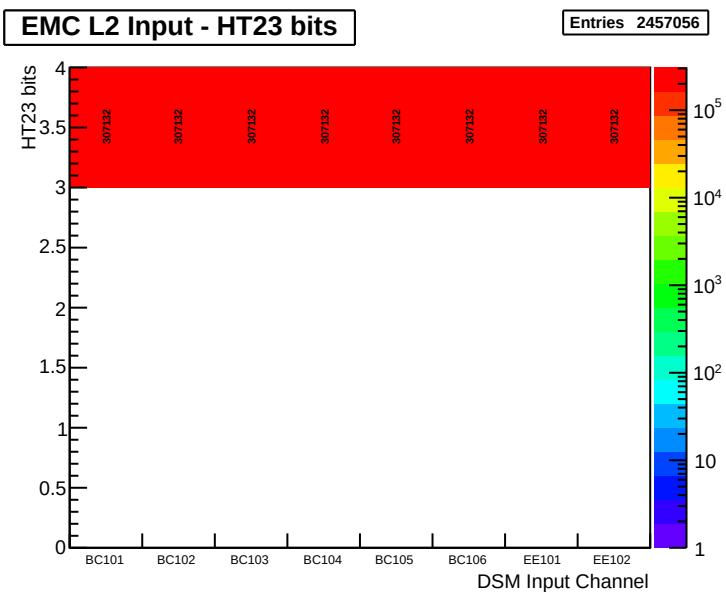

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102
DSM Input Channel

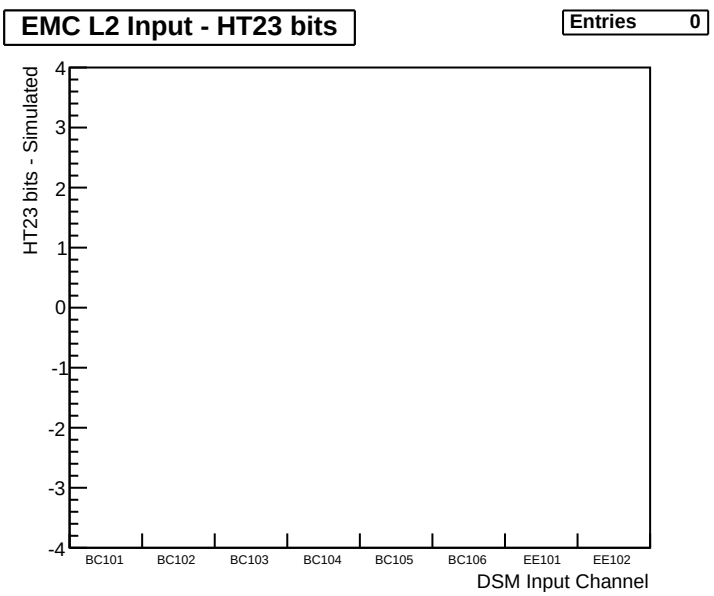

EMC L2 Input - Partial JP Sum
Entries 2457056
Partial JP Sum
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

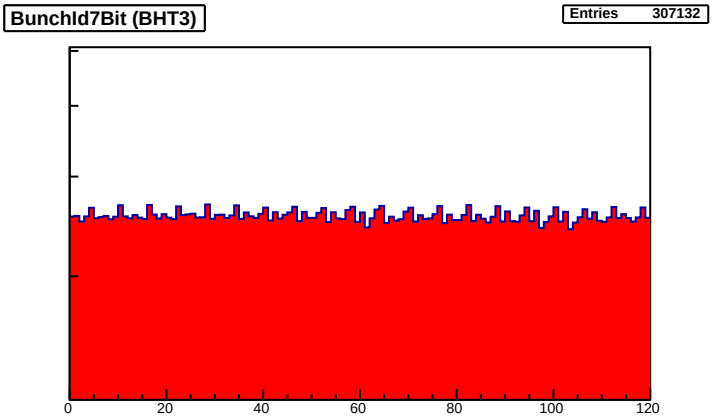

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

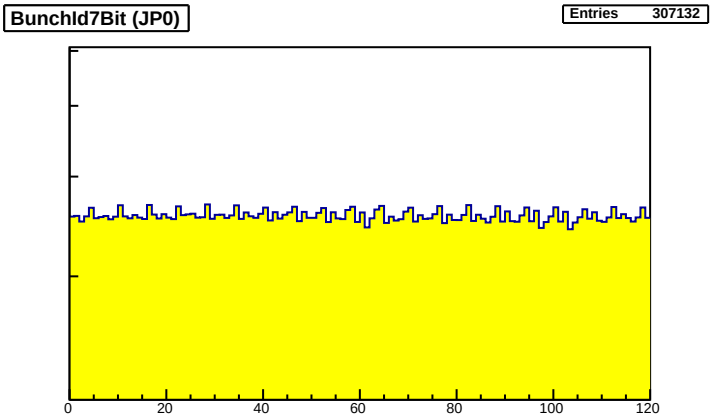

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2457056
HT01 bits/Partial JP ID
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

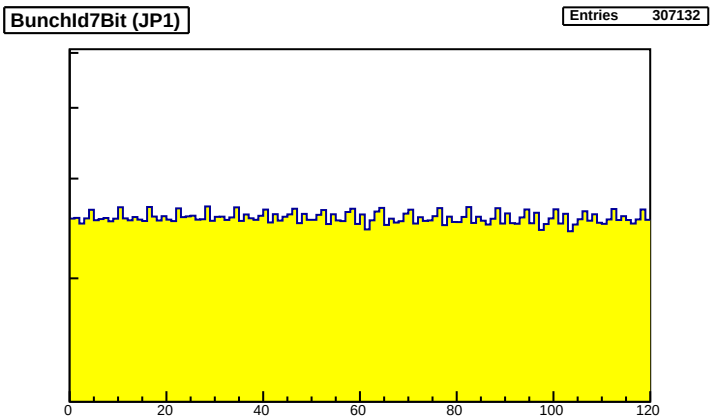

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

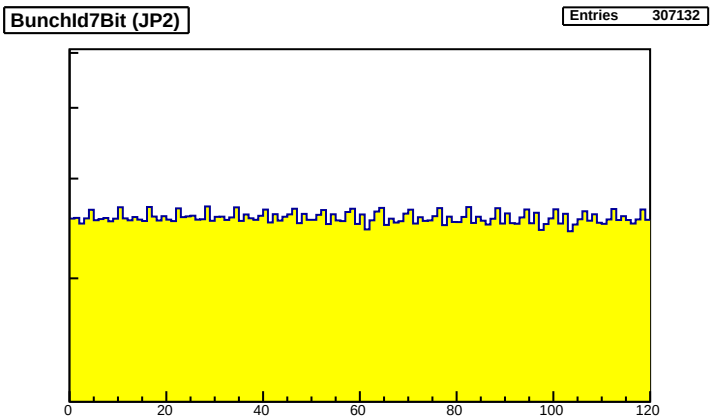

EMC L2 Input - HT23 bits
Entries 2457056
HT23 bits
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

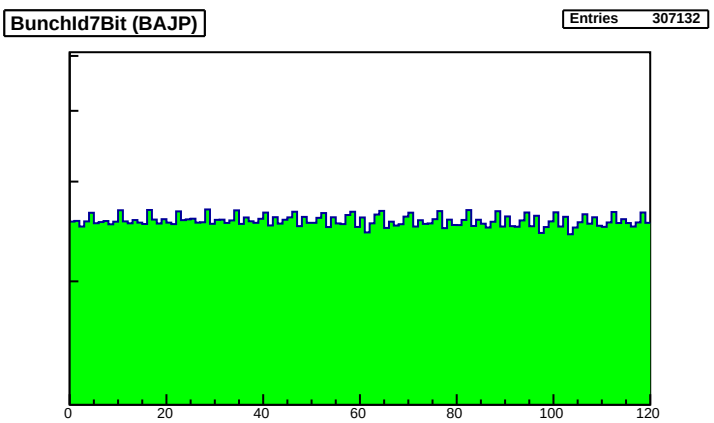

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101
BC102
BC103
BC104
BC105
BC106
EE101
EE102

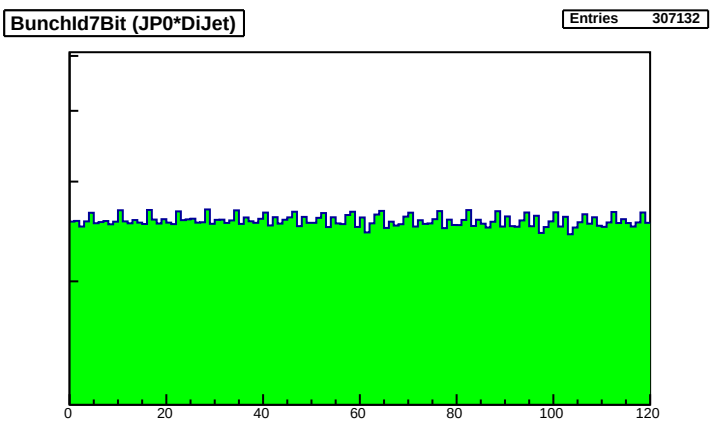

BunchId7Bit (BHT3)
Entries 307132
0 20 40 60 80 100 120

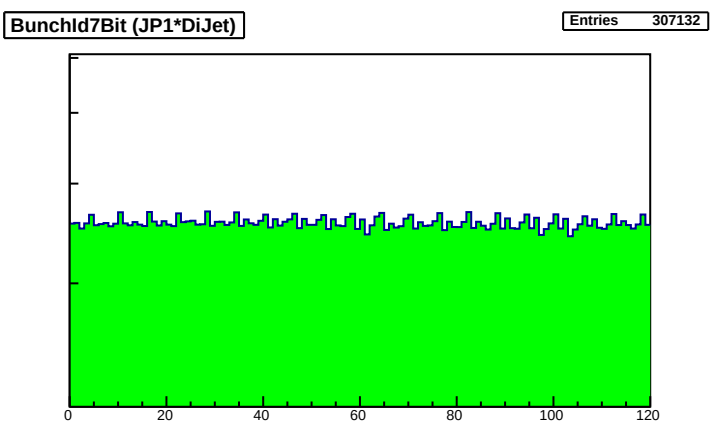

BunchId7Bit (JP0)
Entries 307132
0 20 40 60 80 100 120

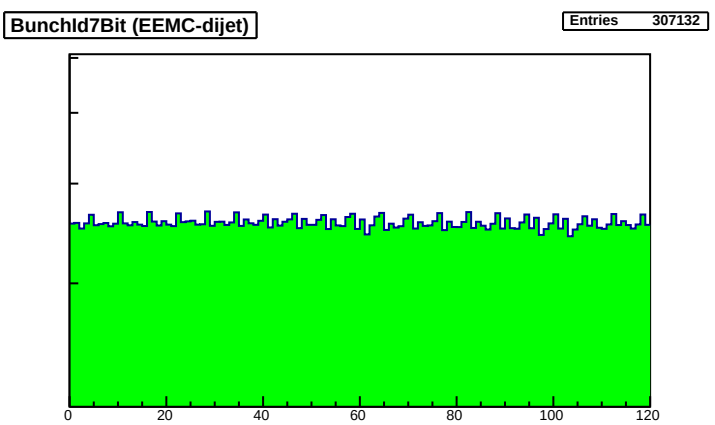

BunchId7Bit (JP1)
Entries 307132
0 20 40 60 80 100 120

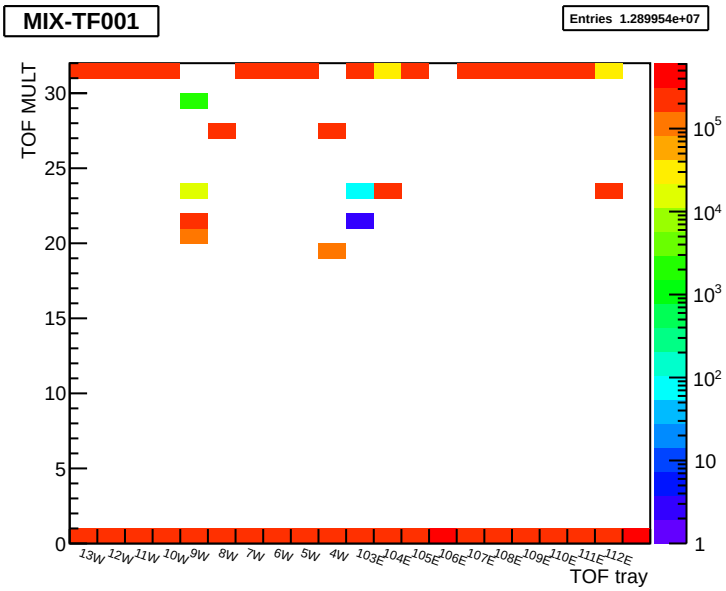

BunchId7Bit (JP2)
Entries 307132
0 20 40 60 80 100 120

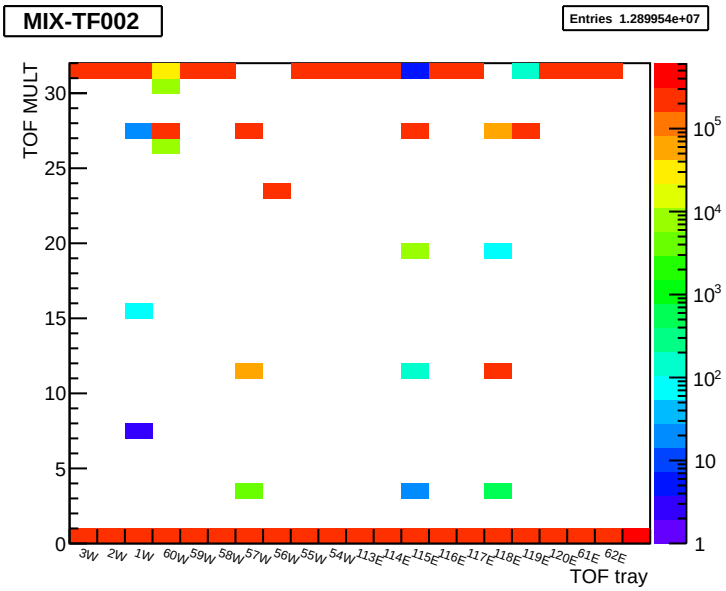

BunchId7Bit (BAJP)
Entries 307132
0 20 40 60 80 100 120

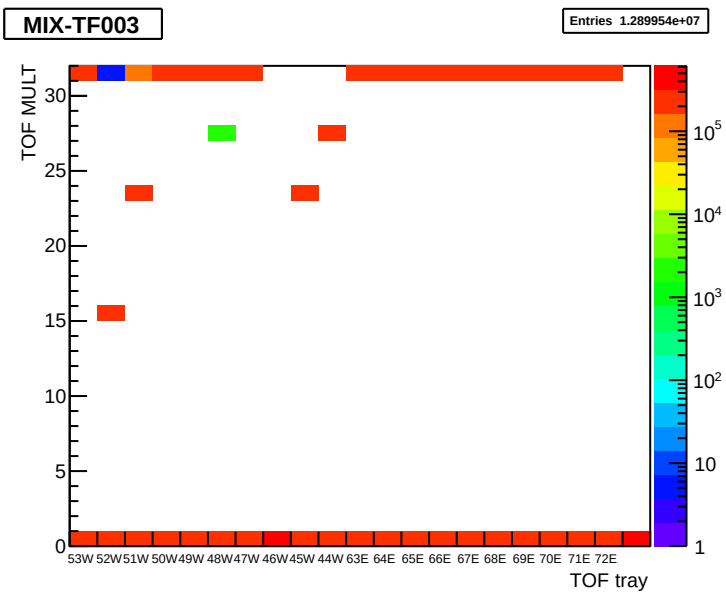

BunchId7Bit (JP0*DiJet)
Entries 307132
0 20 40 60 80 100 120

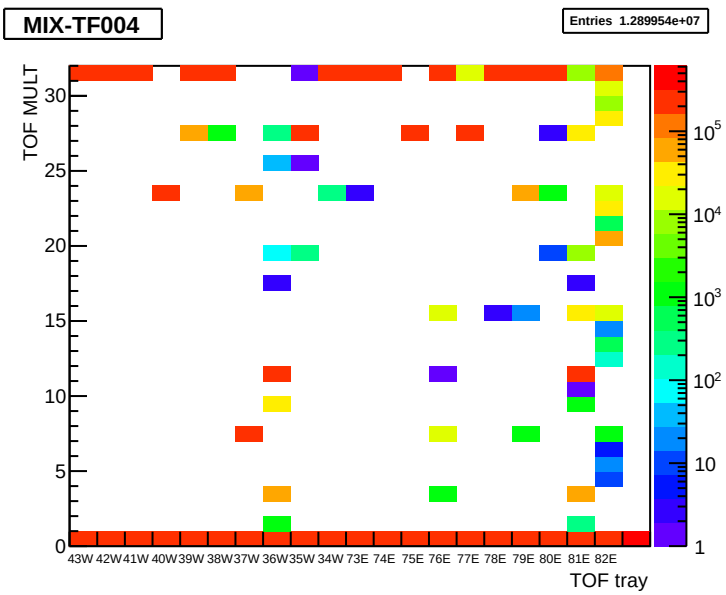

BunchId7Bit (JP1*DiJet)
Entries 307132
0 20 40 60 80 100 120

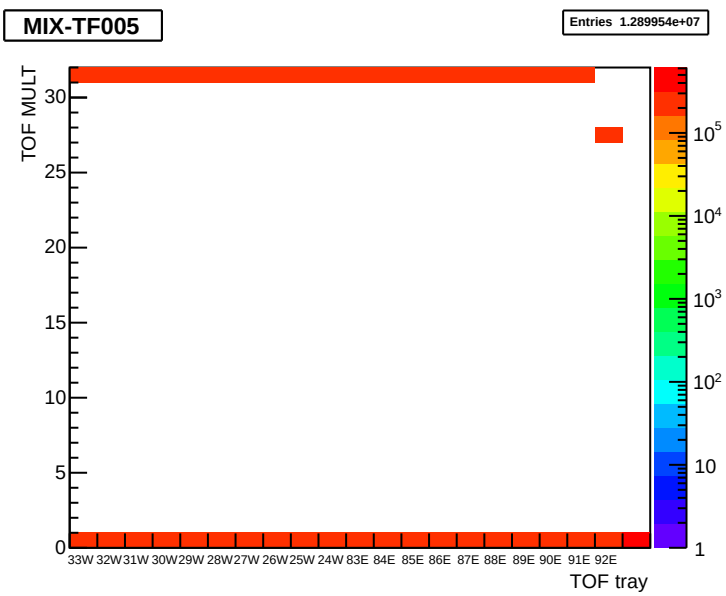

BunchId7Bit (EEMC-dijet)
Entries 307132
0 20 40 60 80 100 120

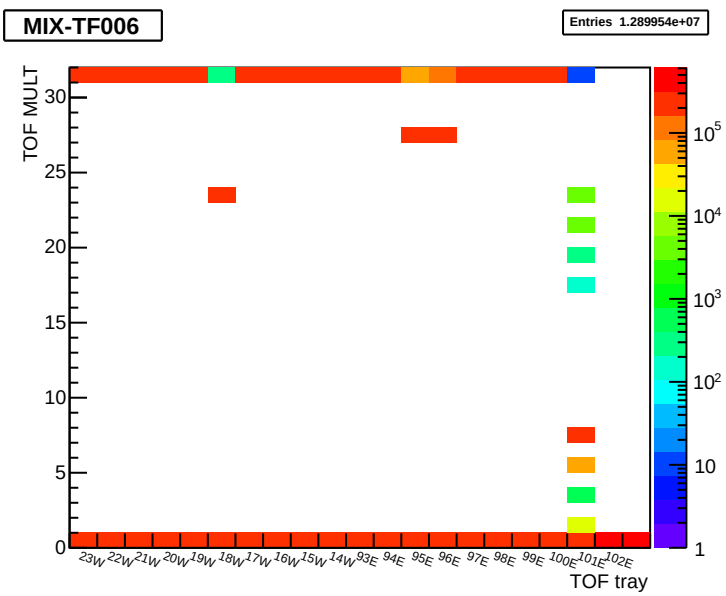

MIX-TF001
Entries 1.289954e+07
TOF MULT
TOF tray
13W 12W 11W 10W 9W 8W 7W 6W 5W 4W 103E 104E 105E 106E 107E 108E 109E 110E 111E 112E

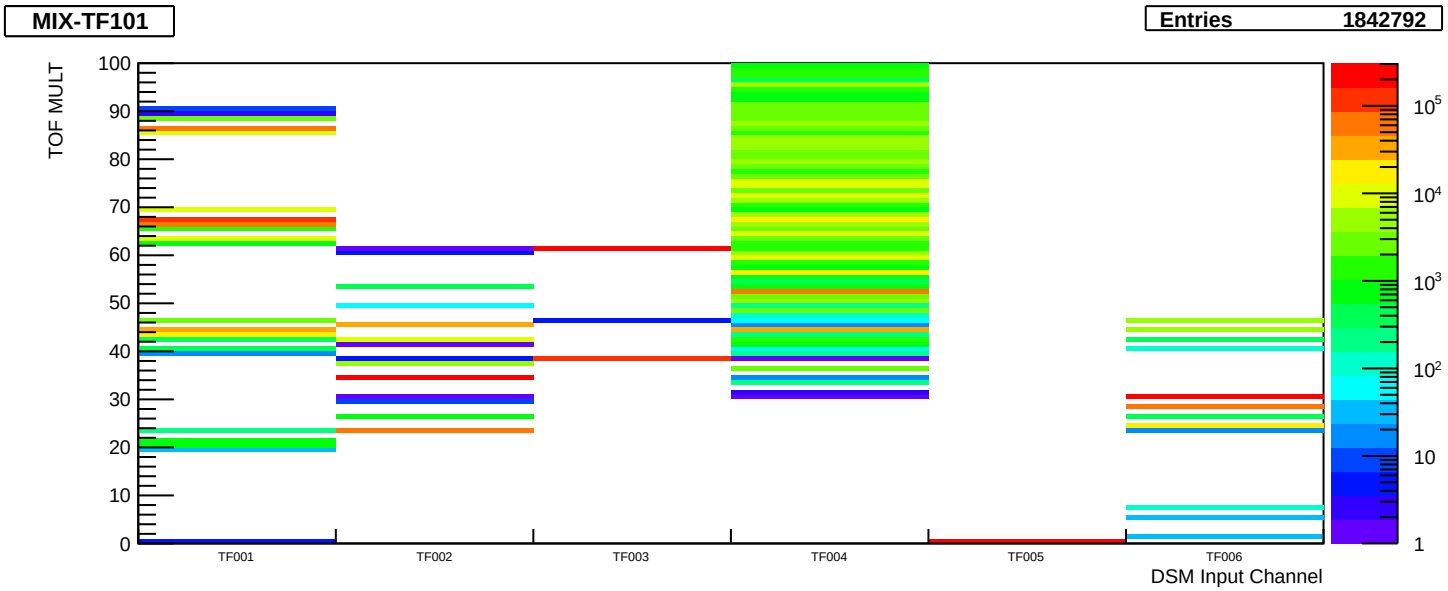

MIX-TF002
Entries 1.289954e+07
TOF MULT
TOF tray
3W 2W 1W 60W 59W 58W 57W 56W 55W 54W 113E 114E 115E 116E 117E 118E 119E 120E 61E 62E

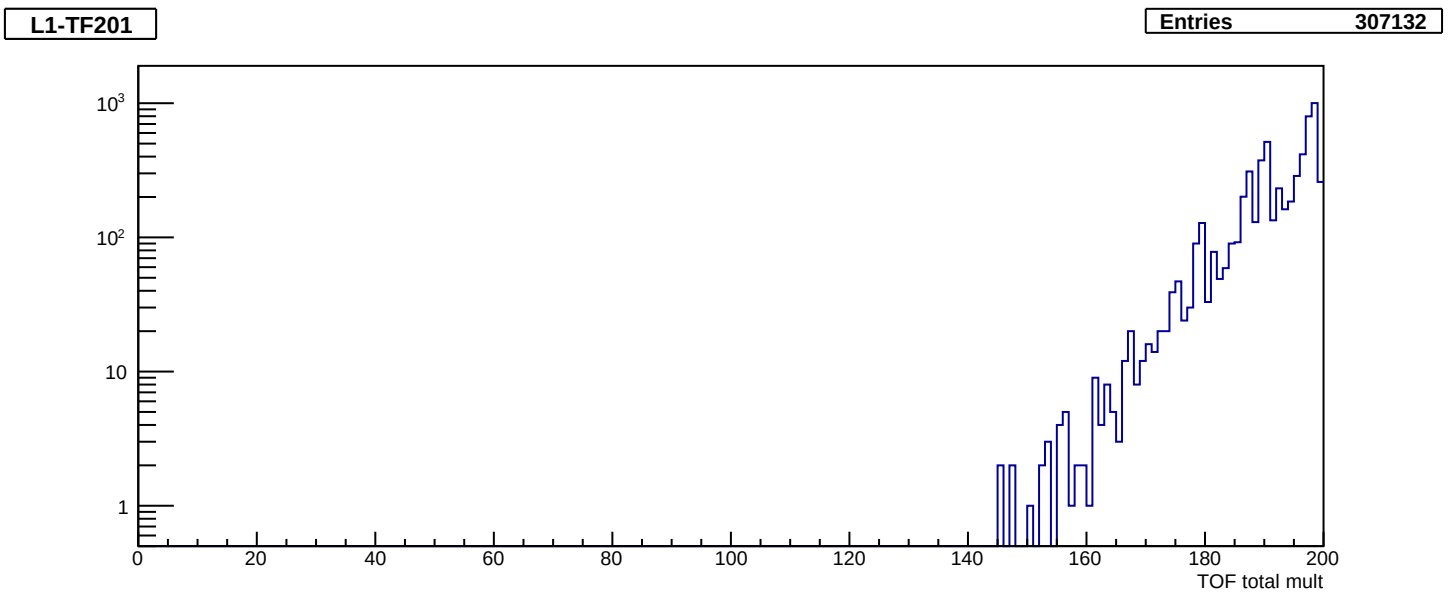

MIX-TF003
Entries 1.289954e+07
TOF MULT
TOF tray
53W 52W 51W 50W 49W 48W 47W 46W 45W 44W 63E 64E 65E 66E 67E 68E 69E 70E 71E 72E

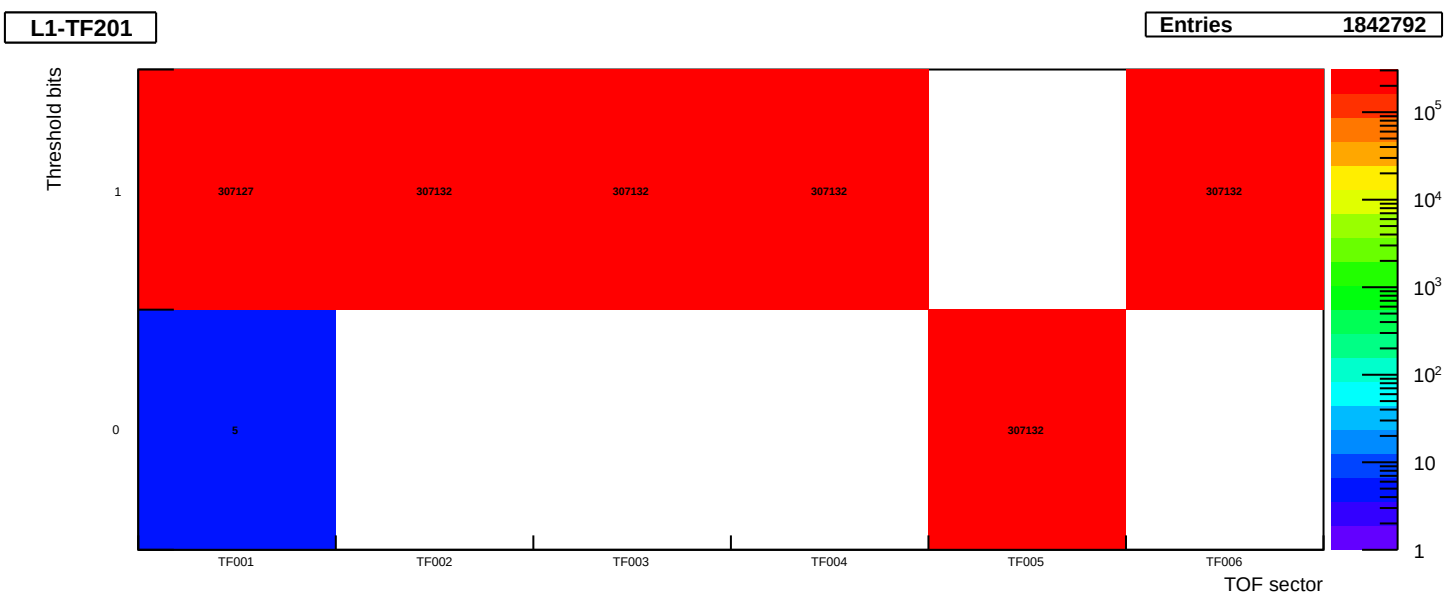

MIX-TF004
Entries 1.289954e+07
TOF MULT
TOF tray
43W 42W 41W 40W 39W 38W 37W 36W 35W 34W 73E 74E 75E 76E 77E 78E 79E 80E 81E 82E

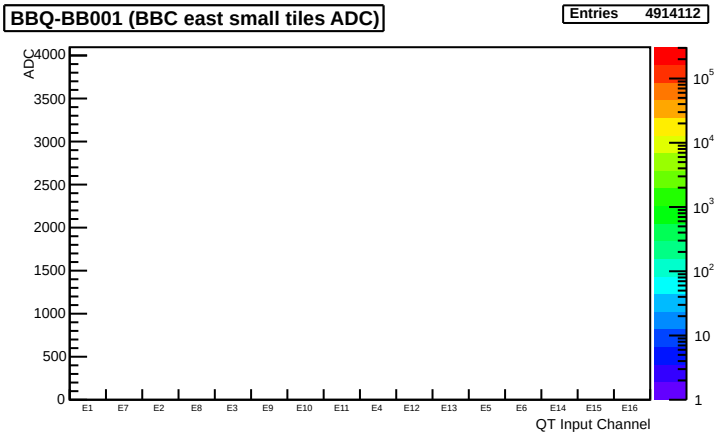

MIX-TF005
Entries 1.289954e+07
TOF MULT
TOF tray
33W 32W 31W 30W 29W 28W 27W 26W 25W 24W 83E 84E 85E 86E 87E 88E 89E 90E 91E 92E

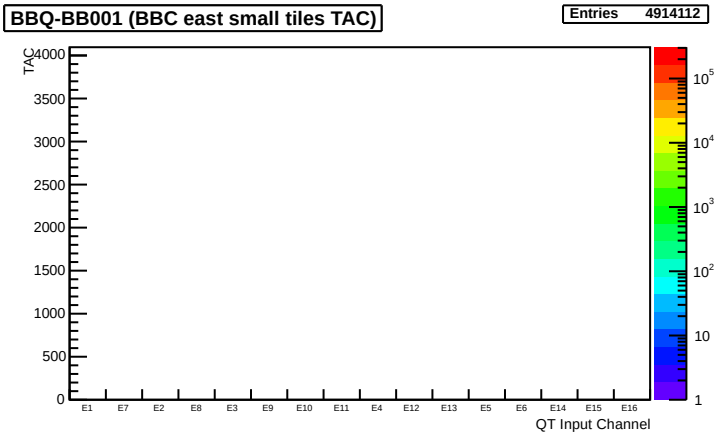

MIX-TF006
Entries 1.289954e+07
TOF MULT
TOF tray
23W 22W 21W 20W 19W 18W 17W 16W 15W 14W 93E 94E 95E 96E 97E 98E 99E 100E 101E 102E

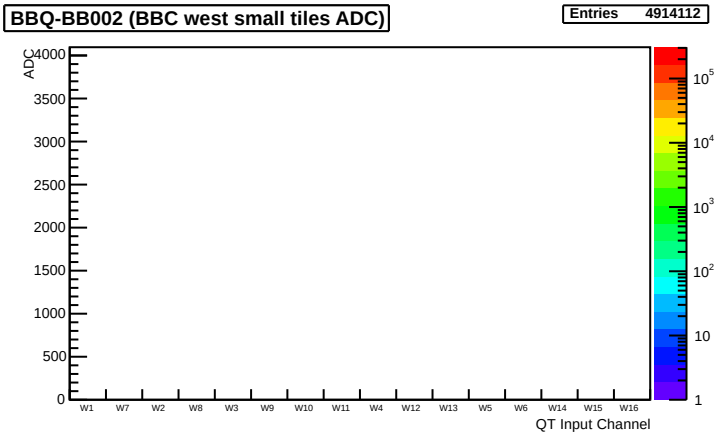

MIX-TF101
Entries 1842792
TOF MULT
DSM Input Channel
TF001
TF002
TF003
TF004
TF005
TF006

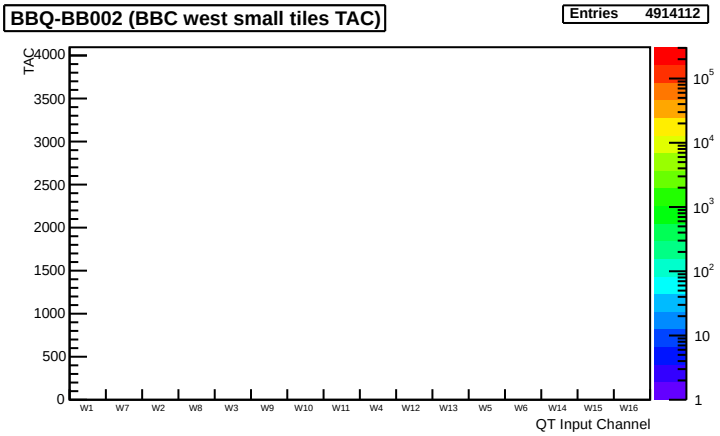

L1-TF201
Entries 307132
TOF total mult

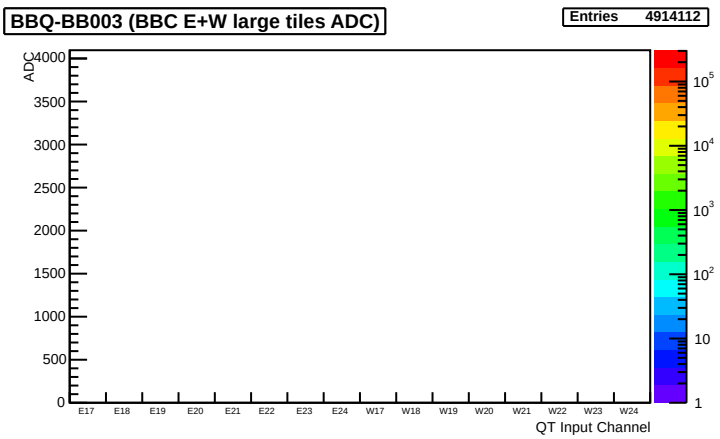

L1-TF201
Entries 1842792
Threshold bits
TOF sector
307127
307132
307132
307132
307132
5
307132
TF001
TF002
TF003
TF004
TF005
TF006

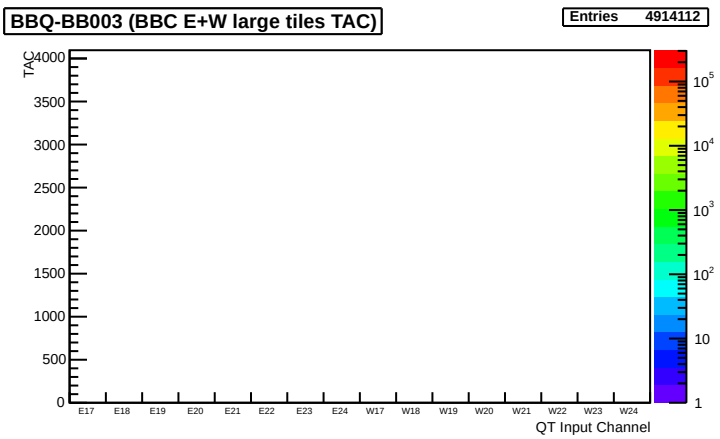

BBQ-BB001 (BBC east small tiles ADC)
Entries 4914112
ADC
QT Input Channel

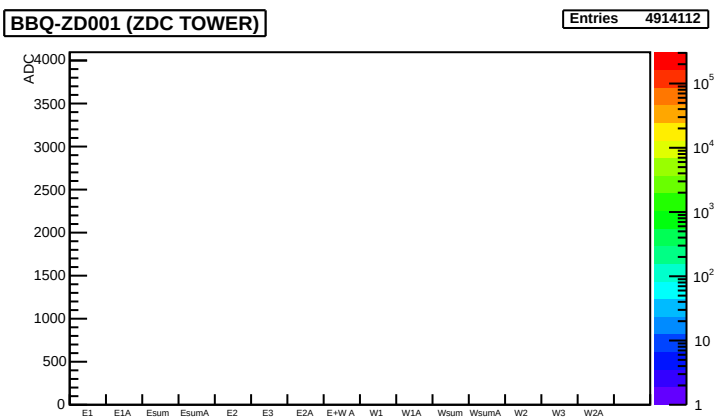

BBQ-BB001 (BBC east small tiles TAC)
Entries 4914112
TAC
QT Input Channel

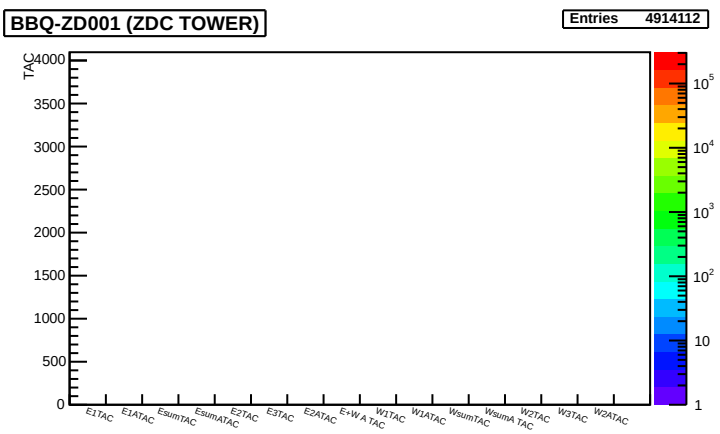

BBQ-BB002 (BBC west small tiles ADC)
Entries 4914112
ADC
QT Input Channel

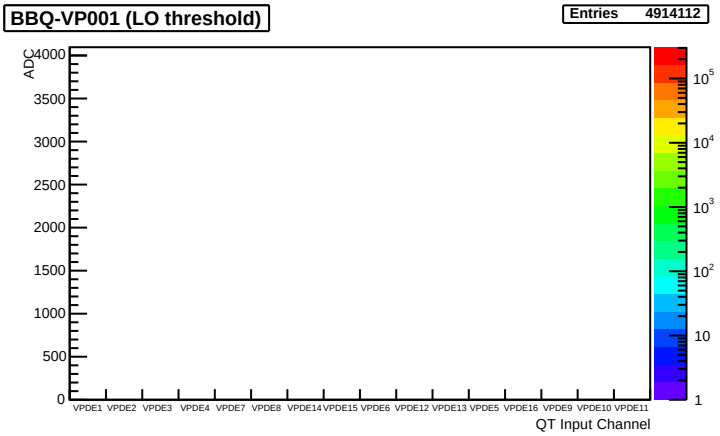

BBQ-BB002 (BBC west small tiles TAC)
Entries 4914112
TAC
QT Input Channel

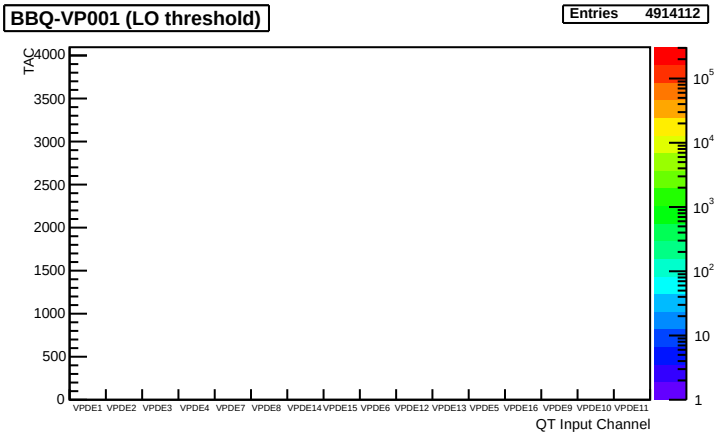

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4914112
ADC
QT Input Channel

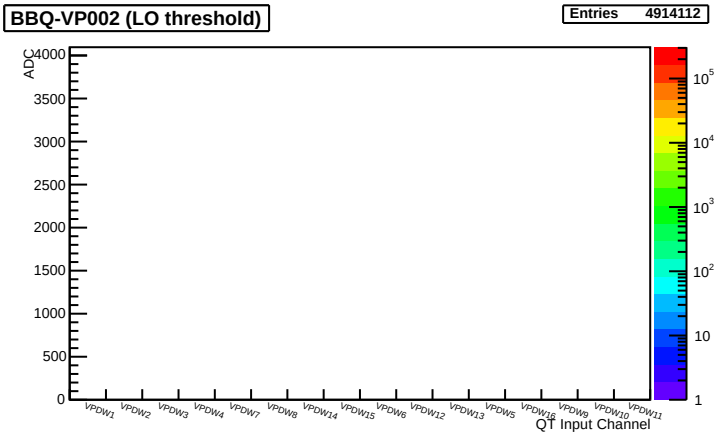

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4914112
TAC
QT Input Channel

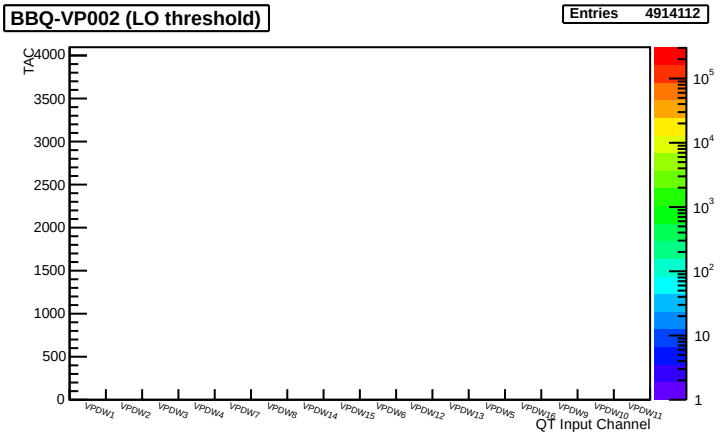

BBQ-ZD001 (ZDC TOWER)
Entries 4914112
ADC

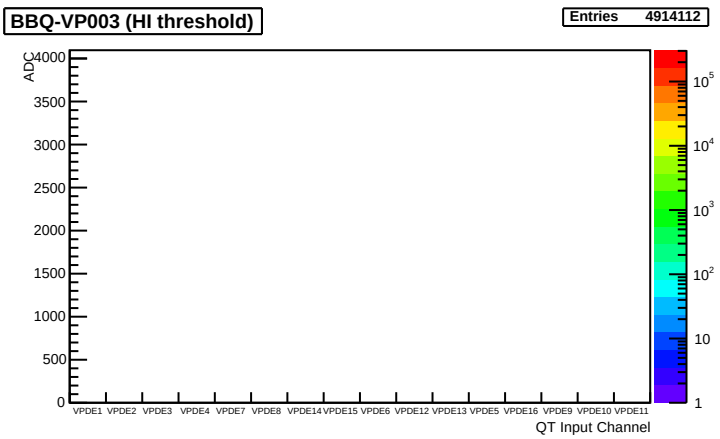

BBQ-ZD001 (ZDC TOWER)
Entries 4914112
TAC

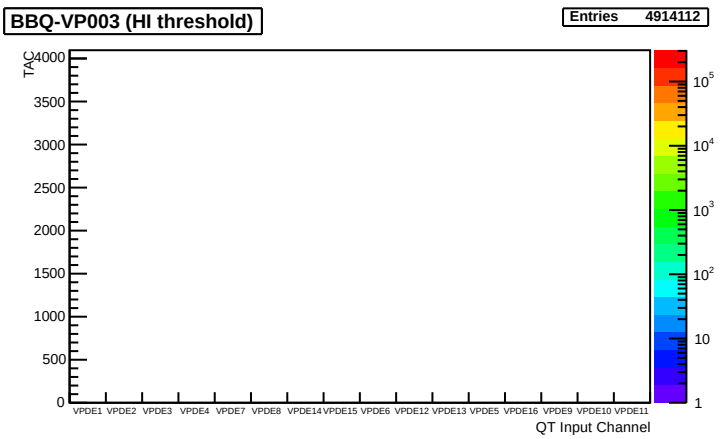

BBQ-VP001 (LO threshold)
Entries 4914112
ADC
QT Input Channel

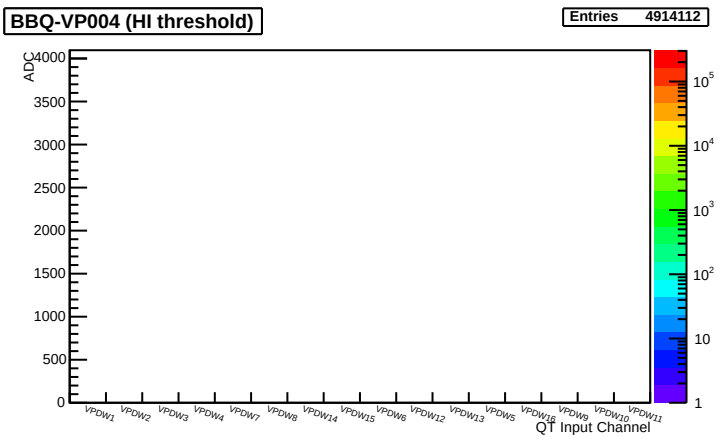

BBQ-VP001 (LO threshold)
Entries 4914112
TAC
QT Input Channel

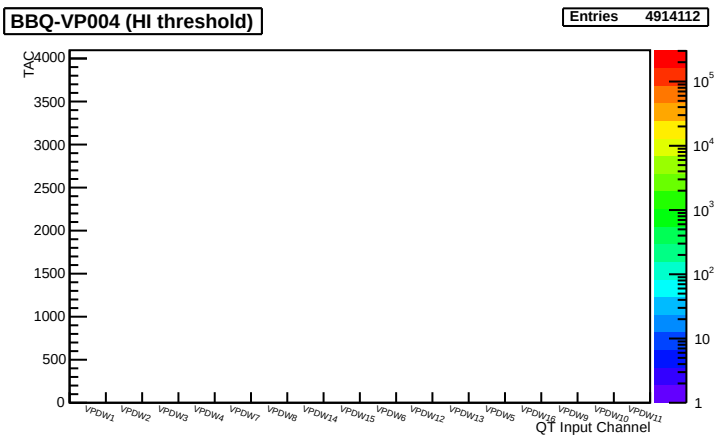

BBQ-VP002 (LO threshold)
Entries 4914112
ADC
QT Input Channel

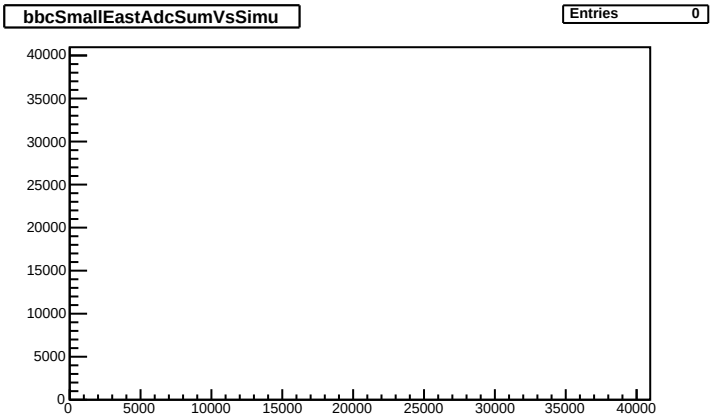

BBQ-VP002 (LO threshold)
Entries 4914112
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 4914112
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 4914112
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 4914112
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 4914112
TAC
QT Input Channel


bbcSmallEastAdcSumVsSimu
Entries 0
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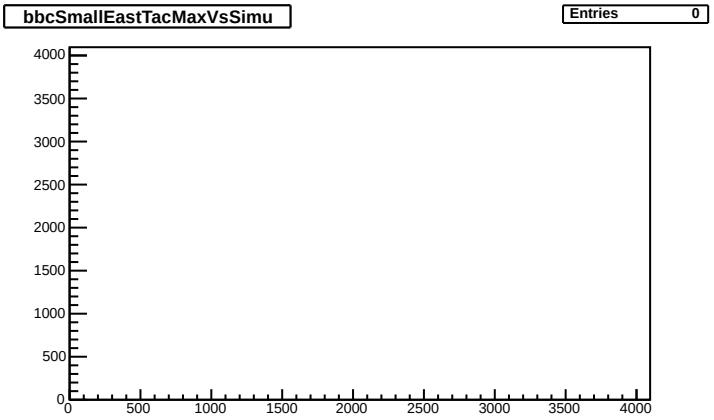
35000
30000
25000
20000
15000
10000
5000
0
0 5000 10000 15000 20000 25000 30000 35000 40000


bbcSmallEastTacMaxVsSimu
Entries 0
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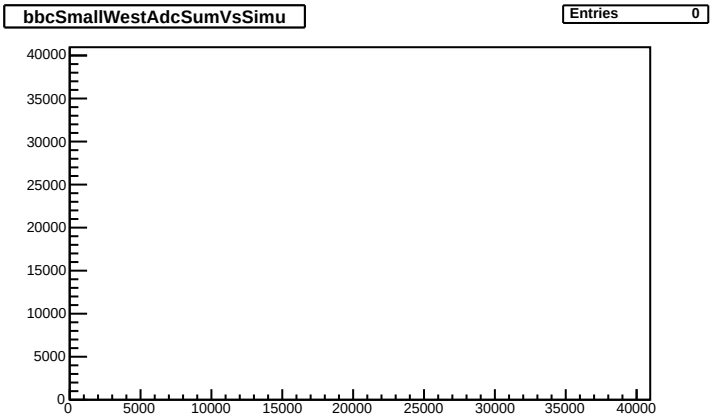
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000


bbcSmallWestAdcSumVsSimu
Entries 0
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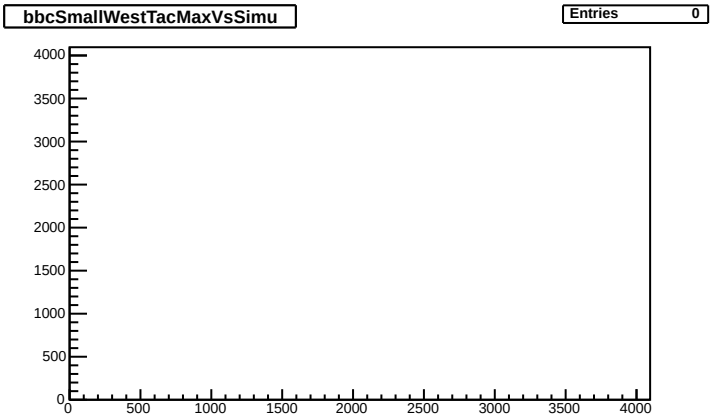
35000
30000
25000
20000
15000
10000
5000
0
0 5000 10000 15000 20000 25000 30000 35000 40000


bbcSmallWestTacMaxVsSimu
Entries 0
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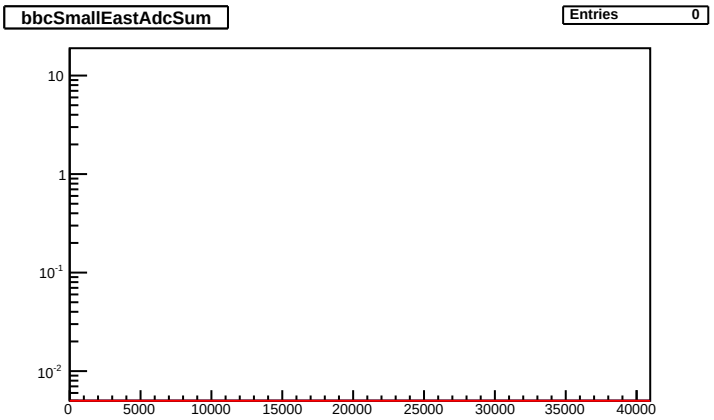
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000


bbcSmallEastAdcSum
Entries 0
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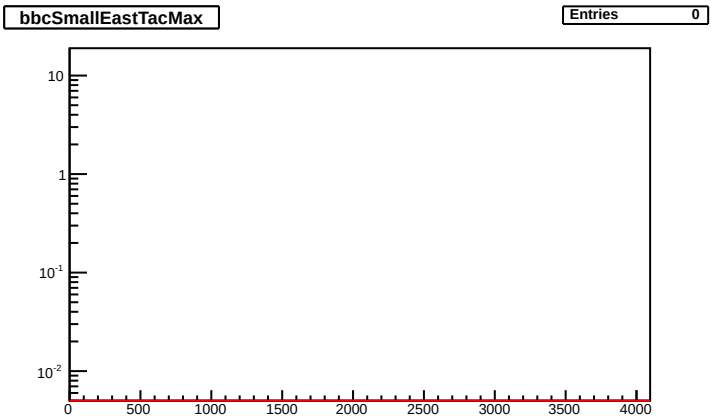
1
10^-1
10^-2
0 5000 10000 15000 20000 25000 30000 35000 40000


bbcSmallEastTacMax
Entries 0
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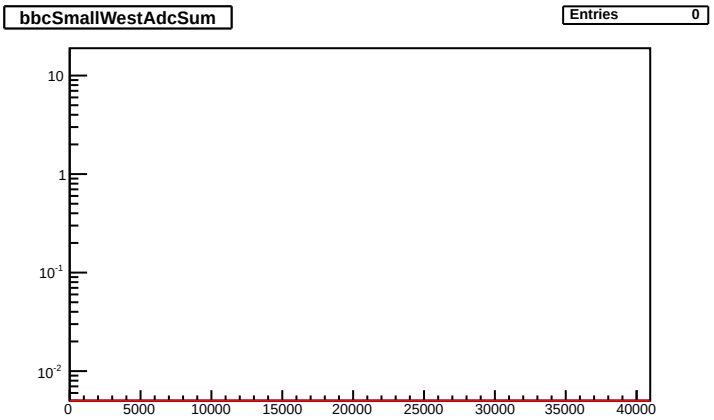
10^-1
10^-2
0 500 1000 1500 2000 2500 3000 3500 4000


bbcSmallWestAdcSum
Entries 0
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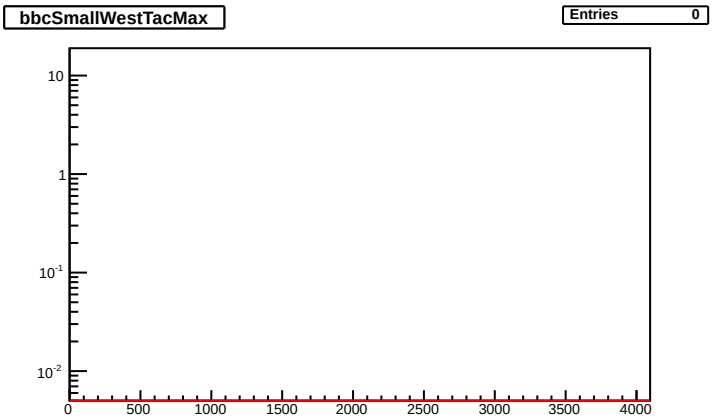
1
10^-1
10^-2
0 5000 10000 15000 20000 25000 30000 35000 40000


bbcSmallWestTacMax
Entries 0
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10^-1
10^-2
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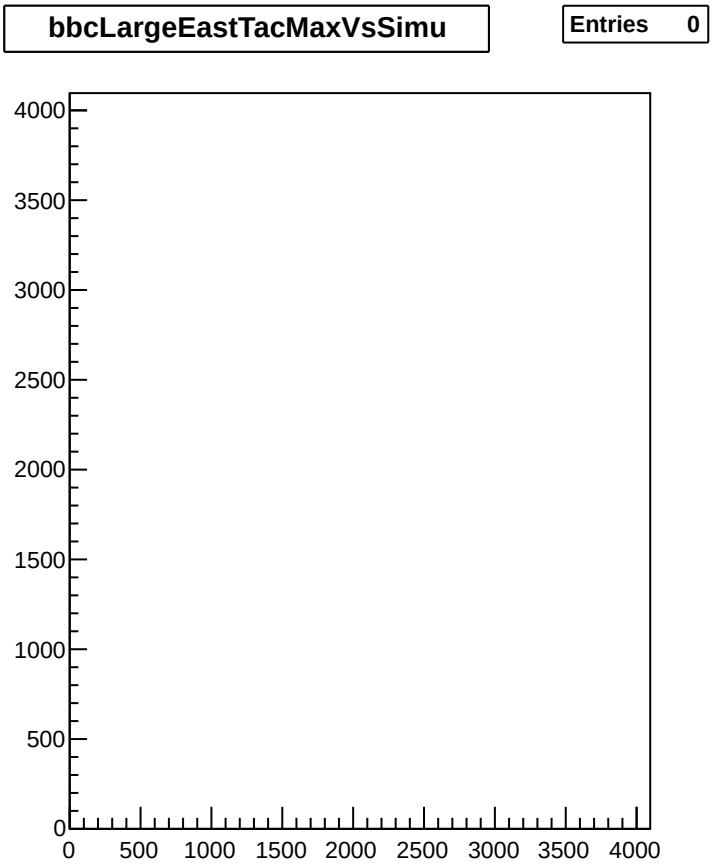
bbcLargeEastTacMaxVsSimu
Entries 0
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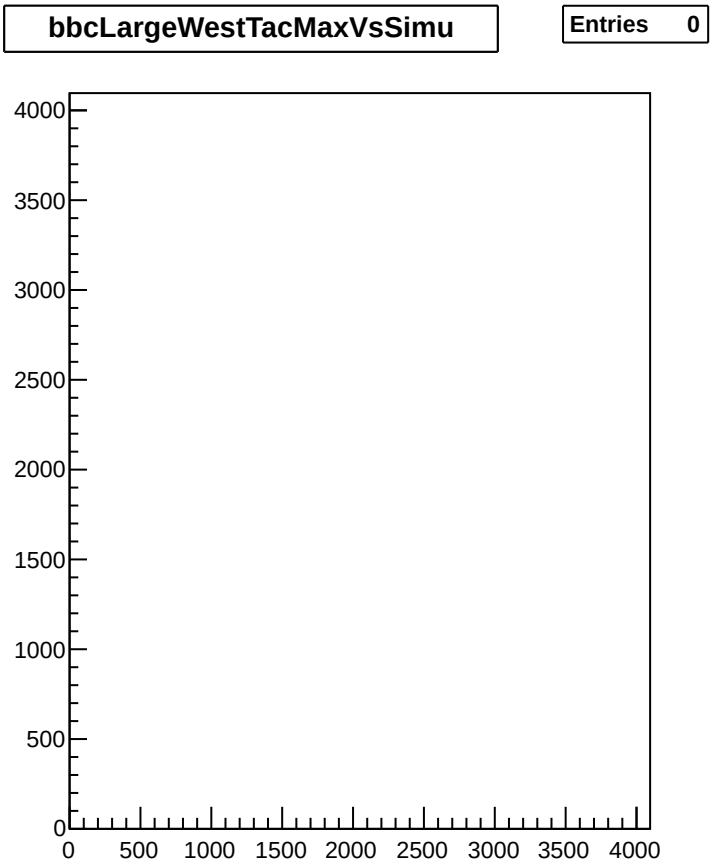
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000


bbcLargeWestTacMaxVsSimu
Entries 0
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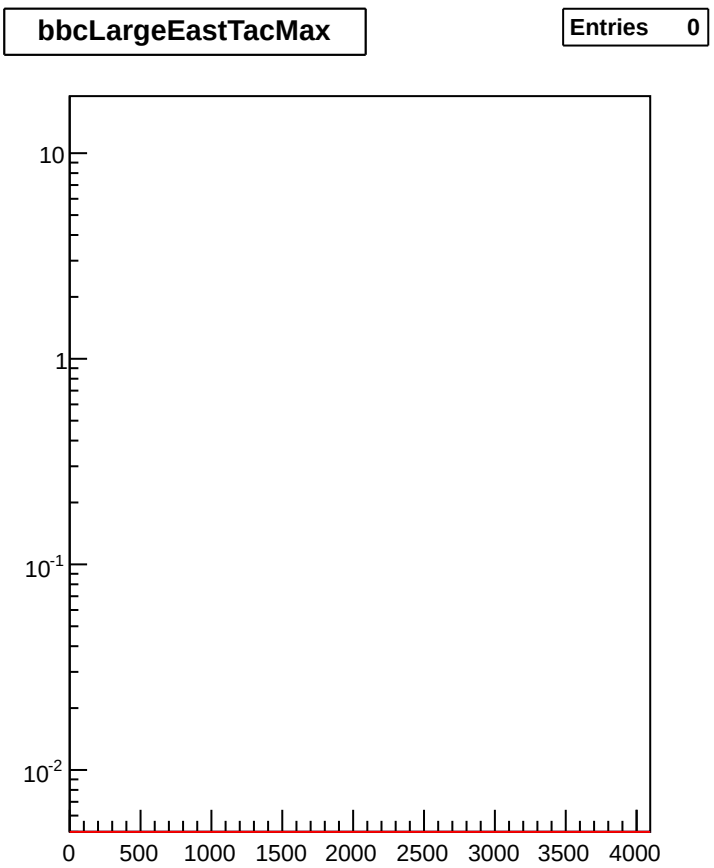
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000


bbcLargeEastTacMax
Entries 0
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10-1
10-2
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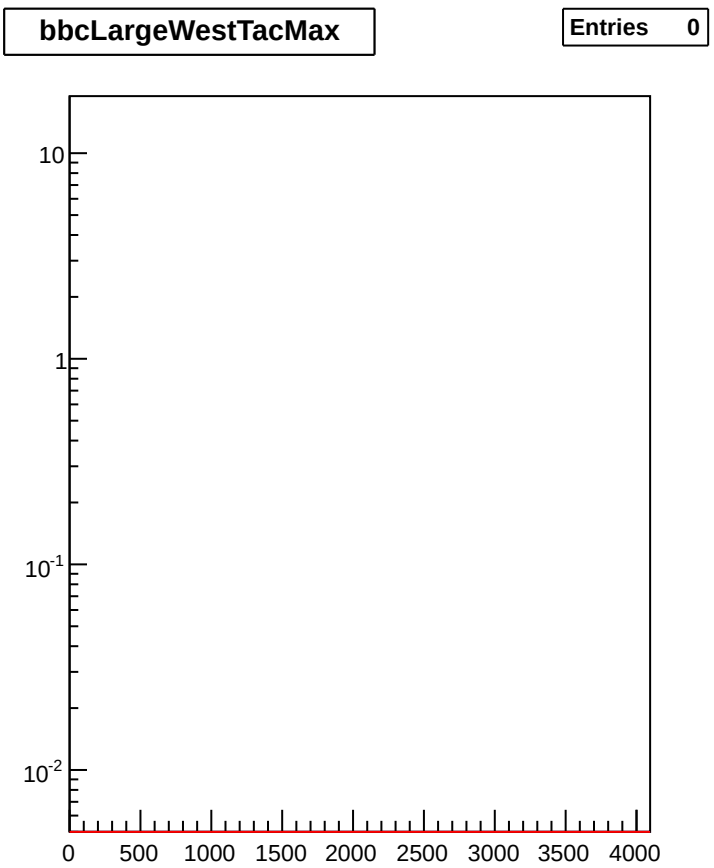
bbcLargeWestTacMax
Entries 0
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10-1
10-2
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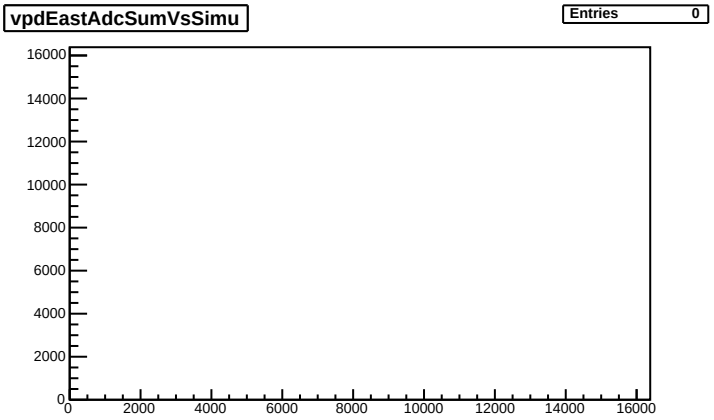
vpdEastAdcSumVsSimu
Entries 0
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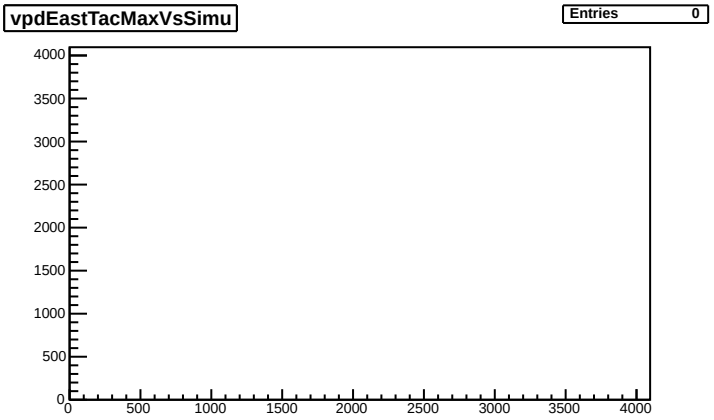
14000
12000
10000
8000
6000
4000
2000
0
0 2000 4000 6000 8000 10000 12000 14000 16000


vpdEastTacMaxVsSimu
Entries 0
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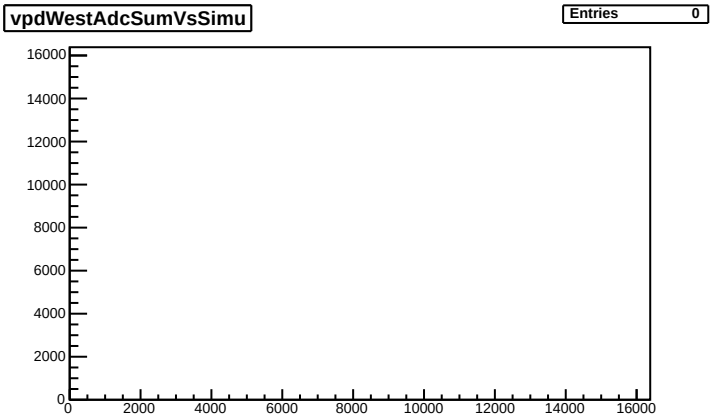
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000


vpdWestAdcSumVsSimu
Entries 0
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12000
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8000
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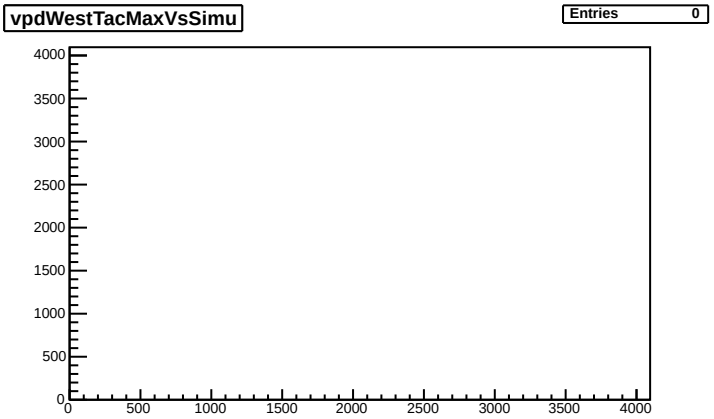
4000
2000
0
0 2000 4000 6000 8000 10000 12000 14000 16000


vpdWestTacMaxVsSimu
Entries 0
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3000
2500
2000
1500
1000
500
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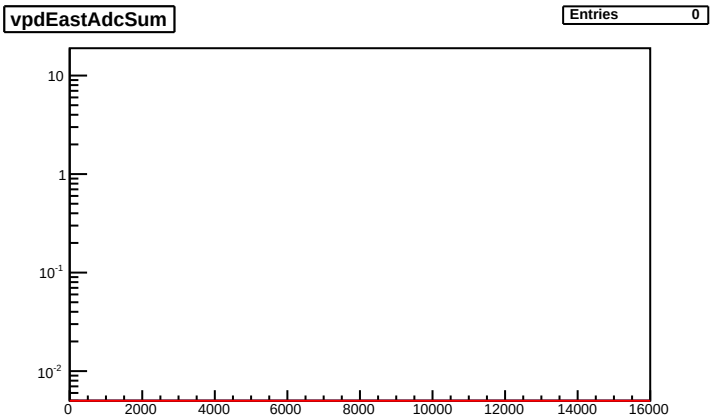
0 500 1000 1500 2000 2500 3000 3500 4000


vpdEastAdcSum
Entries 0
10
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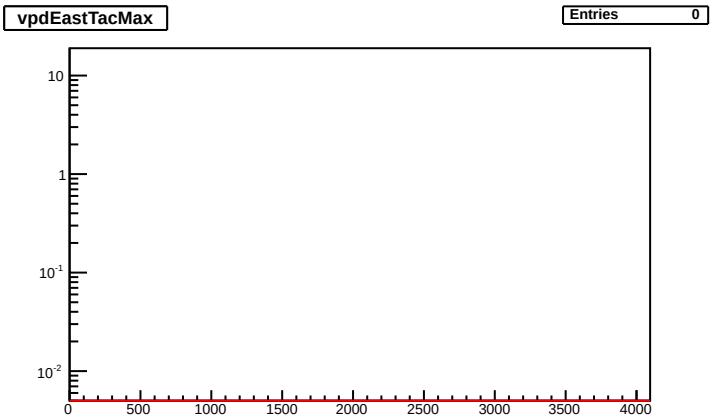
10^-1
10^-2
0 2000 4000 6000 8000 10000 12000 14000 16000


vpdEastTacMax
Entries 0
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10^-1
10^-2
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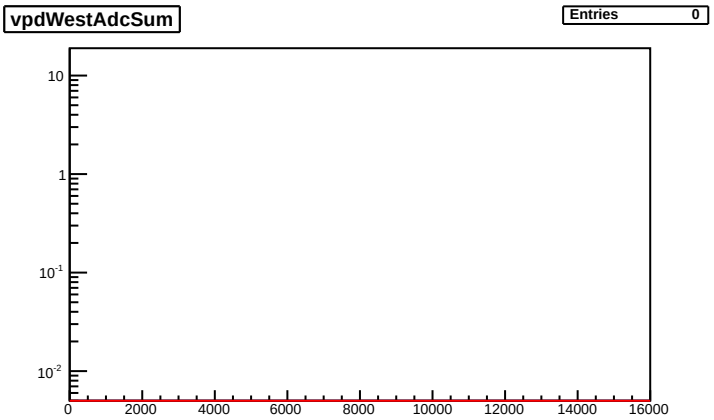
vpdWestAdcSum
Entries 0
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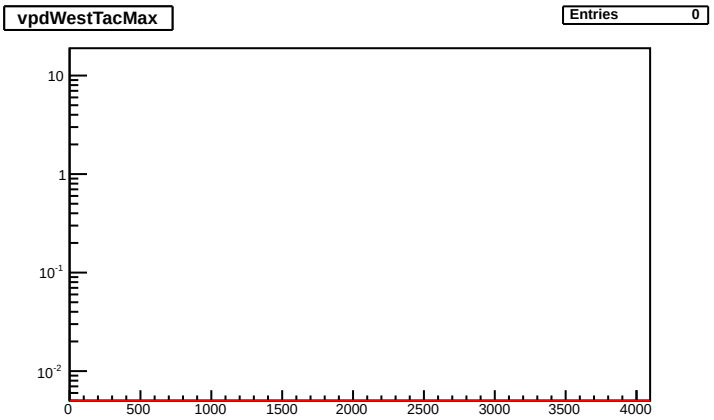
10^-1
10^-2
0 2000 4000 6000 8000 10000 12000 14000 16000


vpdWestTacMax
Entries 0
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10^-1
10^-2
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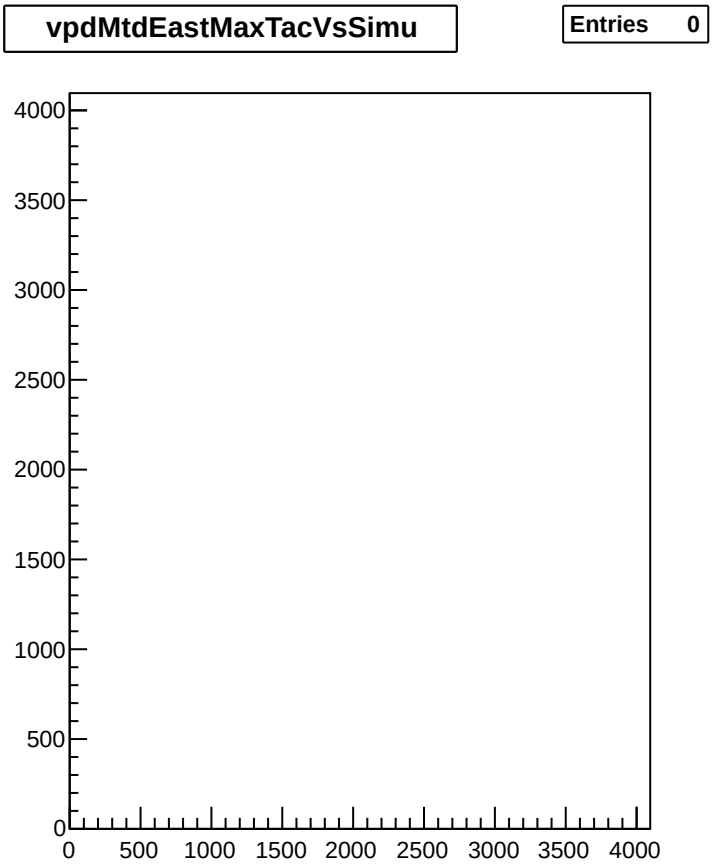
vpdMtdEastMaxTacVsSimu
Entries 0
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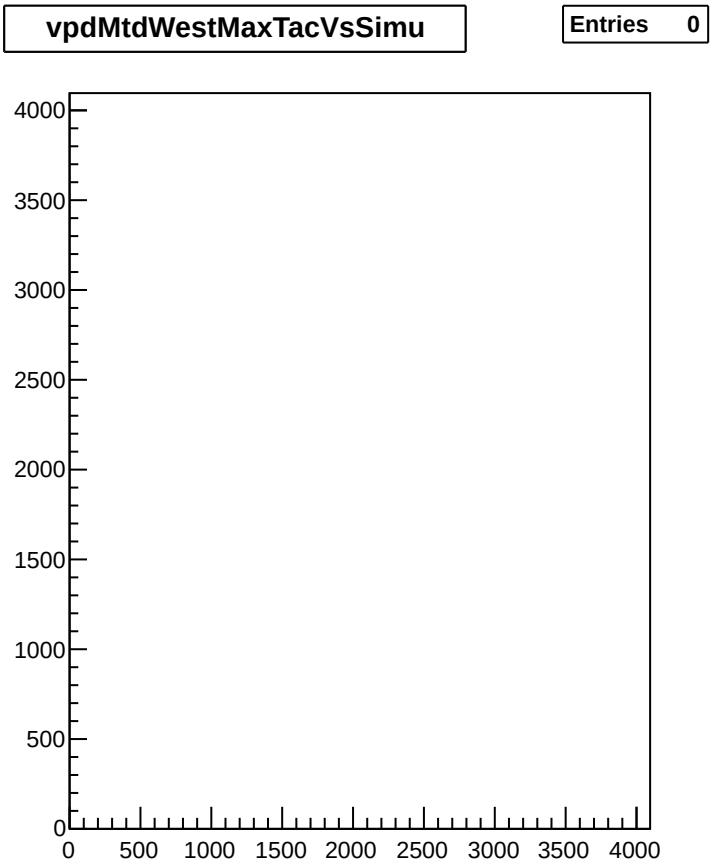
3500
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000


vpdMtdWestMaxTacVsSimu
Entries 0
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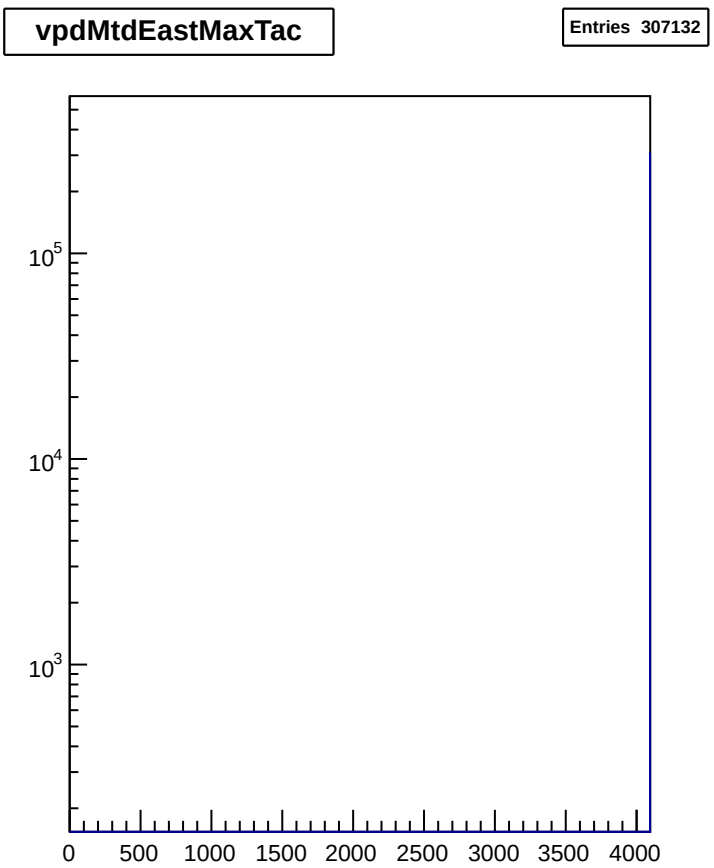
3000
2500
2000
1500
1000
500
0
0 500 1000 1500 2000 2500 3000 3500 4000


vpdMtdEastMaxTac
Entries 307132
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10^4
10^3
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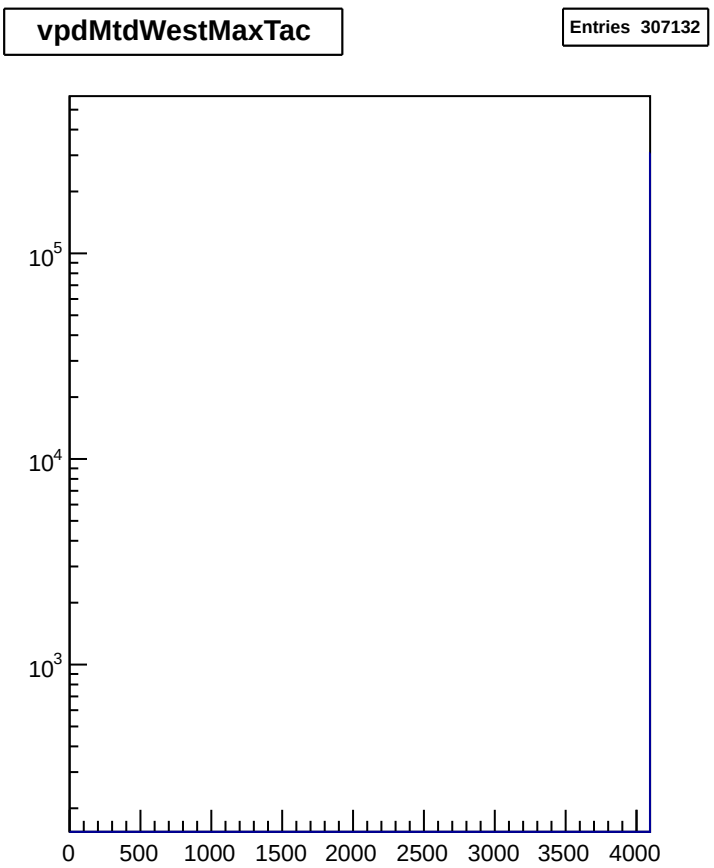
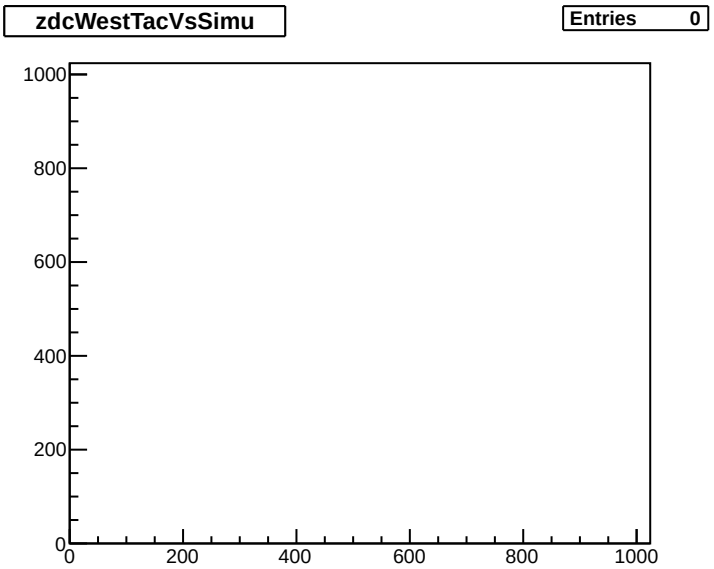

vpdMtdWestMaxTac
Entries 307132
10^5
10^4
10^3
0 500 1000 1500 2000 2500 3000 3500 4000


zdcWestTacVsSimu
Entries 0
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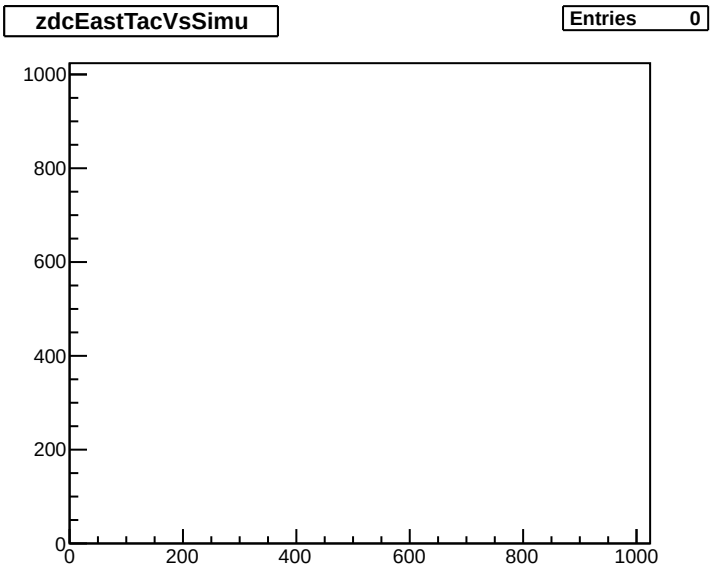
800
600
400
200
0
0
200
400
600
800
1000


zdcEastTacVsSimu
Entries 0
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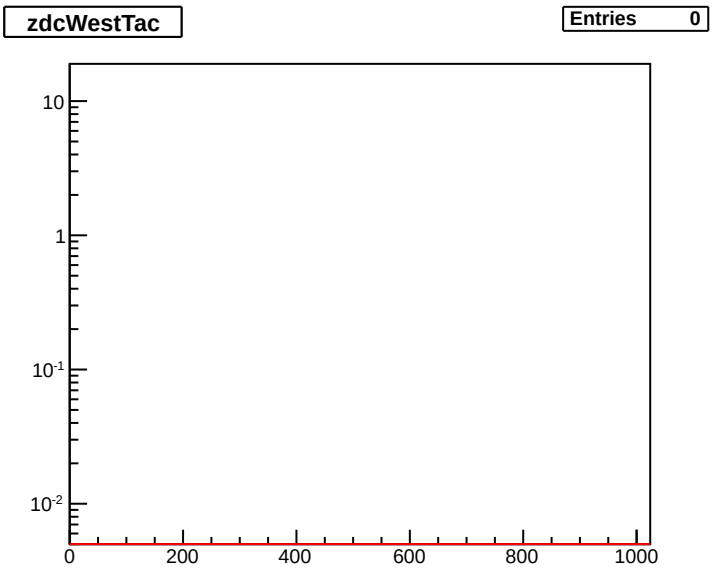
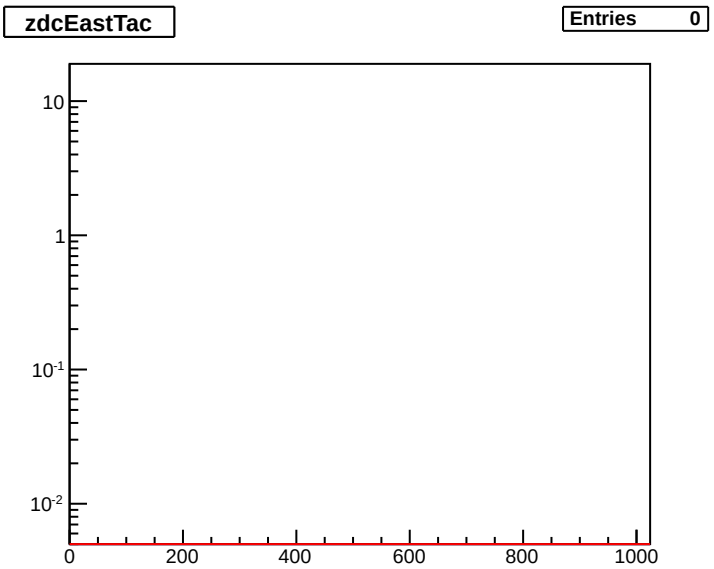
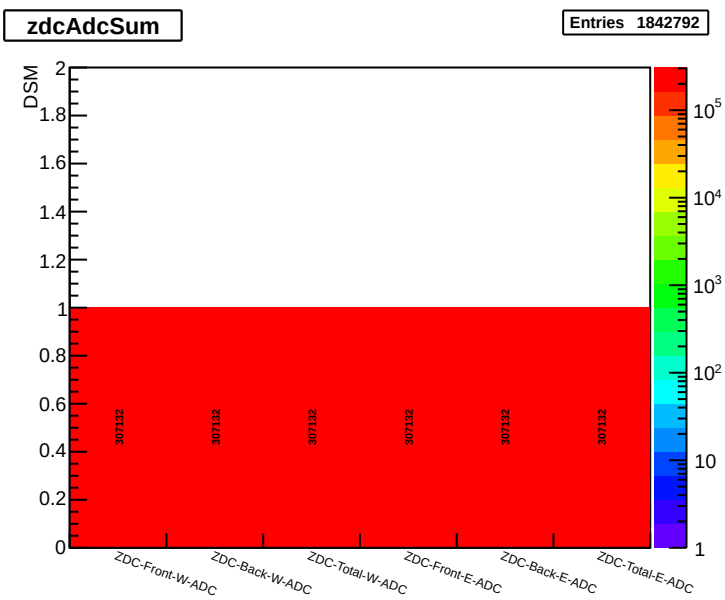
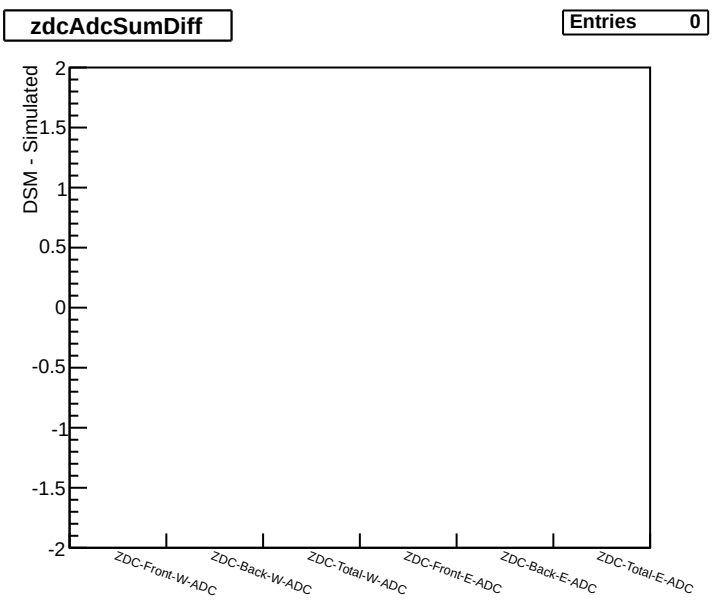
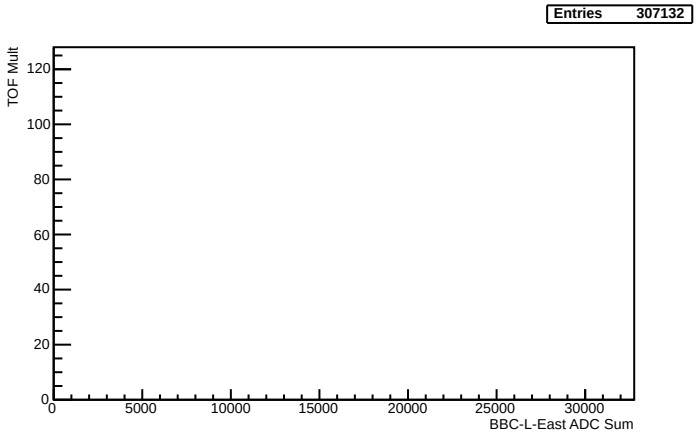
800
600
400
200
0
0
200
400
600
800
1000

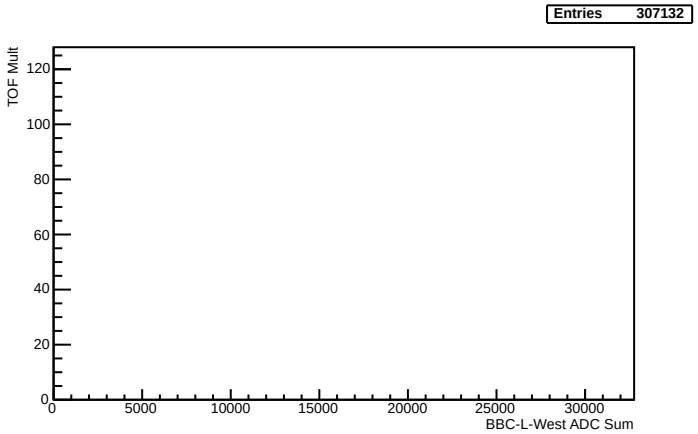

zdcWestTac
Entries 0
10
1
10-1
10-2
0
200
400
600
800
1000

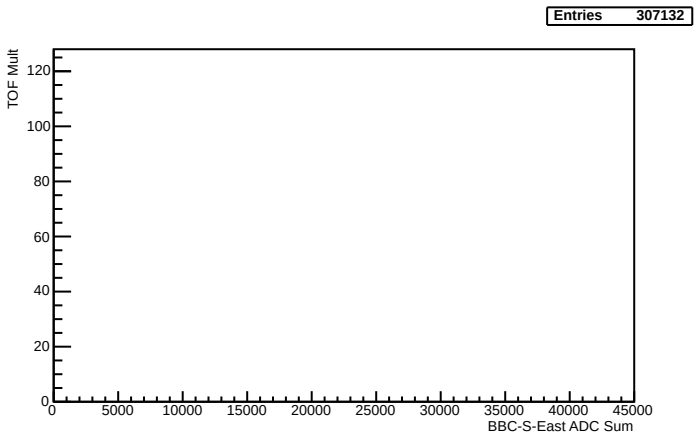

zdcEastTac
Entries 0
10
1
10-1
10-2
0
200
400
600
800
1000

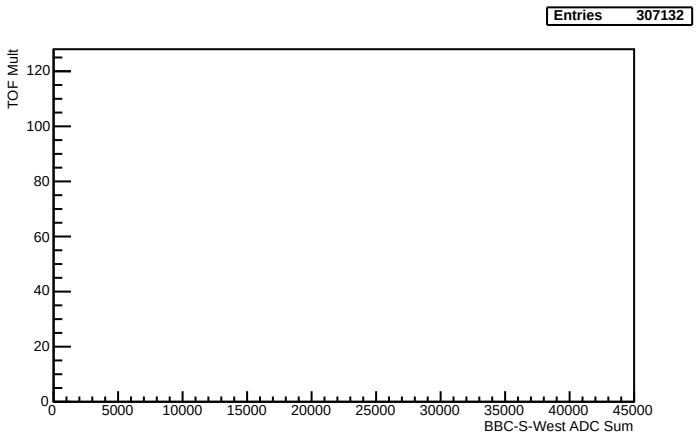

zdcAdcSum
Entries 1842792
DSM
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
307132
307132
307132
307132
307132
307132
10^5
10^4
10^3
10^2
10
1
ZDC-Front-W-ADC
ZDC-Back-W-ADC
ZDC-Total-W-ADC
ZDC-Front-E-ADC
ZDC-Back-E-ADC
ZDC-Total-E-ADC

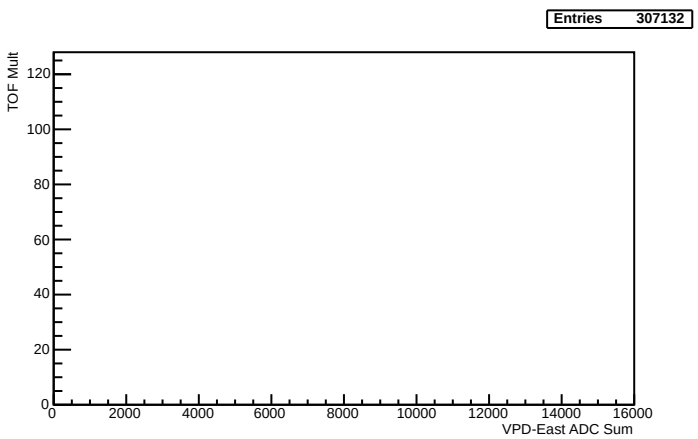

zdcAdcSumDiff
Entries 0
DSM - Simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
ZDC-Front-W-ADC
ZDC-Back-W-ADC
ZDC-Total-W-ADC
ZDC-Front-E-ADC
ZDC-Back-E-ADC
ZDC-Total-E-ADC

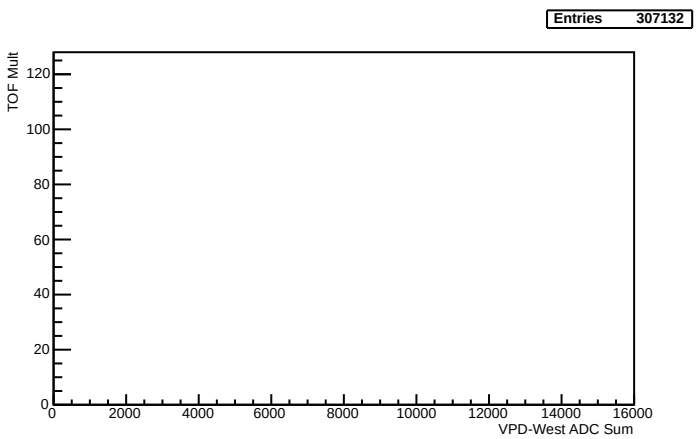

Entries 307132
TOF Mult
BBC-L-East ADC Sum

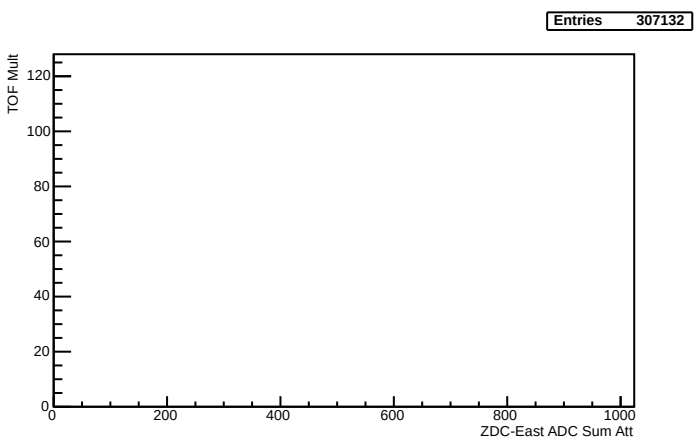

Entries 307132
TOF Mult
BBC-L-West ADC Sum

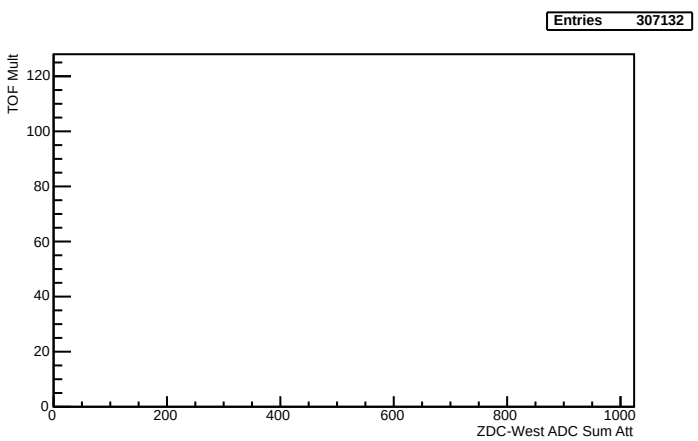

Entries 307132
TOF Mult
BBC-S-East ADC Sum

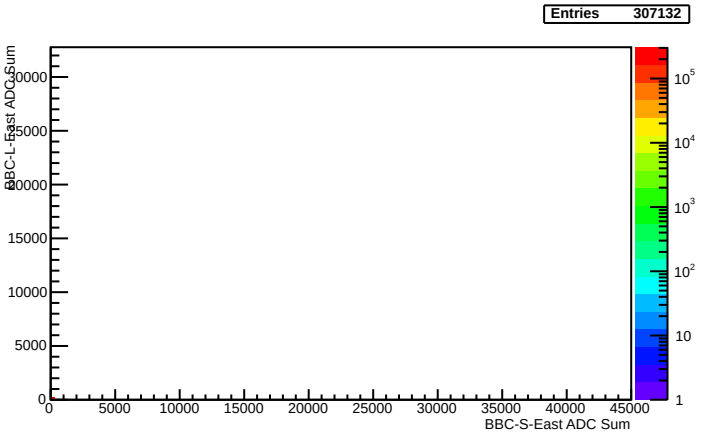

Entries 307132
TOF Mult
BBC-S-West ADC Sum

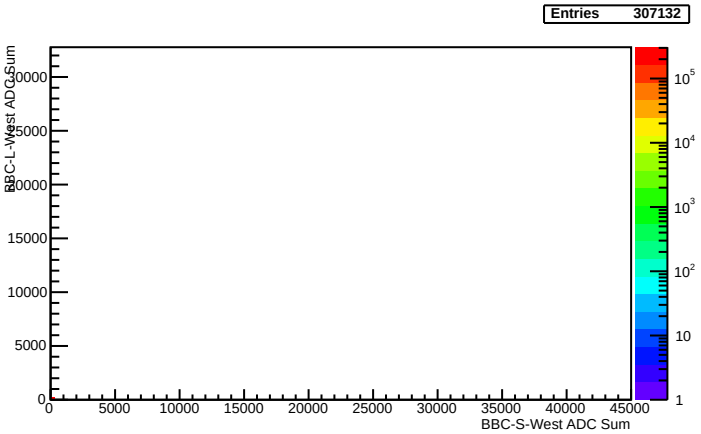

Entries 307132
TOF Mult
VPD-East ADC Sum

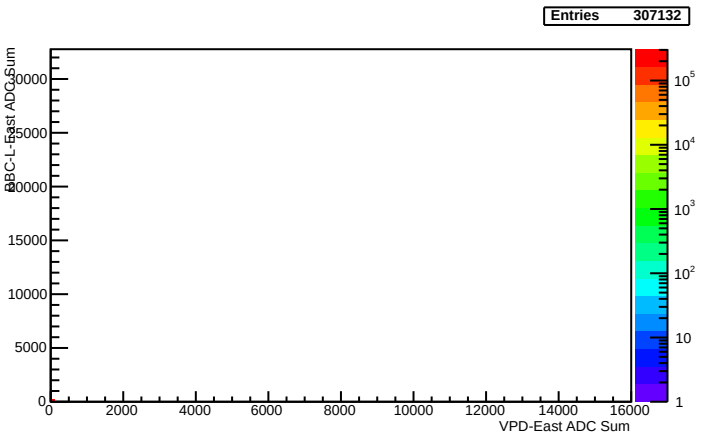

Entries 307132
TOF Mult
VPD-West ADC Sum

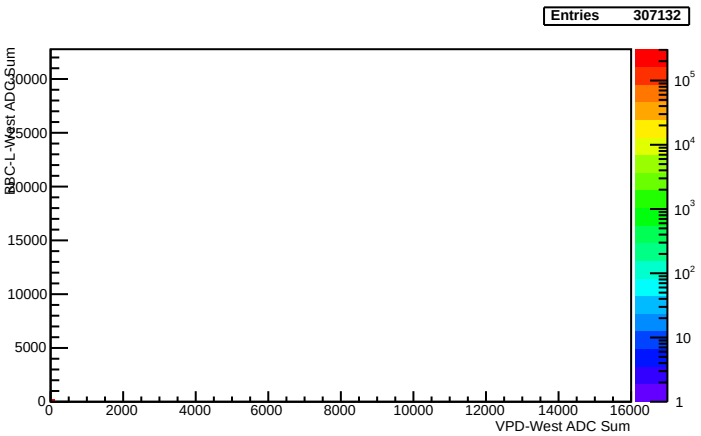

Entries 307132
TOF Mult
ZDC-East ADC Sum Att

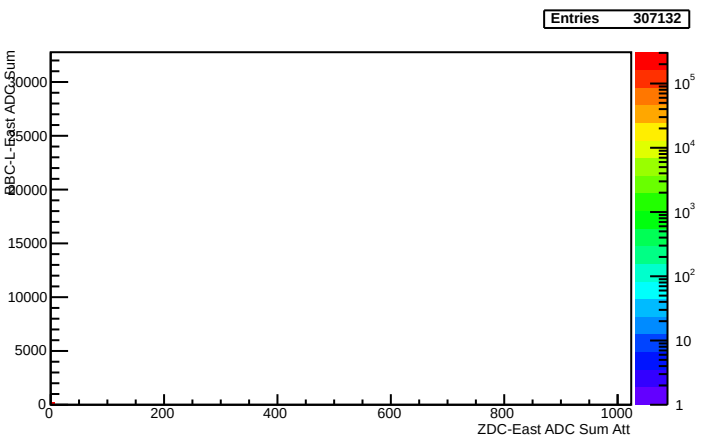

Entries 307132
TOF Mult
ZDC-West ADC Sum Att

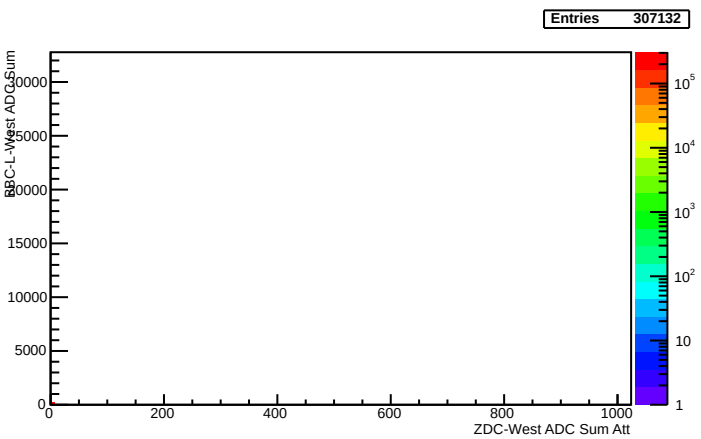

Entries 307132
BBC-L-East ADC Sum
BBC-S-East ADC Sum

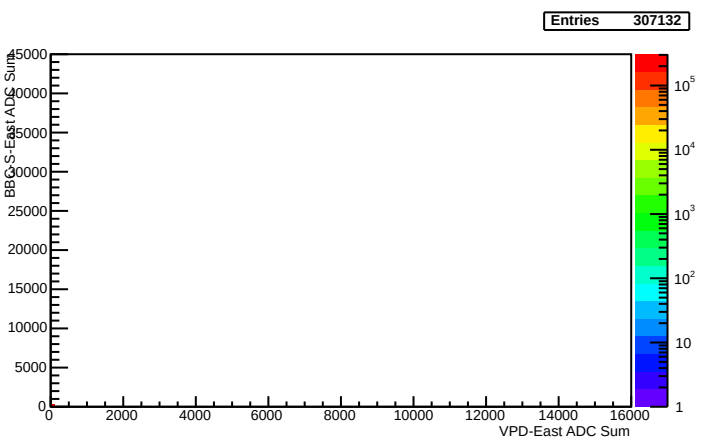

Entries 307132
BBC-L-West ADC Sum
BBC-S-West ADC Sum

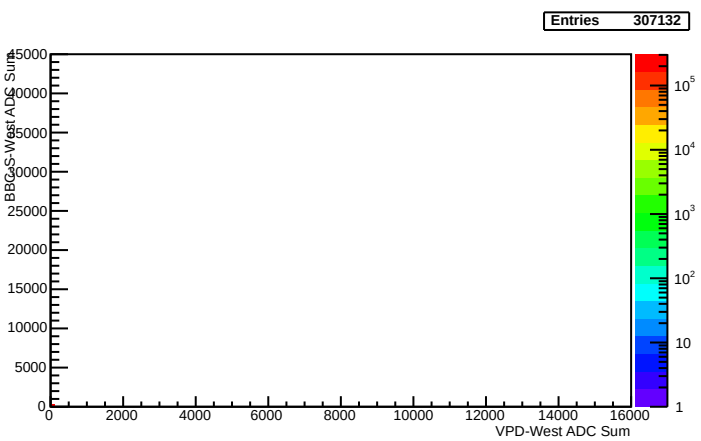

Entries 307132
BBC-L-East ADC Sum
VPD-East ADC Sum

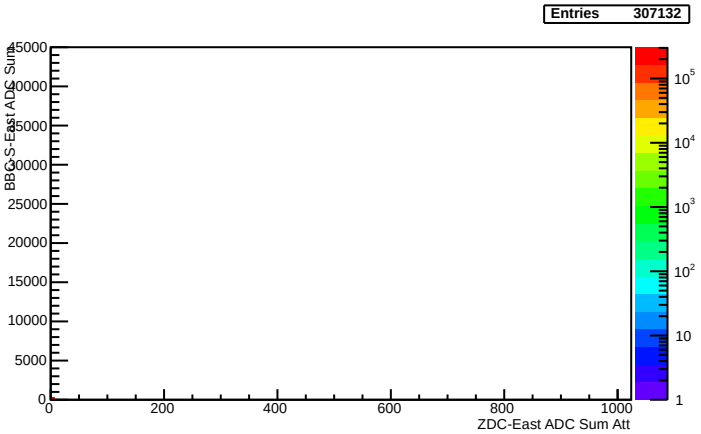

Entries 307132
BBC-L-West ADC Sum
VPD-West ADC Sum

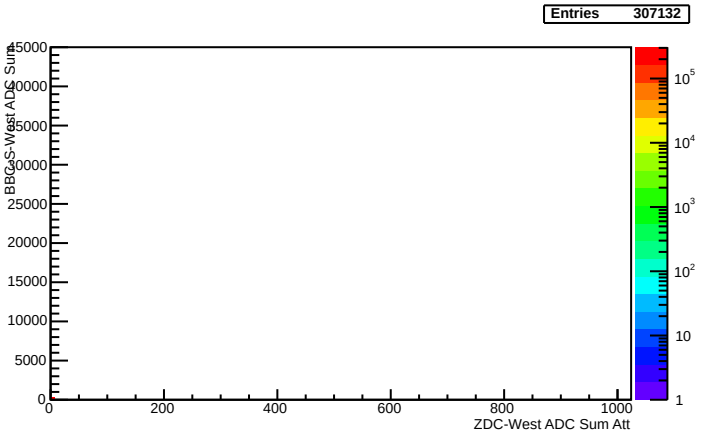

Entries 307132
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

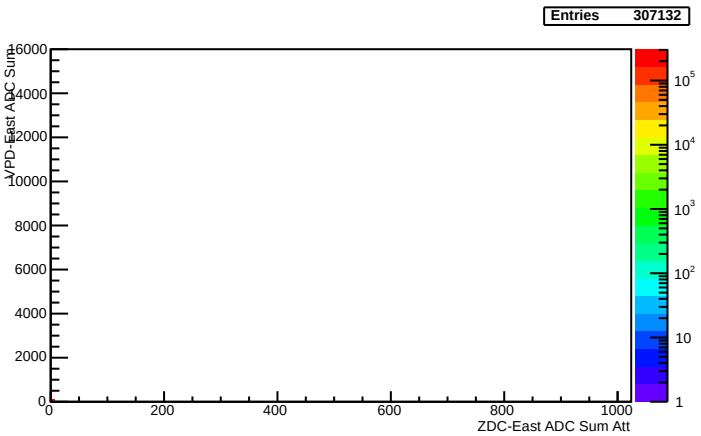

Entries 307132
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

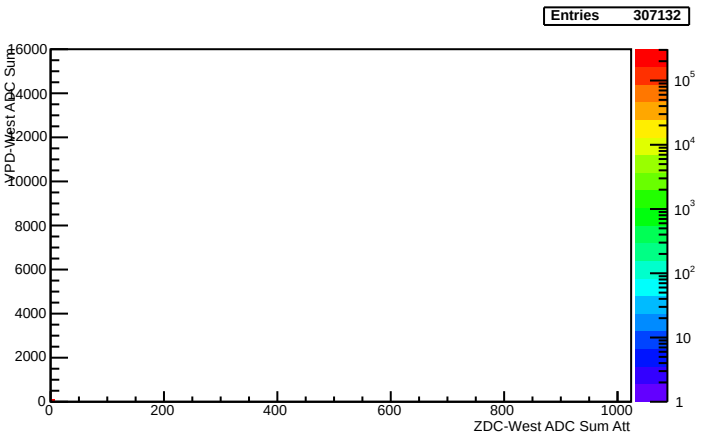

Entries 307132
BBC-S-East ADC Sum
VPD-East ADC Sum

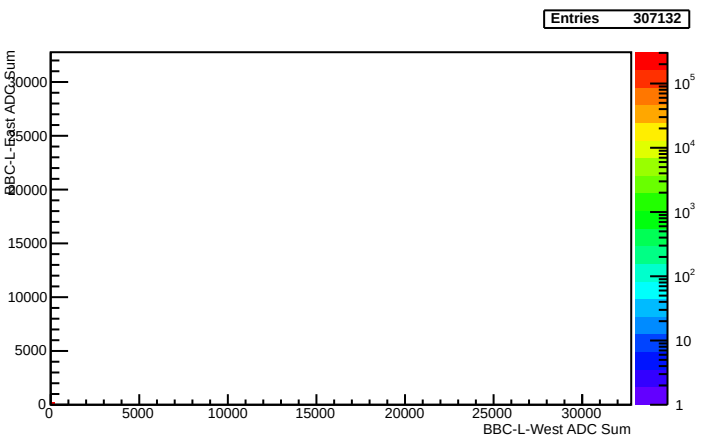

Entries 307132
BBC-S-West ADC Sum
VPD-West ADC Sum

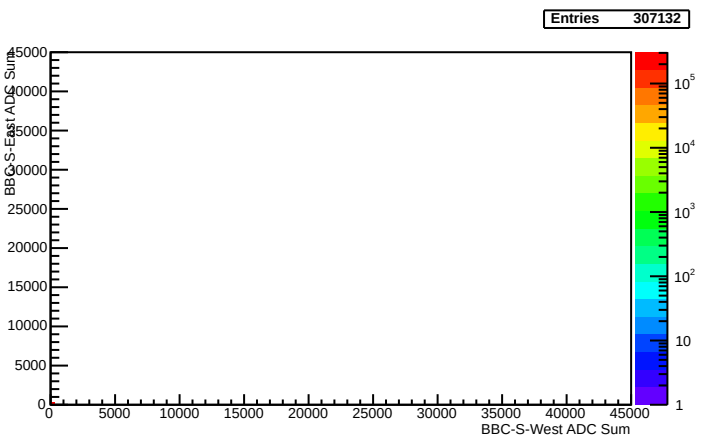

Entries 307132
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

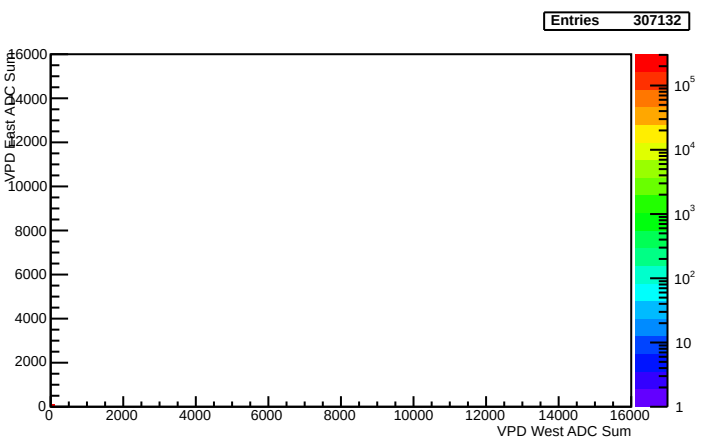

Entries 307132
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

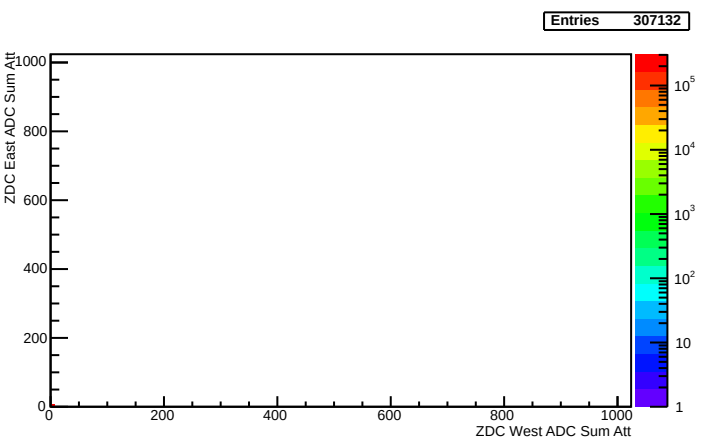

Entries 307132
VPD-East ADC Sum
ZDC-East ADC Sum Att

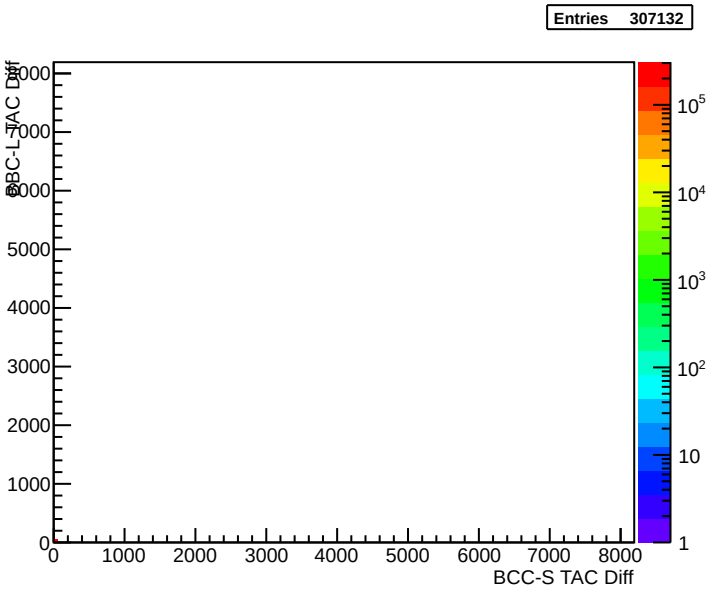

Entries 307132
VPD-West ADC Sum
ZDC-West ADC Sum Att

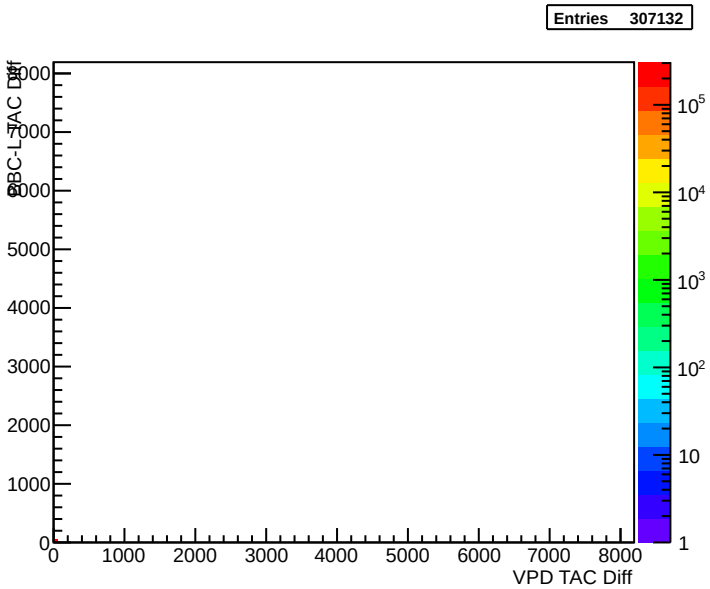

Entries 307132
BBC-L-East ADC Sum
BBC-L-West ADC Sum

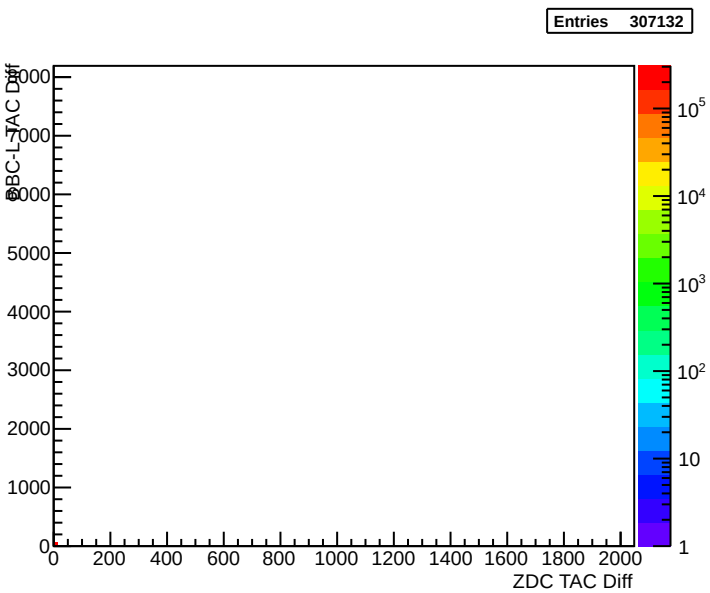

Entries 307132
BBC-S-East ADC Sum
BBC-S-West ADC Sum

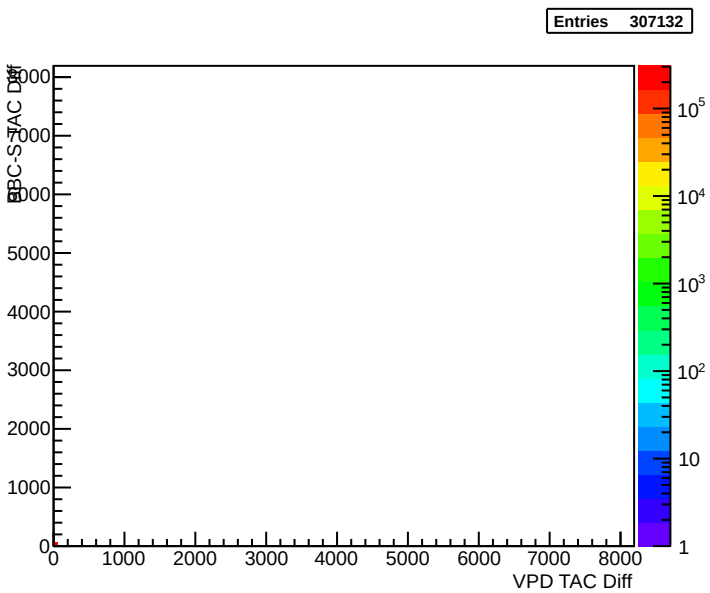

Entries 307132
VPD East ADC Sum
VPD West ADC Sum

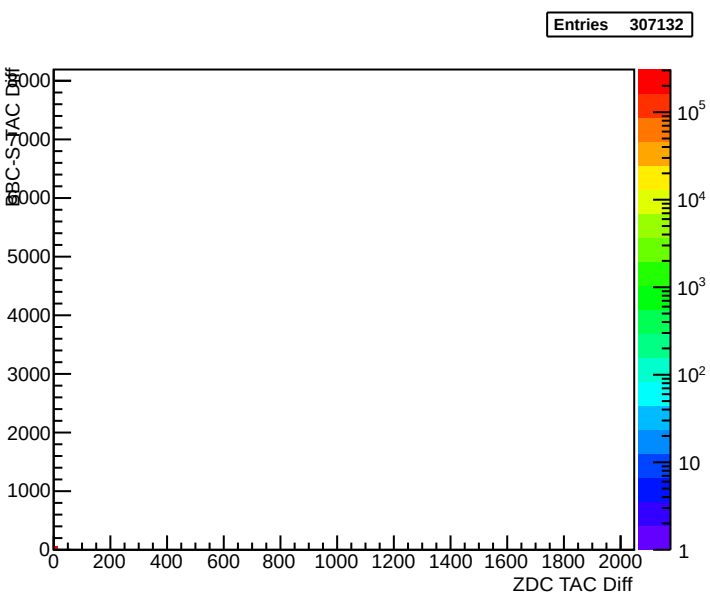

Entries 307132
ZDC East ADC Sum Att
ZDC West ADC Sum Att

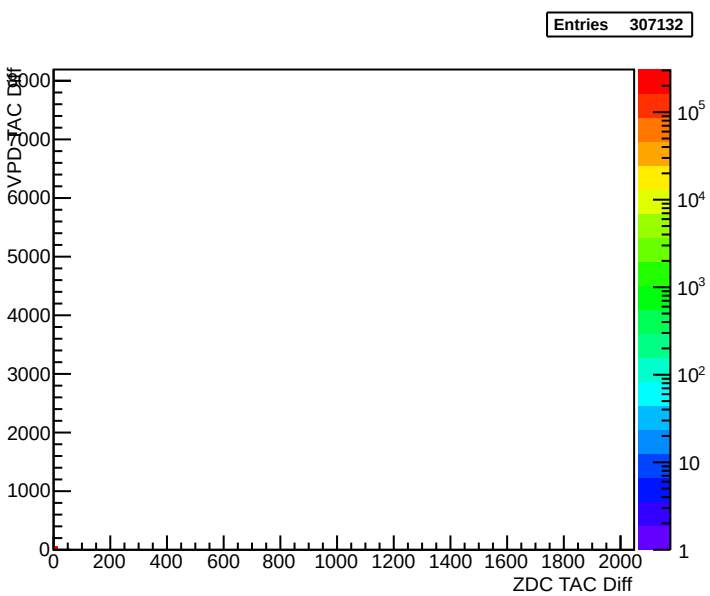

Entries 307132
BBC-L TAC Diff
BCC-S TAC Diff

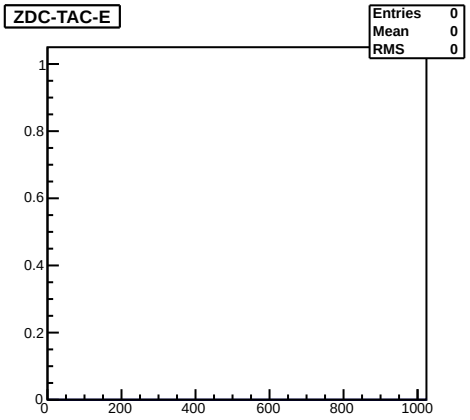

Entries 307132
BBC-L TAC Diff
VPD TAC Diff

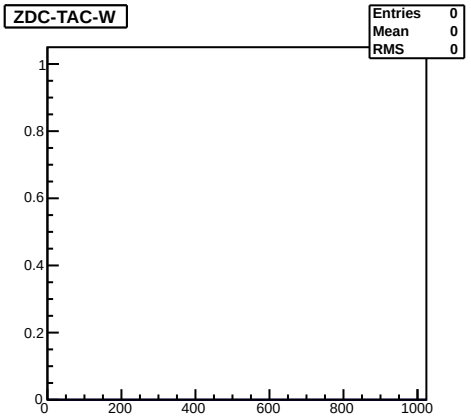

Entries 307132
BBC-L TAC Diff
ZDC TAC Diff

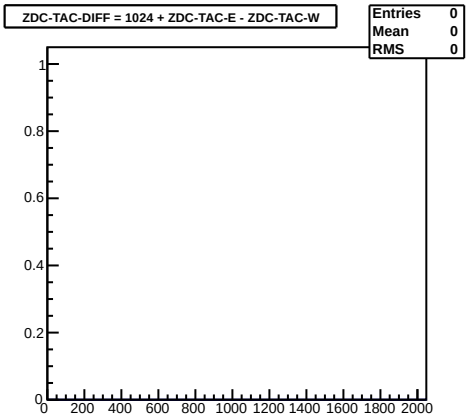

Entries 307132
BBC-S TAC Diff
VPD TAC Diff

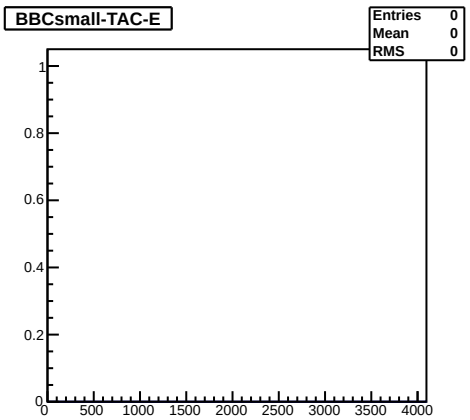

Entries 307132
BBC-S TAC Diff
ZDC TAC Diff

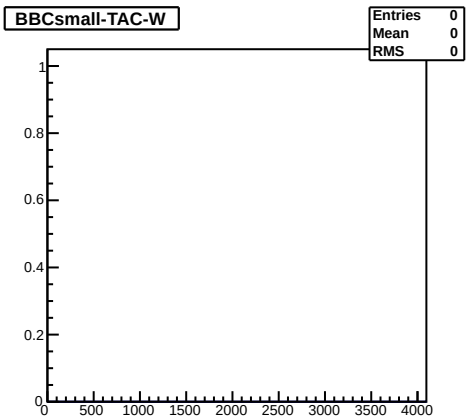

Entries 307132
VPD TAC Diff
ZDC TAC Diff

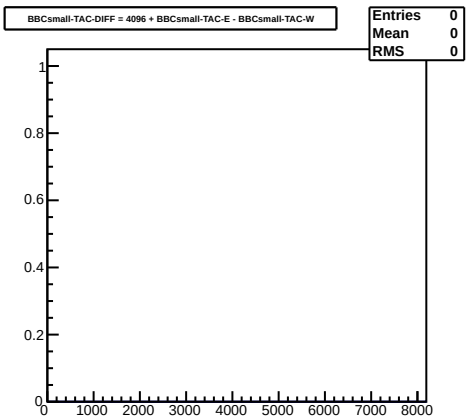

ZDC-TAC-E
Entries 0
Mean 0
RMS 0

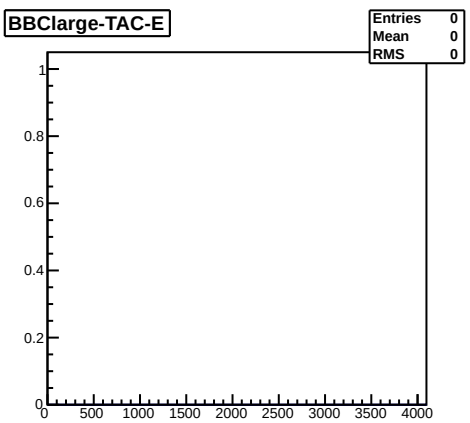

ZDC-TAC-W
Entries 0
Mean 0
RMS 0

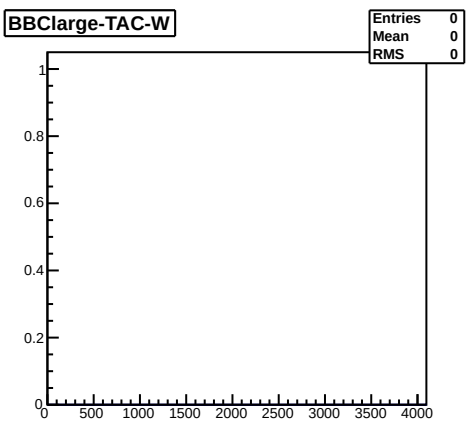

ZDC-TAC-DIFF = 1024 + ZDC-TAC-E - ZDC-TAC-W
Entries 0
Mean 0
RMS 0

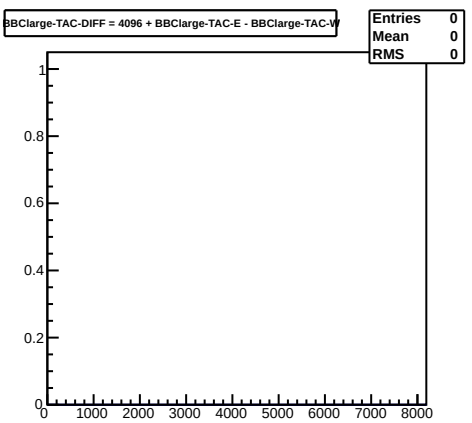

BBCsmall-TAC-E
Entries 0
Mean 0
RMS 0

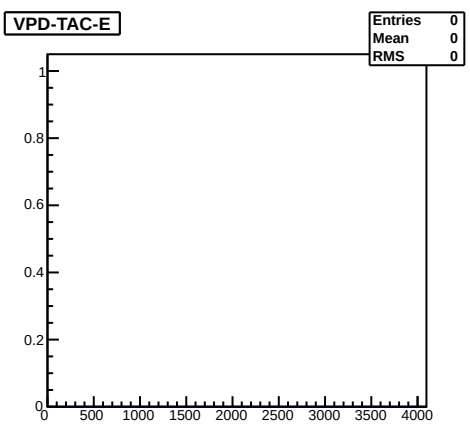

BBCsmall-TAC-W
Entries 0
Mean 0
RMS 0

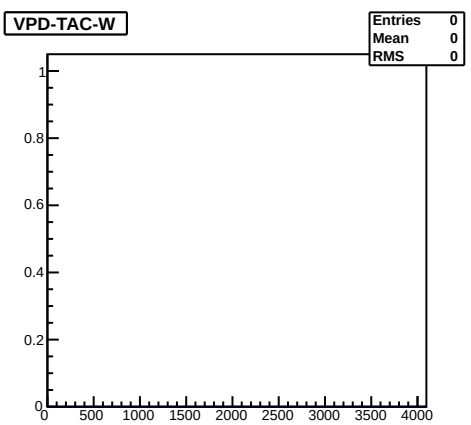

BBCsmall-TAC-DIFF = 4096 + BBCsmall-TAC-E - BBCsmall-TAC-W
Entries 0
Mean 0
RMS 0

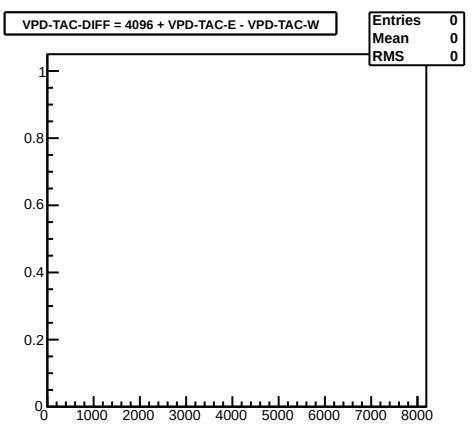

BBClarge-TAC-E
Entries 0
Mean 0
RMS 0

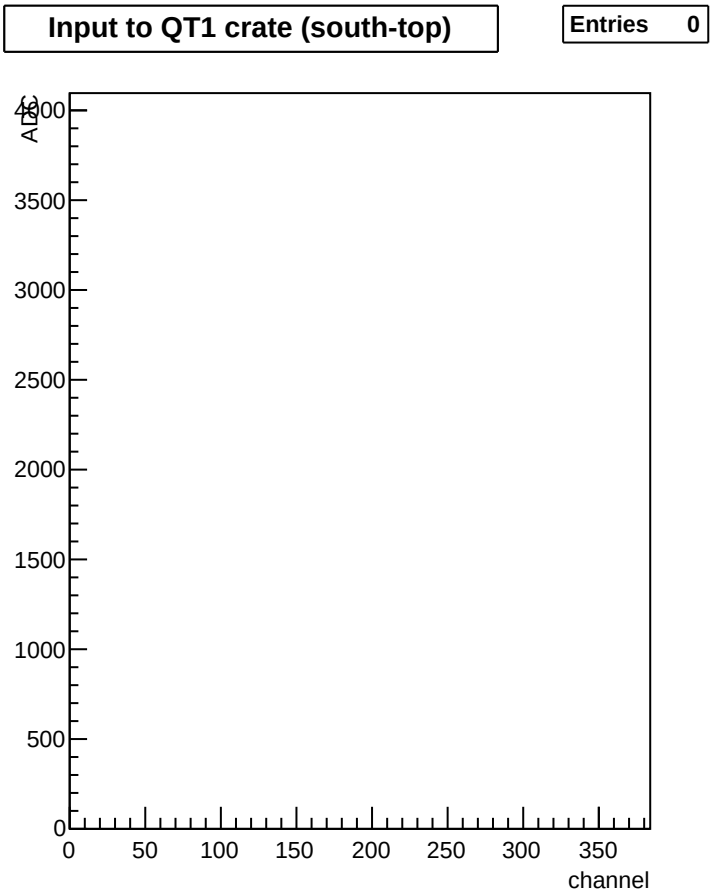

BBClarge-TAC-W
Entries 0
Mean 0
RMS 0

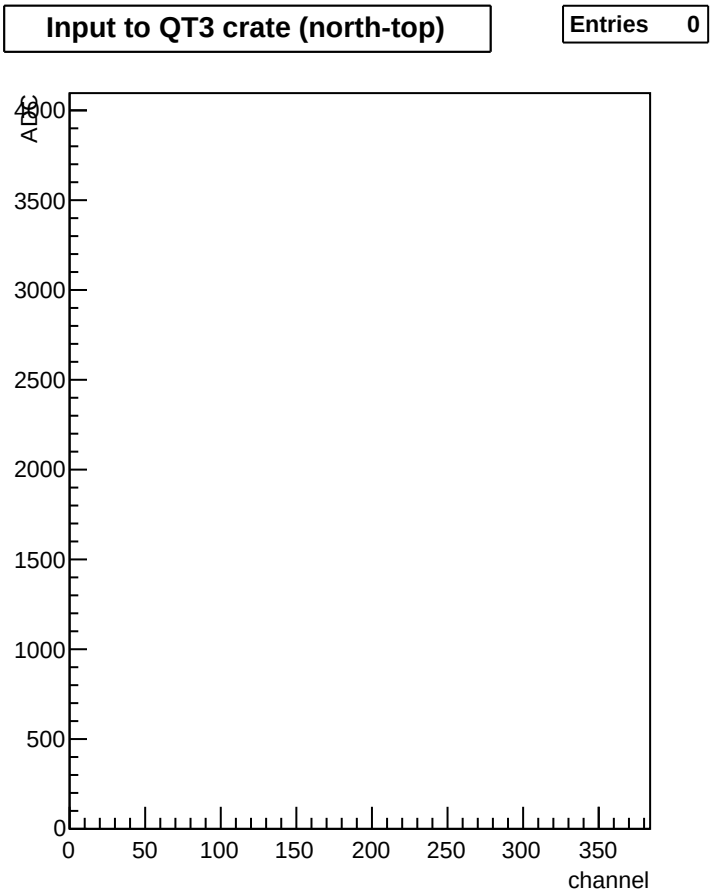

BBClarge-TAC-DIFF = 4096 + BBClarge-TAC-E - BBClarge-TAC-W
Entries 0
Mean 0
RMS 0

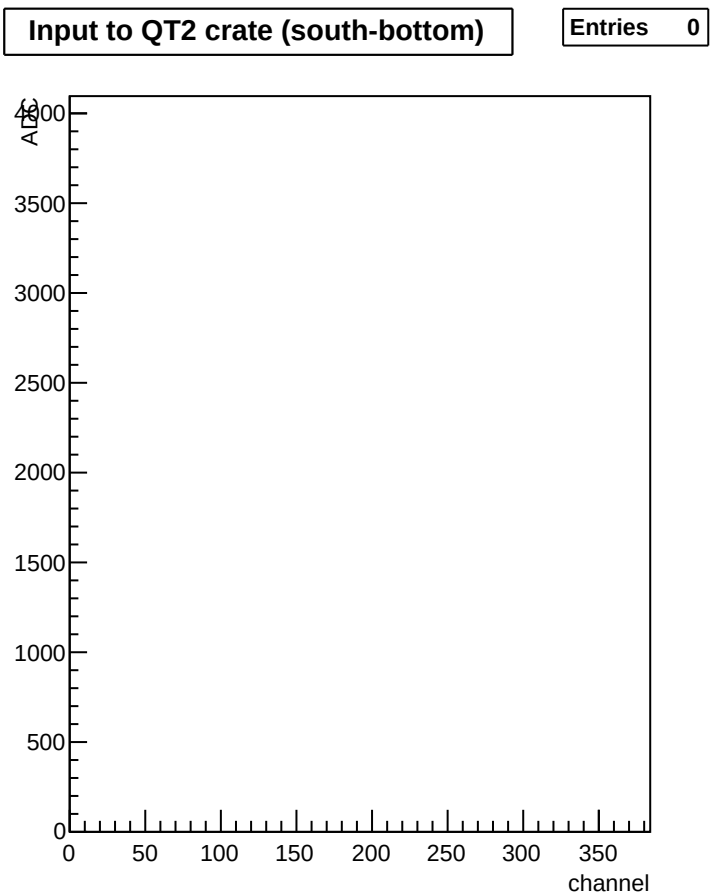

VPD-TAC-E
Entries 0
Mean 0
RMS 0

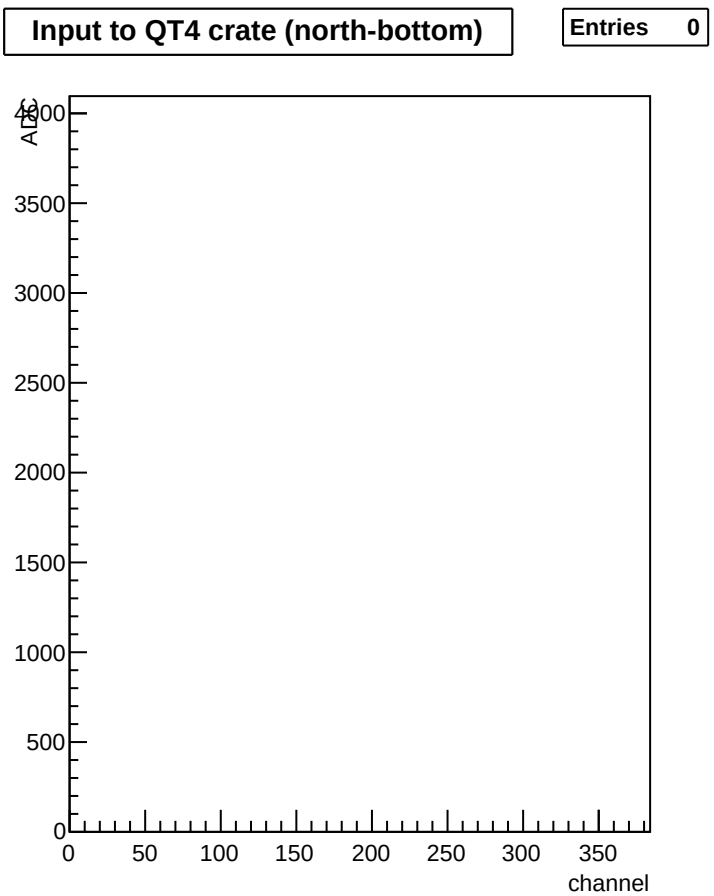

VPD-TAC-W
Entries 0
Mean 0
RMS 0

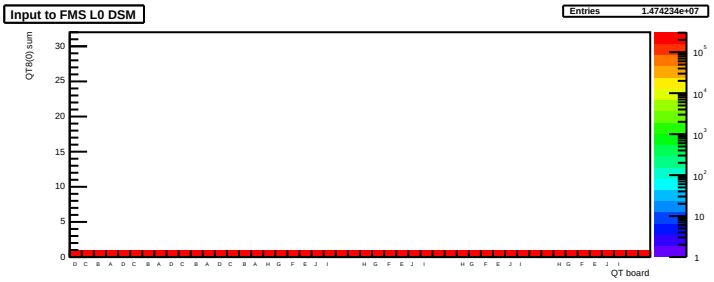

VPD-TAC-DIFF = 4096 + VPD-TAC-E - VPD-TAC-W
Entries 0
Mean 0
RMS 0

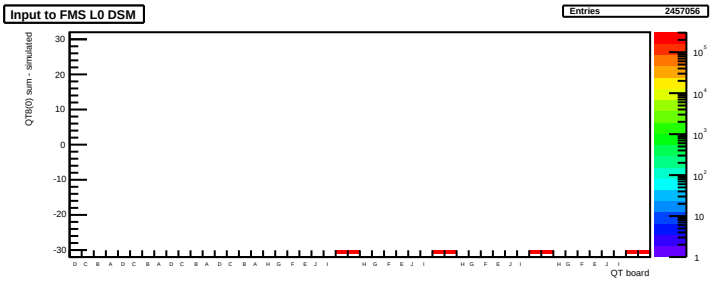

Input to QT1 crate (south-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

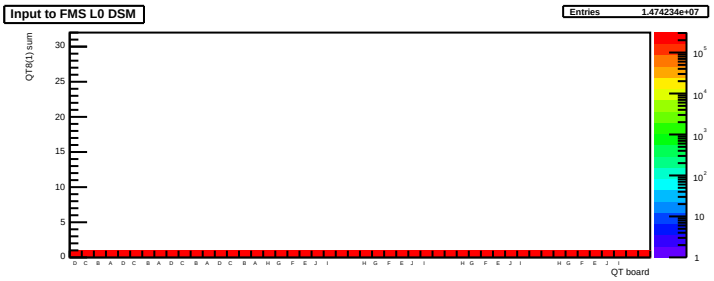

Input to QT3 crate (north-top)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

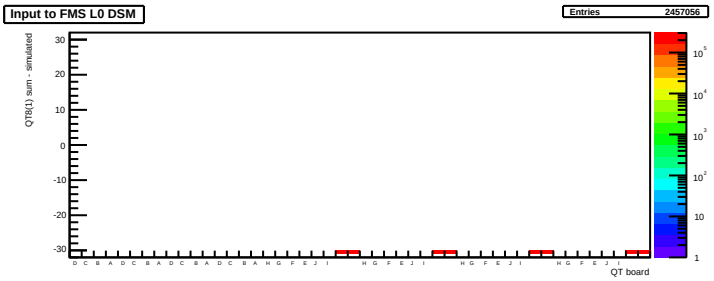

Input to QT2 crate (south-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

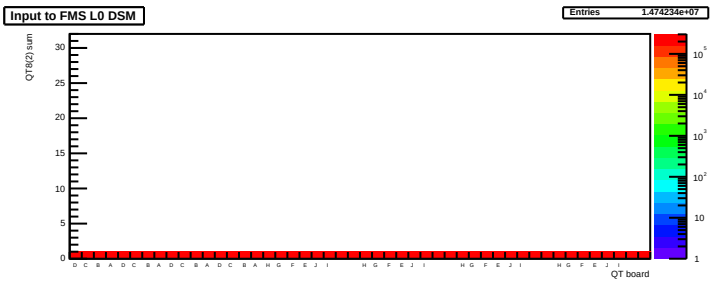

Input to QT4 crate (north-bottom)
Entries 0
ADC
4000
3500
3000
2500
2000
1500
1000
500
0
0 50 100 150 200 250 300 350
channel

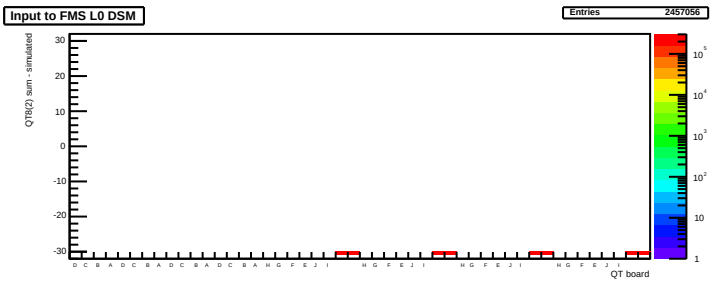

Input to FMS L0 DSM
Entries 1.474234e+07
QT8(0) sum
QT board

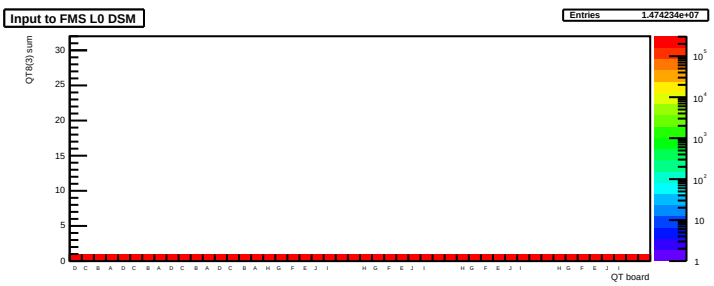

Input to FMS L0 DSM
Entries 2457056
QT8(0) sum - simulated
QT board

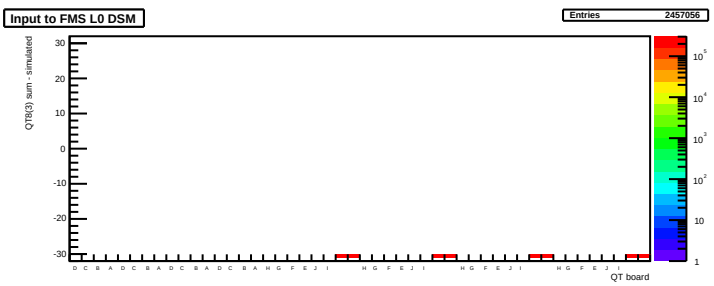

Input to FMS L0 DSM
Entries 1.474234e+07
QT8(1) sum
QT board

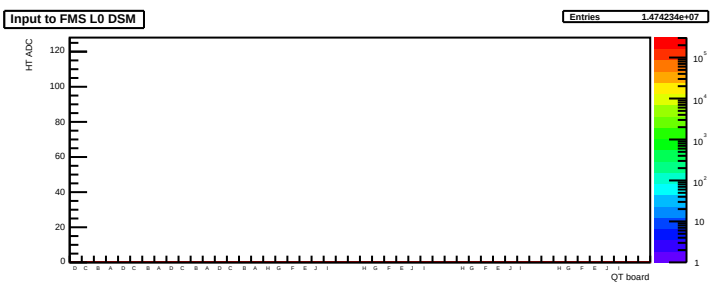

Input to FMS L0 DSM
Entries 2457056
QT8(1) sum - simulated
QT board

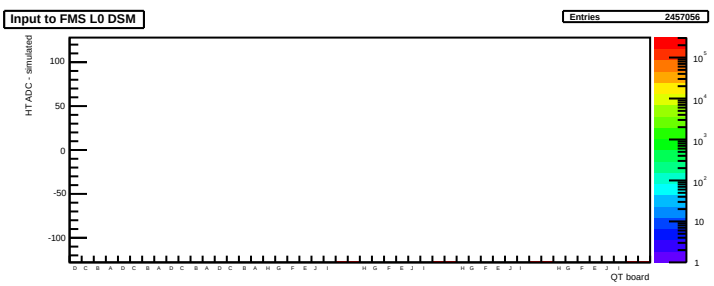

Input to FMS L0 DSM
Entries 1.474234e+07
QT8(2) sum
QT board

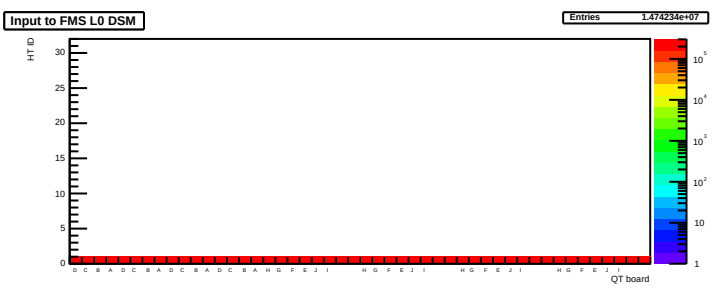

Input to FMS L0 DSM
Entries 2457056
QT8(2) sum - simulated
QT board

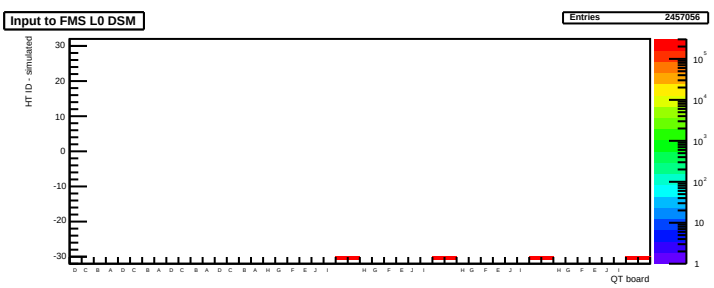

Input to FMS L0 DSM
Entries 1.474234e+07
QT8(3) sum
QT board

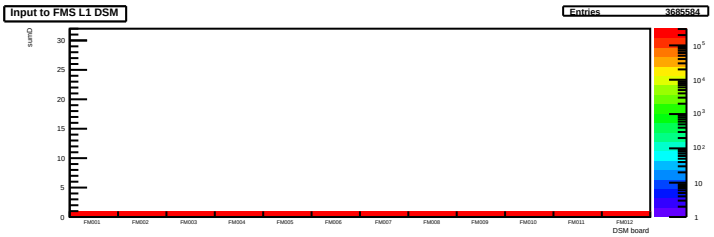

Input to FMS L0 DSM
Entries 2457056
QT8(3) sum - simulated
QT board

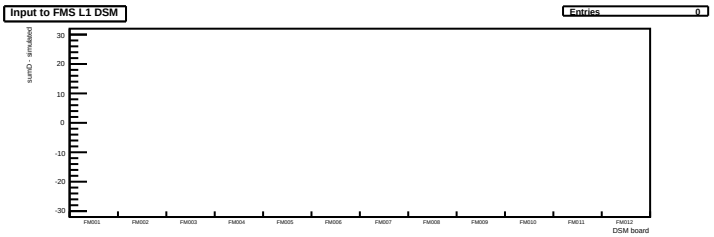

Input to FMS L0 DSM
Entries 1.474234e+07
HT ADC
QT board

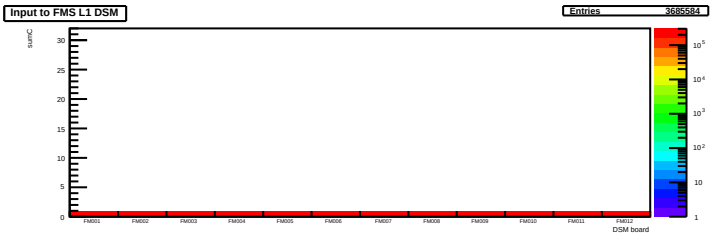

Input to FMS L0 DSM
Entries 2457056
HT ADC - simulated
QT board

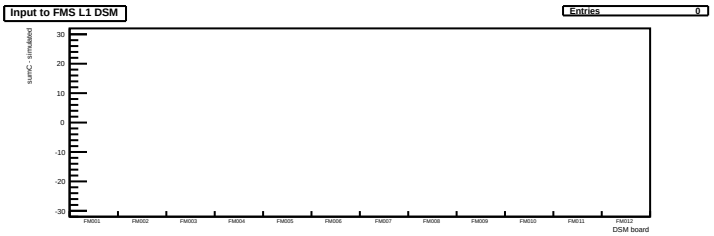

Input to FMS L0 DSM
Entries 1.474234e+07
HT ID
QT board

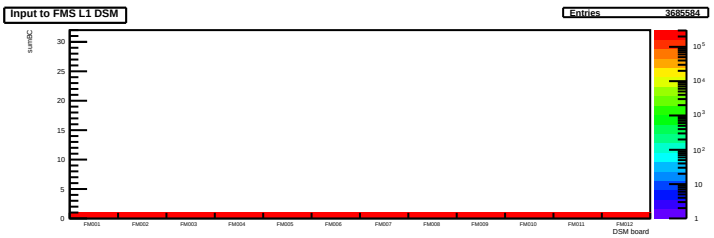

Input to FMS L0 DSM
Entries 2457056
HT ID - simulated
QT board

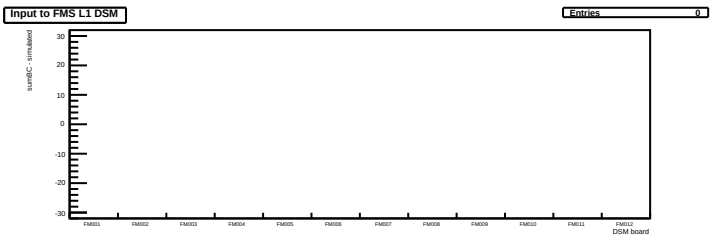

Input to FMS L1 DSM
Entries 3685584
sumD
DSM board

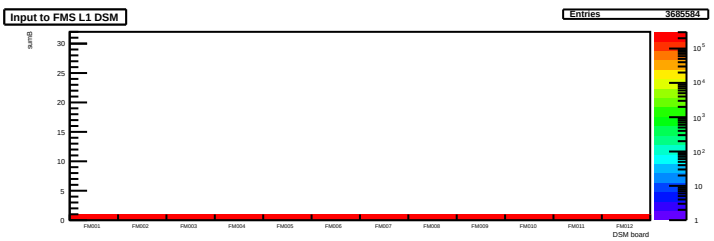

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

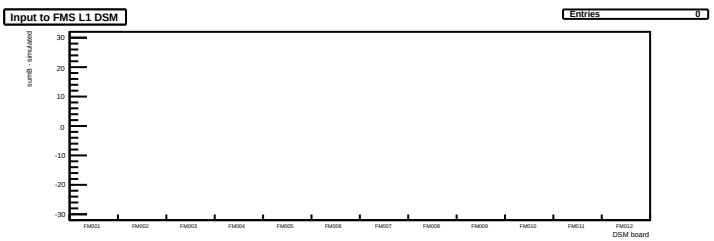

Input to FMS L1 DSM
Entries 3685584
sumC
DSM board

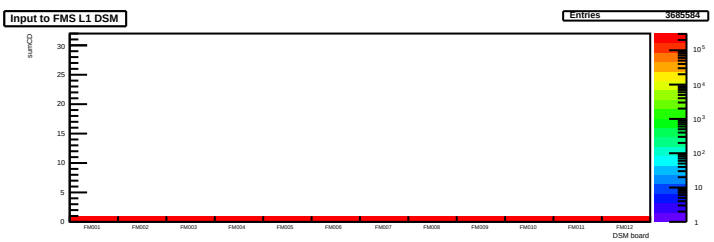

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

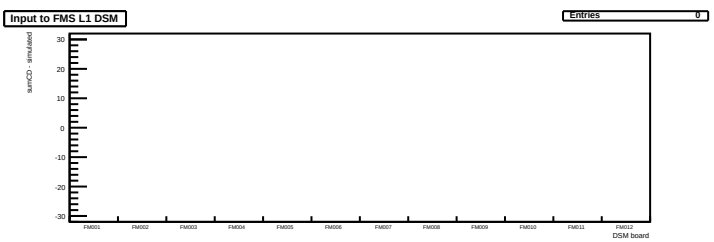

Input to FMS L1 DSM
Entries 3685584
sumBC
DSM board

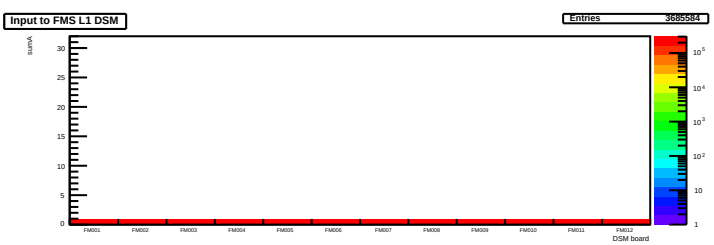

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

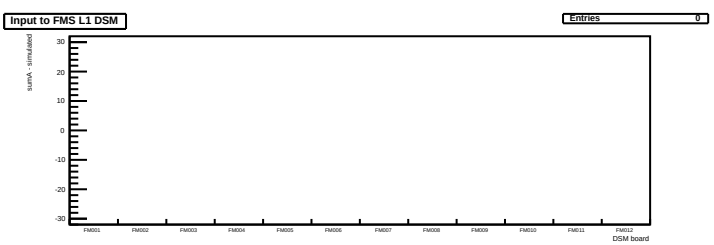

Input to FMS L1 DSM
Entries 3685584
sumB
DSM board

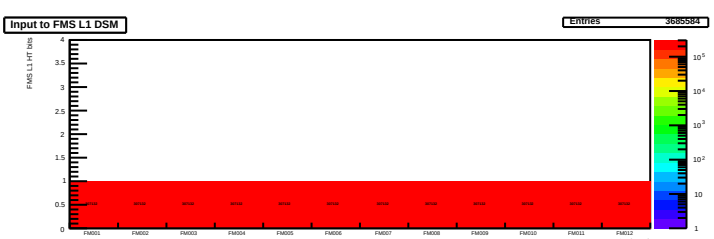

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

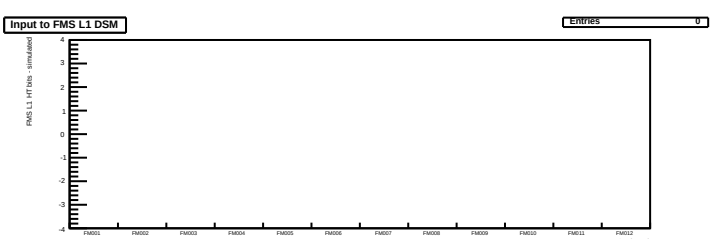

Input to FMS L1 DSM
Entries 3685584
sumCD
DSM board

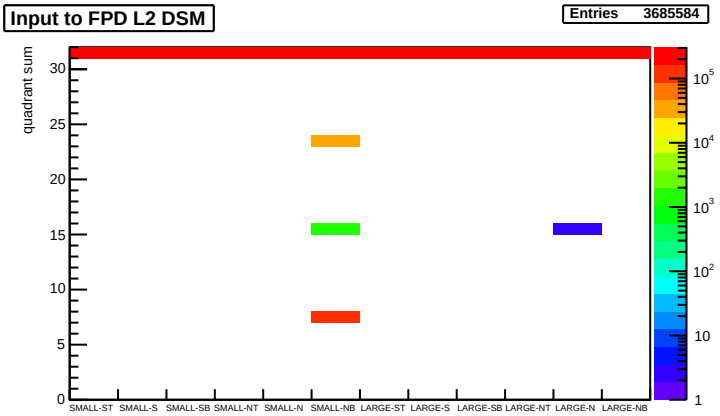

Input to FMS L1 DSM
Entries 0
sumCD - simulated
DSM board

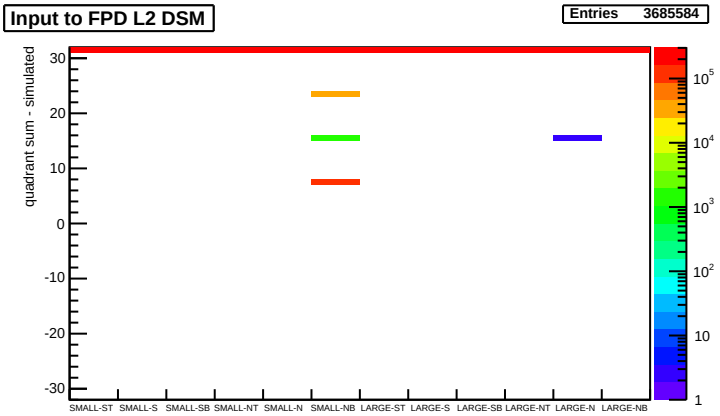

Input to FMS L1 DSM
Entries 3685584
sumA
DSM board

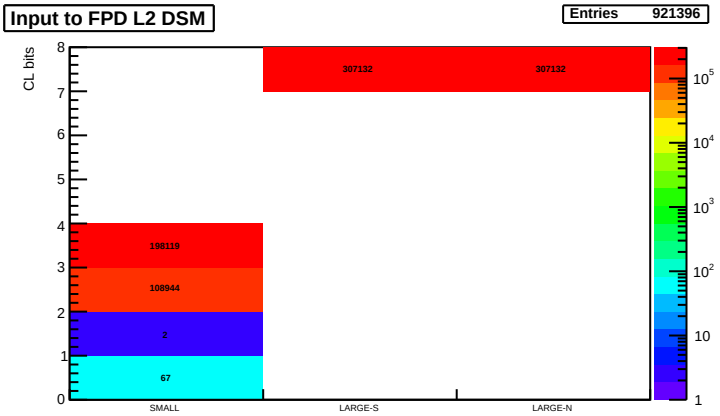

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

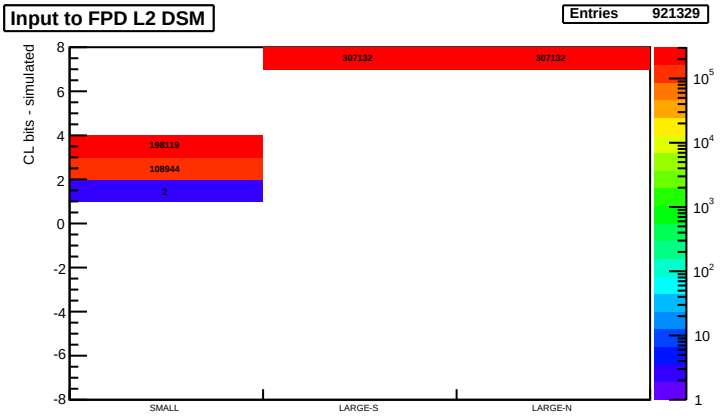

Input to FMS L1 DSM
Entries 3685584
FMS L1 HT bits
DSM board

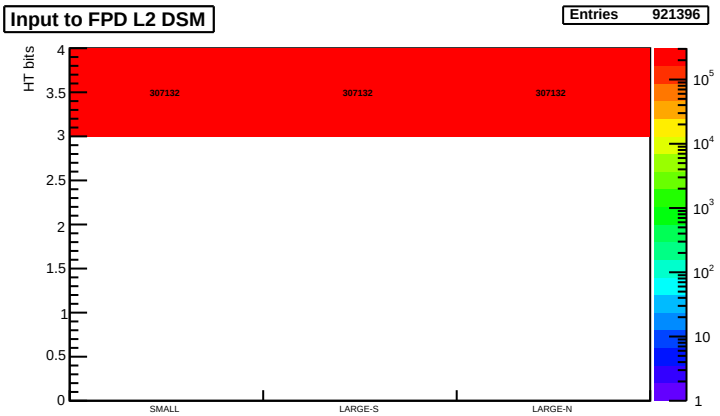

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

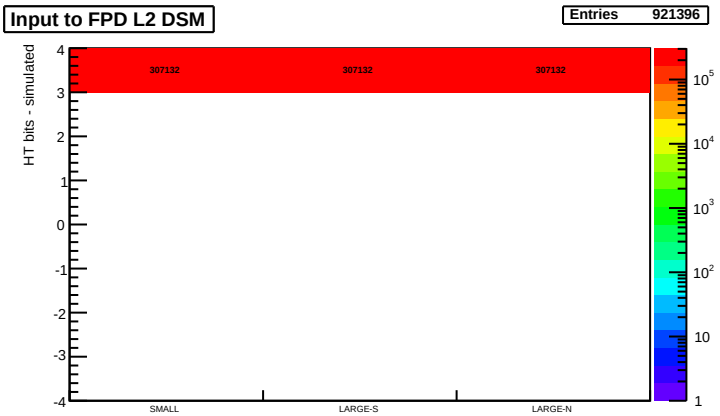

Input to FPD L2 DSM
Entries 3685584
quadrant sum
SMALL-ST SMALL-S SMALL-SB SMALL-NT SMALL-N SMALL-NB LARGE-ST LARGE-S LARGE-SB LARGE-NT LARGE-N LARGE-NB

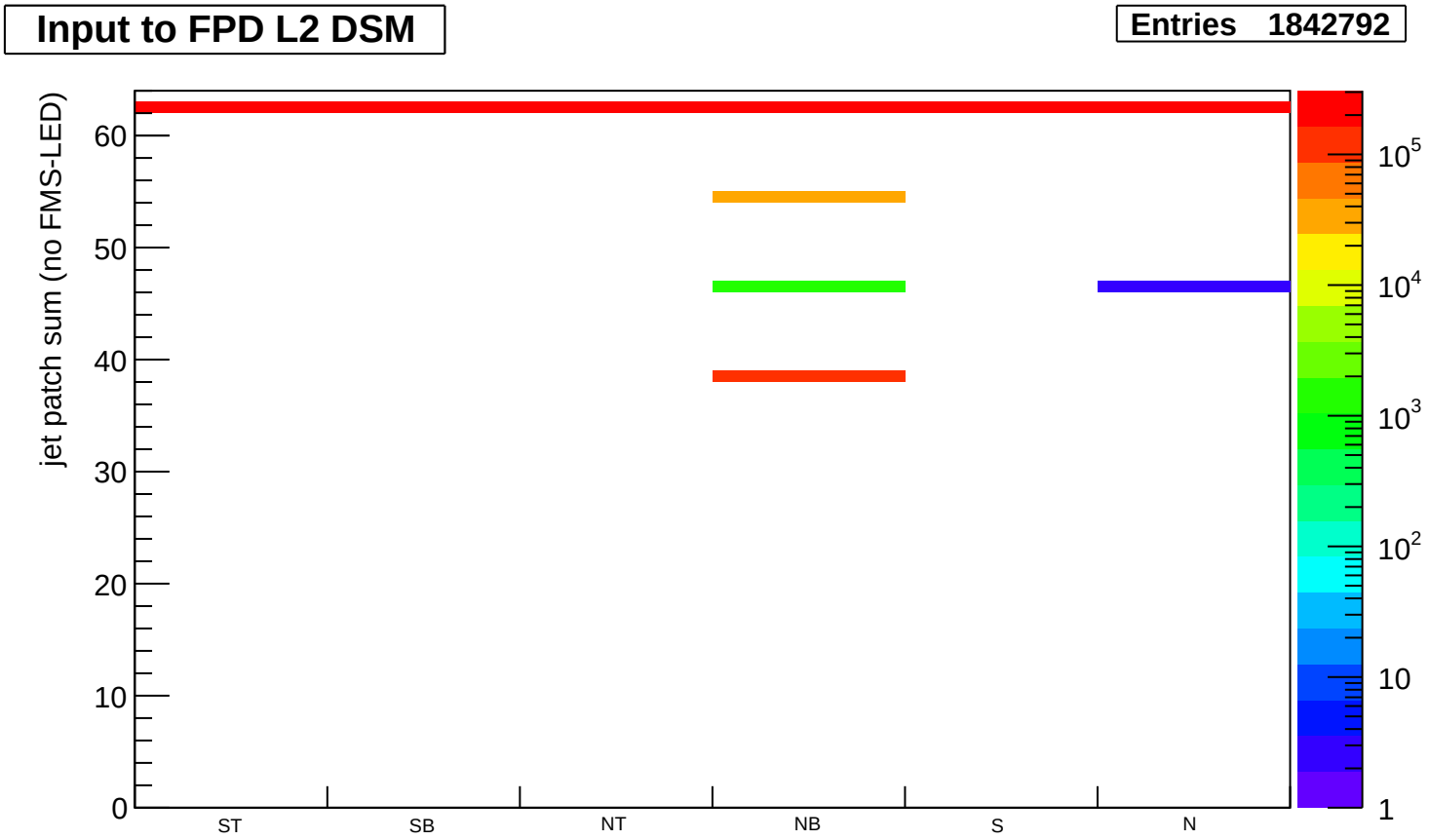

Input to FPD L2 DSM
Entries 3685584
quadrant sum - simulated
SMALL-ST SMALL-S SMALL-SB SMALL-NT SMALL-N SMALL-NB LARGE-ST LARGE-S LARGE-SB LARGE-NT LARGE-N LARGE-NB

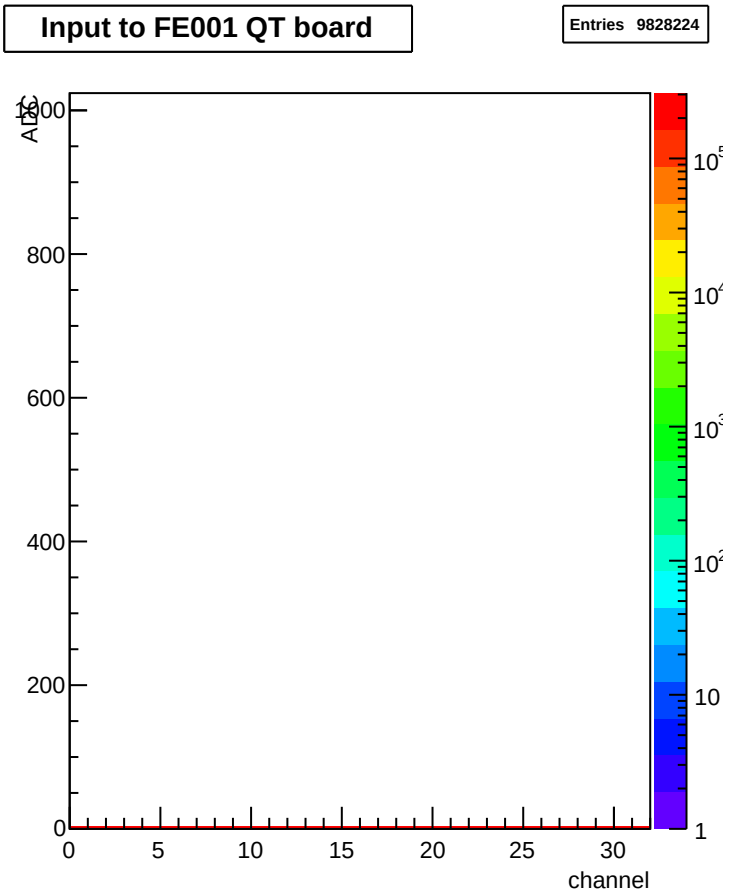

Input to FPD L2 DSM
Entries 921396
CL bits
307132
307132
198119
108944
2
67
SMALL
LARGE-S
LARGE-N

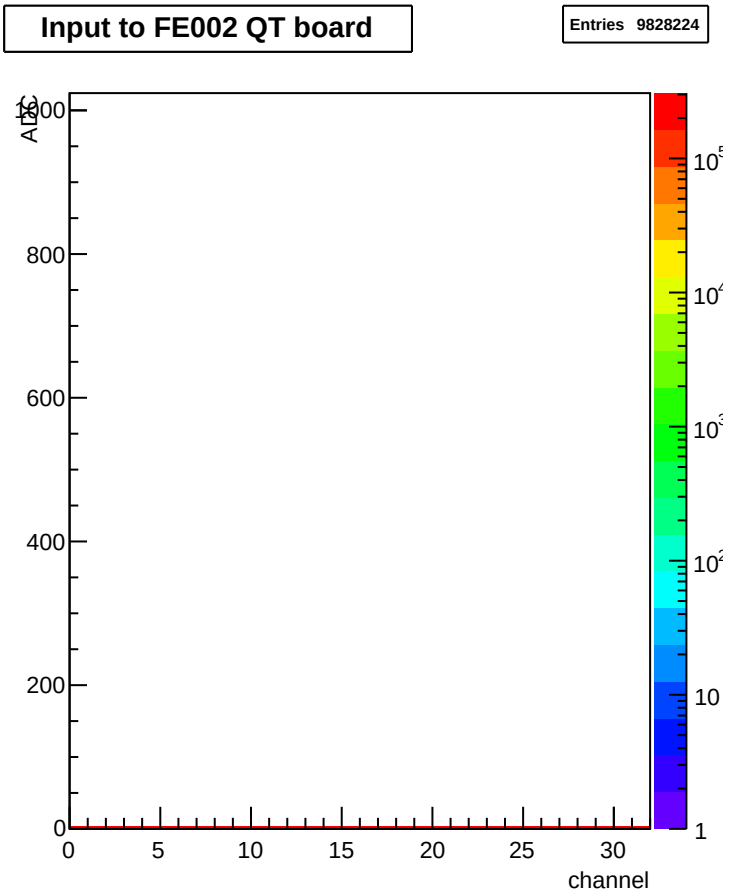

Input to FPD L2 DSM
Entries 921329
CL bits - simulated
307132
307132
198119
108944
2
SMALL
LARGE-S
LARGE-N

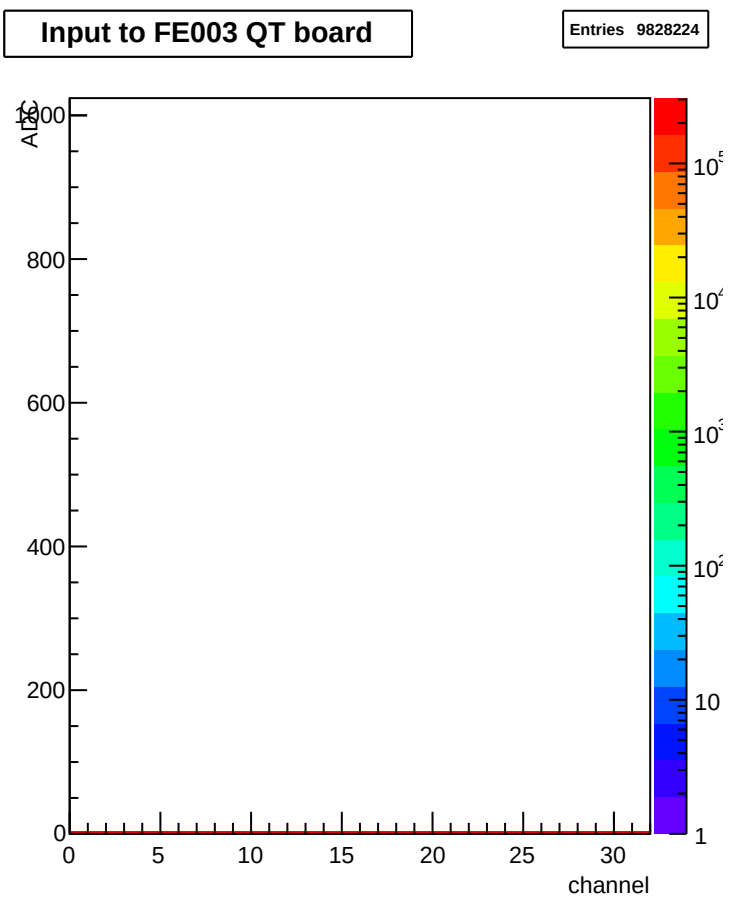

Input to FPD L2 DSM
Entries 921396
HT bits
307132
307132
307132
SMALL
LARGE-S
LARGE-N

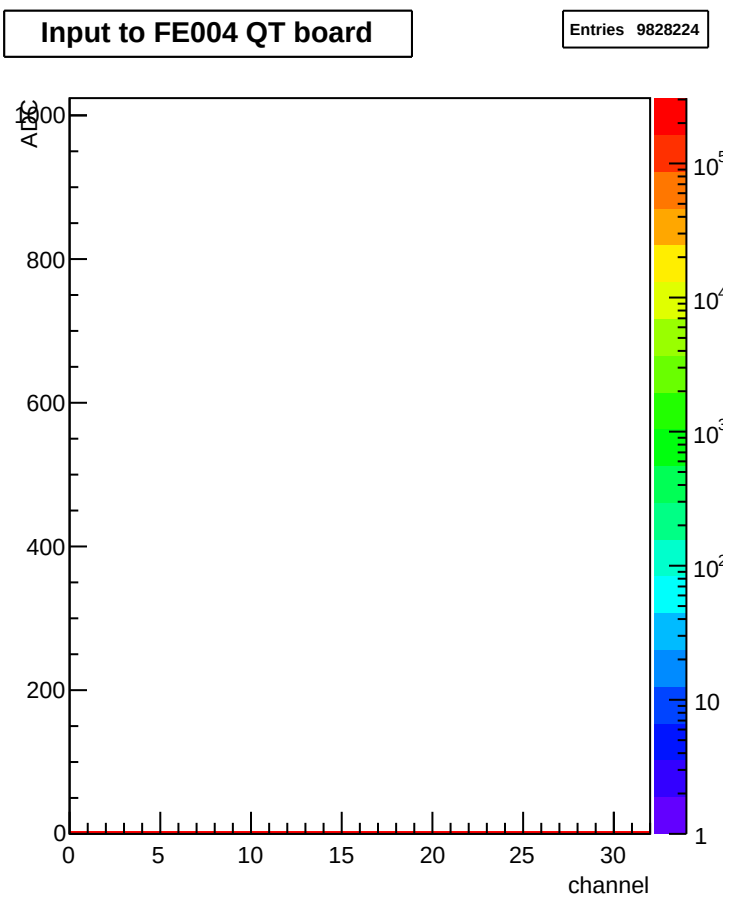

Input to FPD L2 DSM
Entries 921396
HT bits - simulated
307132
307132
307132
SMALL
LARGE-S
LARGE-N

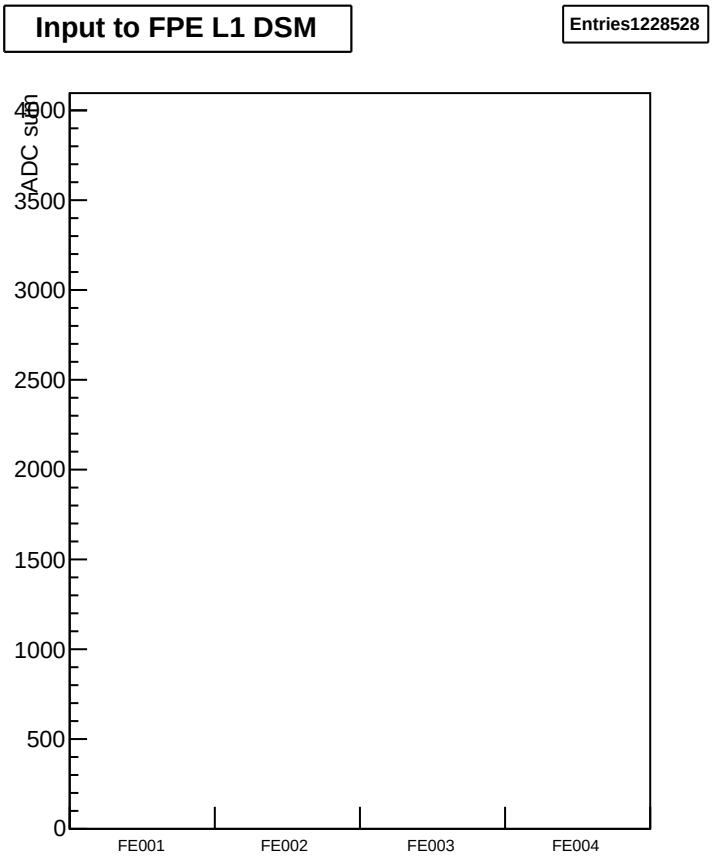

Input to FPD L2 DSM
Entries 1842792
jet patch sum (no FMS-LED)
0
10
20
30
40
50
60
ST
SB
NT
NB
S
N
1
10
10^2
10^3
10^4
10^5

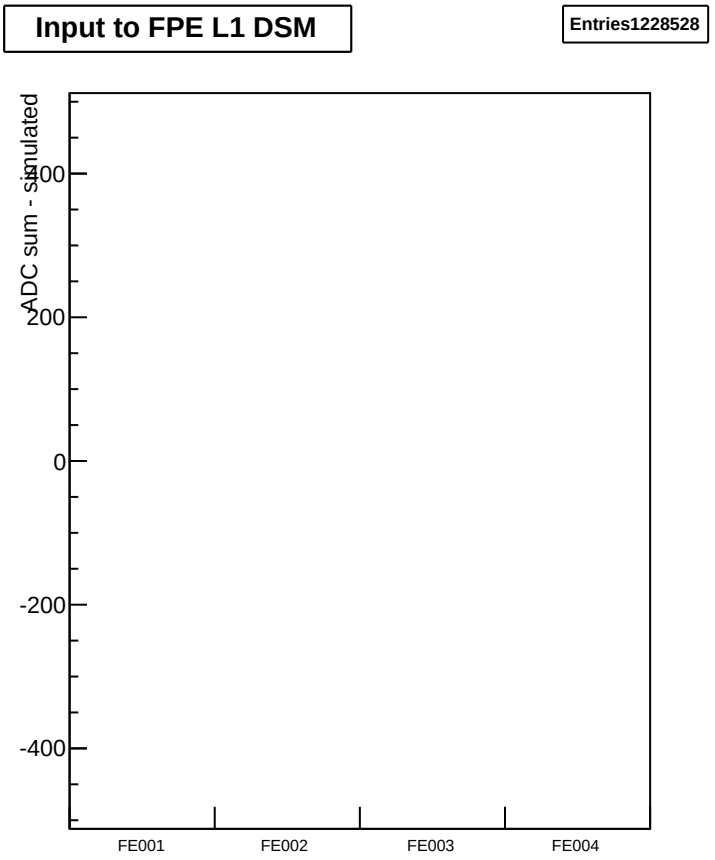

Input to FE001 QT board
Entries 9828224
ADC
channel

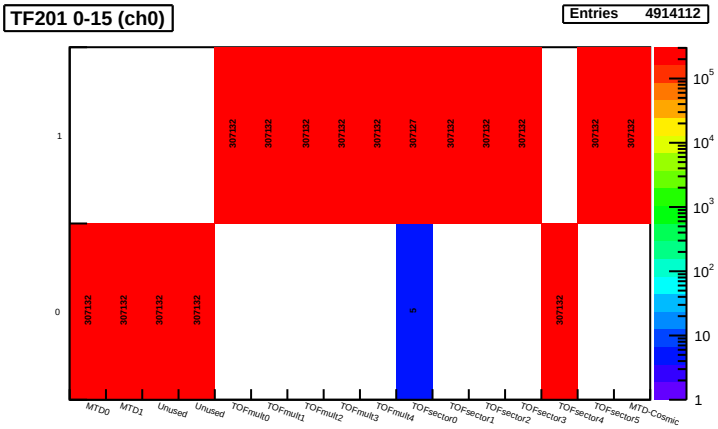

Input to FE002 QT board
Entries 9828224
ADC
channel

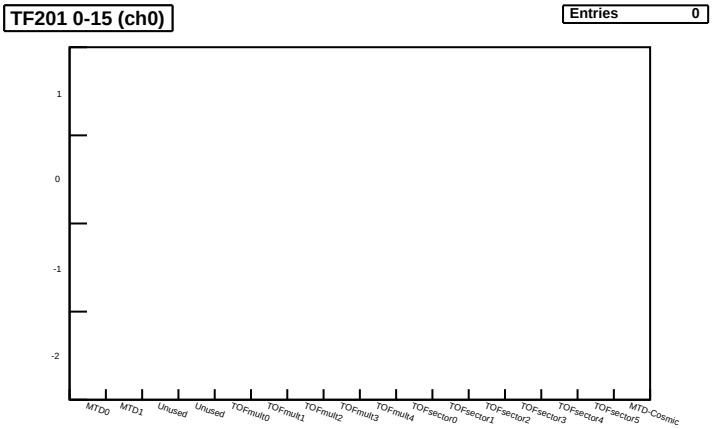

Input to FE003 QT board
Entries 9828224
ADC
channel

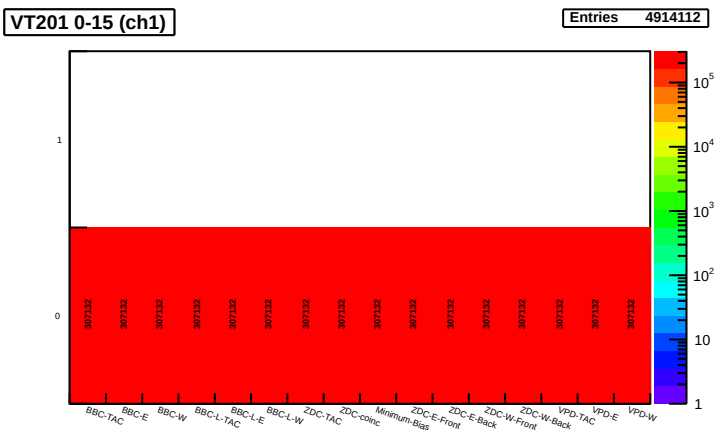

Input to FE004 QT board
Entries 9828224
ADC
channel

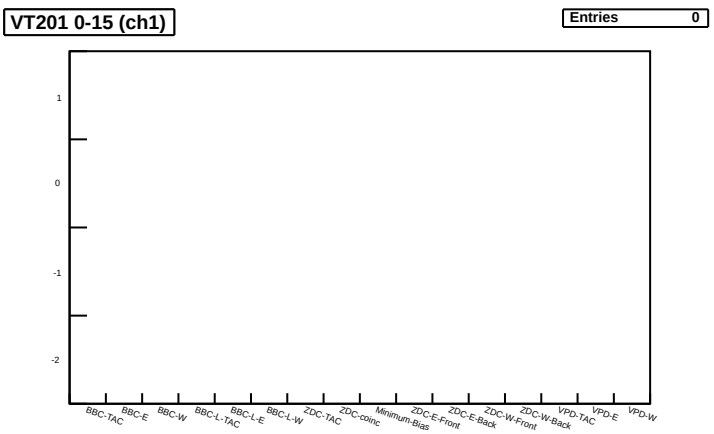

Input to FPE L1 DSM
Entries1228528
ADC sum
4000
3500
3000
2500
2000
1500
1000
500
0
FE001
FE002
FE003
FE004

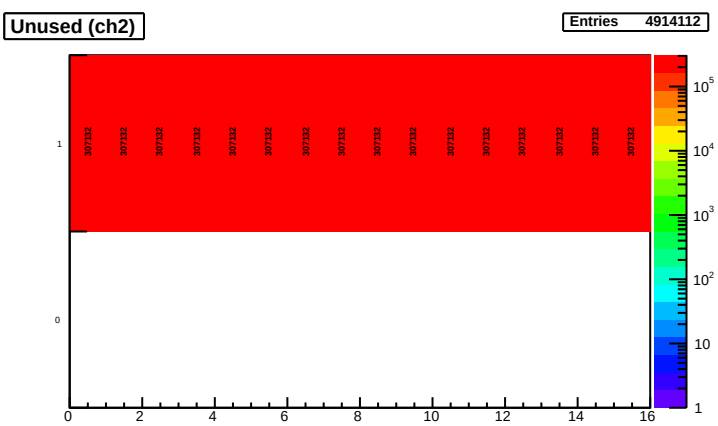

Input to FPE L1 DSM
Entries1228528
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004

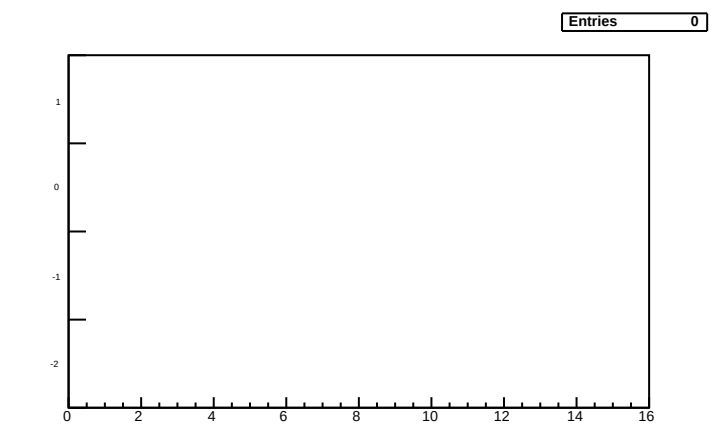

TF201 0-15 (ch0)
Entries 4914112
MTD0 MTD1 Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

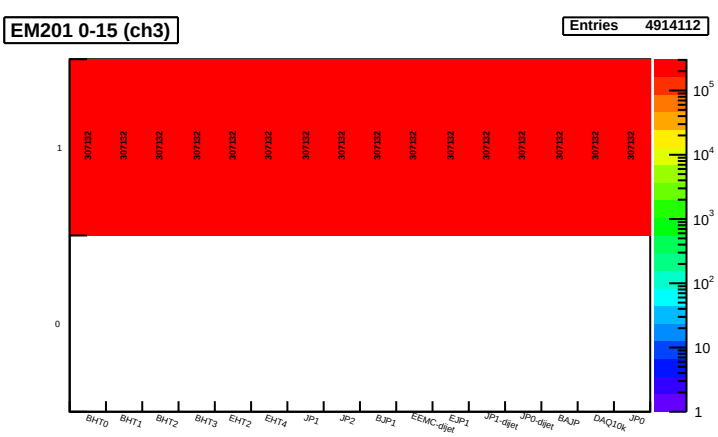

TF201 0-15 (ch0)
Entries 0
MTD0 MTD1 Unused Unused TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5 MTD-Cosmic

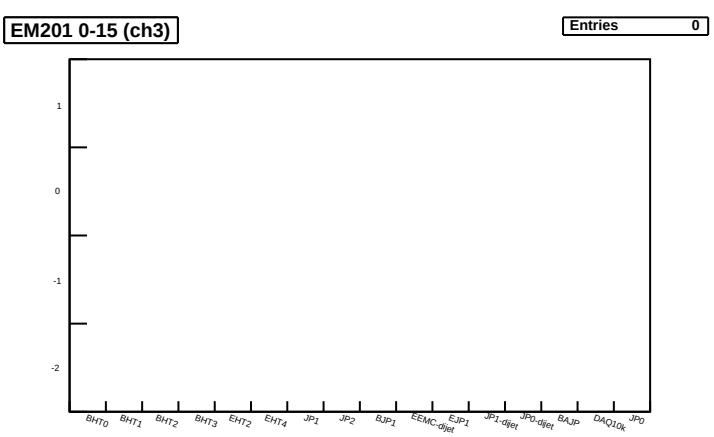

VT201 0-15 (ch1)
Entries 4914112
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-coinc Minimum-Bias ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W


VT201 0-15 (ch1)
Entries 0
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-coinc Minimum-Bias ZDC-E-Front ZDC-E-Back ZDC-W-Front ZDC-W-Back VPD-TAC VPD-E VPD-W

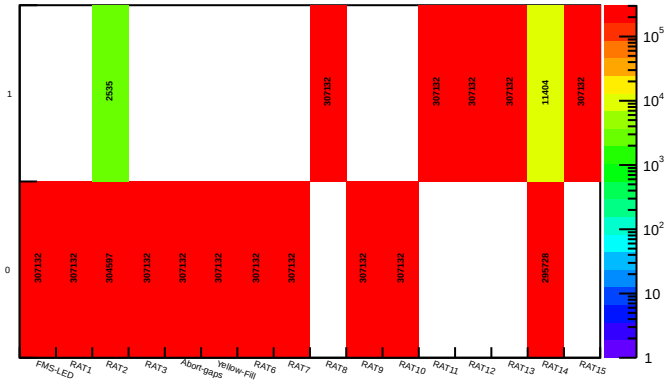

Unused (ch2)
Entries 4914112

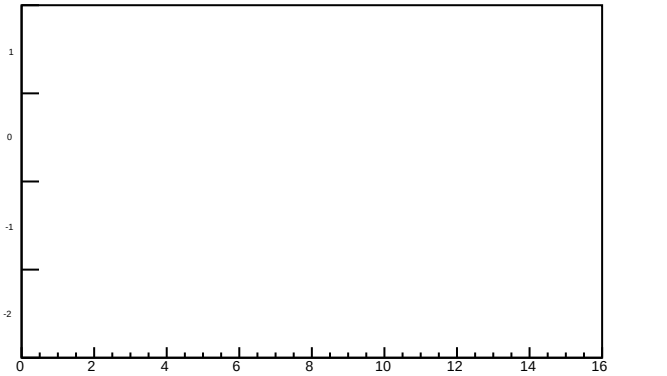

Entries 0


EM201 0-15 (ch3)
Entries 4914112
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 EEMC-diJet EJP1 JP1-diJet JP0-diJet BAJP DAQ10k JP0

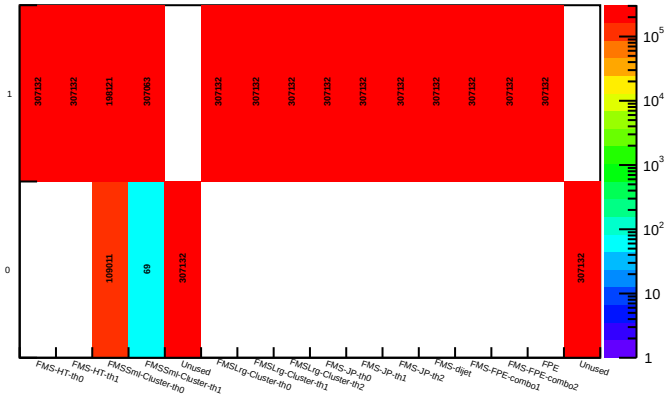

EM201 0-15 (ch3)
Entries 0
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 EEMC-diJet EJP1 JP1-diJet JP0-diJet BAJP DAQ10k JP0

RAT board (ch4)

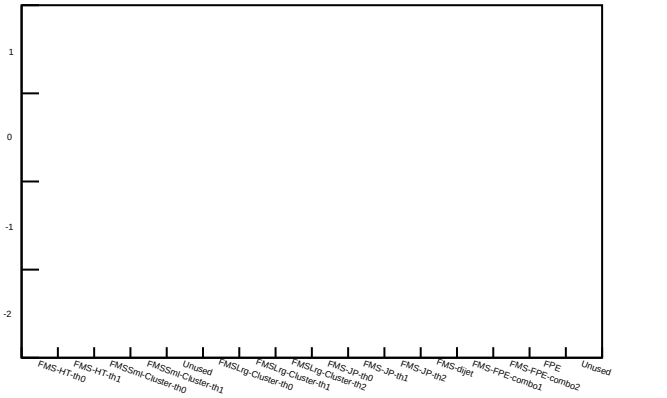




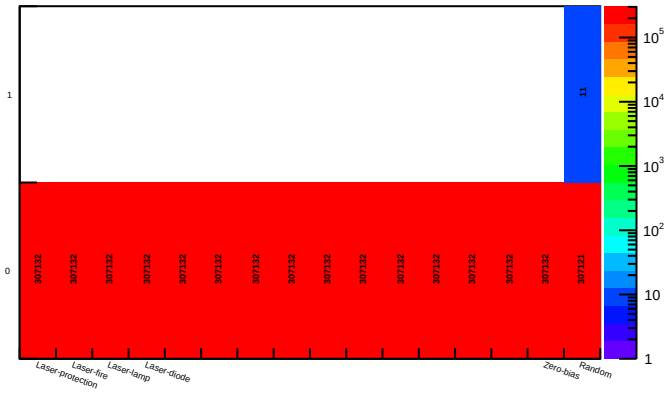
FP201 0-15 (ch5)

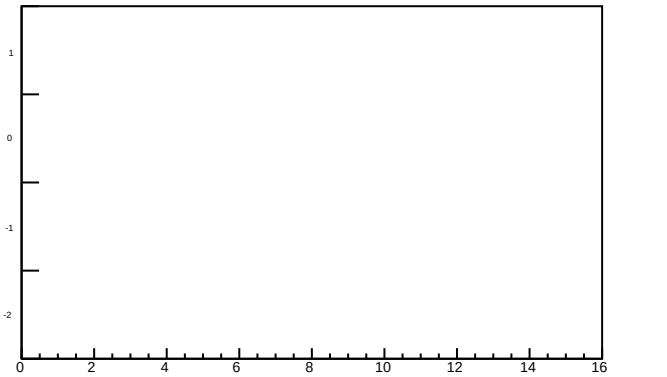
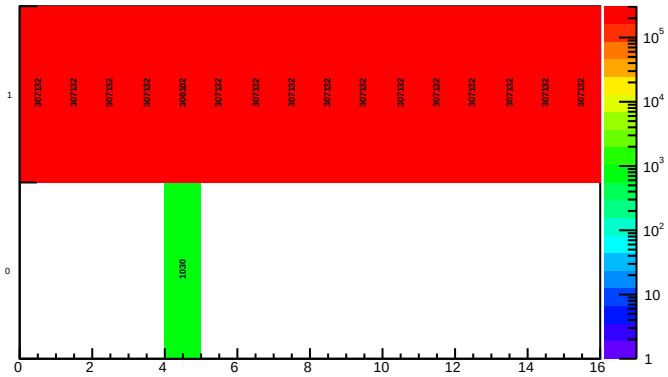
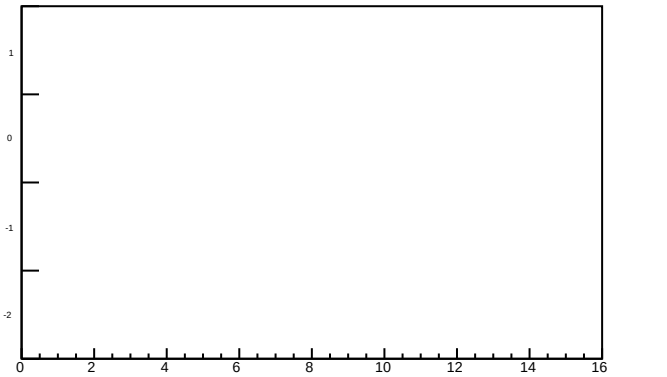


FP201 0-15 (ch5)



ST201 0-15 (ch6)





Unused (ch7)