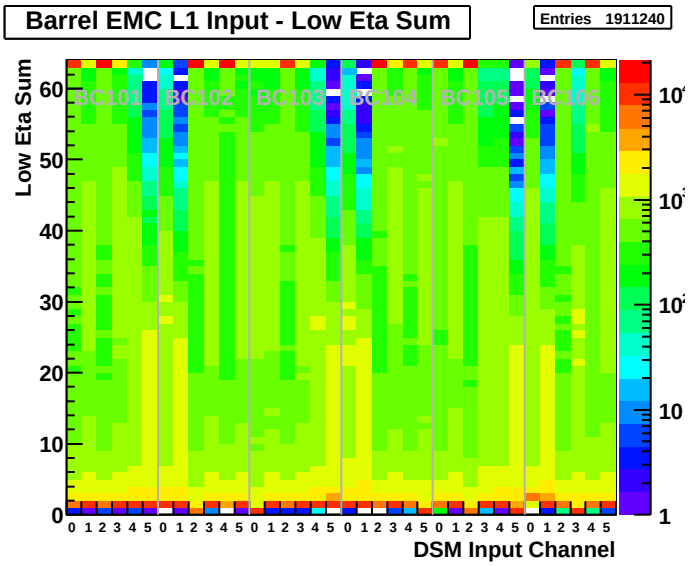
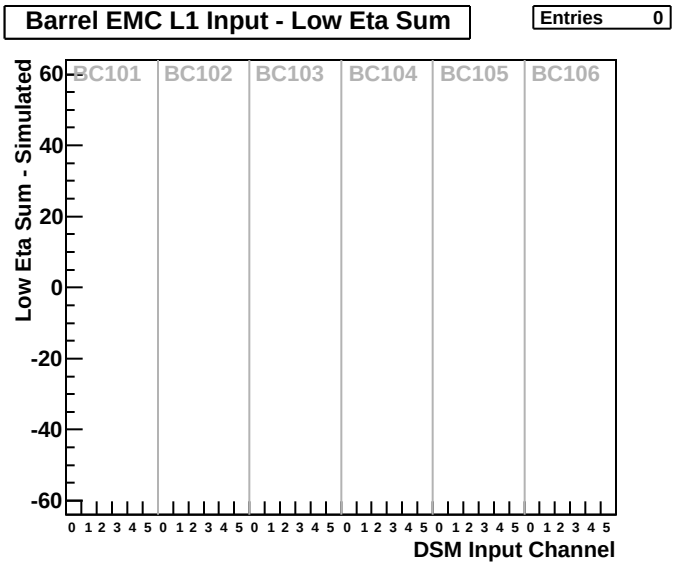
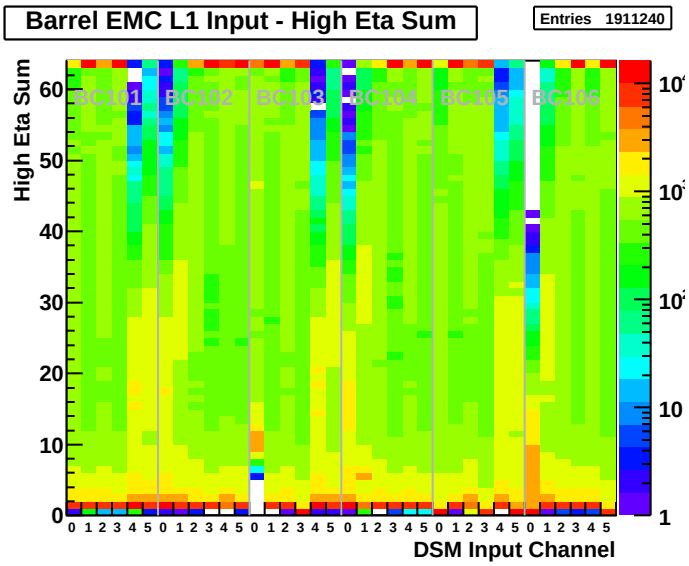
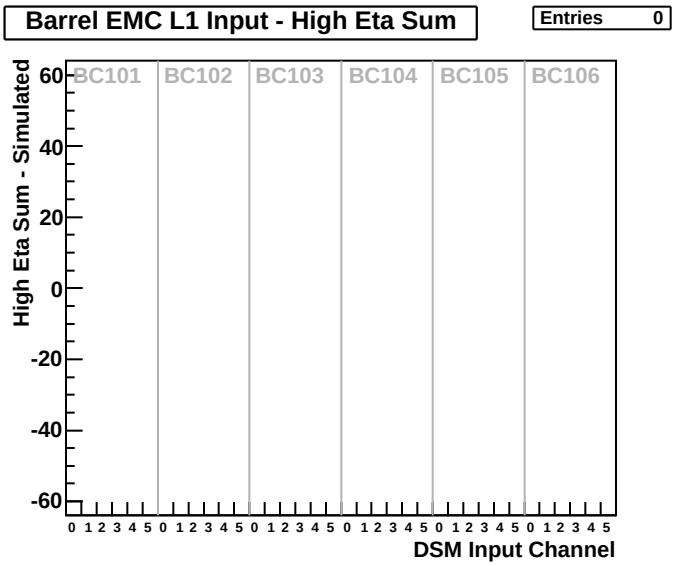
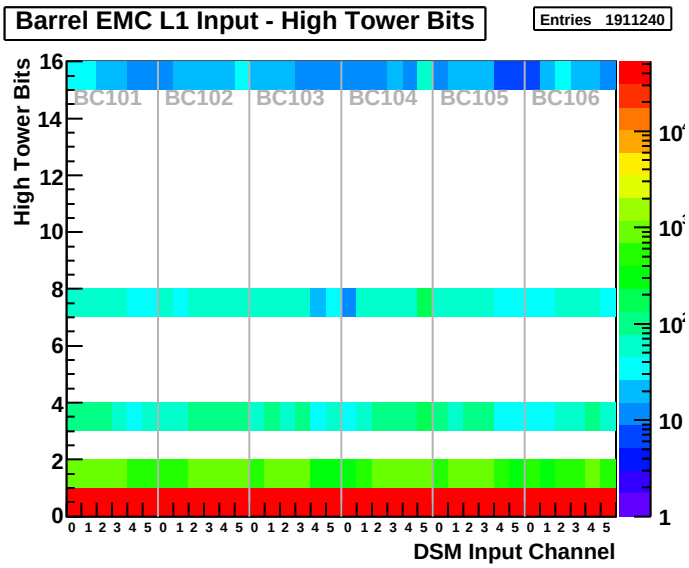
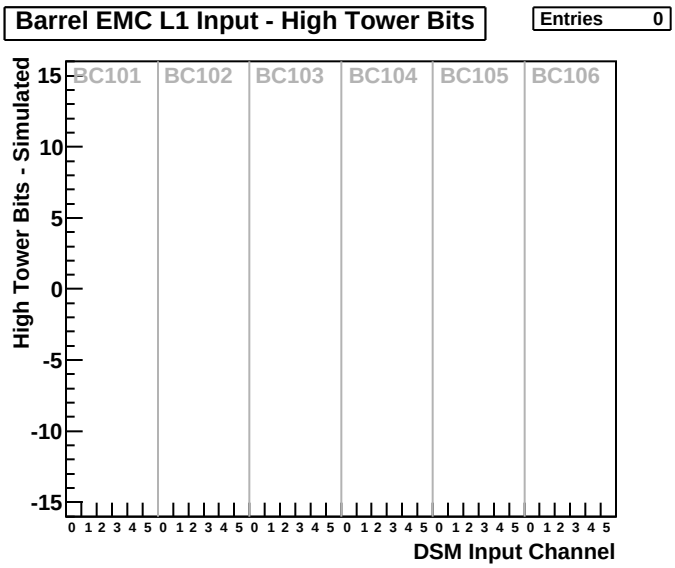

Barrel EMC L1 Input - Low Eta Sum
Entries 1911240
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

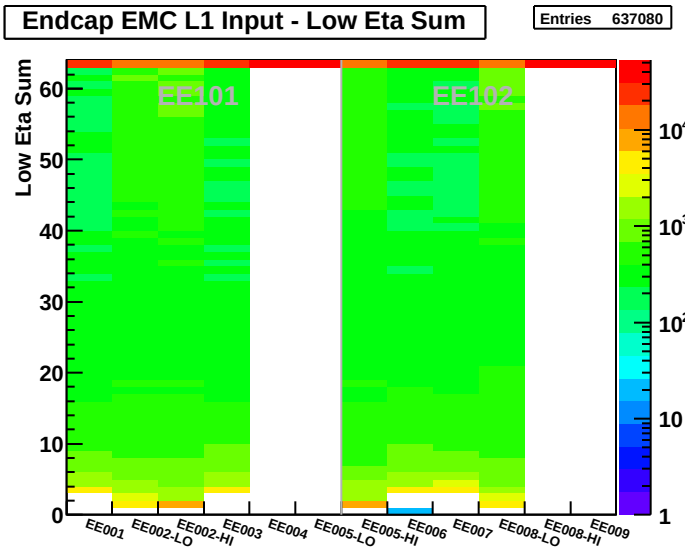

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

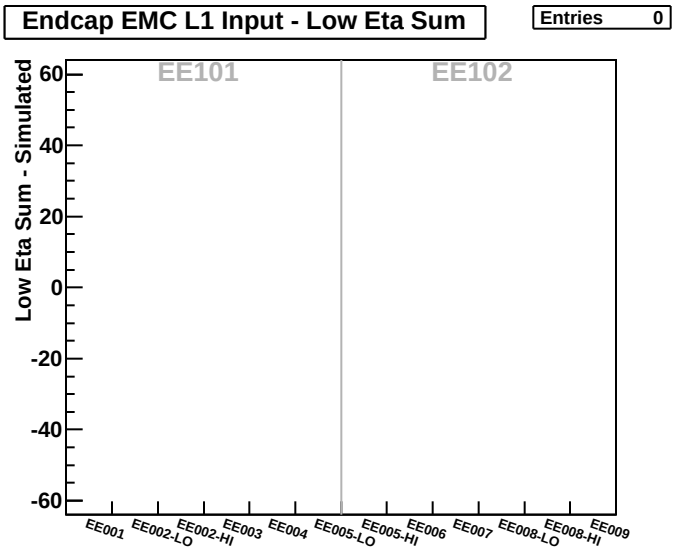

Barrel EMC L1 Input - High Eta Sum
Entries 1911240
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

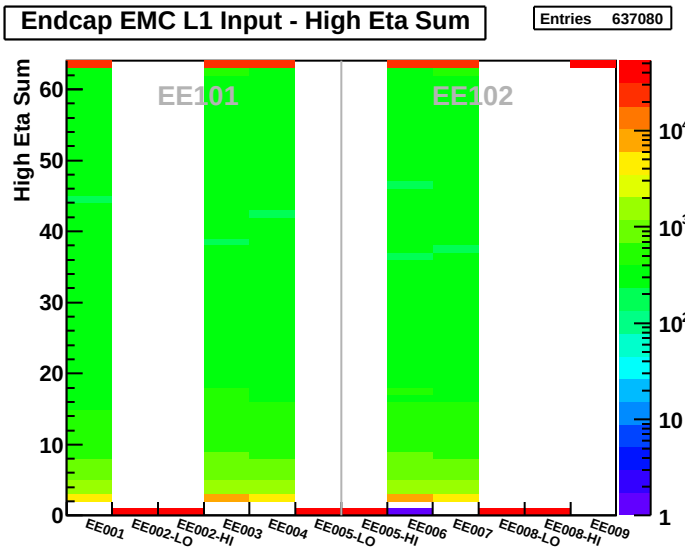

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

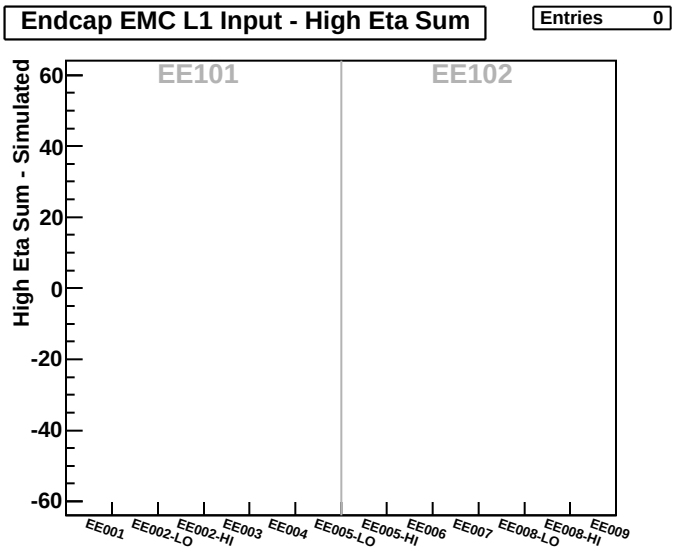

Barrel EMC L1 Input - High Tower Bits
Entries 1911240
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

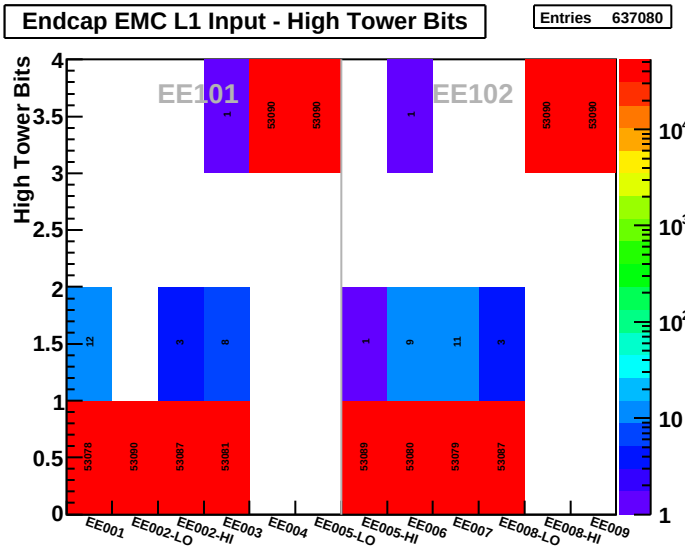

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

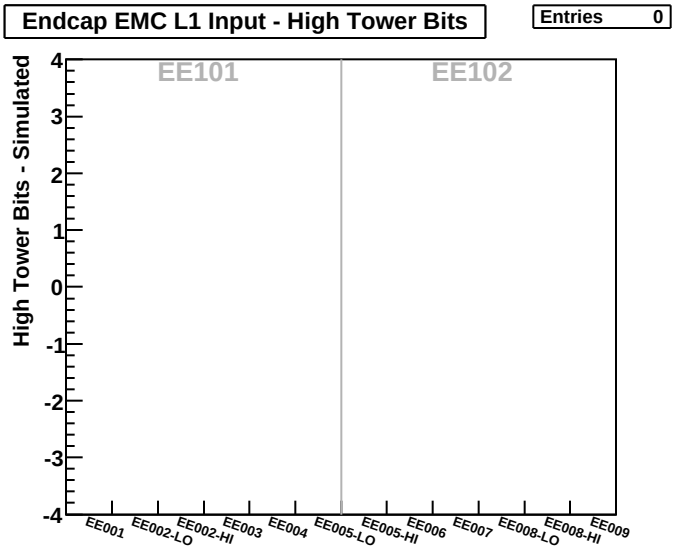

Endcap EMC L1 Input - Low Eta Sum
Entries 637080
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

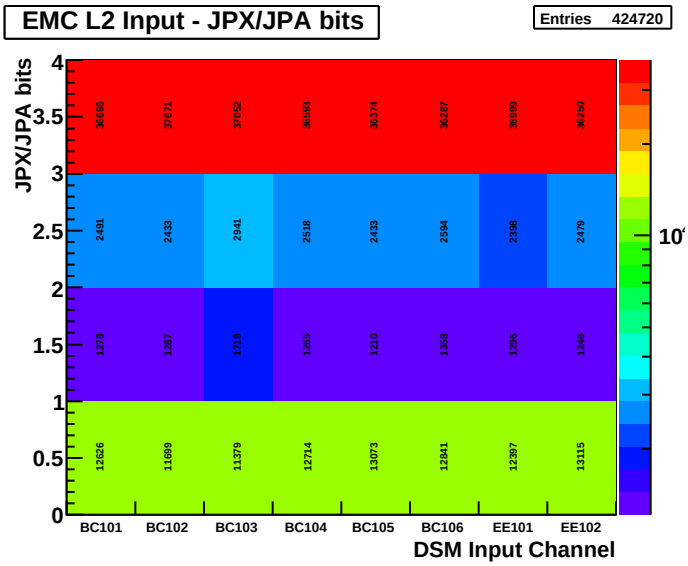

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

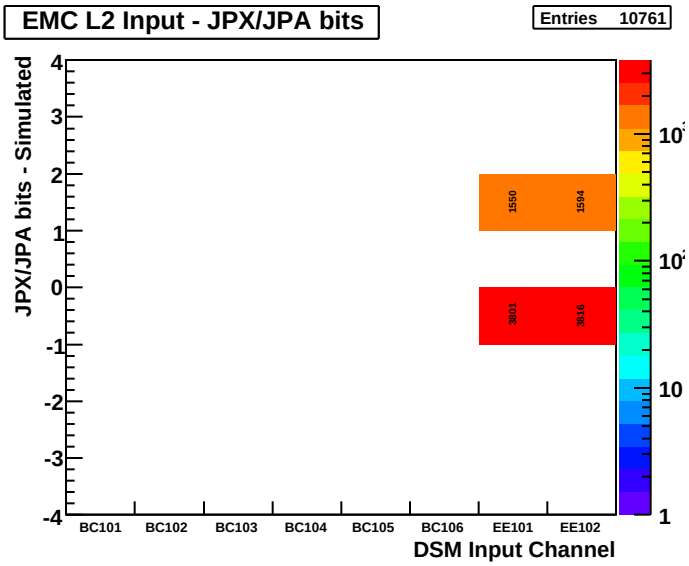

Endcap EMC L1 Input - High Eta Sum
Entries 637080
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

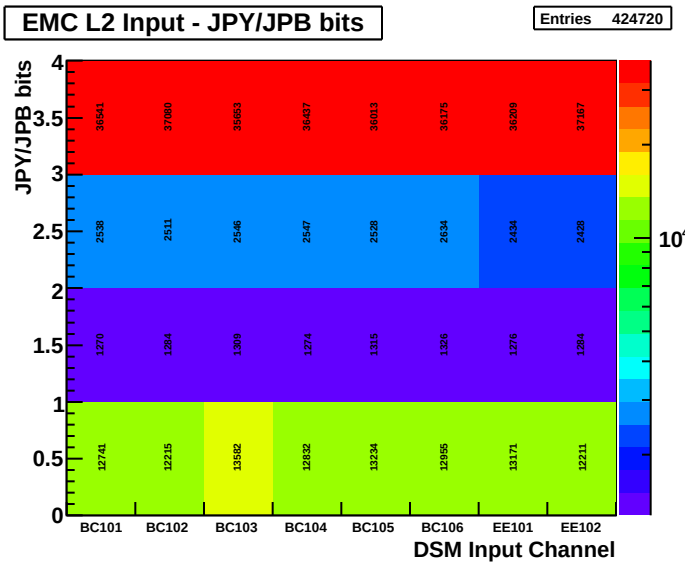

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

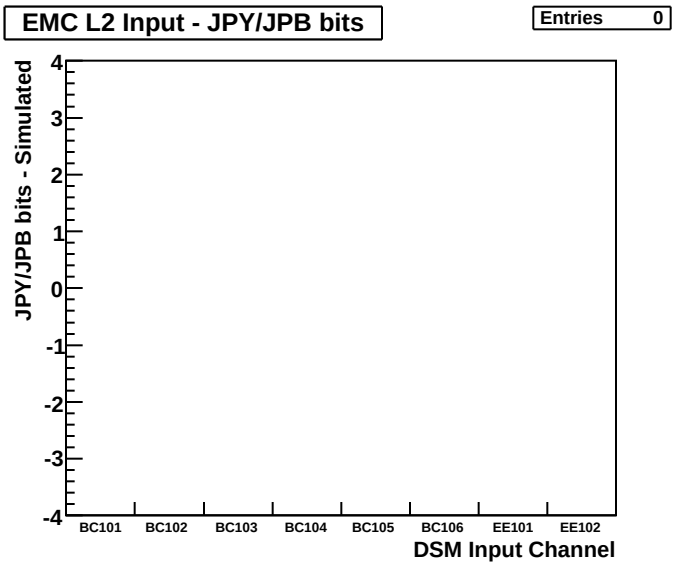

Endcap EMC L1 Input - High Tower Bits
Entries 637080
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

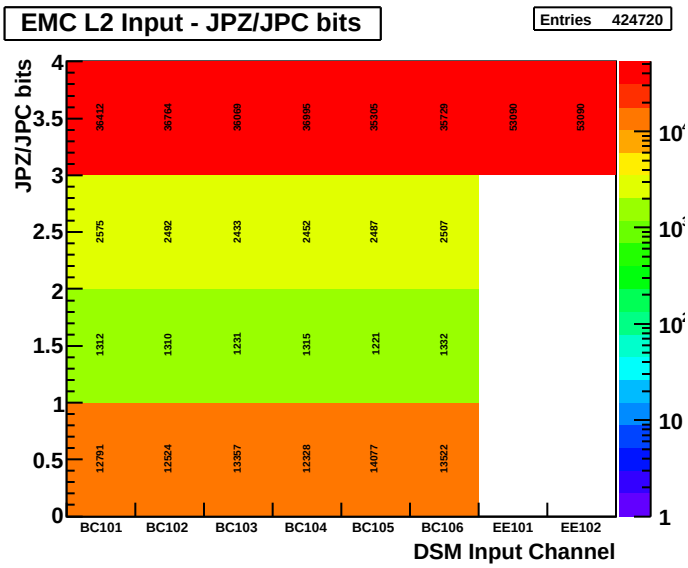

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

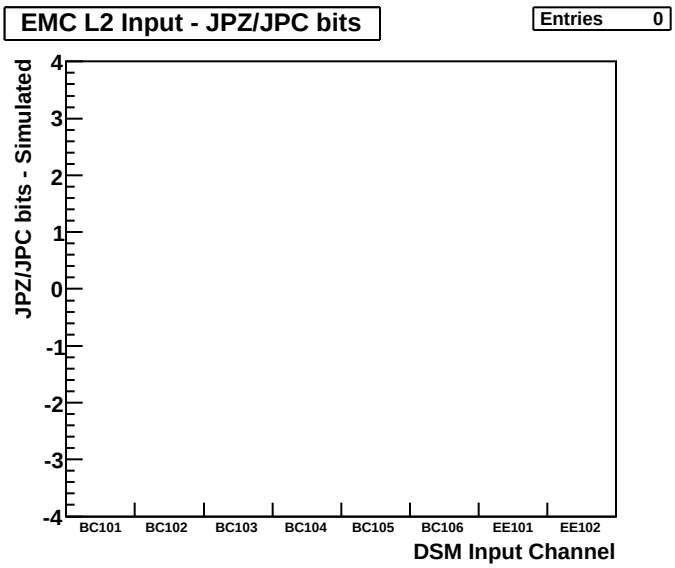

EMC L2 Input - JPX/JPA bits
Entries 424720
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
36695 37671 37052 36593 36374 36287 36999 36250
2491 2433 2941 2518 2433 2594 2398 2479
1276 1287 1718 1265 1210 1368 1296 1246
12626 11699 11379 12714 13073 12841 12397 13115

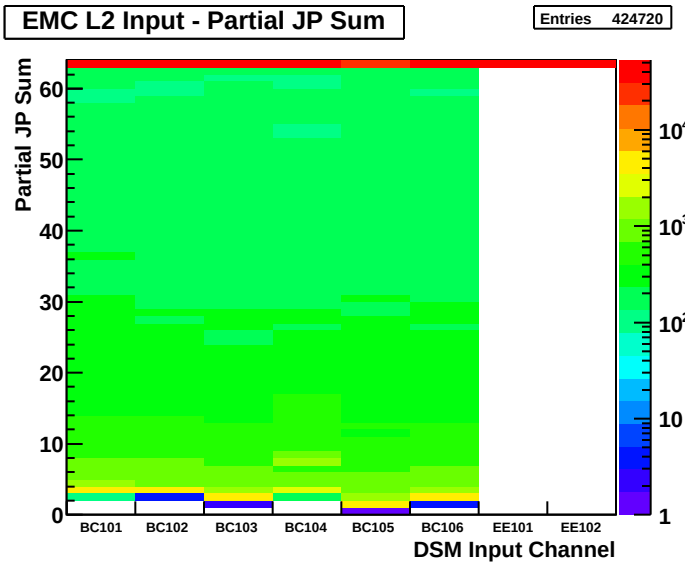

EMC L2 Input - JPX/JPA bits
Entries 10761
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1550 1594
3801 3816

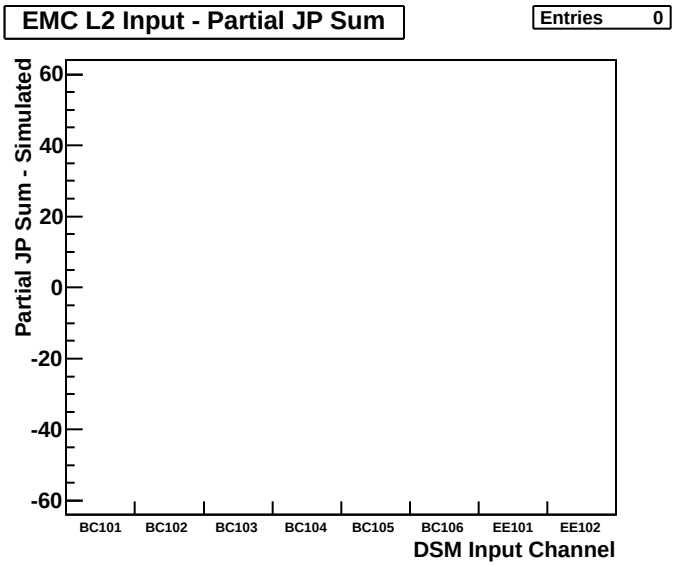

EMC L2 Input - JPY/JPB bits
Entries 424720
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
36541 37060 35663 36437 36013 36175 36209 37167
2538 2511 2546 2547 2528 2634 2434 2428
1270 1284 1309 1274 1315 1326 1276 1284
12741 12215 13582 12832 13234 12955 13171 12211

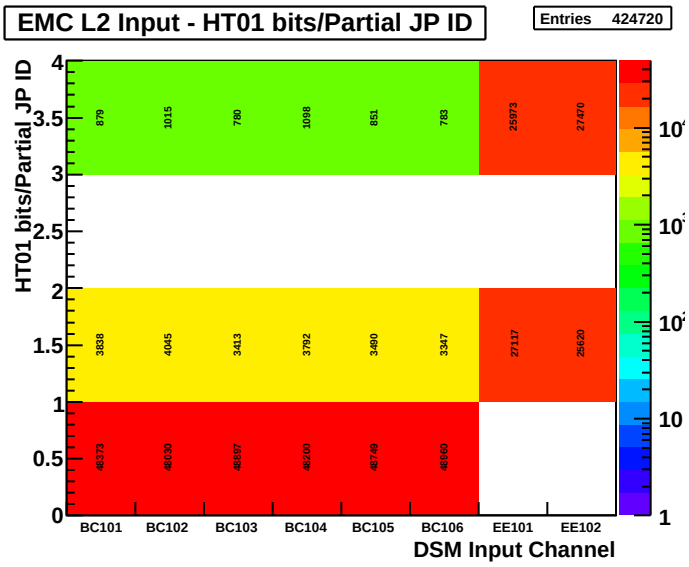

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

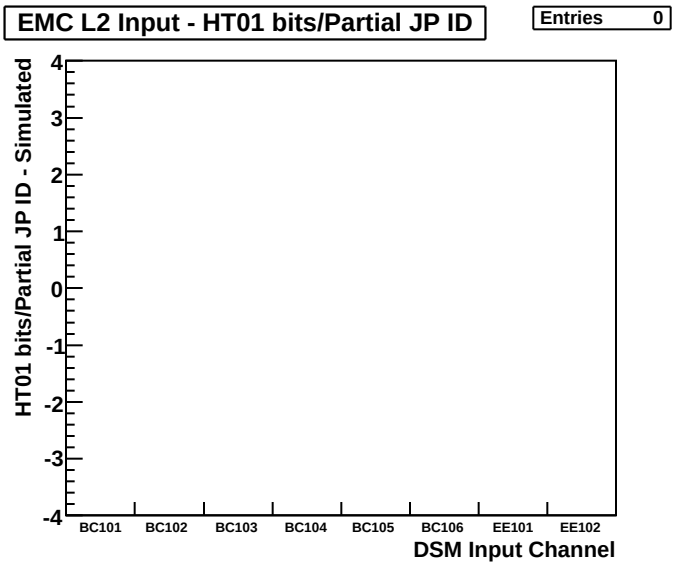

EMC L2 Input - JPZ/JPC bits
Entries 424720
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
36412 36764 36069 36995 35305 35729 53090 53090
2575 2492 2433 2452 2487 2507
1312 1310 1231 1315 1221 1332
12791 12824 13357 12328 14077 13522

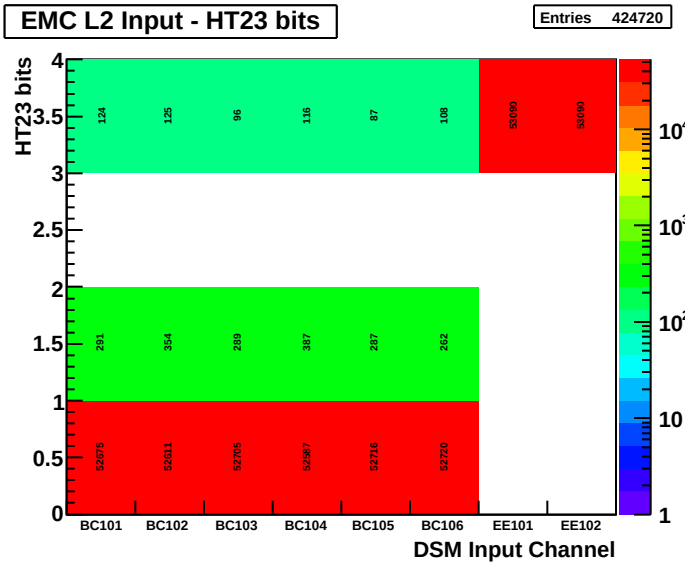

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

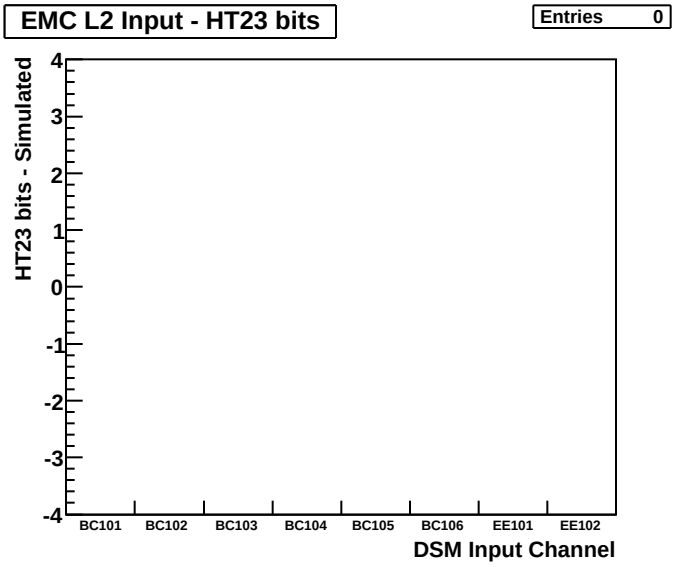

EMC L2 Input - Partial JP Sum
Entries 424720
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

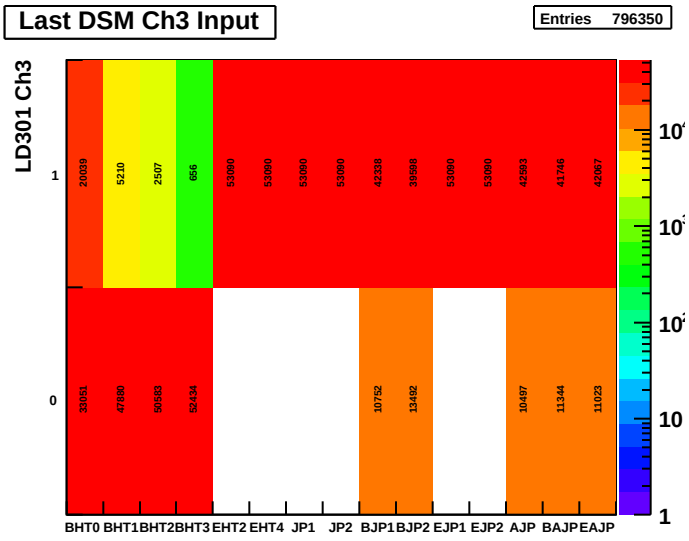

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

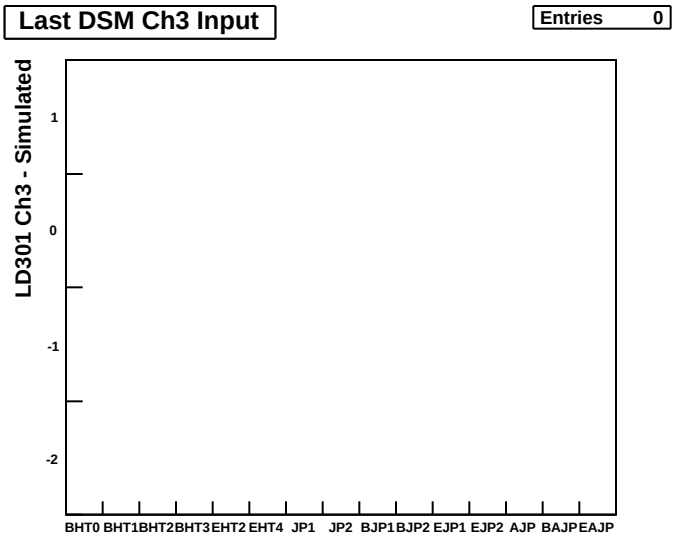

EMC L2 Input - HT01 bits/Partial JP ID
Entries 424720
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

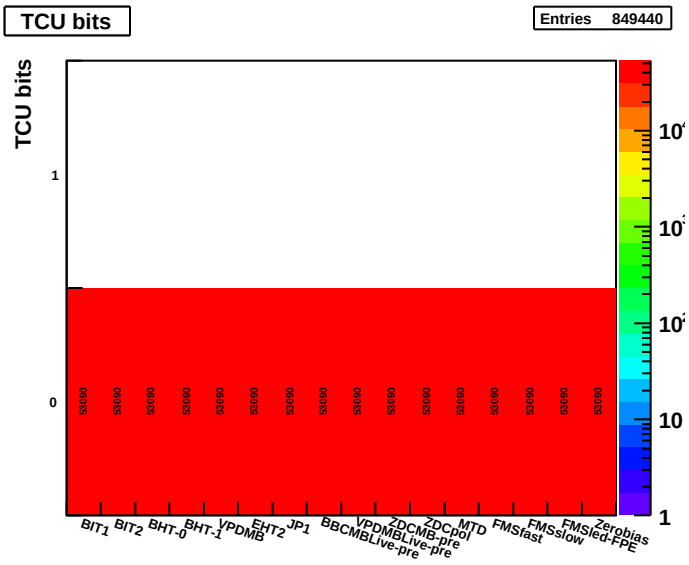

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

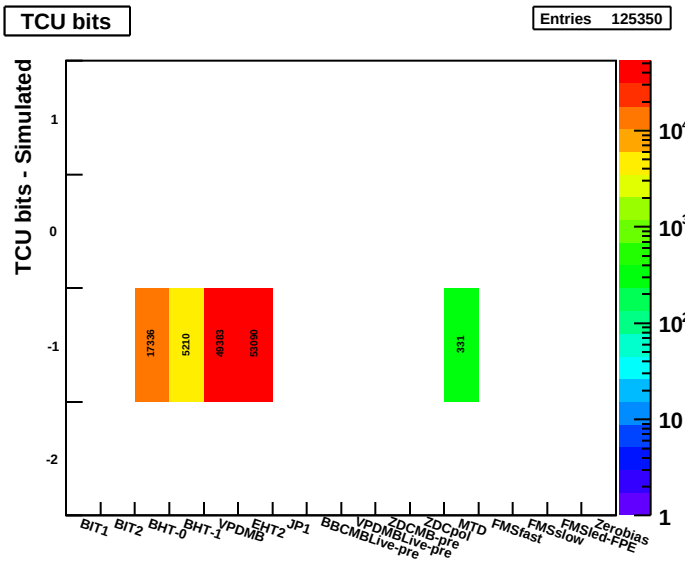

EMC L2 Input - HT23 bits
Entries 424720
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

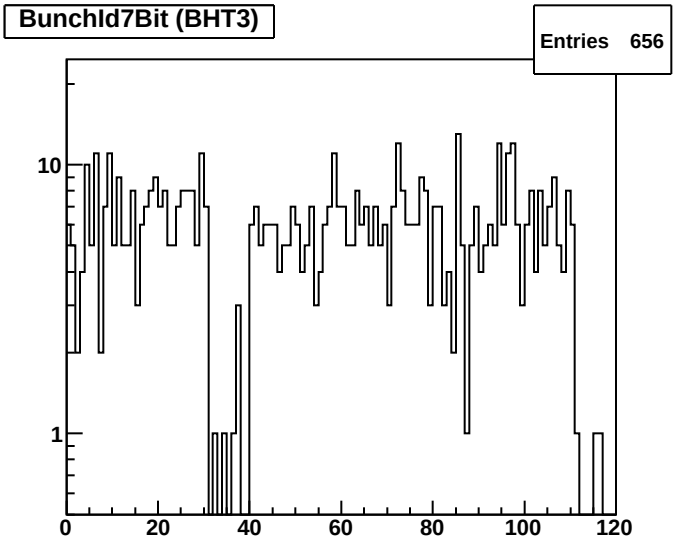

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

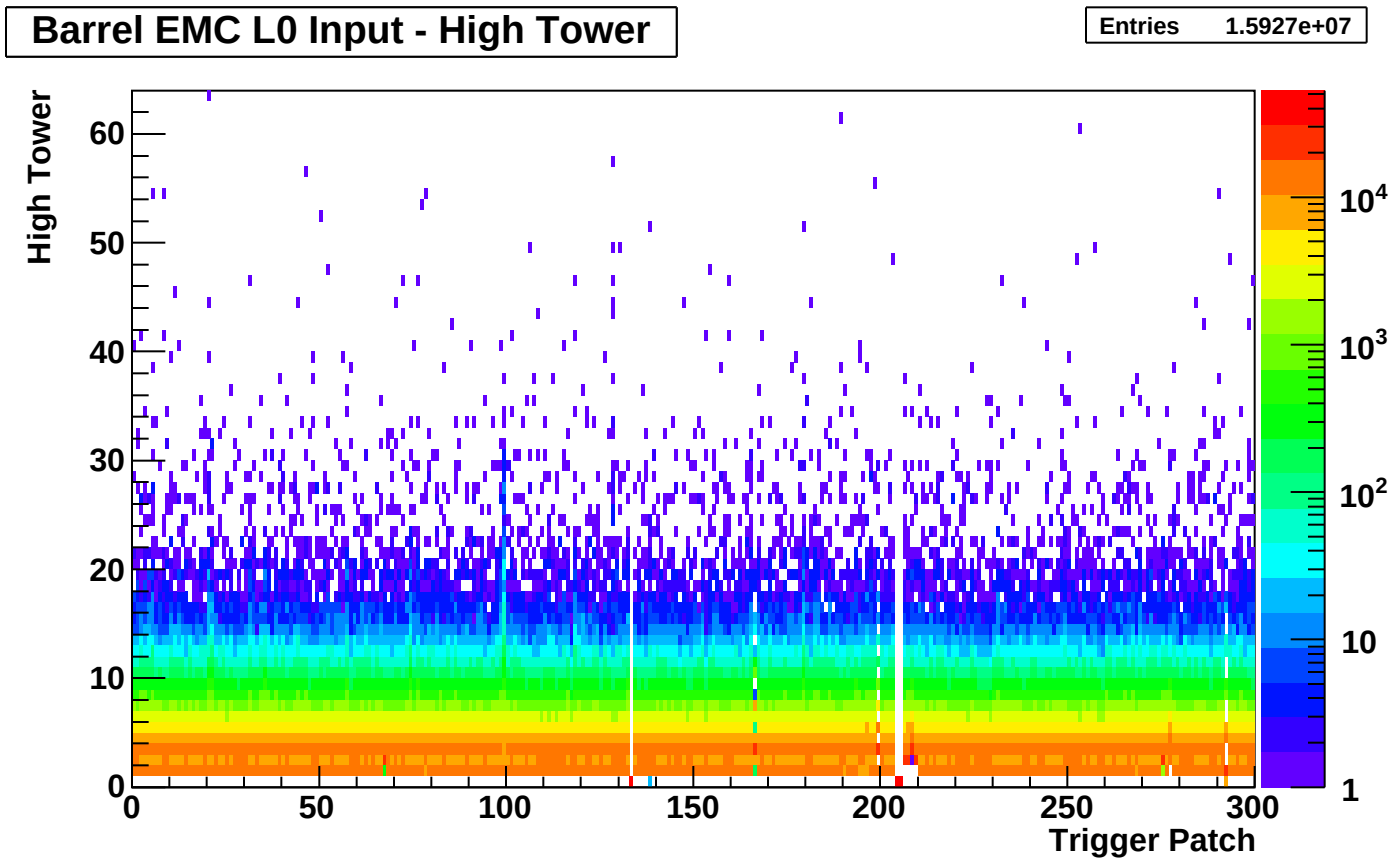

Last DSM Ch3 Input
Entries 796350
LD301 Ch3
1
0
20039
5210
2507
656
53090
53090
53090
53090
42338
39998
53090
53090
42993
41746
42067
33051
47880
50583
52434
10752
13492
10497
11344
11023
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP
1

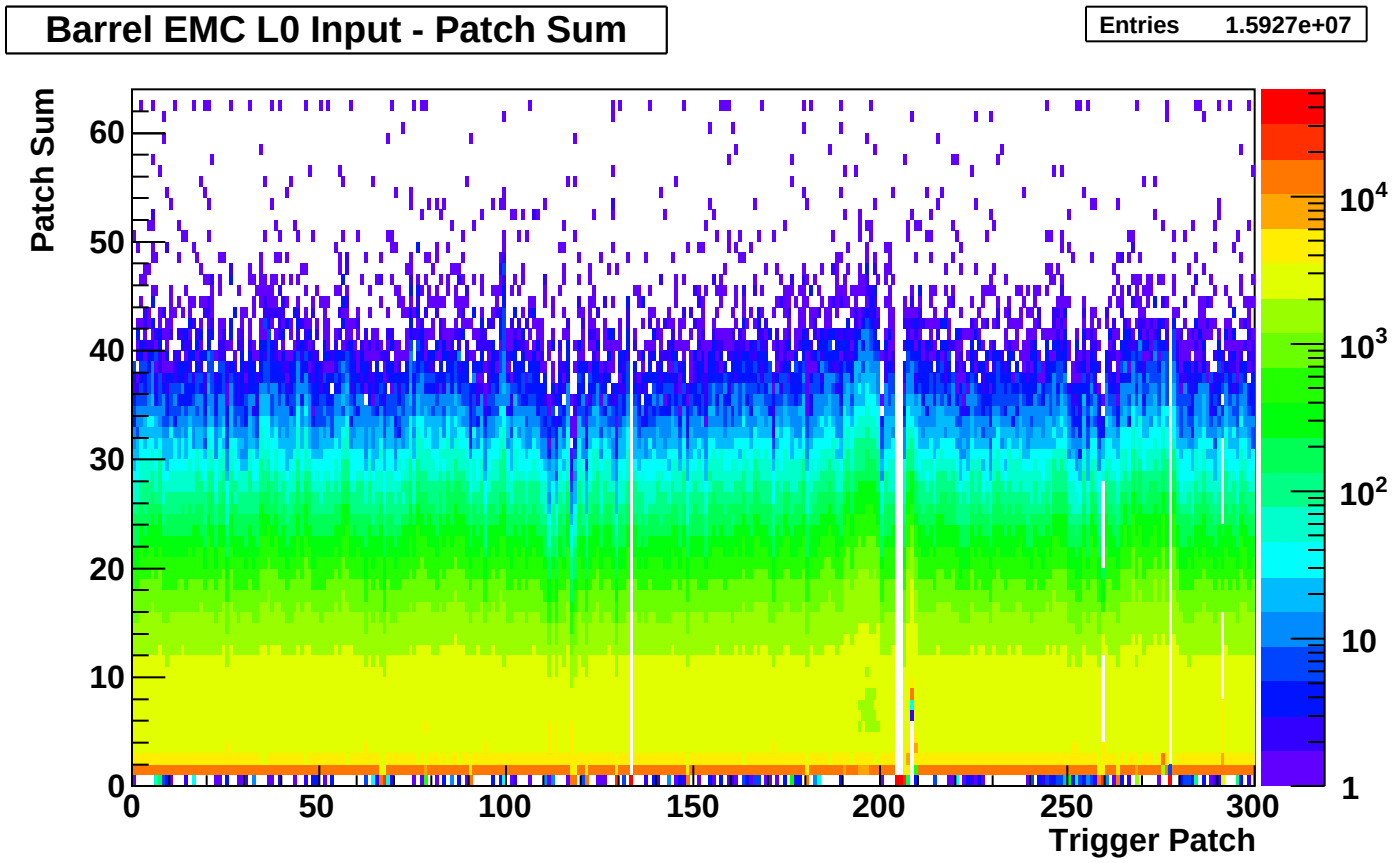

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
1
0
-1
-2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

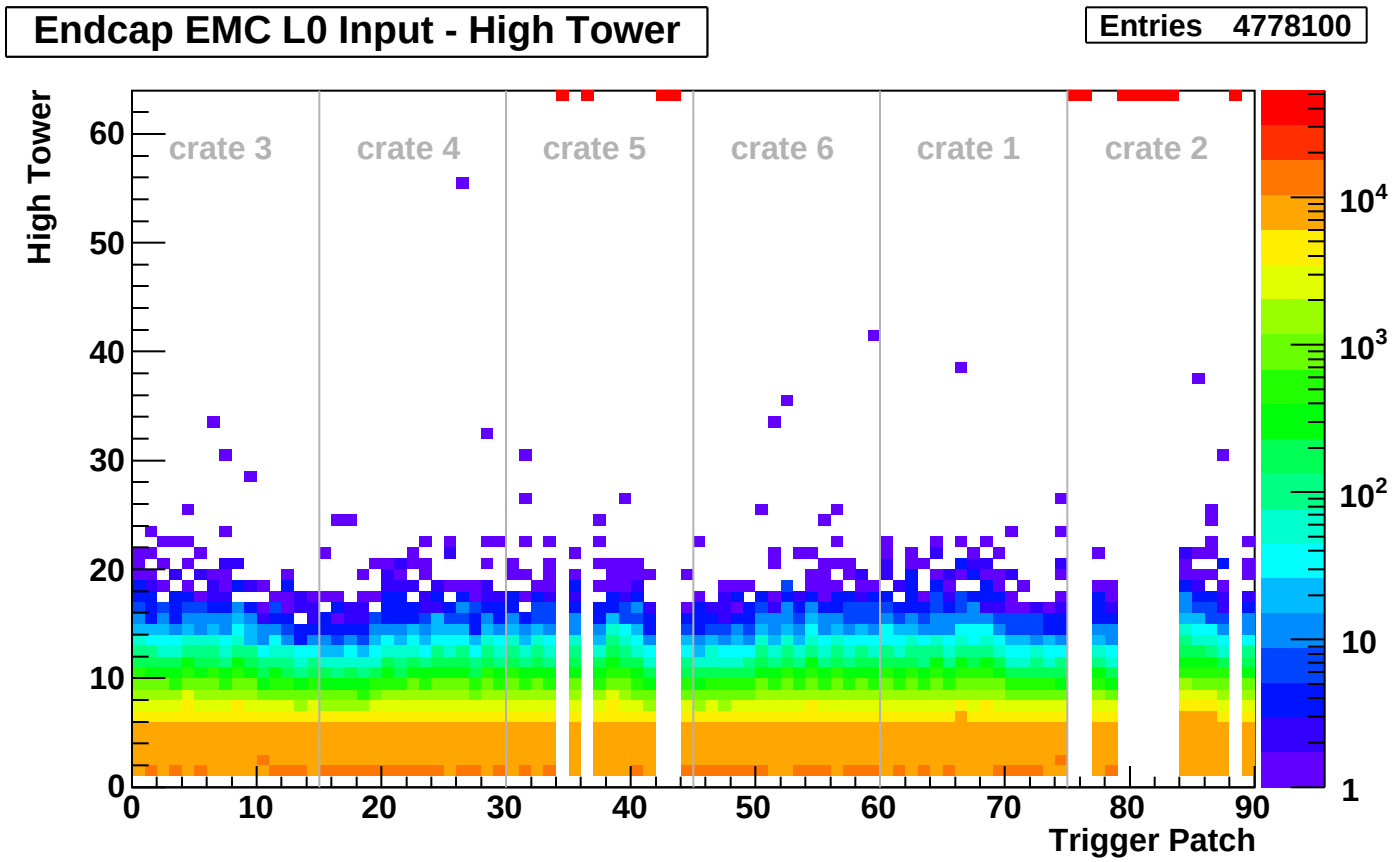

TCU bits
Entries 849440
TCU bits
1
0
53090
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias
1

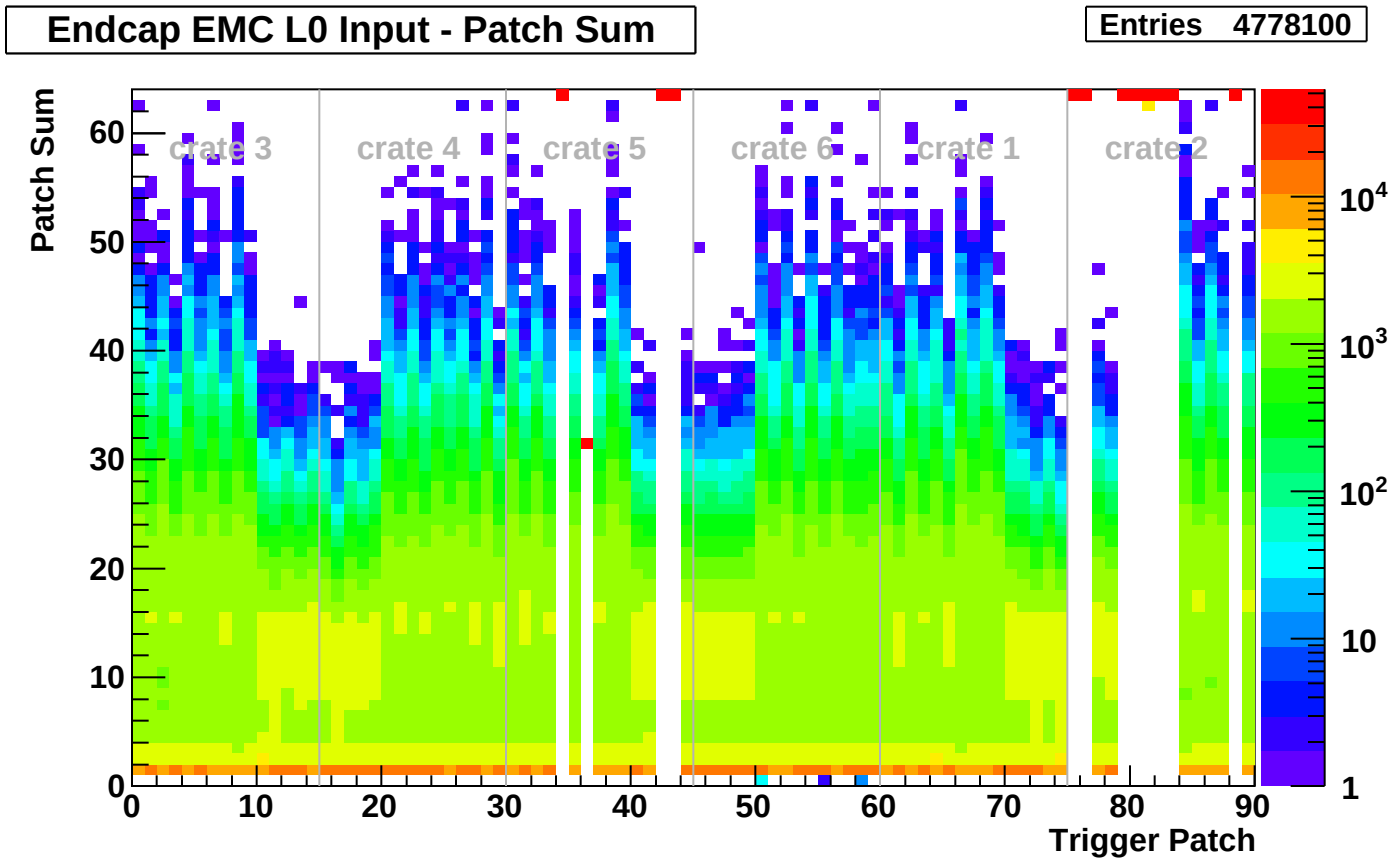

TCU bits
Entries 125350
TCU bits - Simulated
1
0
-1
-2
17336
5210
49383
53090
331
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias
1

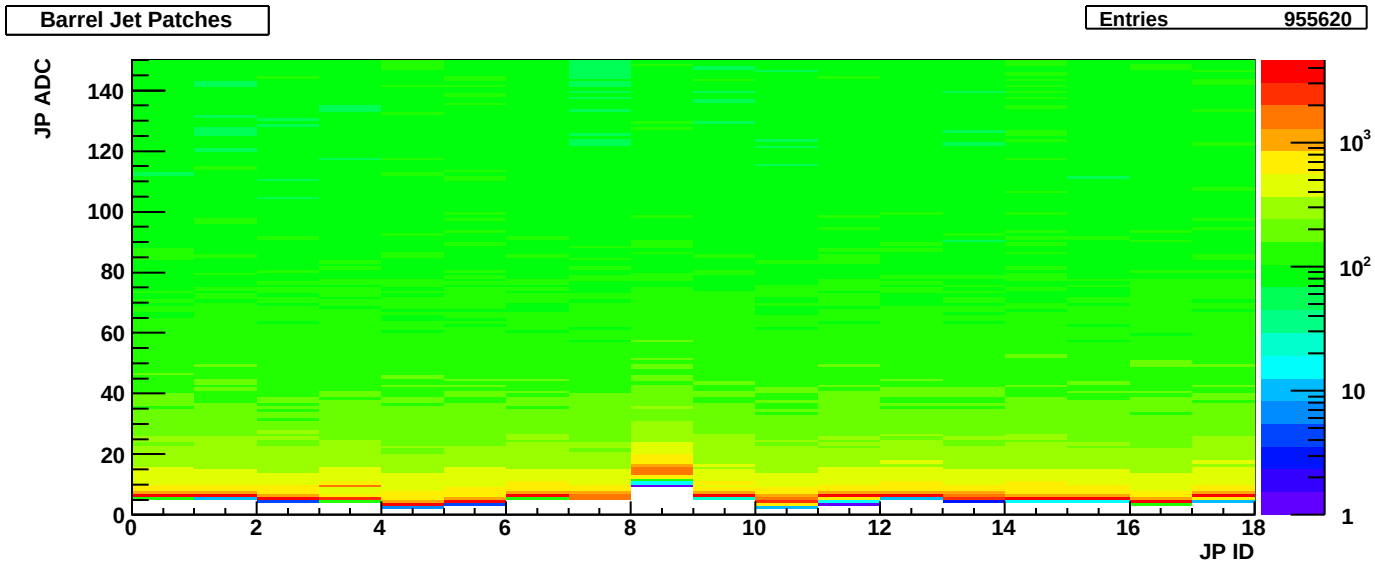

BunchId7Bit (BHT3)
Entries 656
10
1
0
20
40
60
80
100
120

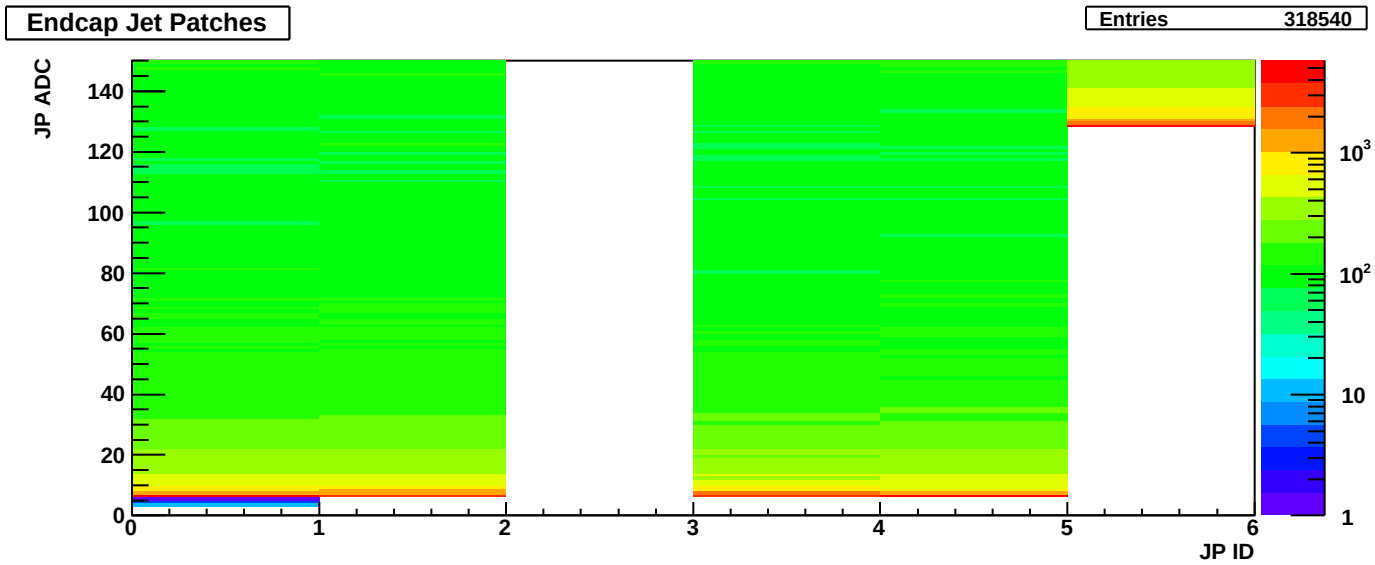

Barrel EMC L0 Input - High Tower
Entries 1.5927e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

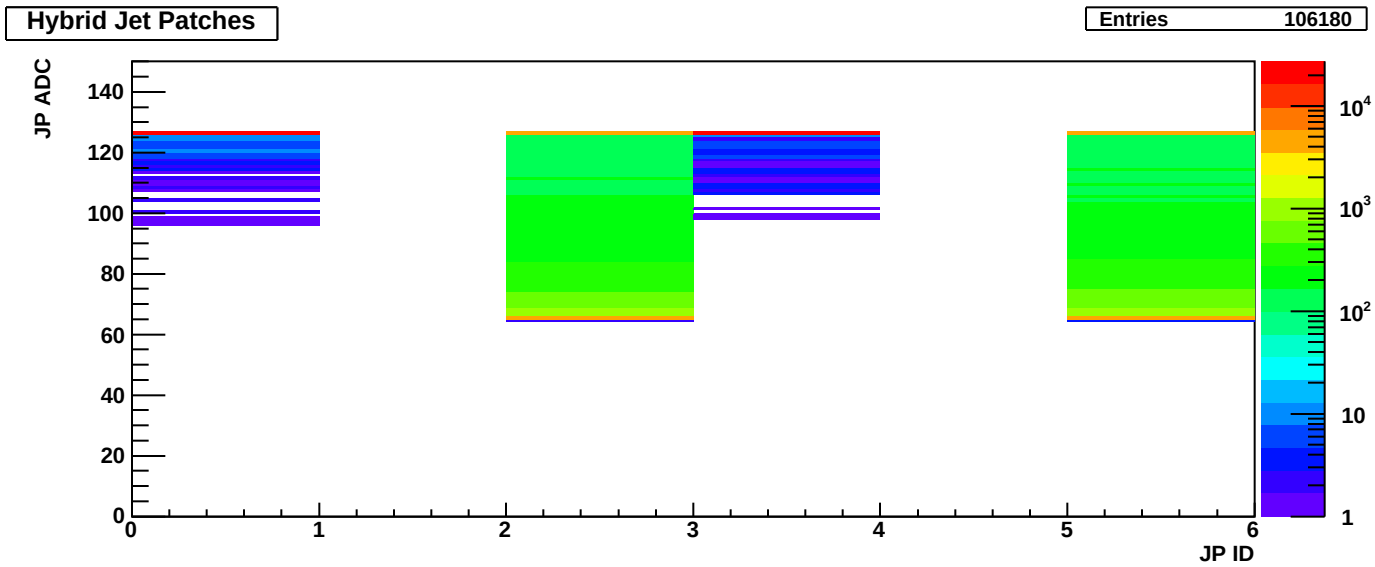

Barrel EMC L0 Input - Patch Sum
Entries 1.5927e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

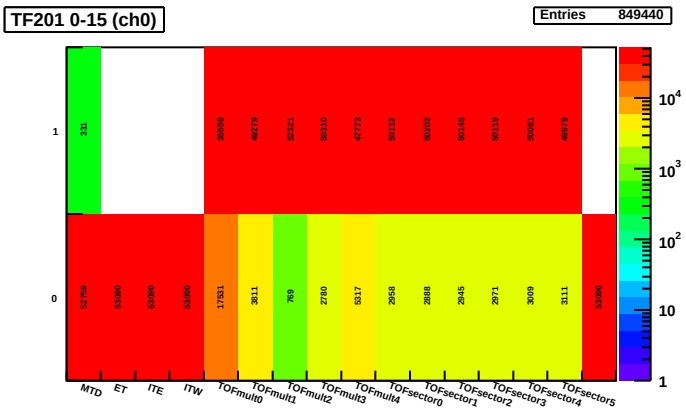

Endcap EMC L0 Input - High Tower
Entries 4778100
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

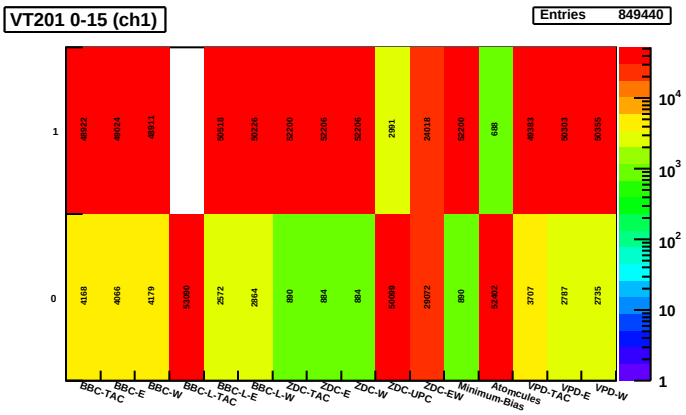

Endcap EMC L0 Input - Patch Sum
Entries 4778100
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

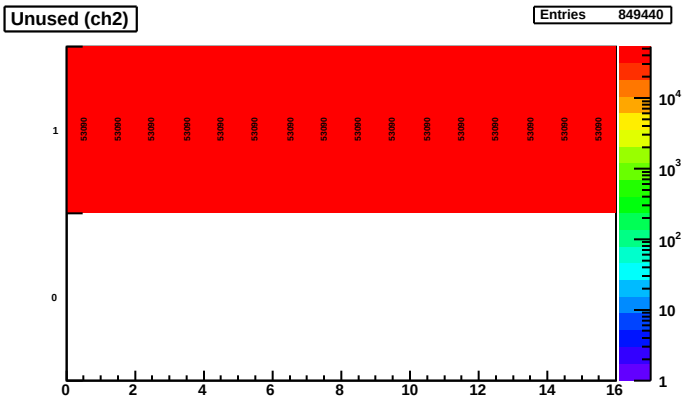

Barrel Jet Patches
Entries 955620
JP ADC
JP ID

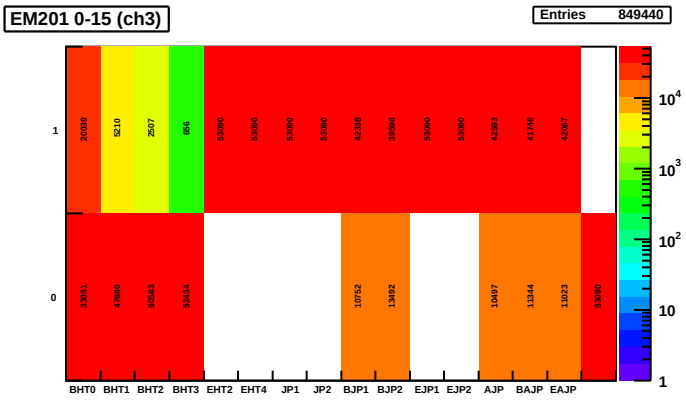

Endcap Jet Patches
Entries 318540
JP ADC
JP ID

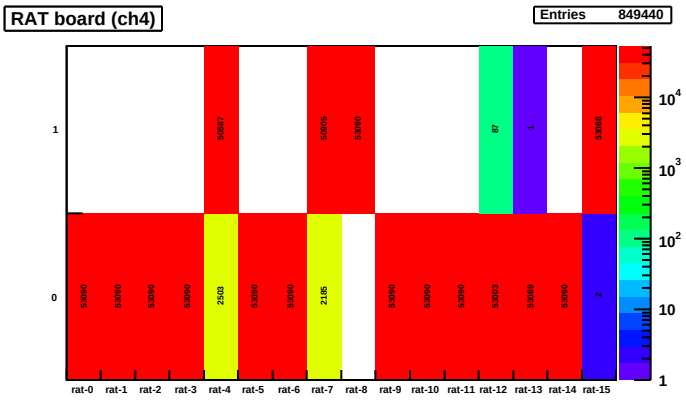

Hybrid Jet Patches
Entries 106180
JP ADC
JP ID

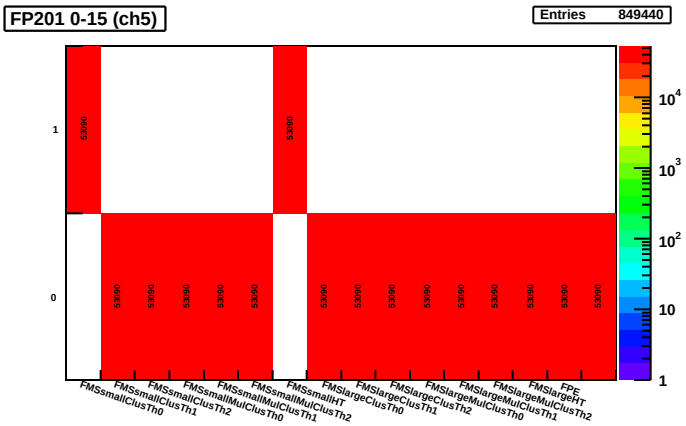

TF201 0-15 (ch0)
Entries 849440
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

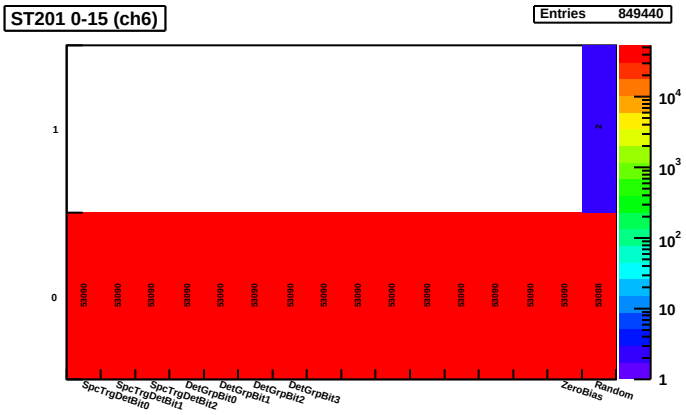

VT201 0-15 (ch1)
Entries 849440
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Atomcules Minimum-Bias VPD-TAC VPD-E VPD-W

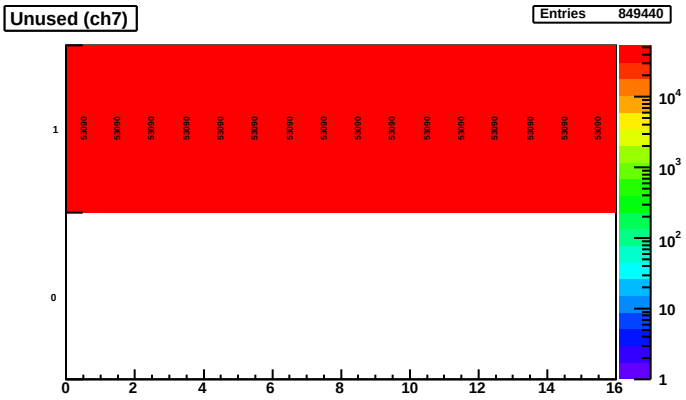

Unused (ch2)
Entries 849440

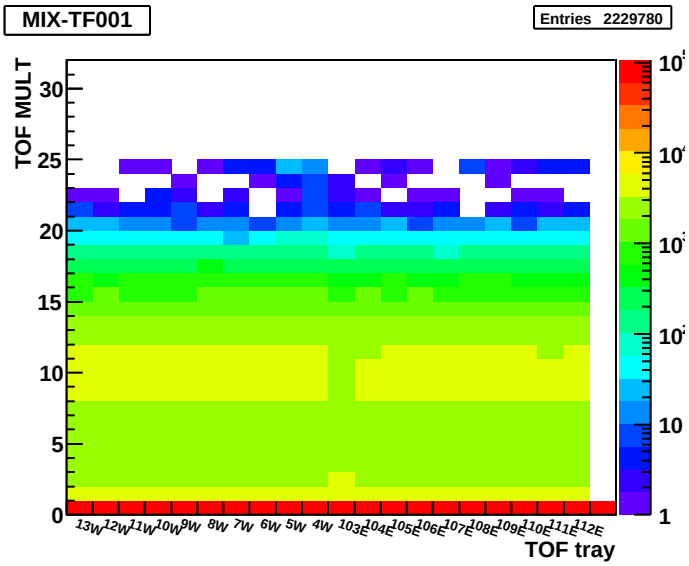

EM201 0-15 (ch3)
Entries 849440
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

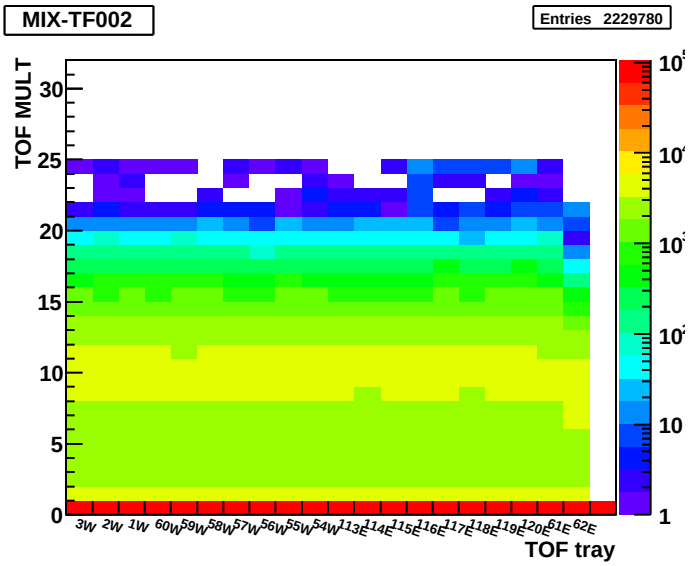

RAT board (ch4)
Entries 849440
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

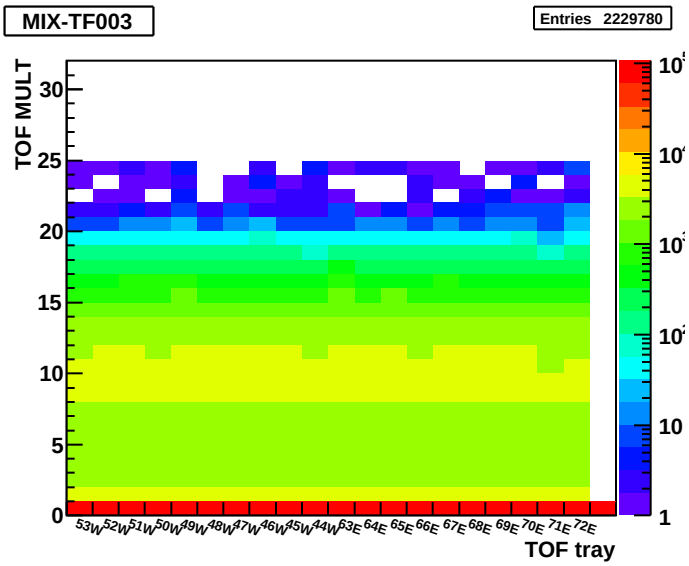

FP201 0-15 (ch5)
Entries 849440
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

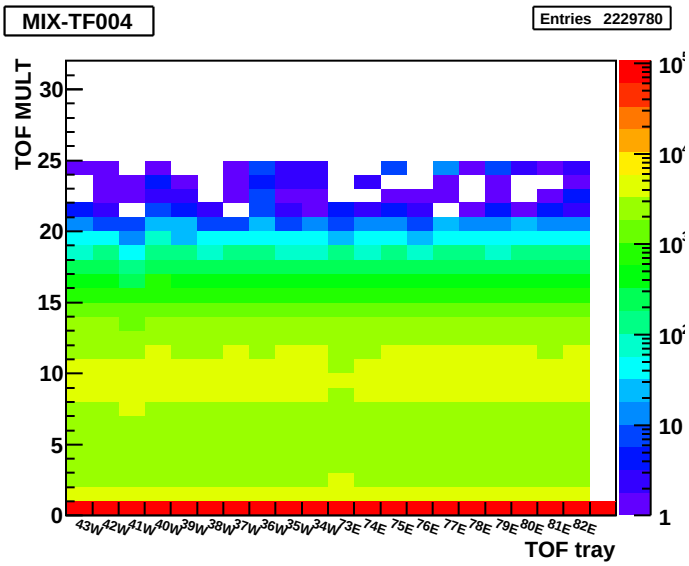

ST201 0-15 (ch6)
Entries 849440
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

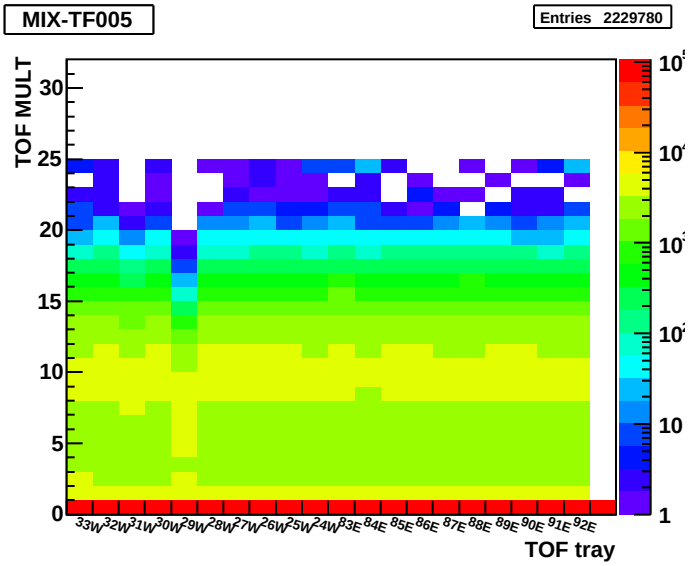

Unused (ch7)
Entries 849440

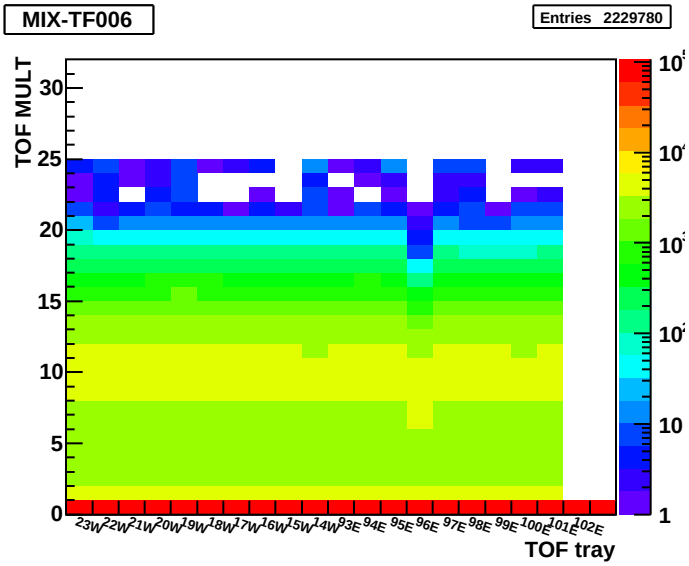

MIX-TF001
Entries 2229780
TOF MULT
TOF tray

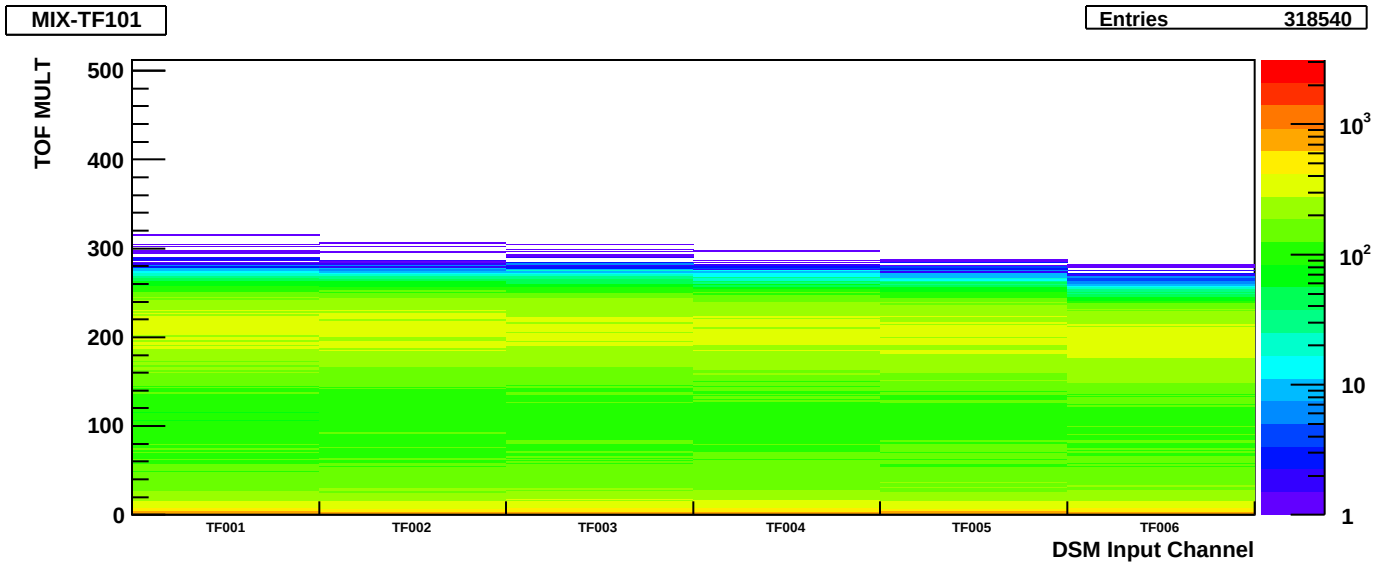

MIX-TF002
Entries 2229780
TOF MULT
TOF tray

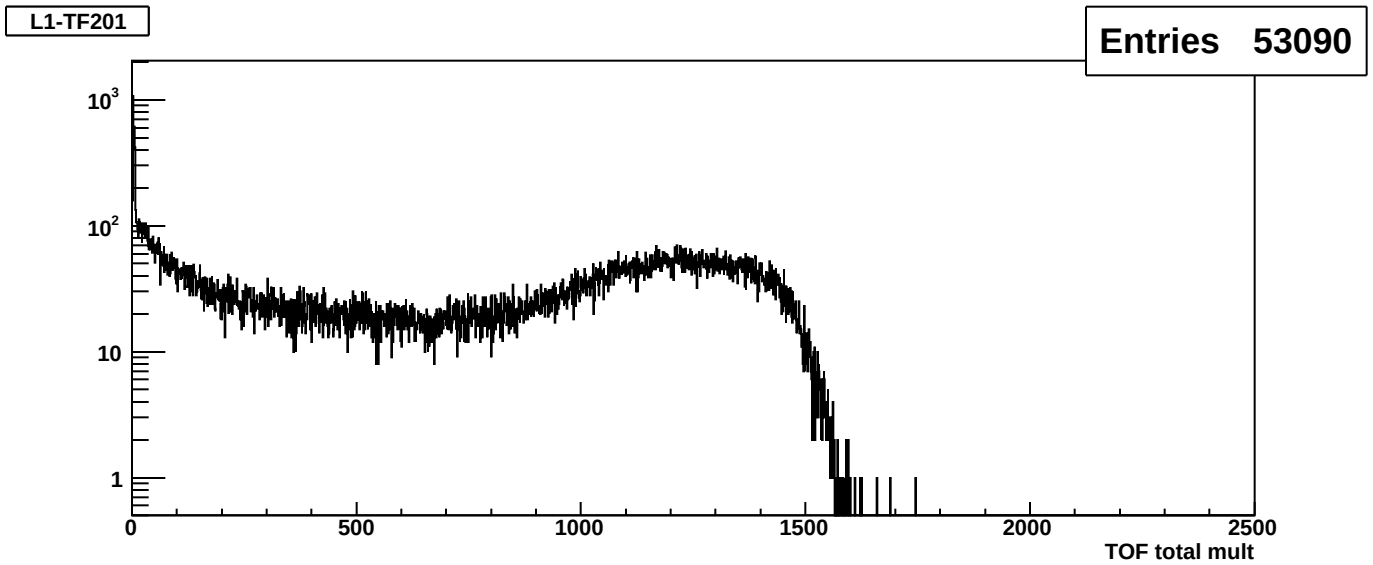

MIX-TF003
Entries 2229780
TOF MULT
TOF tray

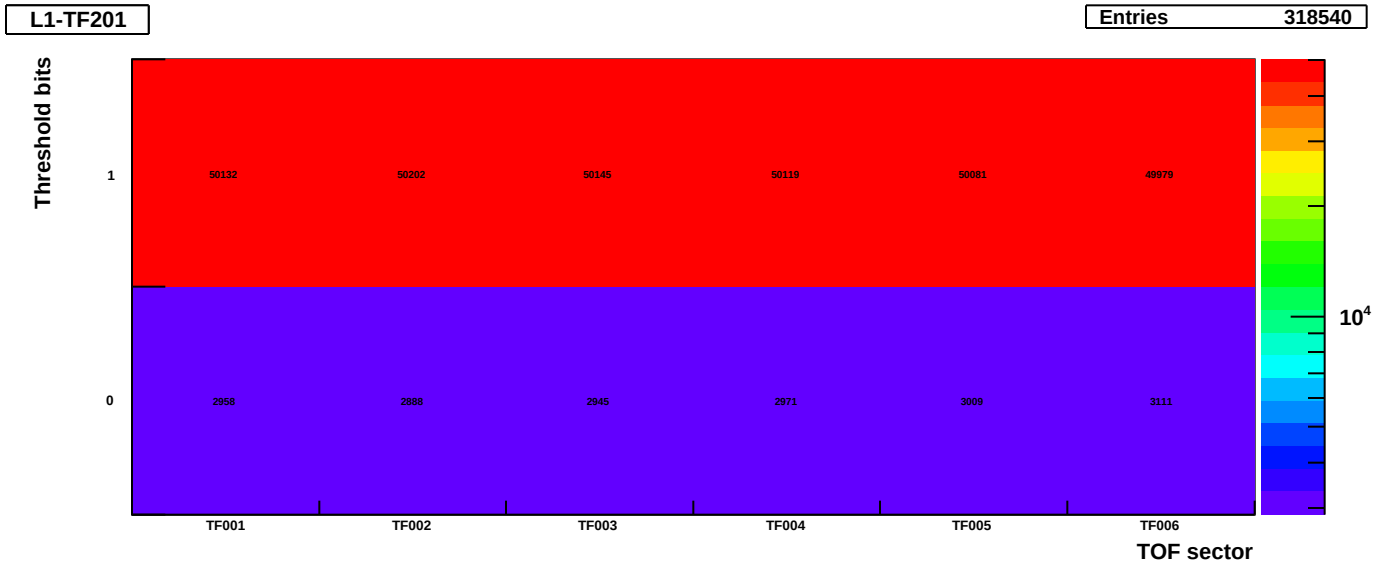

MIX-TF004
Entries 2229780
TOF MULT
TOF tray

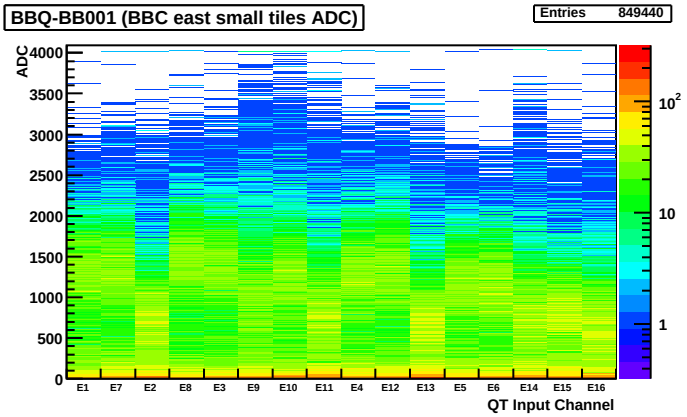

MIX-TF005
Entries 2229780
TOF MULT
TOF tray

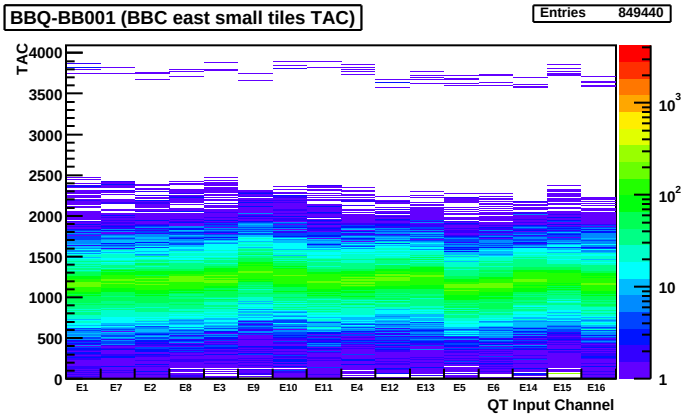

MIX-TF006
Entries 2229780
TOF MULT
TOF tray

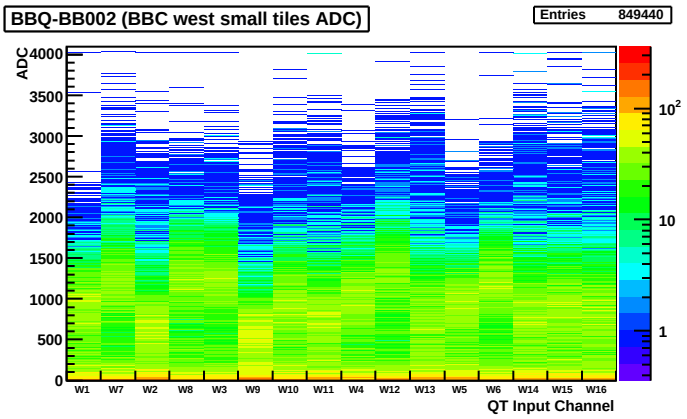

MIX-TF101
Entries 318540
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^3
10^2
10
1

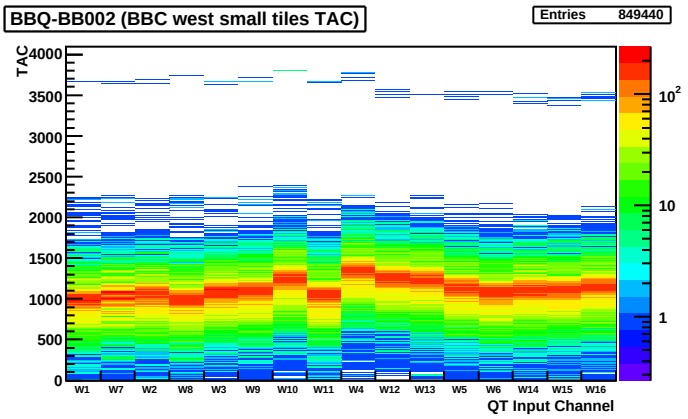

L1-TF201
Entries 53090
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

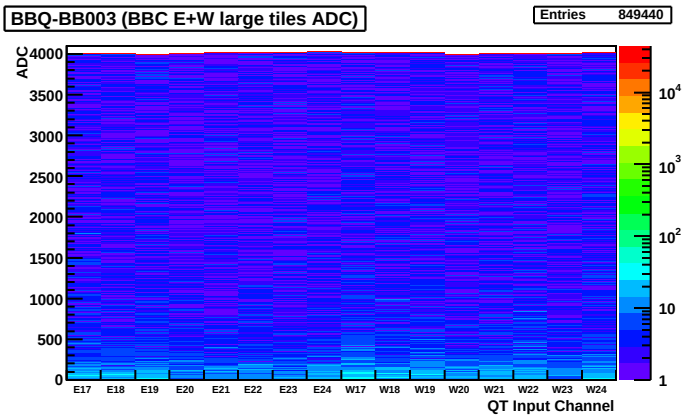

L1-TF201
Entries 318540
Threshold bits
1
0
50132
50202
50145
50119
50081
49979
2958
2888
2945
2971
3009
3111
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4

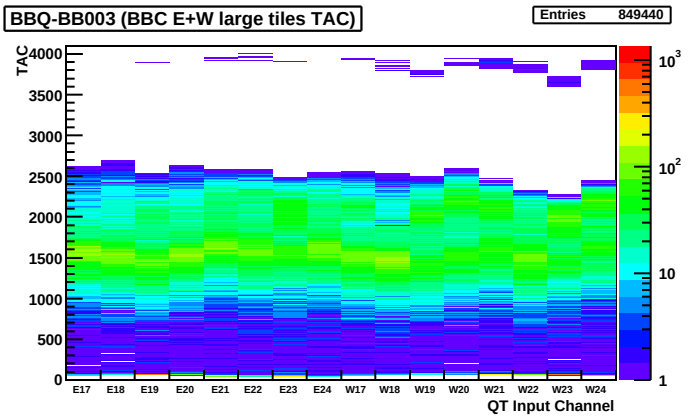

BBQ-BB001 (BBC east small tiles ADC)
Entries 849440
ADC
QT Input Channel

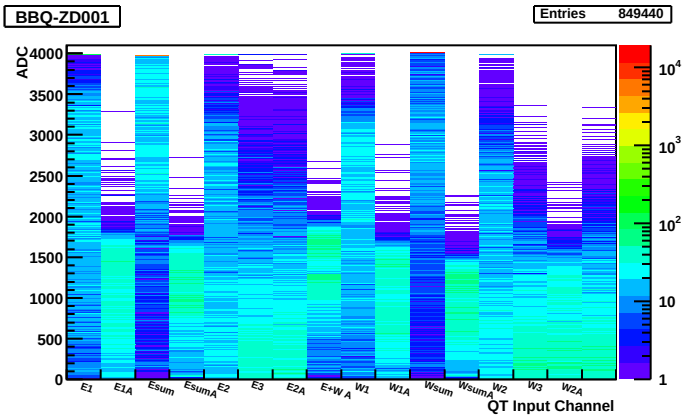

BBQ-BB001 (BBC east small tiles TAC)
Entries 849440
TAC
QT Input Channel

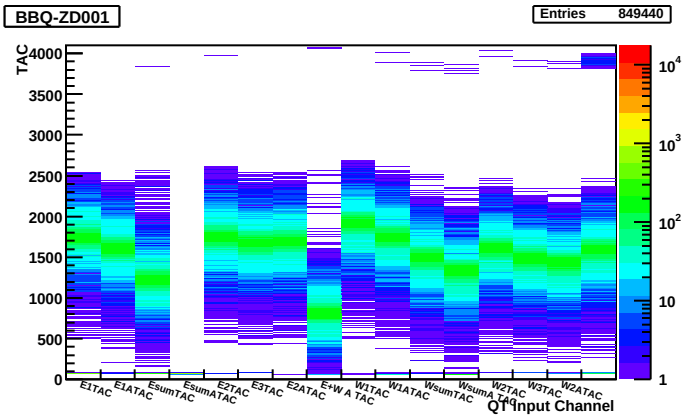

BBQ-BB002 (BBC west small tiles ADC)
Entries 849440
ADC
QT Input Channel

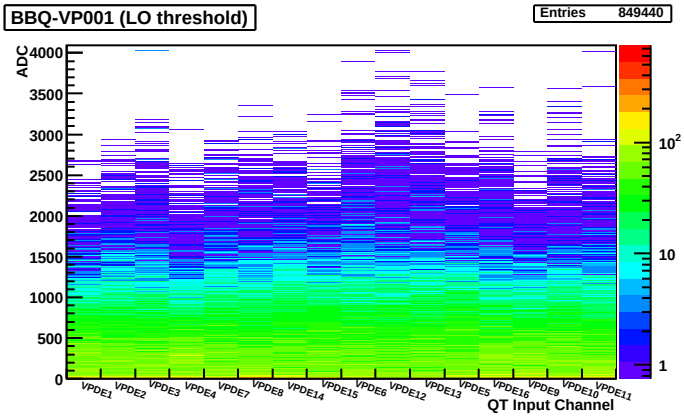

BBQ-BB002 (BBC west small tiles TAC)
Entries 849440
TAC
QT Input Channel

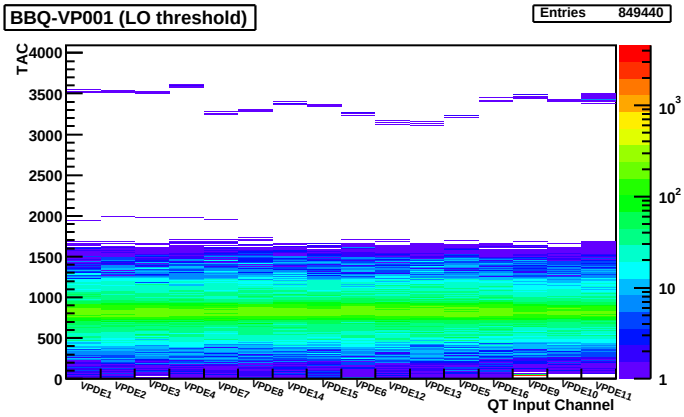

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 849440
ADC
QT Input Channel

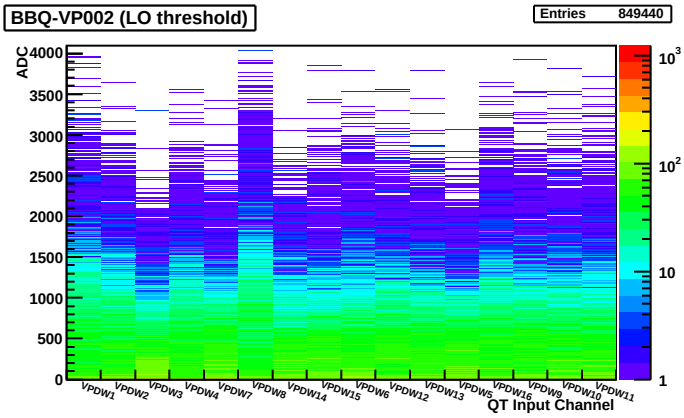

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 849440
TAC
QT Input Channel

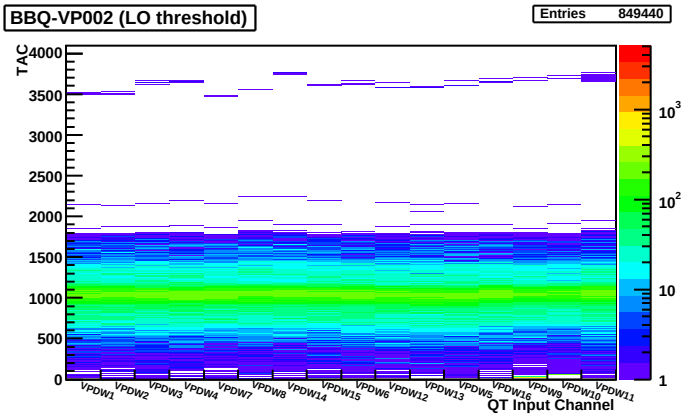

BBQ-ZD001
Entries 849440
ADC
QT Input Channel

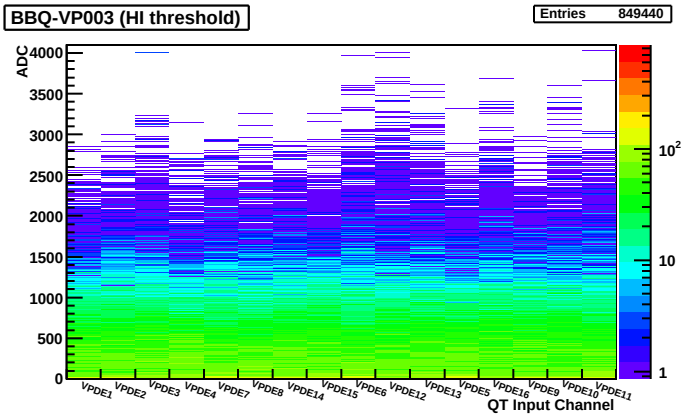

BBQ-ZD001
Entries 849440
TAC
QT Input Channel

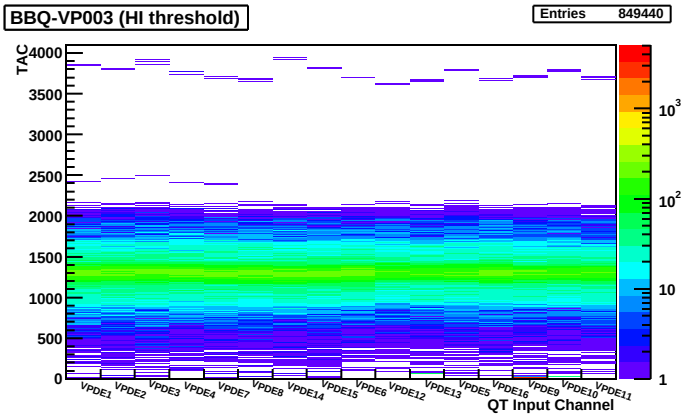

BBQ-VP001 (LO threshold)
Entries 849440
ADC
QT Input Channel

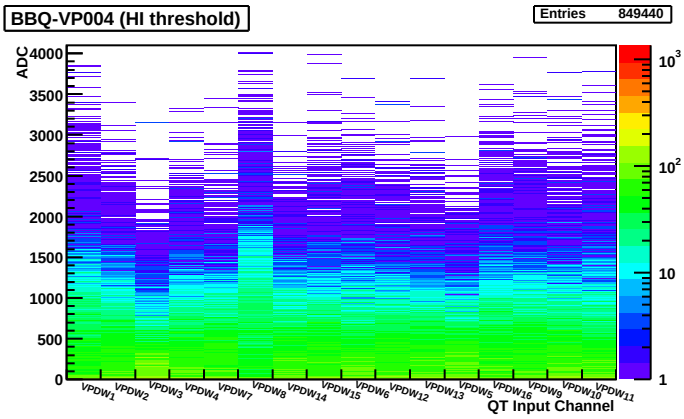

BBQ-VP001 (LO threshold)
Entries 849440
TAC
QT Input Channel

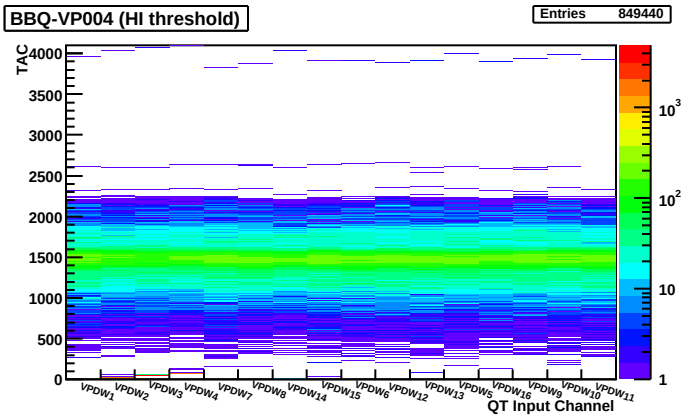

BBQ-VP002 (LO threshold)
Entries 849440
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 849440
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 849440
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 849440
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 849440
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 849440
TAC
QT Input Channel