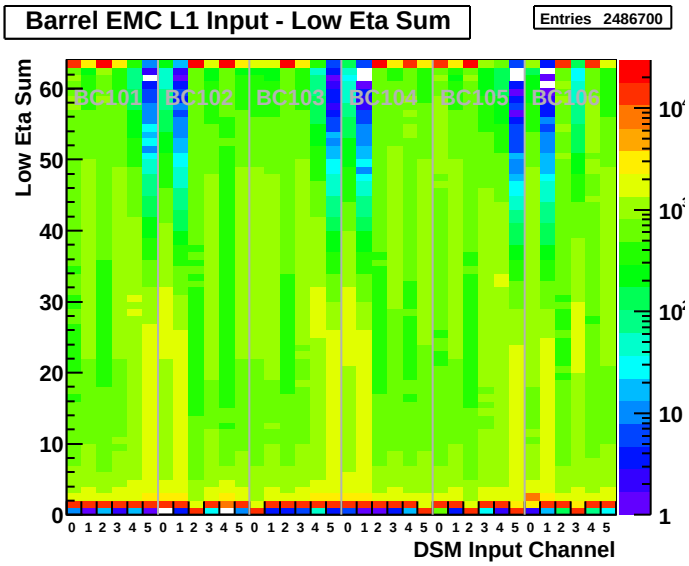
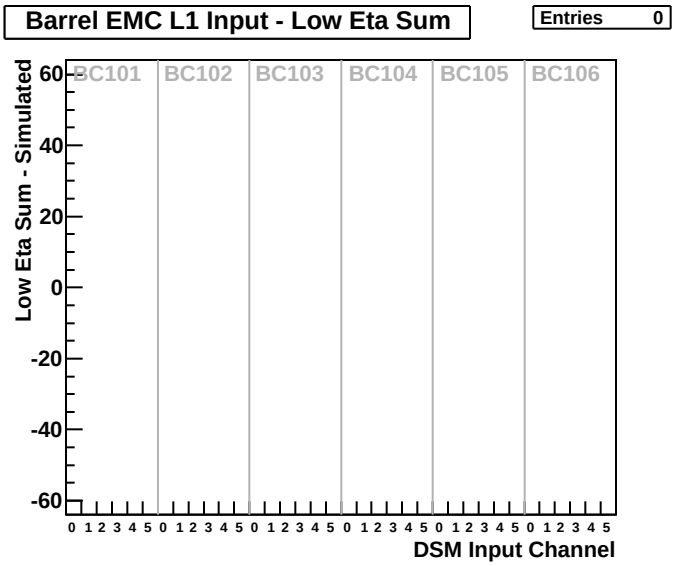
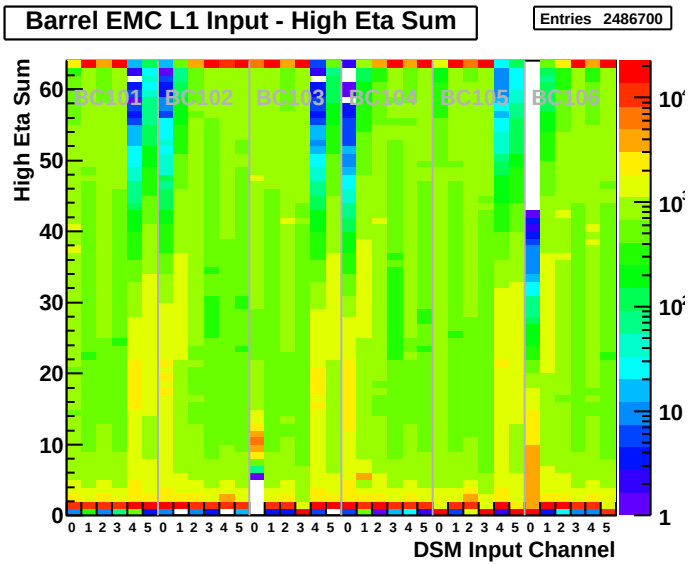
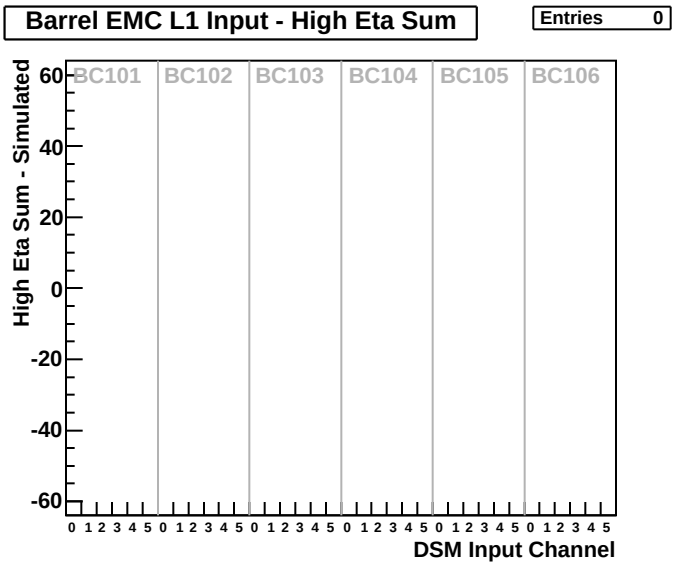
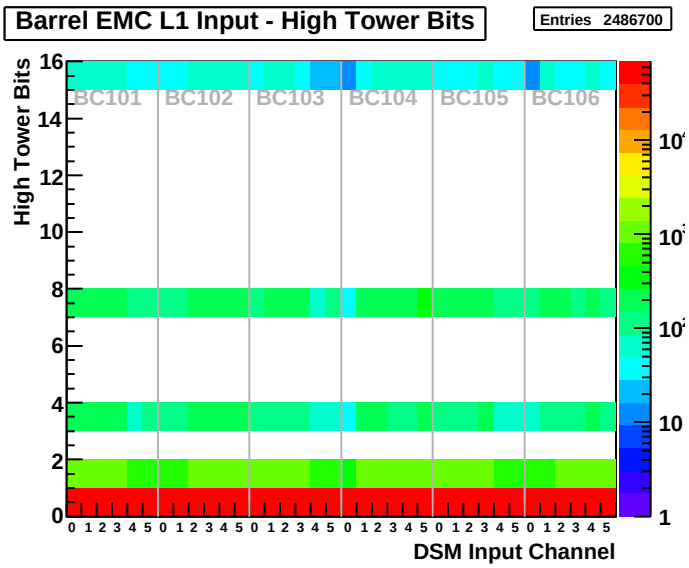
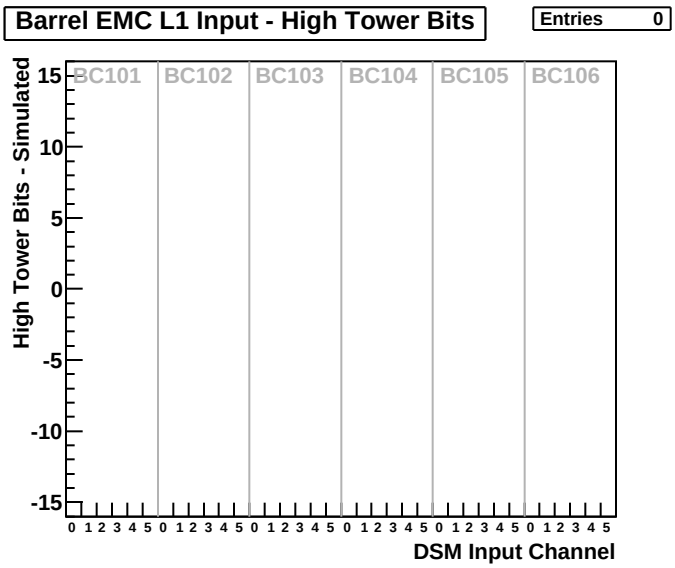

Barrel EMC L1 Input - Low Eta Sum
Entries 2486700
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

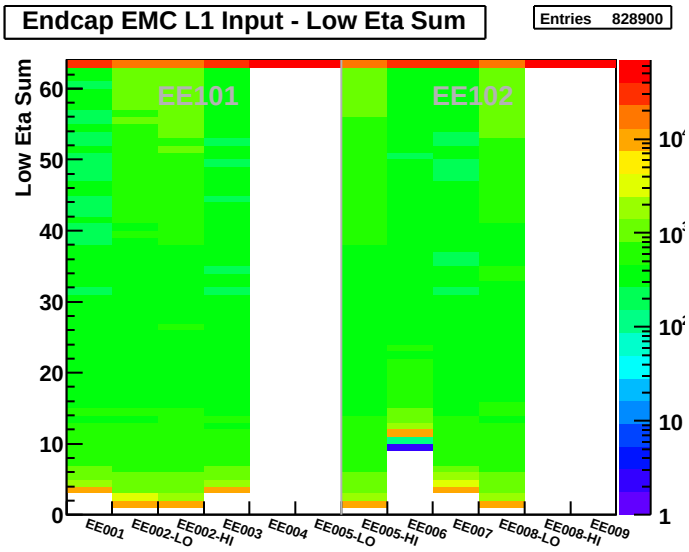

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

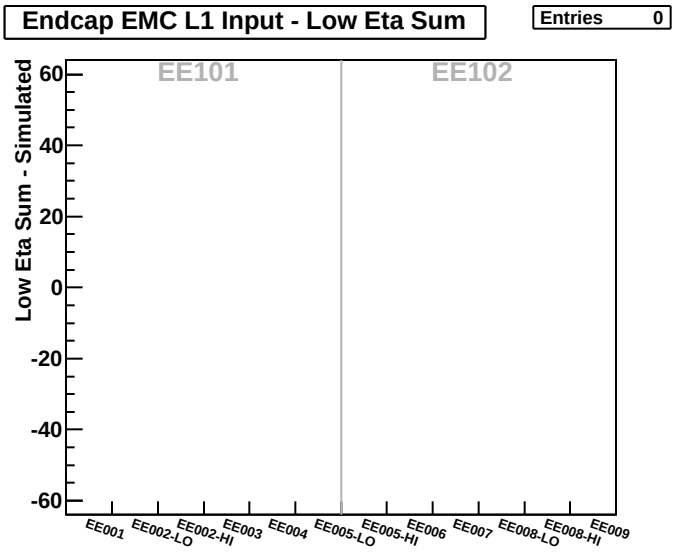

Barrel EMC L1 Input - High Eta Sum
Entries 2486700
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

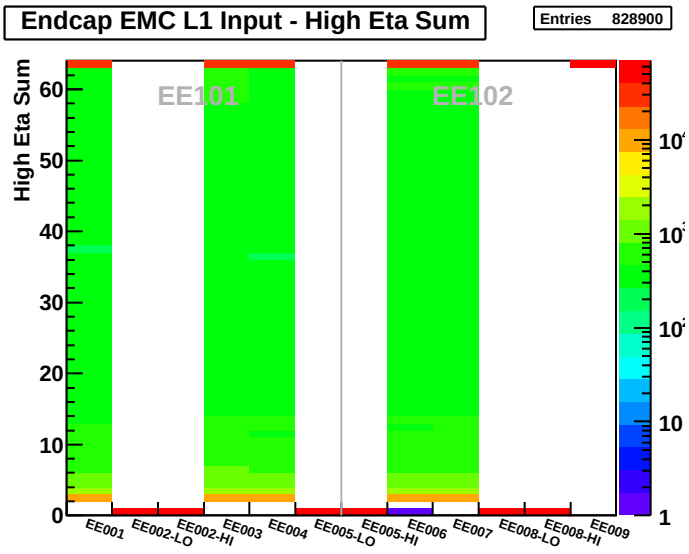

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

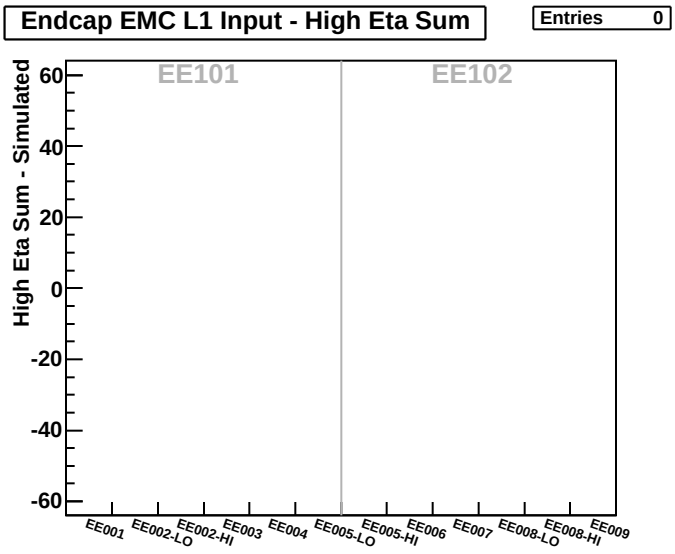

Barrel EMC L1 Input - High Tower Bits
Entries 2486700
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

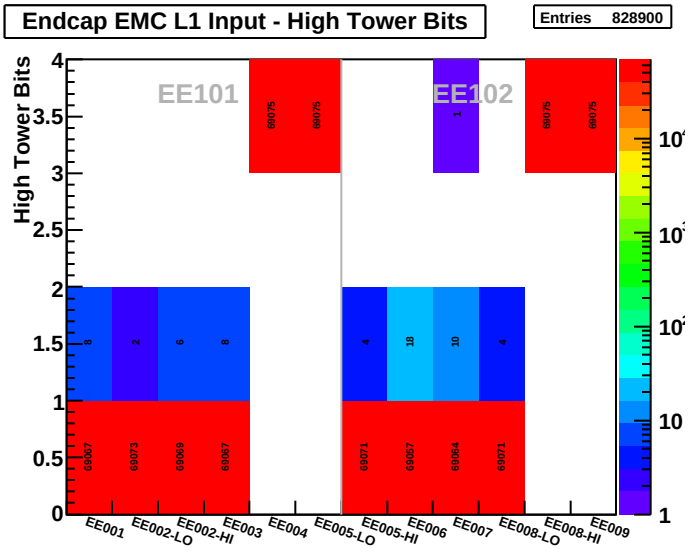

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

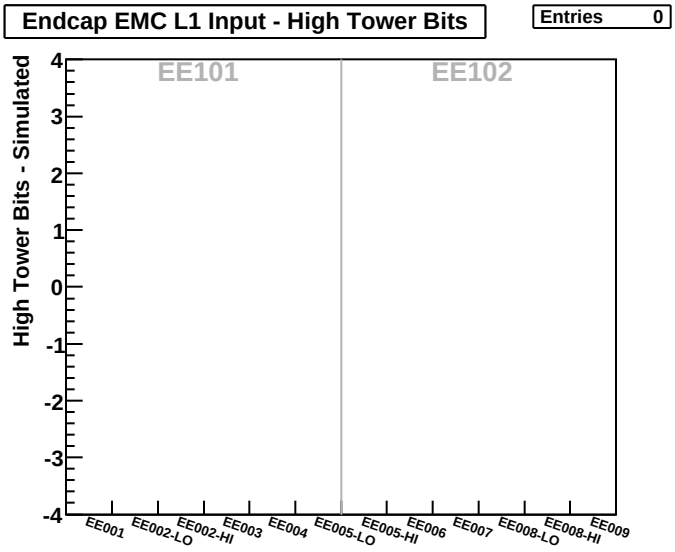

Endcap EMC L1 Input - Low Eta Sum
Entries 828900
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

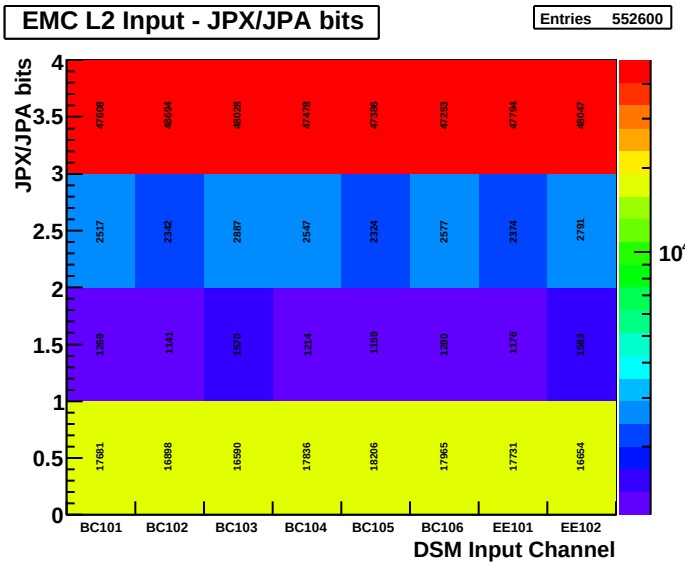

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

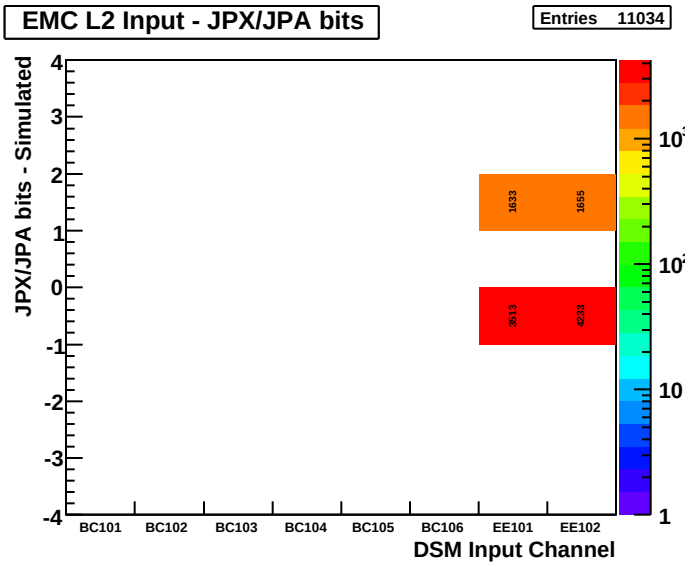

Endcap EMC L1 Input - High Eta Sum
Entries 828900
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

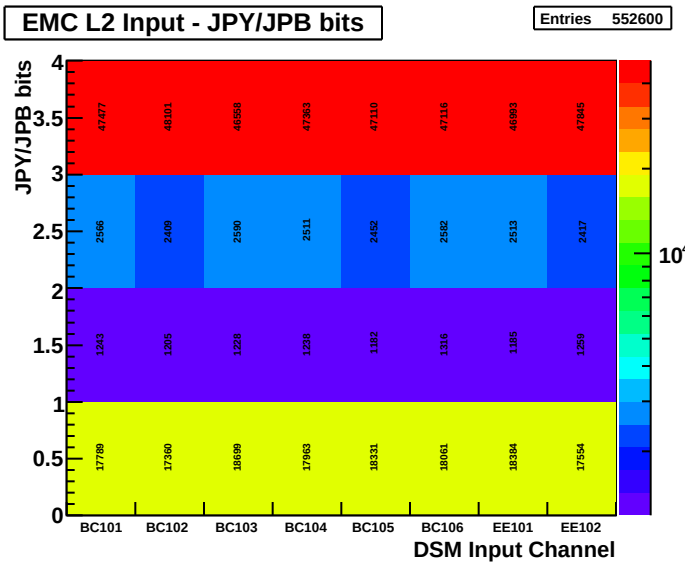

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

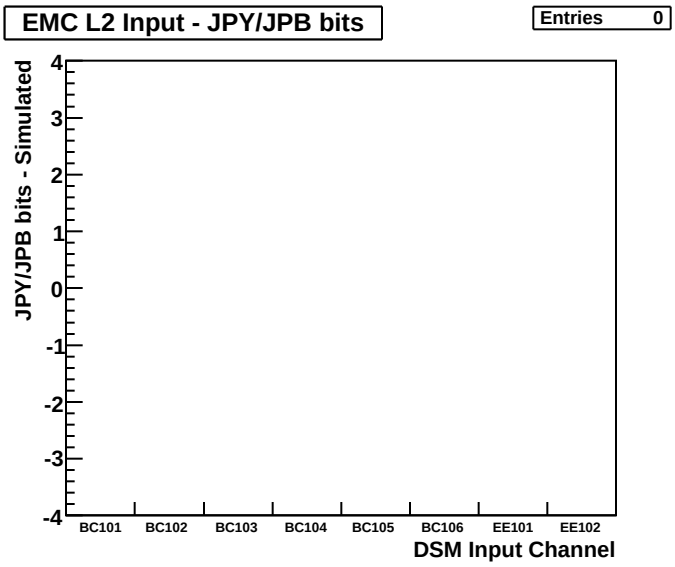

Endcap EMC L1 Input - High Tower Bits
Entries 828900
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

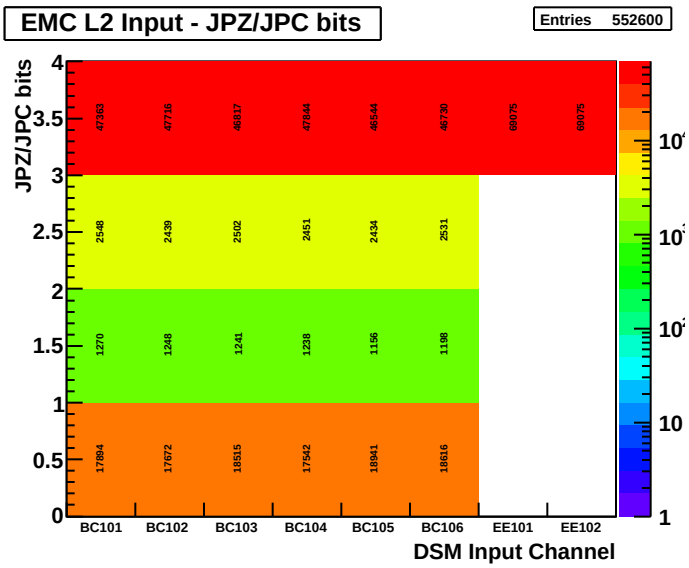

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

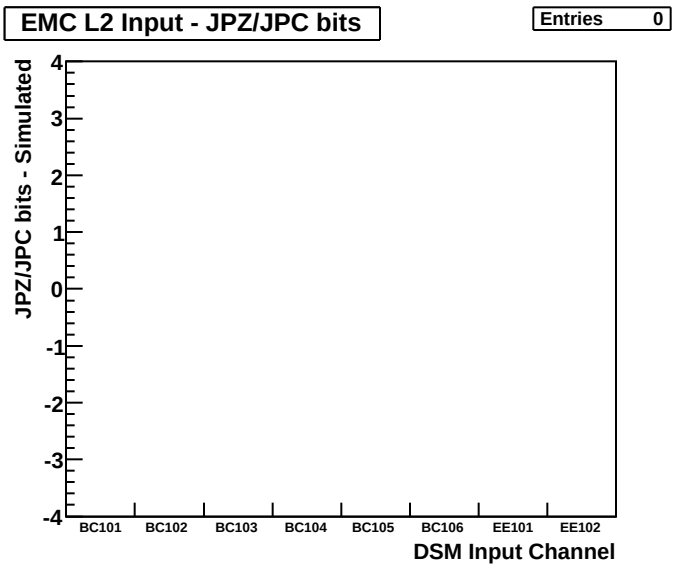

EMC L2 Input - JPX/JPA bits
Entries 552600
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
47608 48694 48028 47478 47386 47253 47794 48047
2517 2342 2887 2547 2324 2577 2374 2791
1269 1141 1570 1214 1159 1280 1176 1583
17681 16898 16590 17836 18206 17965 17731 16654

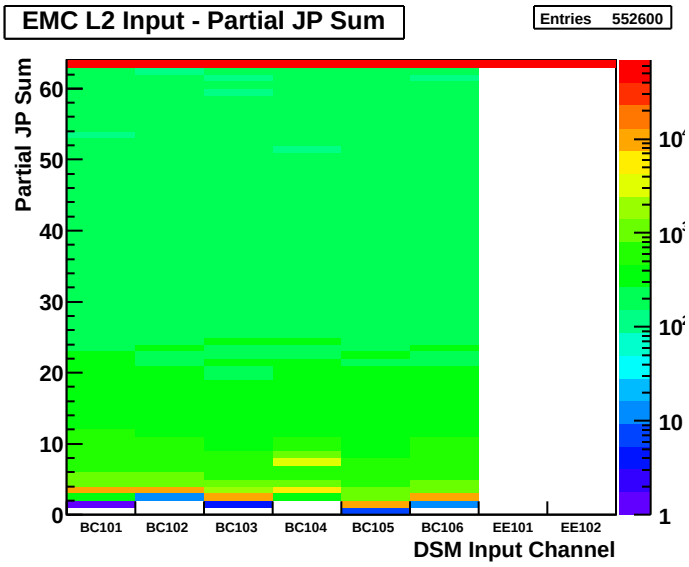

EMC L2 Input - JPX/JPA bits
Entries 11034
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1633 1655
3513 4233

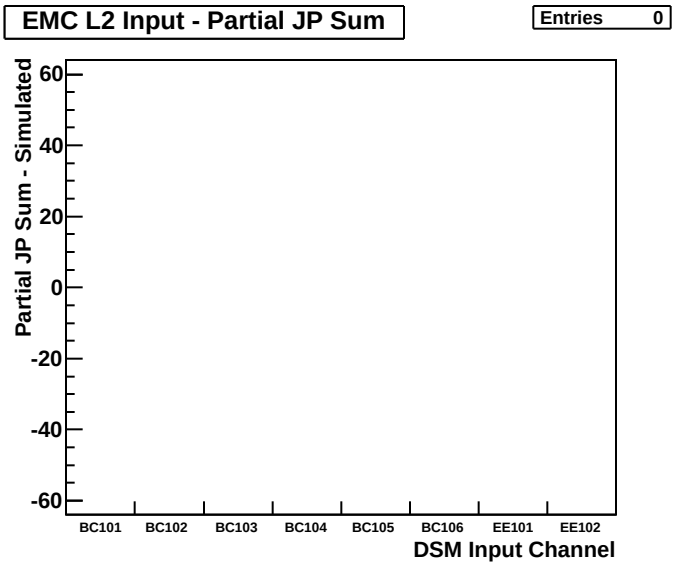

EMC L2 Input - JPY/JPB bits
Entries 552600
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
47477 43101 45558 47363 47110 47116 46993 47845
2566 2409 2590 2511 2452 2582 2513 2417
1243 1205 1220 1238 1182 1316 1185 1259
17789 17360 18699 17963 18331 18061 18384 17554

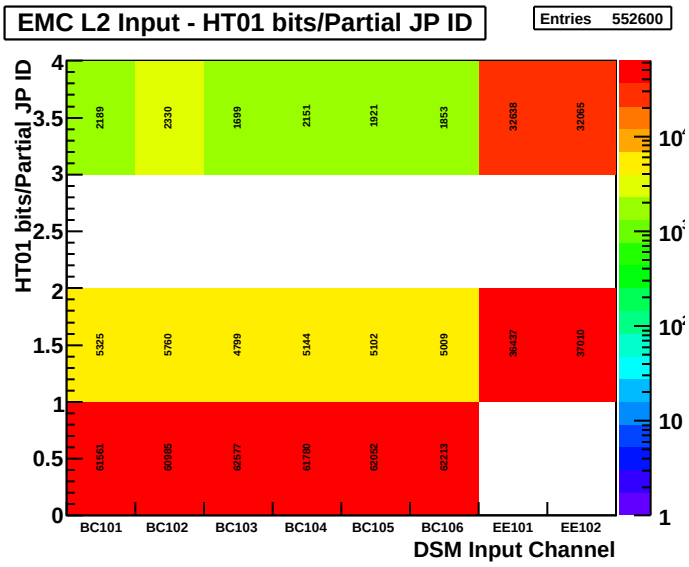

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

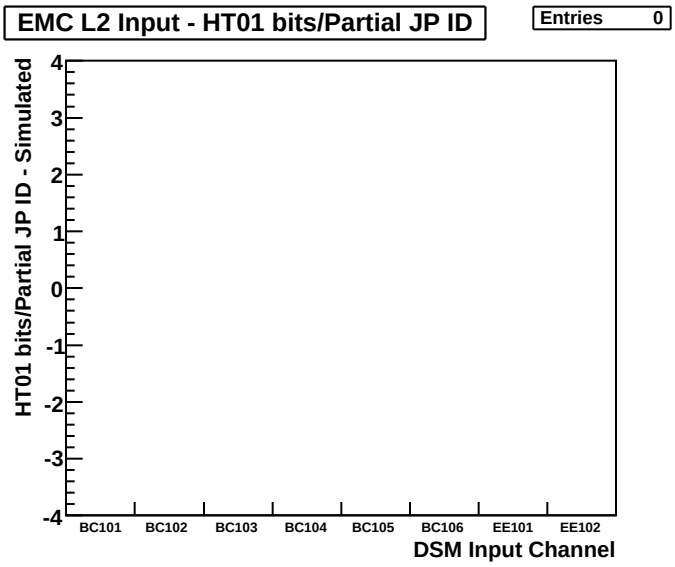

EMC L2 Input - JPZ/JPC bits
Entries 552600
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
47363 47716 46817 47844 46544 46730 69075 69075
2548 2439 2502 2451 2434 2531
1270 1248 1241 1238 1156 1198
17894 17672 18615 17542 18941 18616

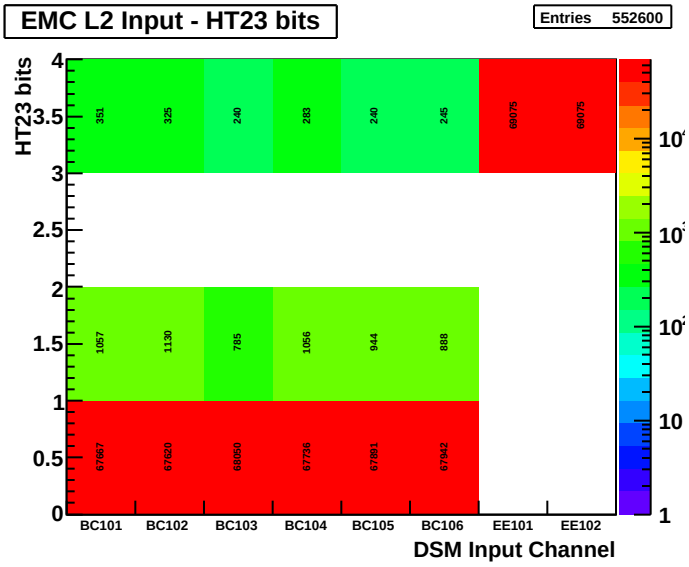

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

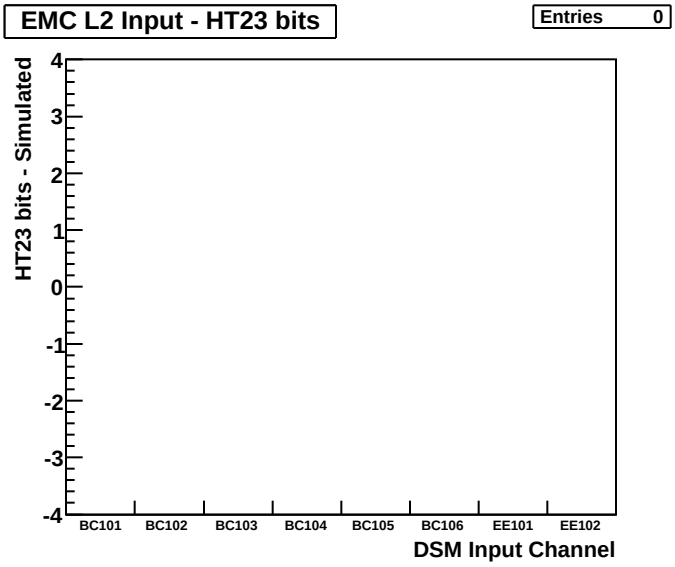

EMC L2 Input - Partial JP Sum
Entries 552600
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

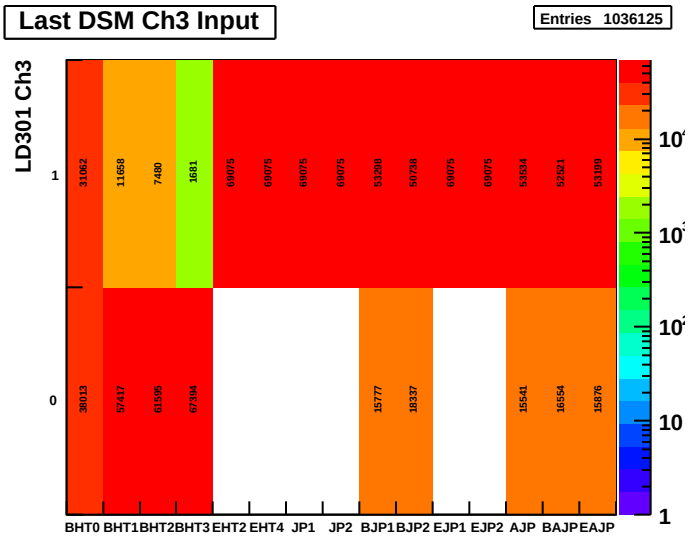

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

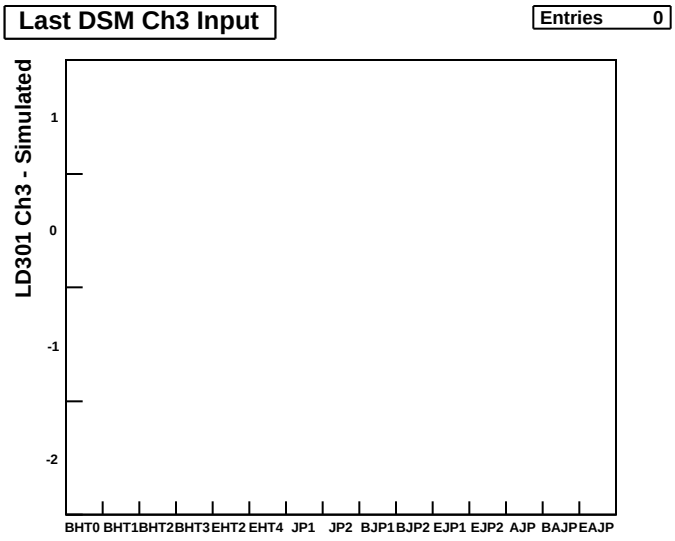

EMC L2 Input - HT01 bits/Partial JP ID
Entries 552600
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

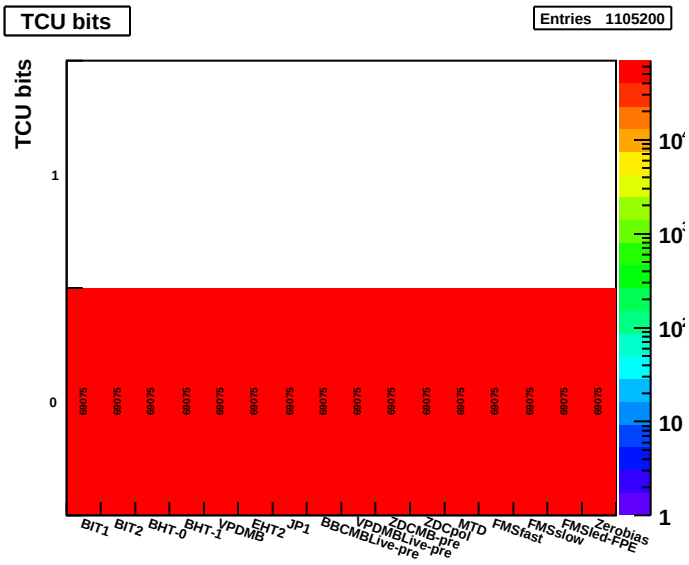

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

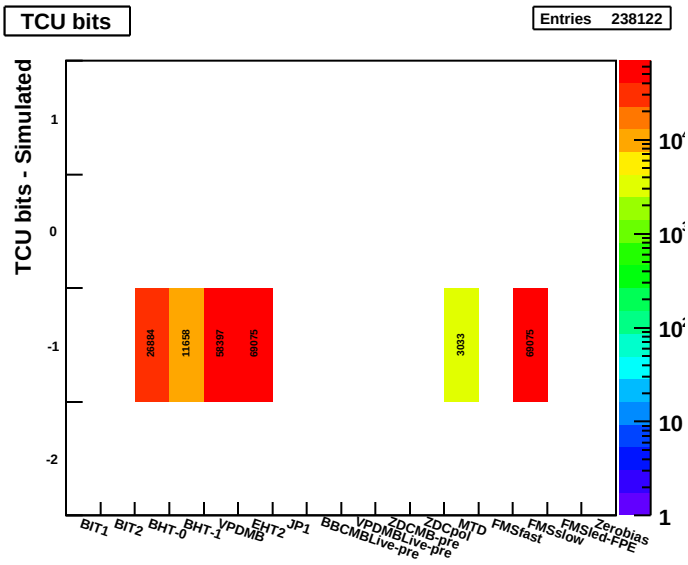

EMC L2 Input - HT23 bits
Entries 552600
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

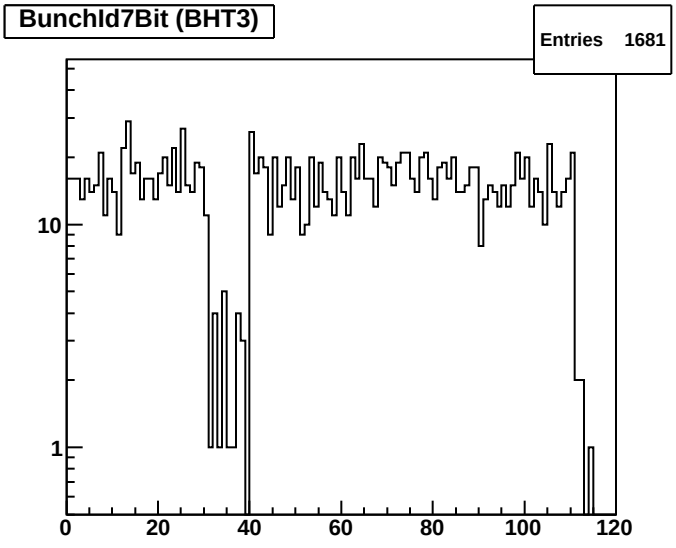

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

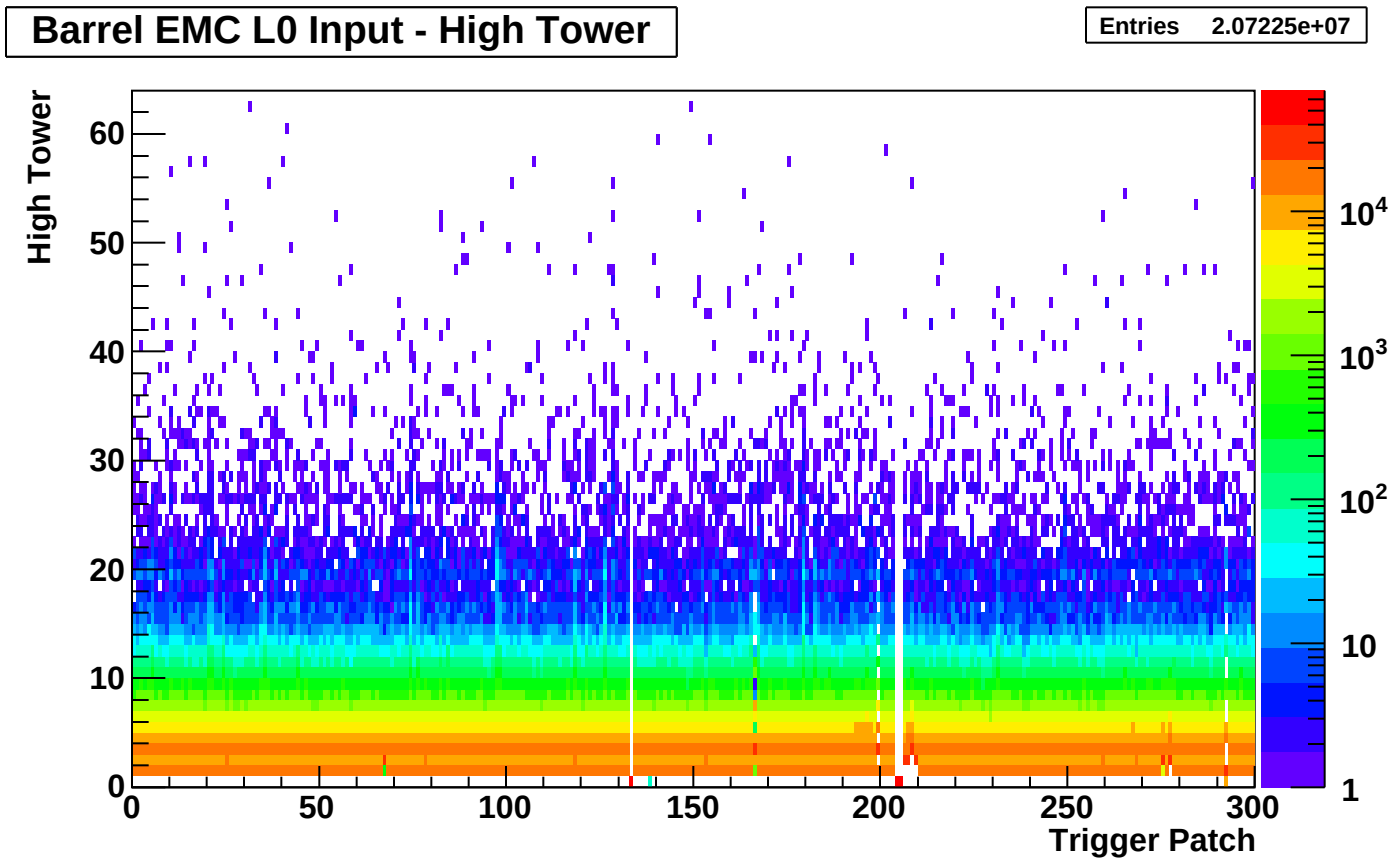

Last DSM Ch3 Input
Entries 1036125
LD301 Ch3
1
0
31062
11658
7480
1681
69075
69075
69075
69075
53288
50738
69075
69075
53534
52521
53199
38013
57417
61595
67394
15777
18337
15541
16554
15876
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP
10
1

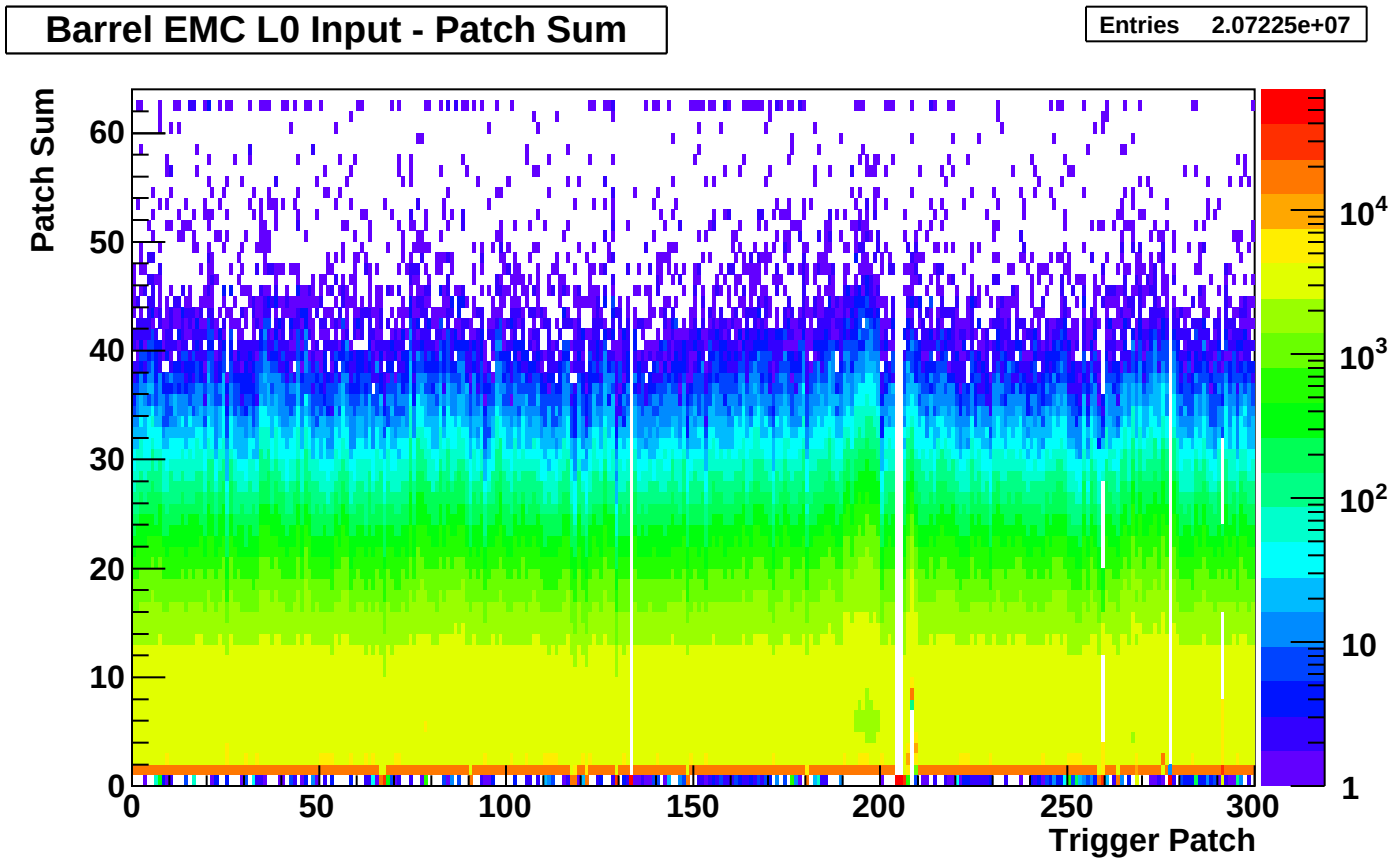

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
1
0
-1
-2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

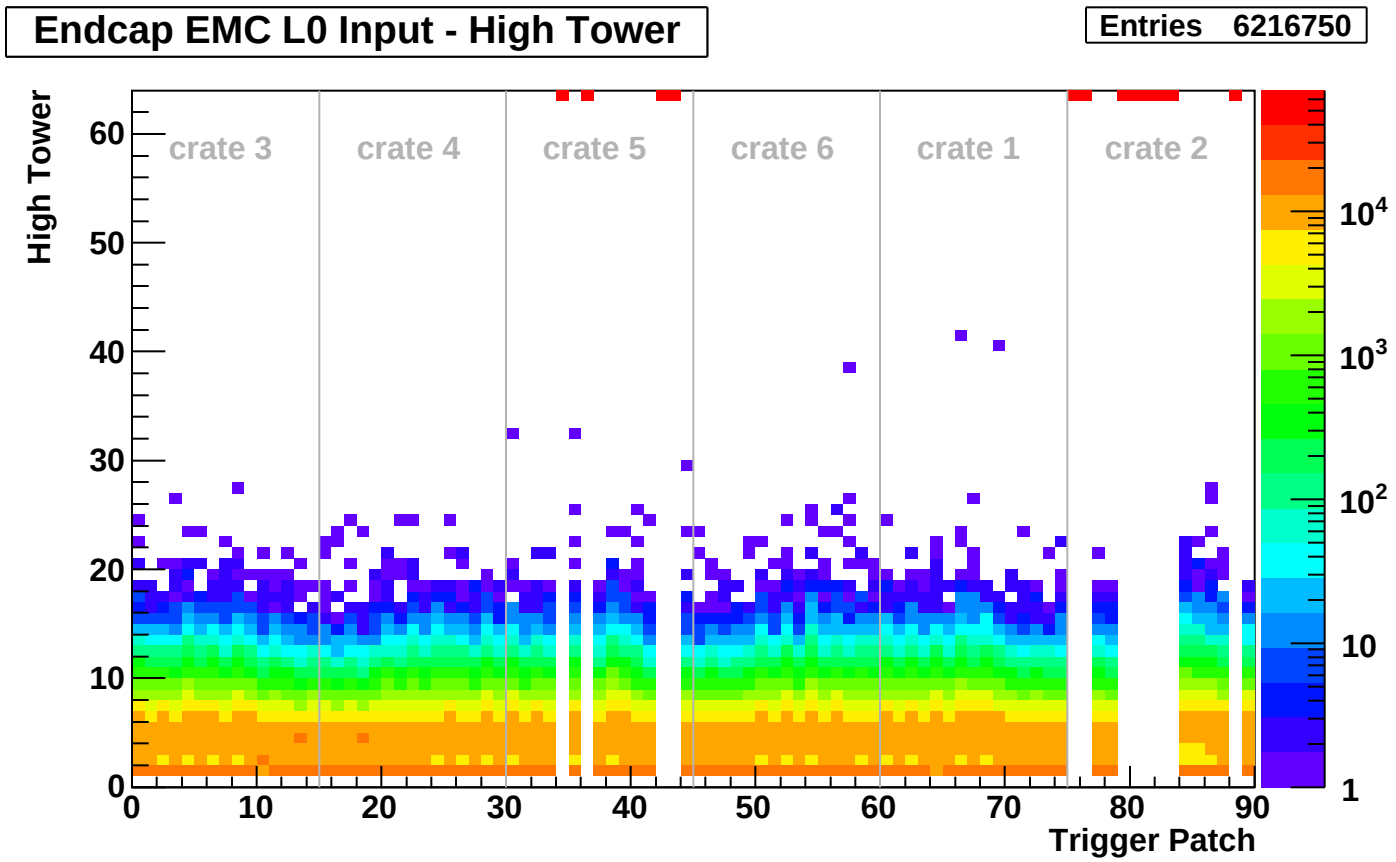

TCU bits
Entries 1105200
TCU bits
1
0
69075
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias
10
1

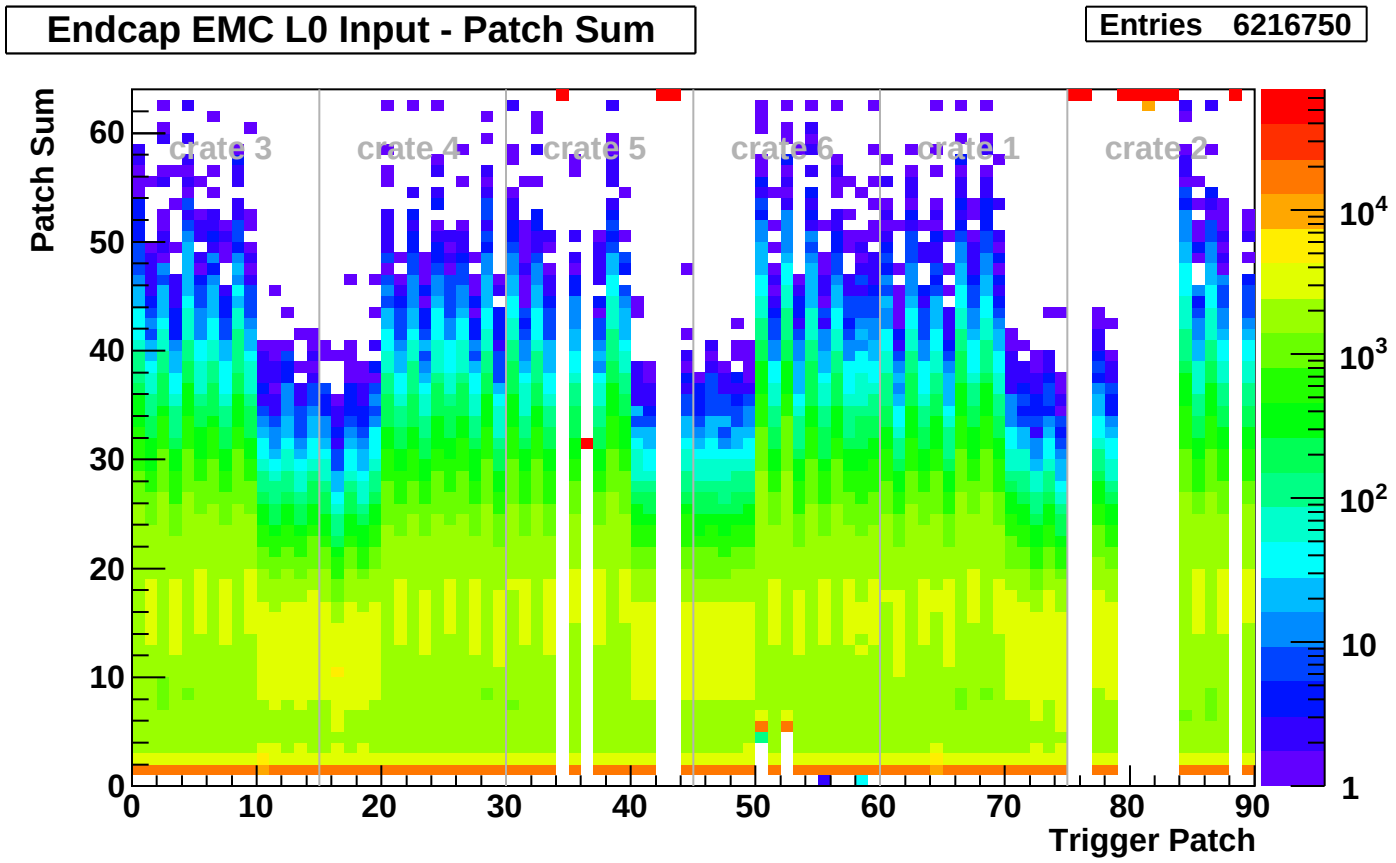

TCU bits
Entries 238122
TCU bits - Simulated
1
0
-1
-2
26884
11658
58397
69075
3033
69075
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias
10
1

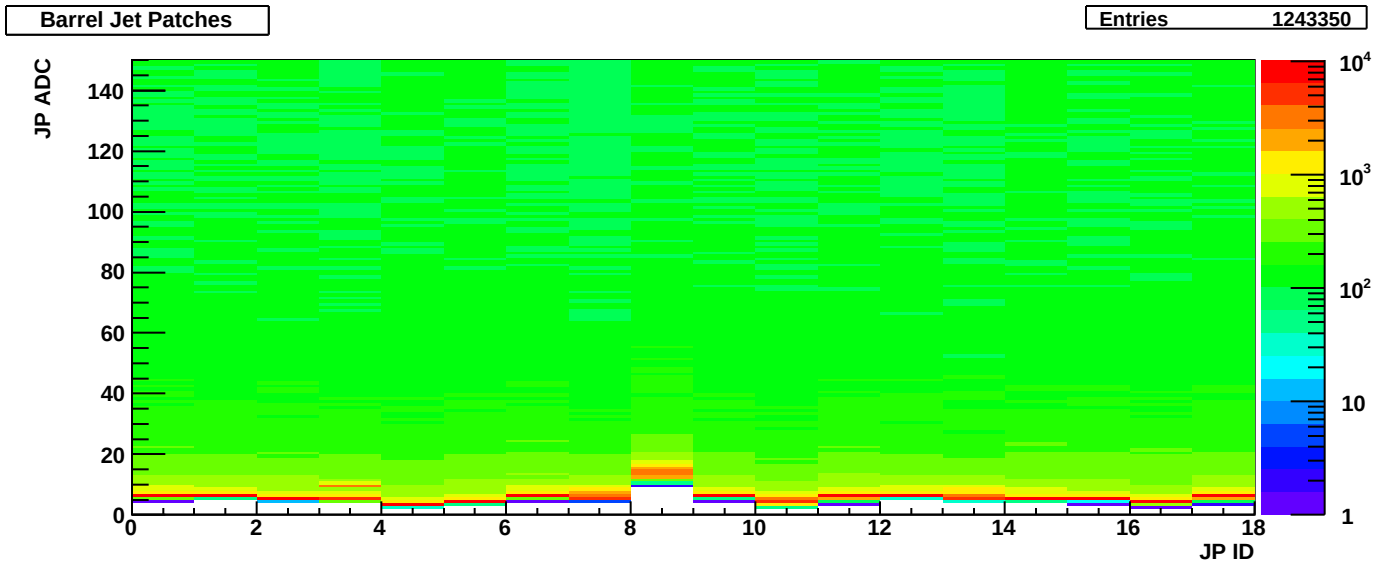

BunchId7Bit (BHT3)
Entries 1681
10
1
0
20
40
60
80
100
120

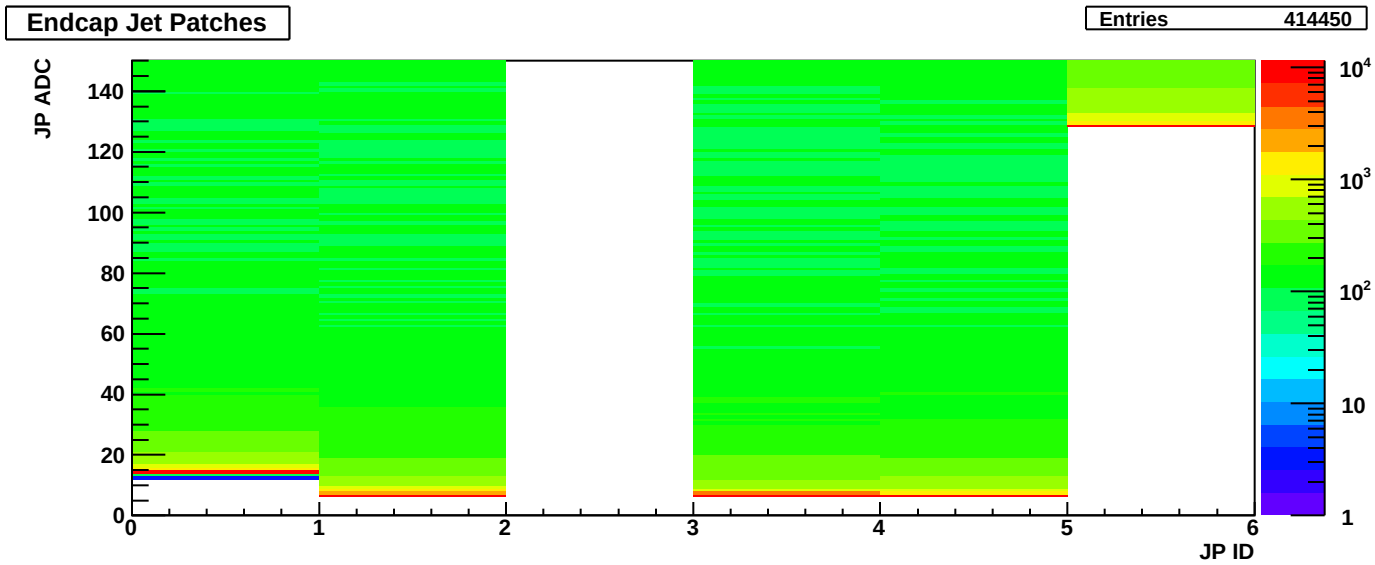

Barrel EMC L0 Input - High Tower
Entries 2.07225e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

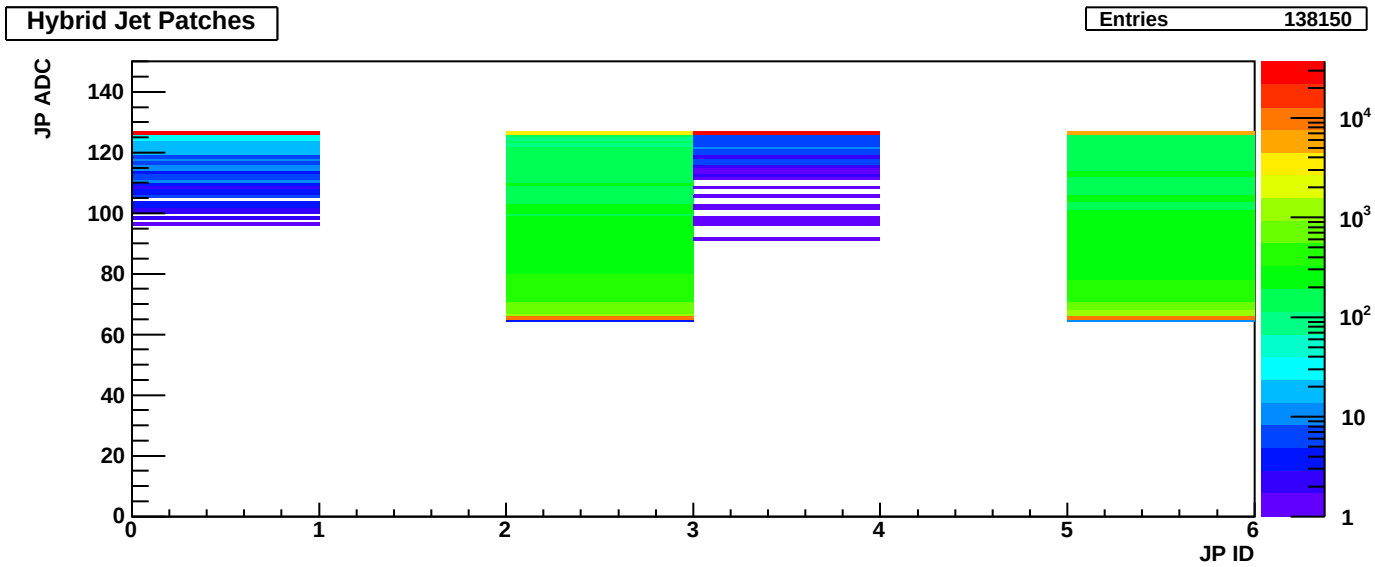

Barrel EMC L0 Input - Patch Sum
Entries 2.07225e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

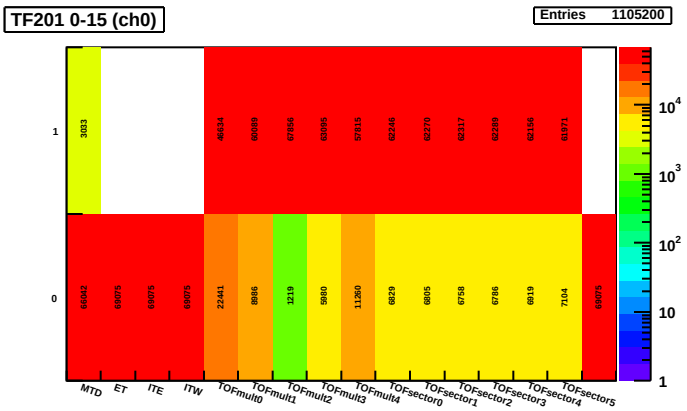

Endcap EMC L0 Input - High Tower
Entries 6216750
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

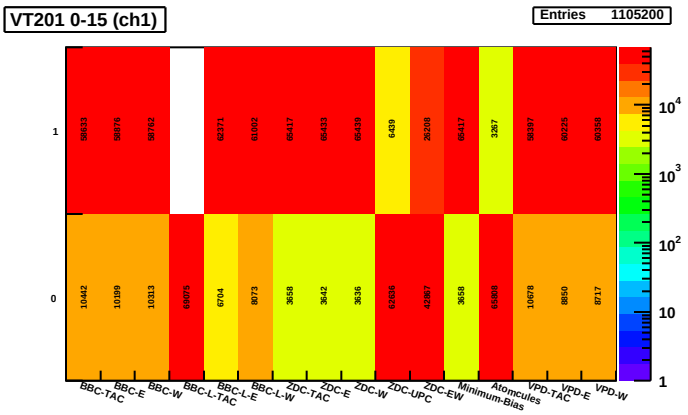

Endcap EMC L0 Input - Patch Sum
Entries 6216750
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

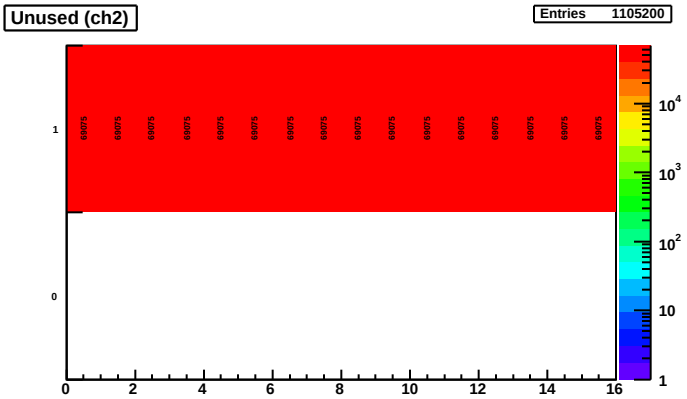

Barrel Jet Patches
Entries 1243350
JP ADC
JP ID

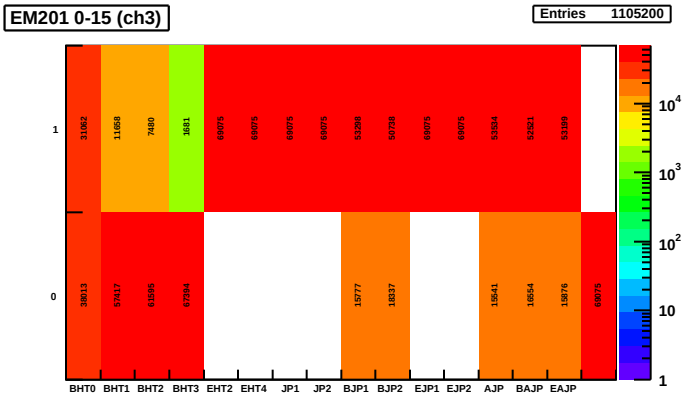

Endcap Jet Patches
Entries 414450
JP ADC
JP ID

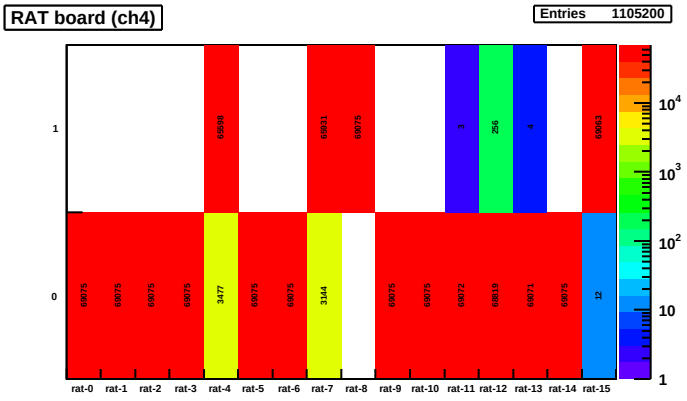

Hybrid Jet Patches
Entries 138150
JP ADC
JP ID

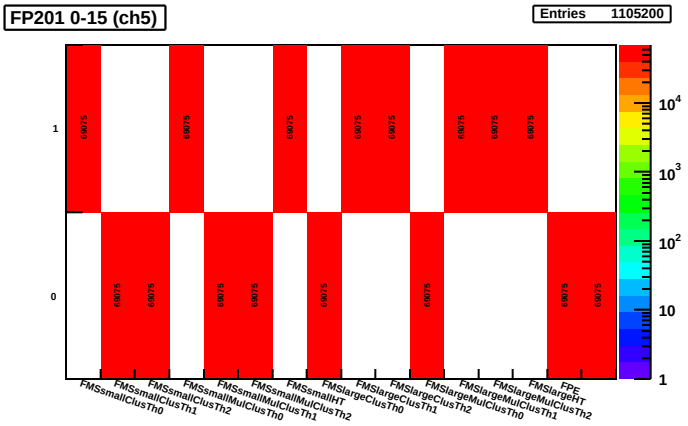

TF201 0-15 (ch0)
Entries 1105200
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

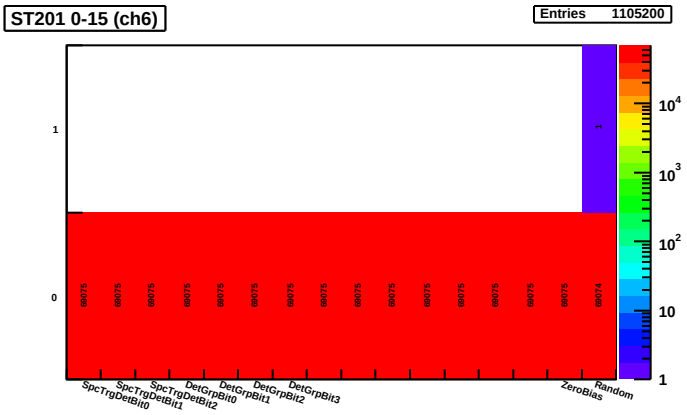

VT201 0-15 (ch1)
Entries 1105200
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

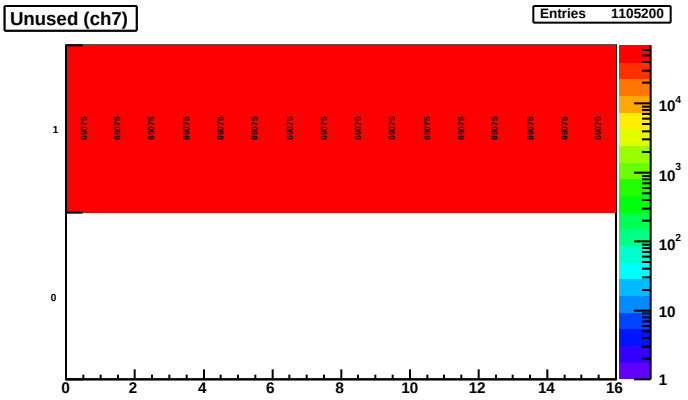

Unused (ch2)
Entries 1105200

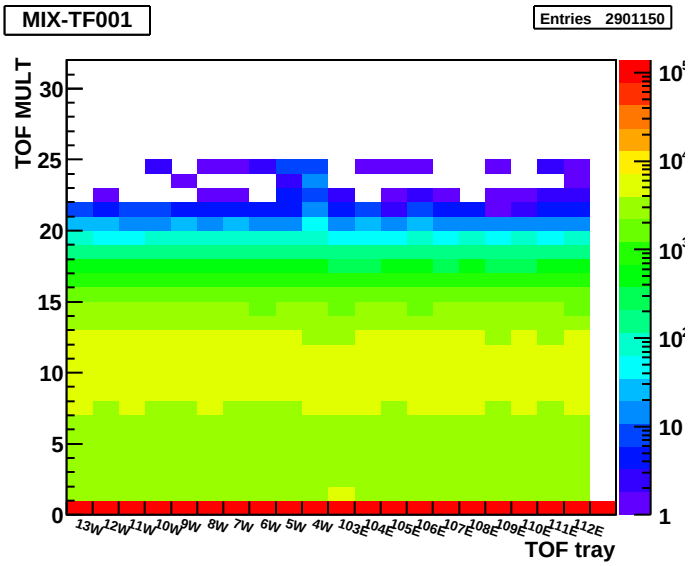

EM201 0-15 (ch3)
Entries 1105200
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

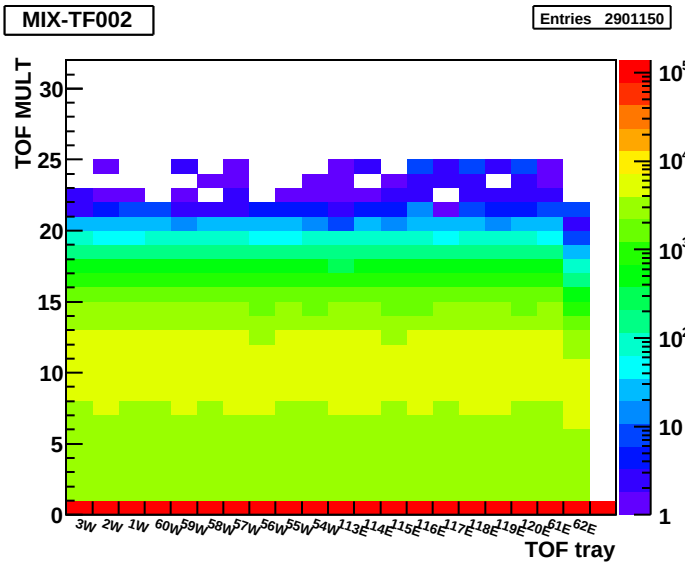

RAT board (ch4)
Entries 1105200
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

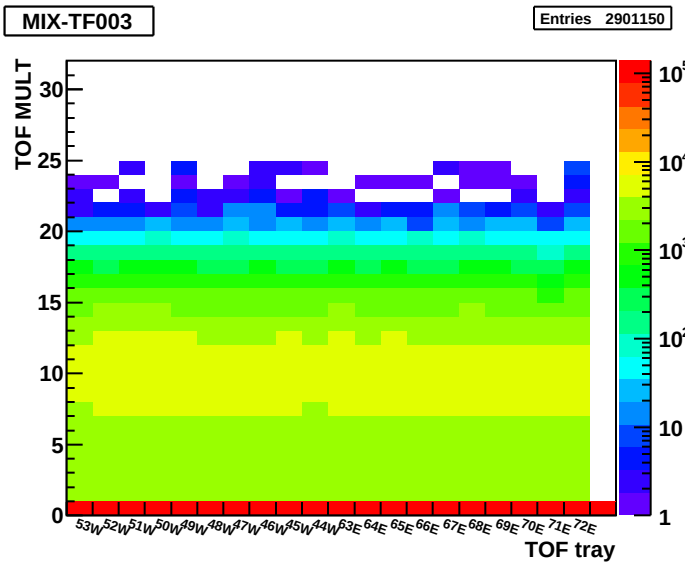

FP201 0-15 (ch5)
Entries 1105200
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

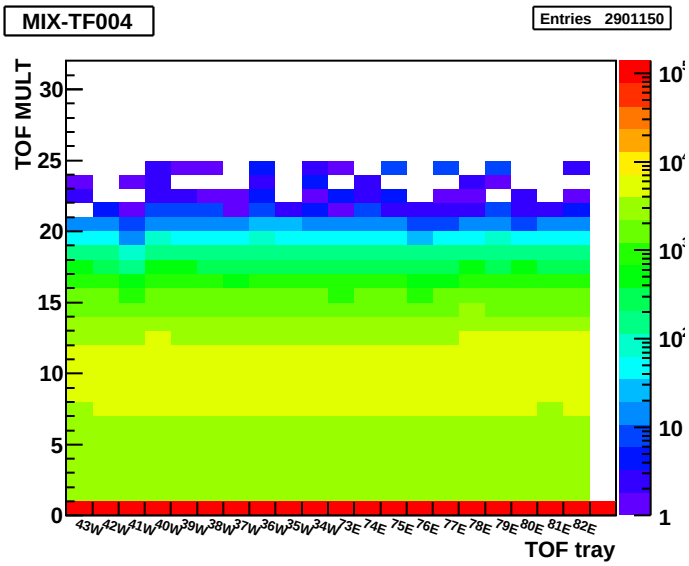

ST201 0-15 (ch6)
Entries 1105200
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

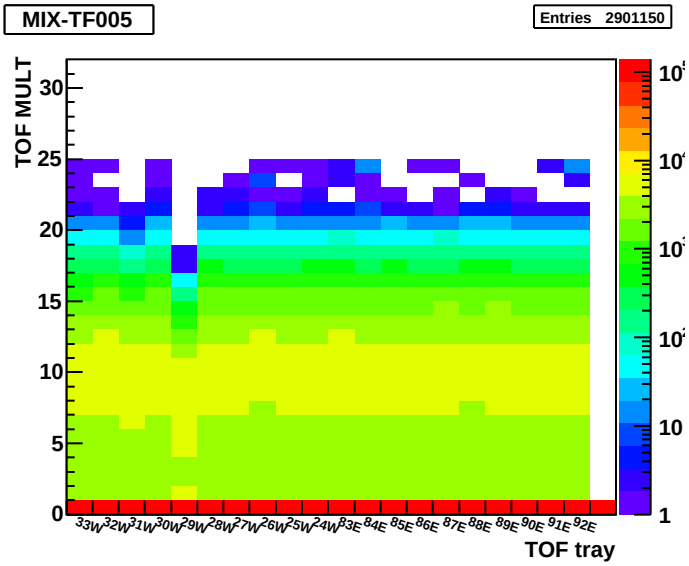

Unused (ch7)
Entries 1105200

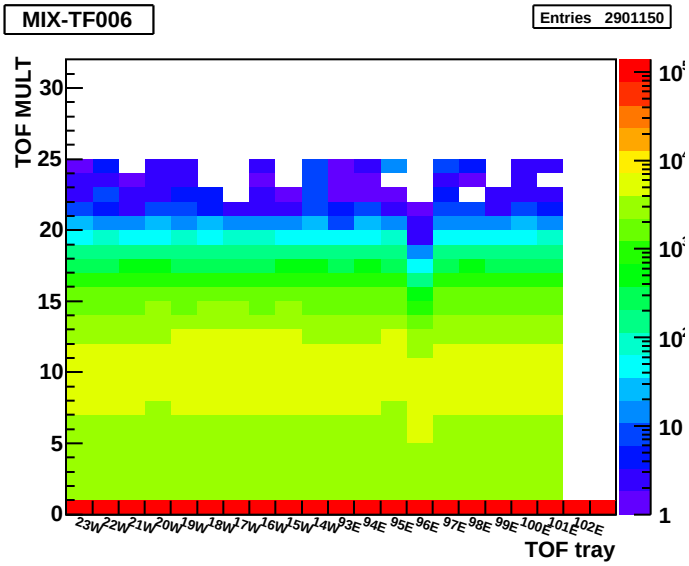

MIX-TF001
Entries 2901150
TOF MULT
TOF tray

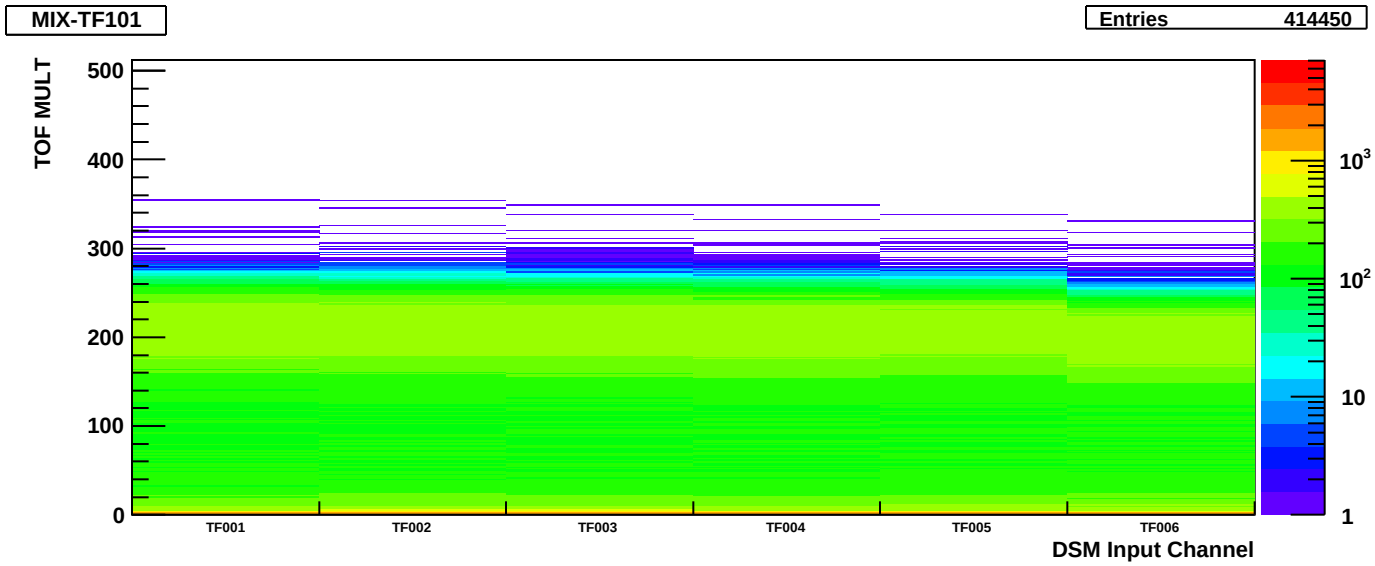

MIX-TF002
Entries 2901150
TOF MULT
TOF tray

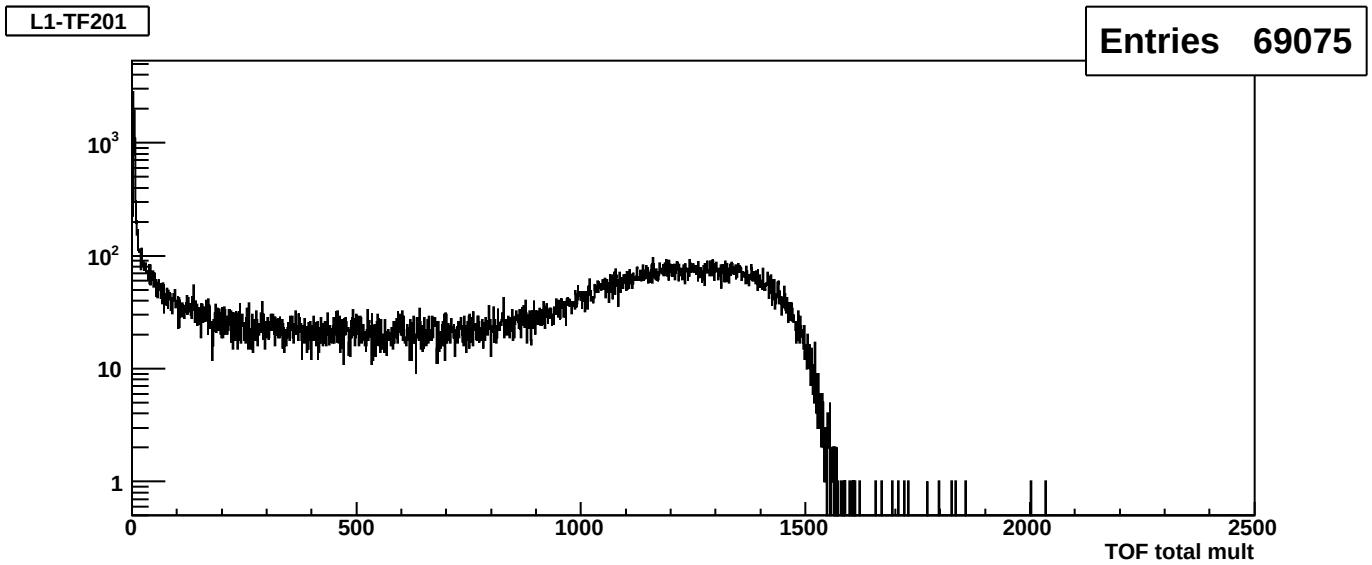

MIX-TF003
Entries 2901150
TOF MULT
TOF tray

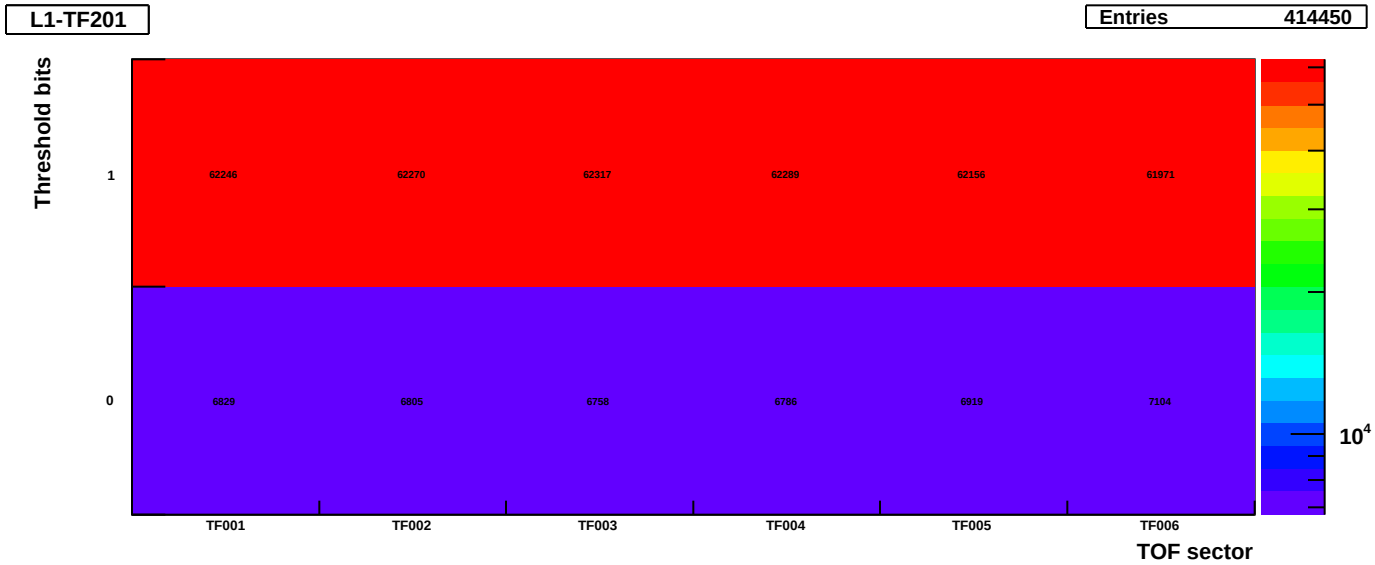

MIX-TF004
Entries 2901150
TOF MULT
TOF tray

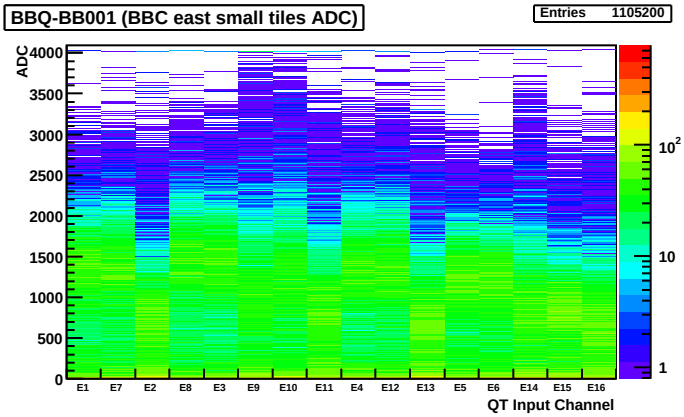

MIX-TF005
Entries 2901150
TOF MULT
TOF tray

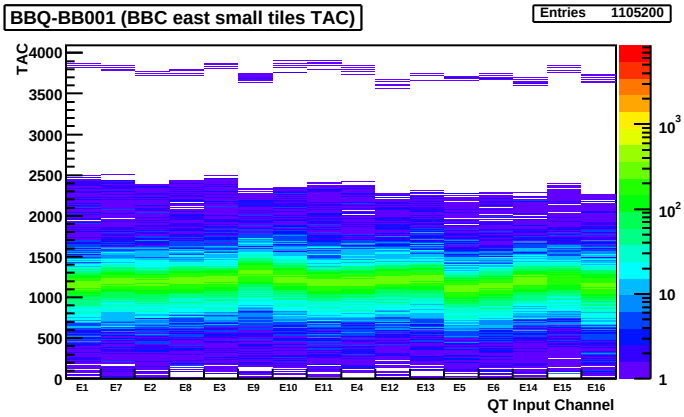

MIX-TF006
Entries 2901150
TOF MULT
TOF tray

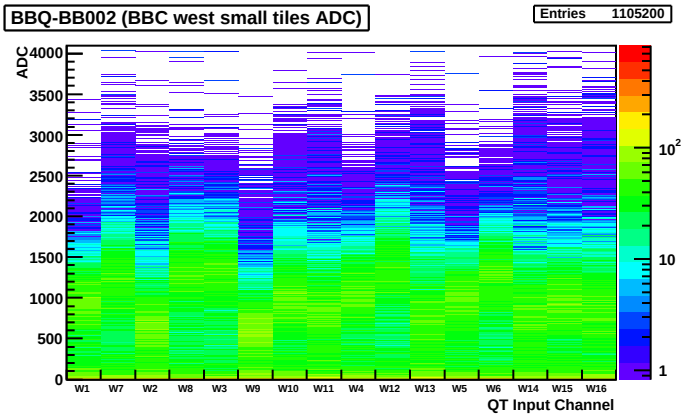

MIX-TF101
Entries 414450
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^3
10^2
10
1

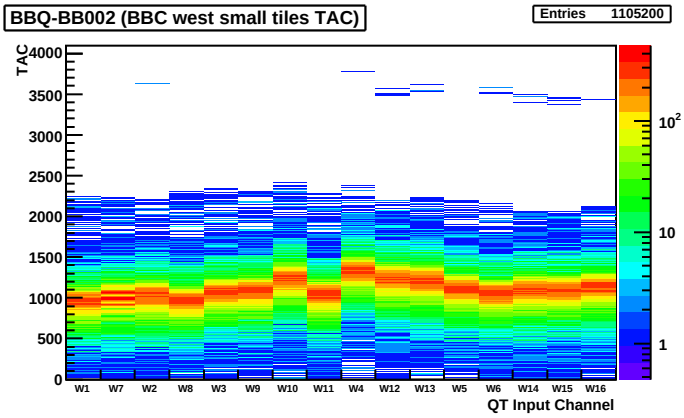

L1-TF201
Entries 69075
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

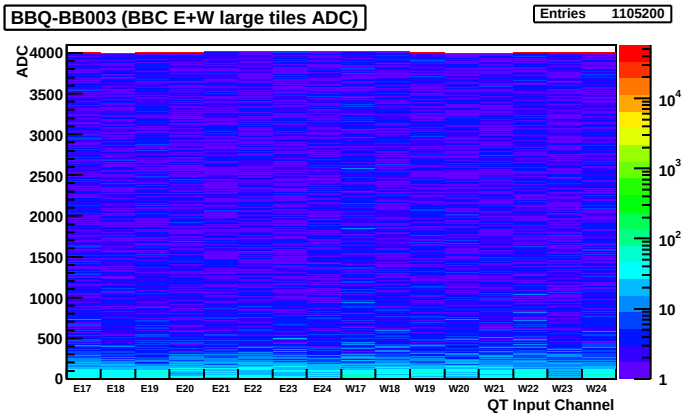

L1-TF201
Entries 414450
Threshold bits
1
0
62246
62270
62317
62289
62156
61971
6829
6805
6758
6786
6919
7104
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4

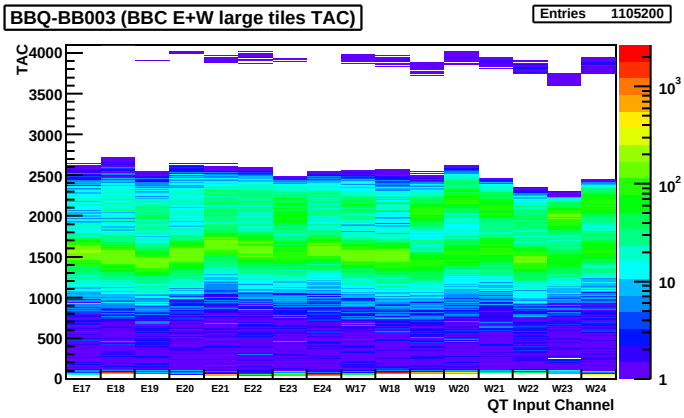

BBQ-BB001 (BBC east small tiles ADC)
Entries 1105200
ADC
QT Input Channel

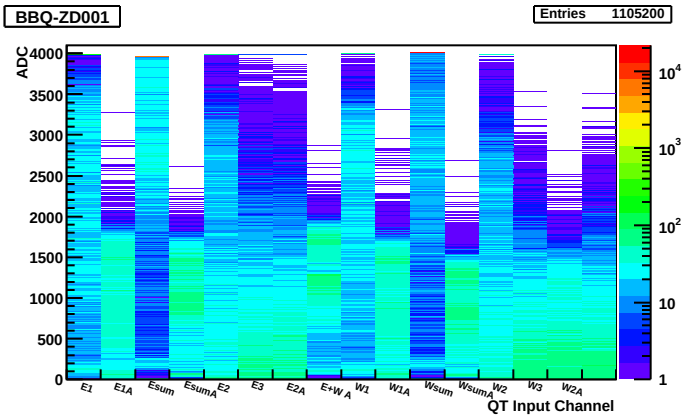

BBQ-BB001 (BBC east small tiles TAC)
Entries 1105200
TAC
QT Input Channel

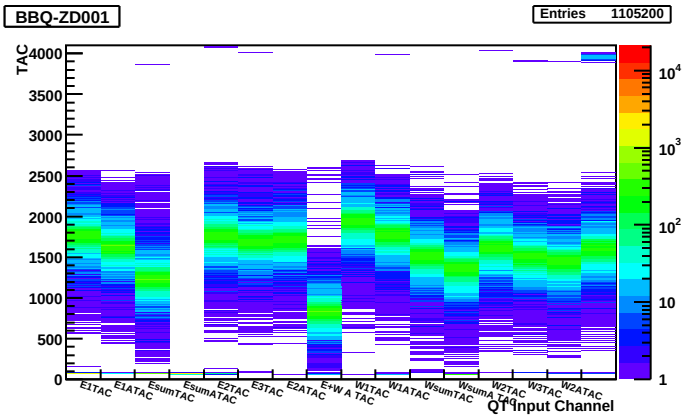

BBQ-BB002 (BBC west small tiles ADC)
Entries 1105200
ADC
QT Input Channel

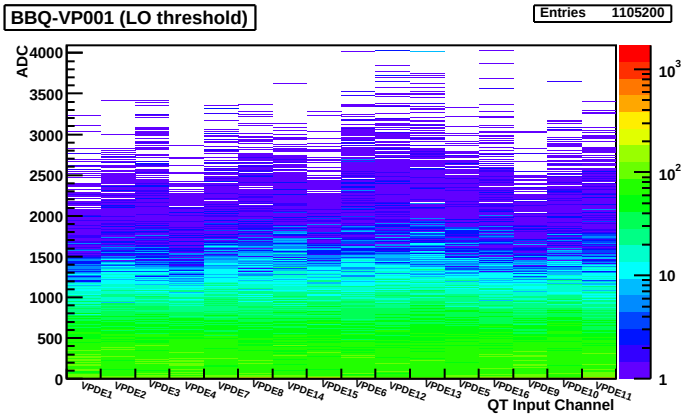

BBQ-BB002 (BBC west small tiles TAC)
Entries 1105200
TAC
QT Input Channel

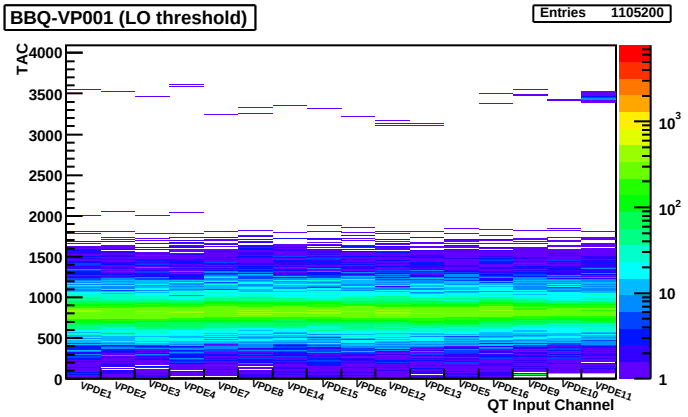

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1105200
ADC
QT Input Channel

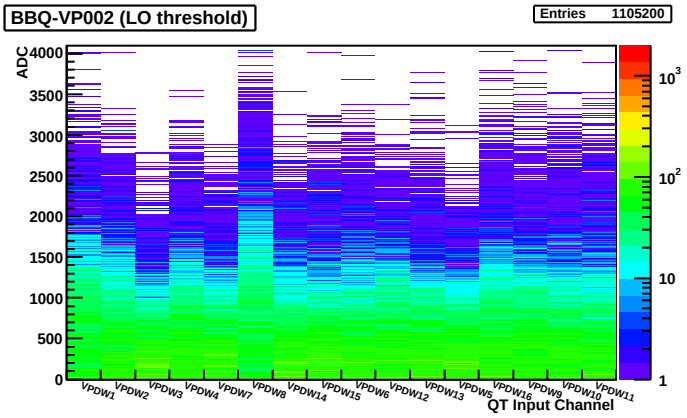

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1105200
TAC
QT Input Channel

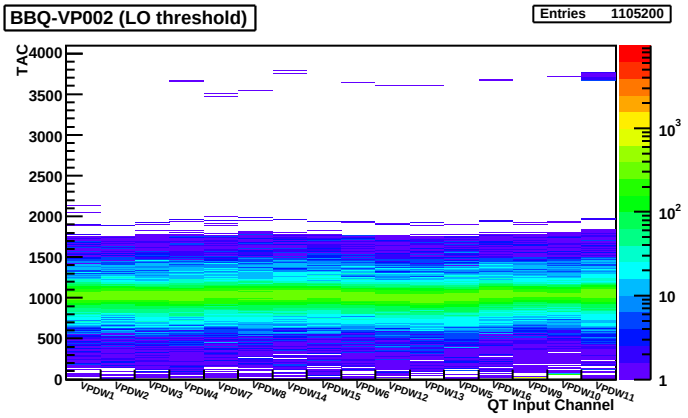

BBQ-ZD001
Entries 1105200
ADC
QT Input Channel

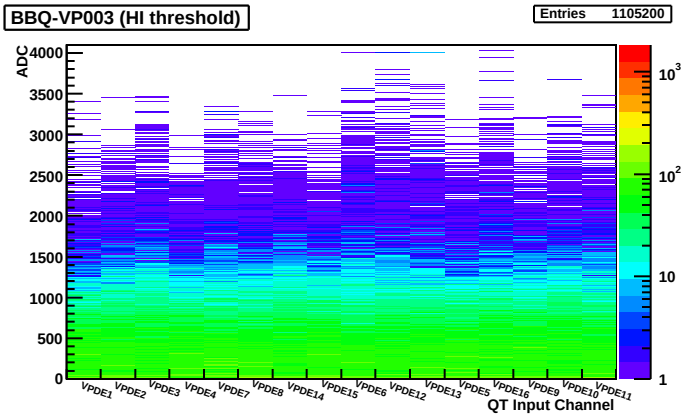

BBQ-ZD001
Entries 1105200
TAC
QT Input Channel

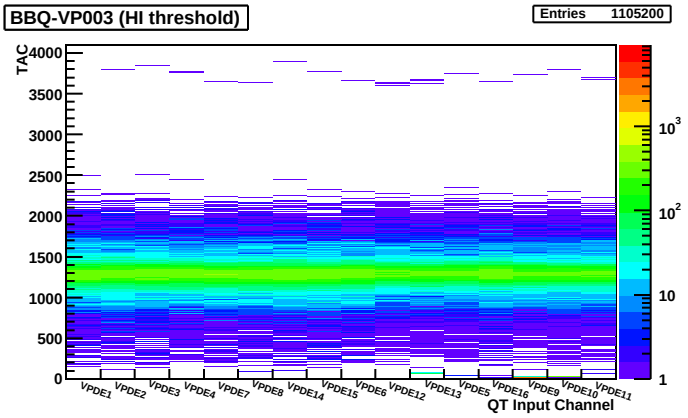

BBQ-VP001 (LO threshold)
Entries 1105200
ADC
QT Input Channel

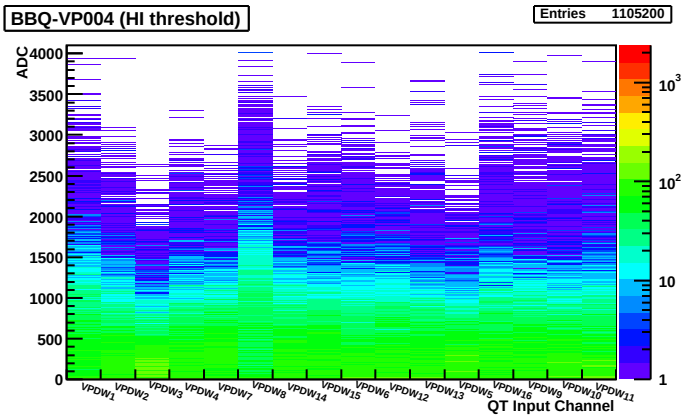

BBQ-VP001 (LO threshold)
Entries 1105200
TAC
QT Input Channel

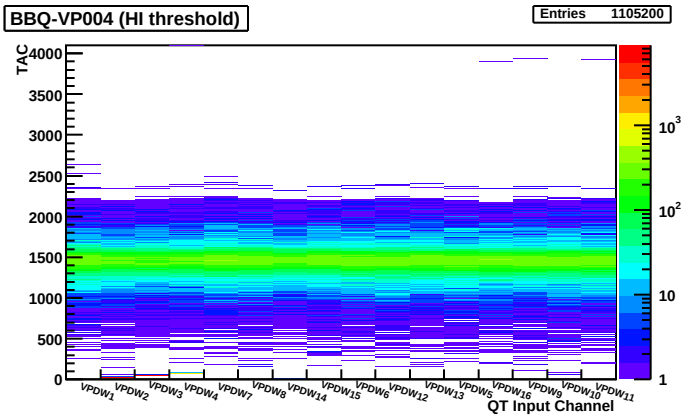

BBQ-VP002 (LO threshold)
Entries 1105200
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 1105200
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 1105200
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 1105200
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 1105200
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 1105200
TAC
QT Input Channel