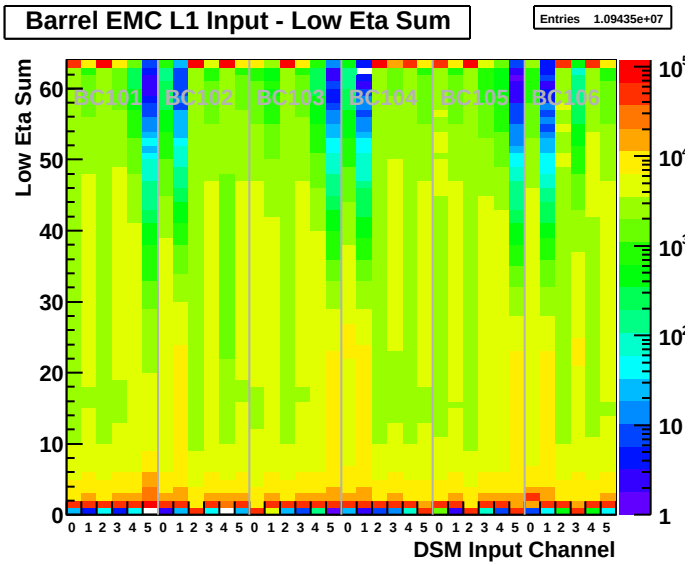
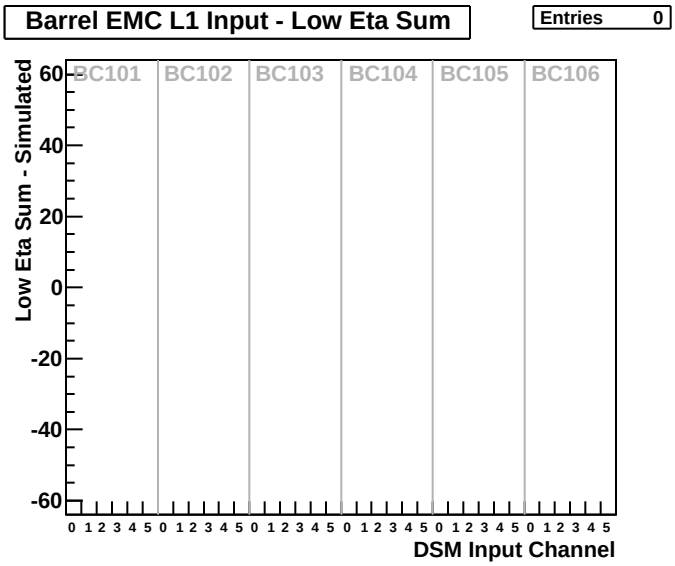
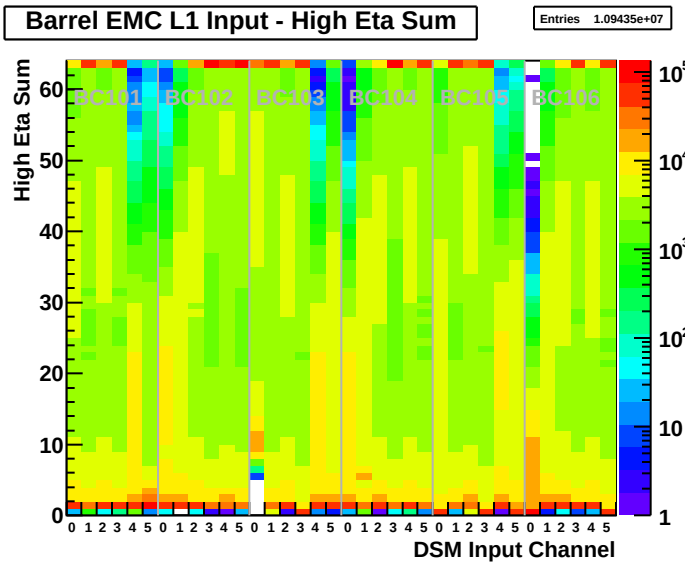
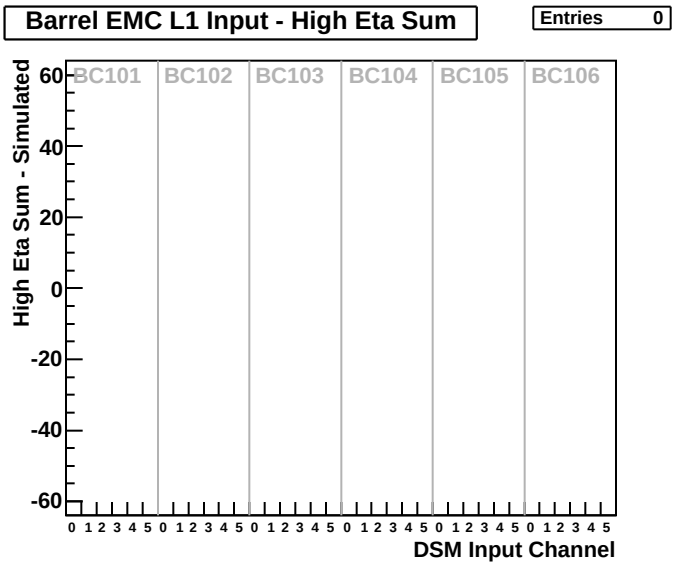
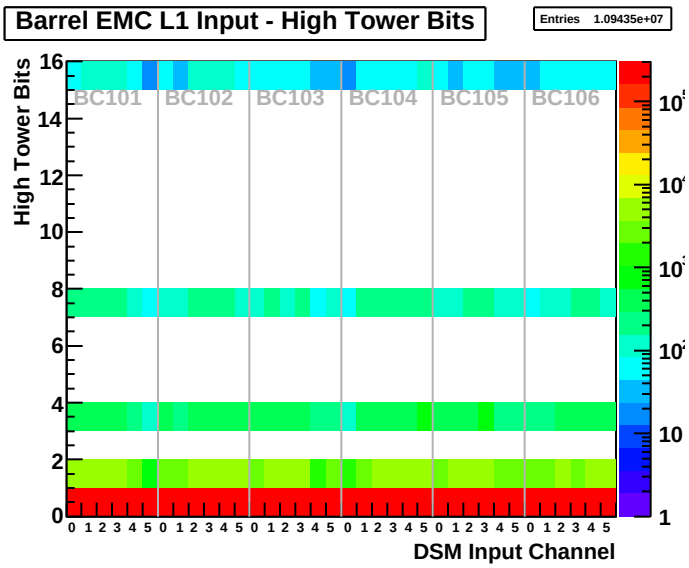
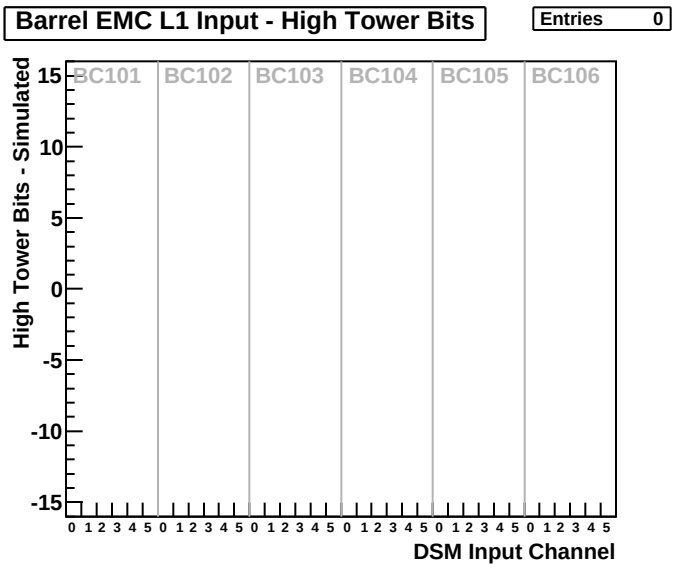

Barrel EMC L1 Input - Low Eta Sum
Entries 1.09435e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

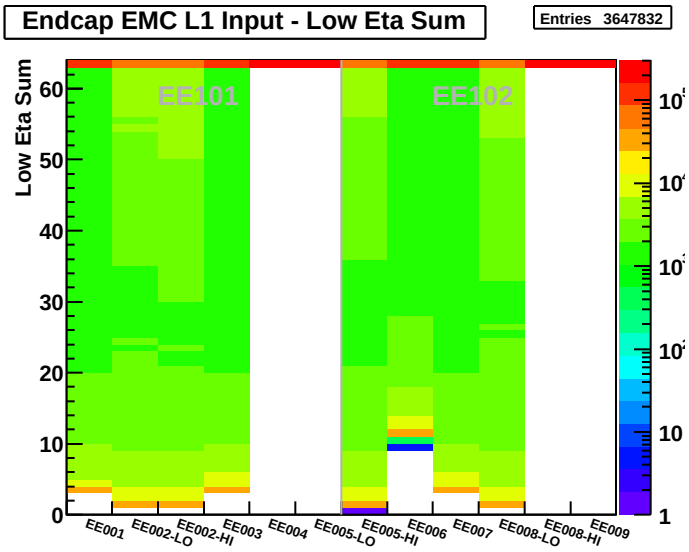

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

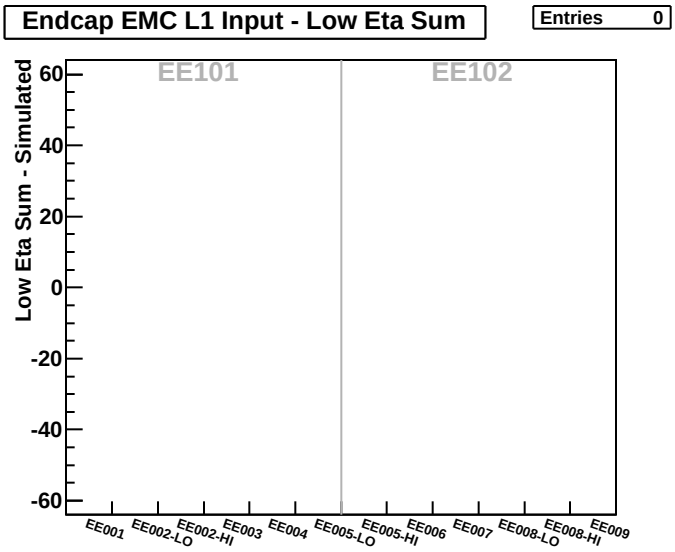

Barrel EMC L1 Input - High Eta Sum
Entries 1.09435e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

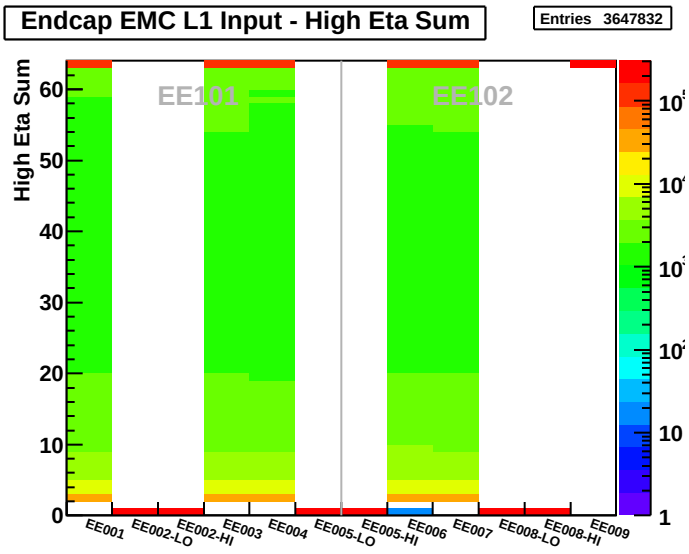

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

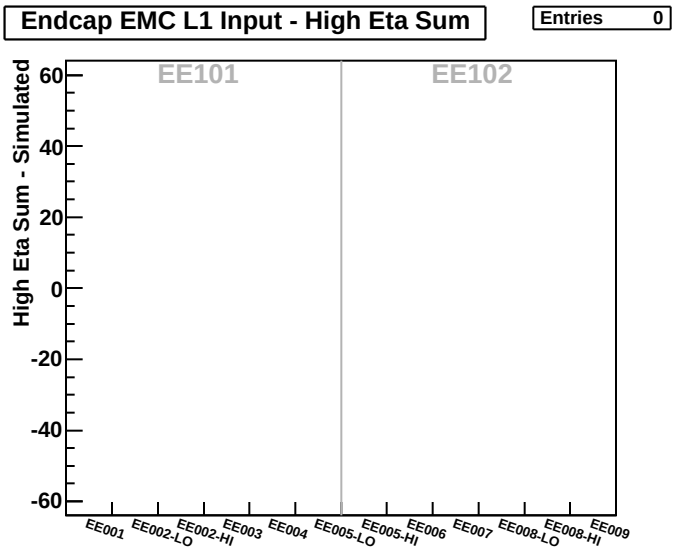

Barrel EMC L1 Input - High Tower Bits
Entries 1.09435e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

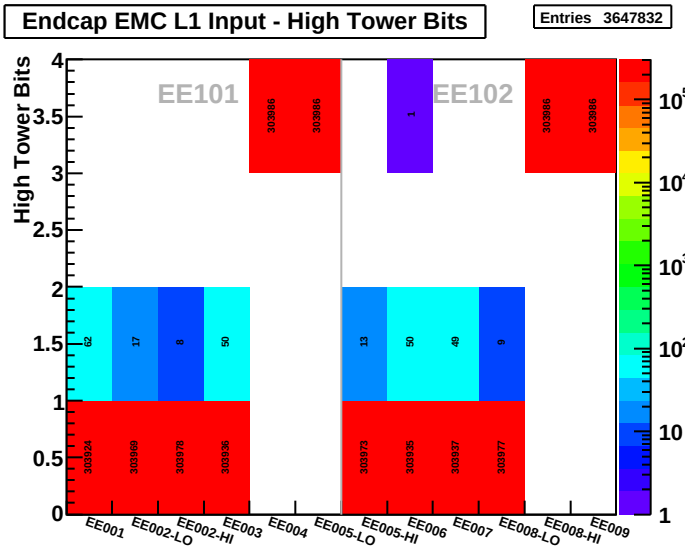

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

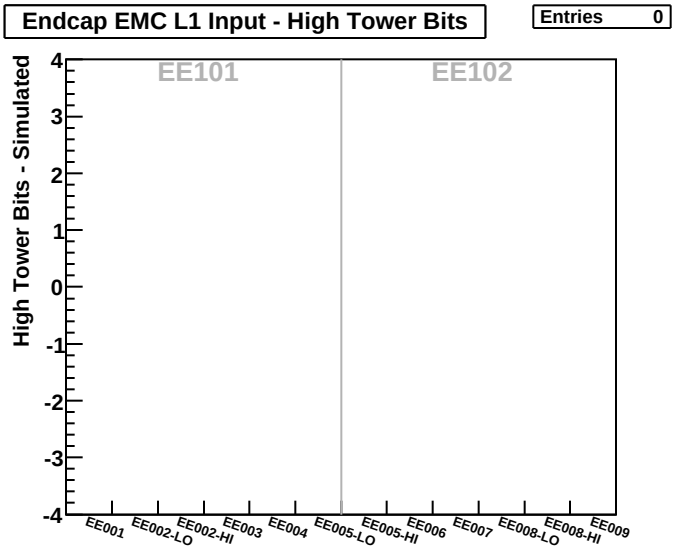

Endcap EMC L1 Input - Low Eta Sum
Entries 3647832
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

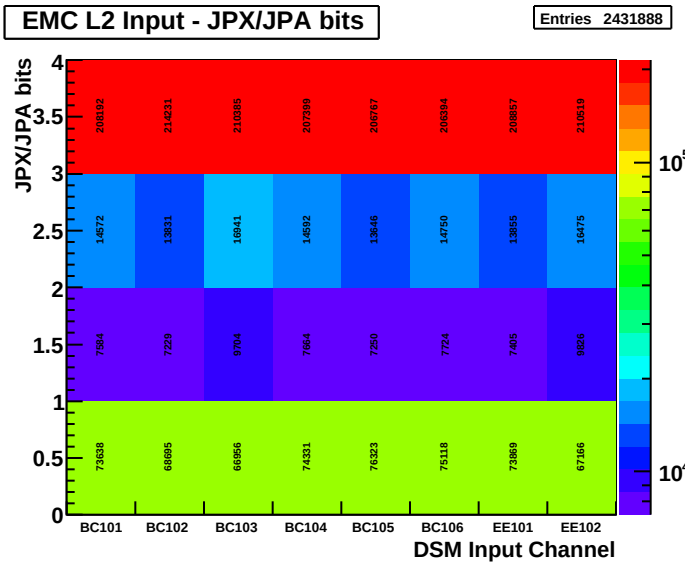

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

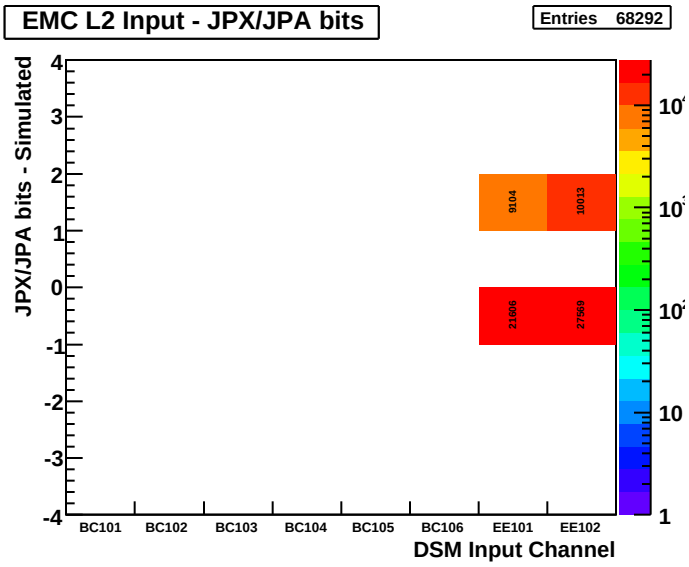

Endcap EMC L1 Input - High Eta Sum
Entries 3647832
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

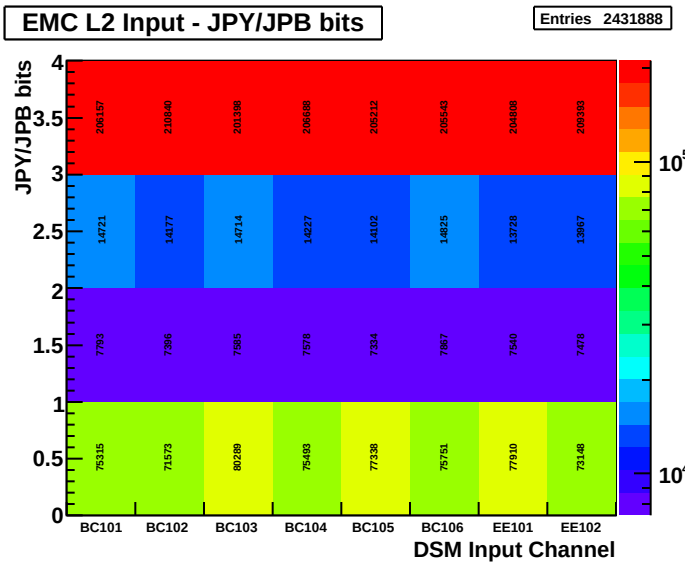

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

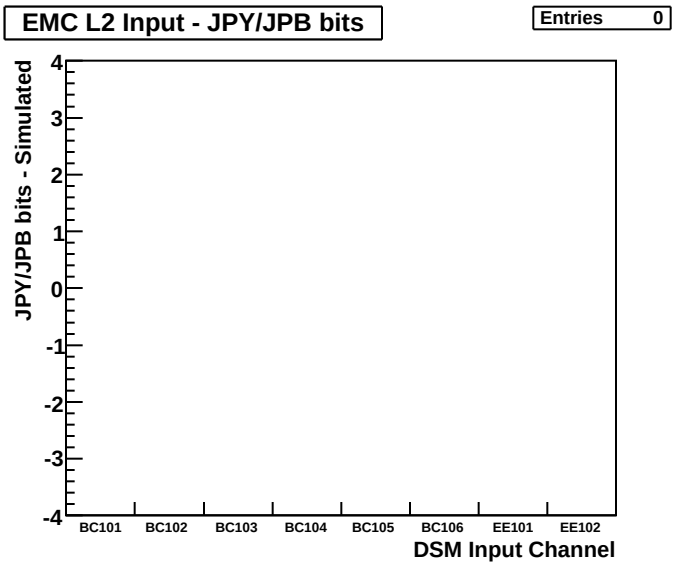

Endcap EMC L1 Input - High Tower Bits
Entries 3647832
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

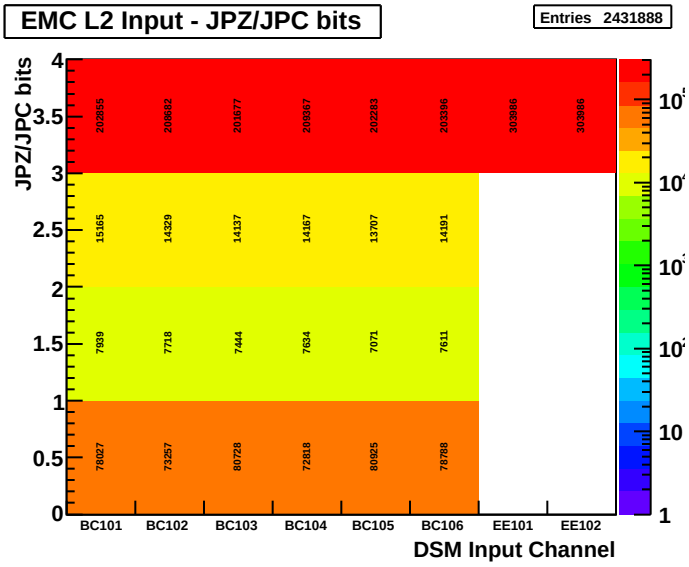

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

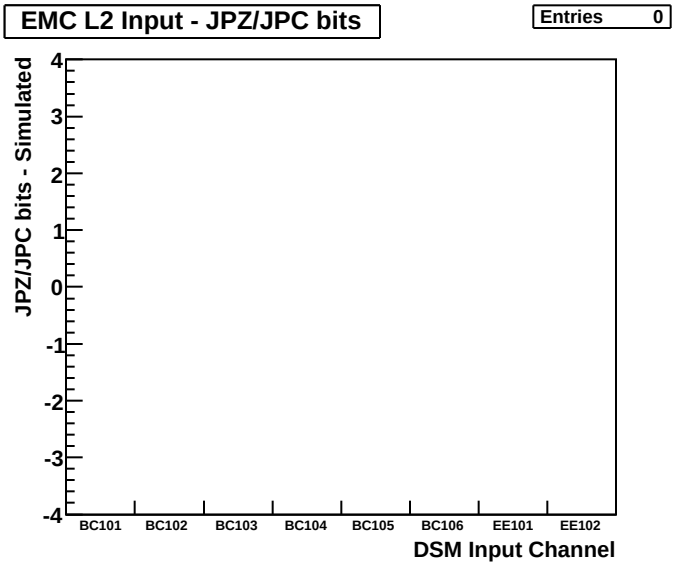

EMC L2 Input - JPX/JPA bits
Entries 2431888
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
206192 214231 210385 207399 206767 206394 208857 210519
14572 13831 16941 14592 13646 14750 13855 16475
7584 7229 9704 7664 7250 7724 7405 9626
73638 68695 66956 74331 76323 75118 73869 67166

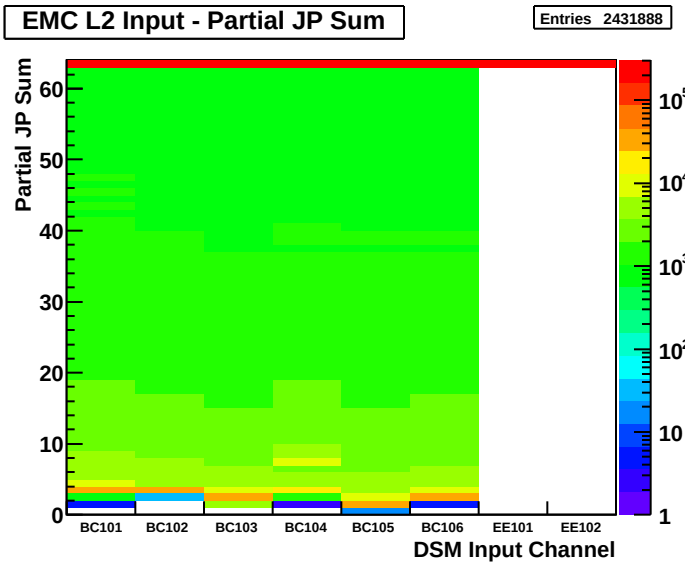

EMC L2 Input - JPX/JPA bits
Entries 68292
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
9104 10013
21606 27559

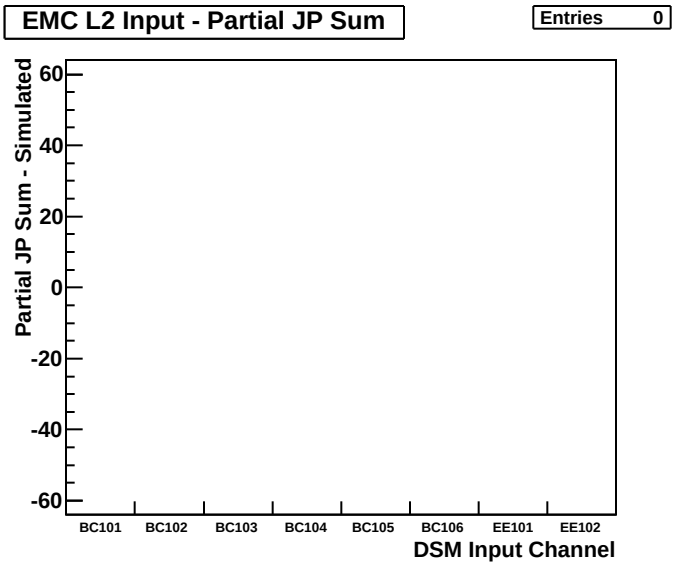

EMC L2 Input - JPY/JPB bits
Entries 2431888
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
206157 210840 201388 206688 205212 205543 204808 209333
14721 14177 14714 14227 14102 14825 13728 13967
7793 7396 7585 7578 7334 7857 7540 7478
75315 71573 80289 75493 77338 75751 77910 73148

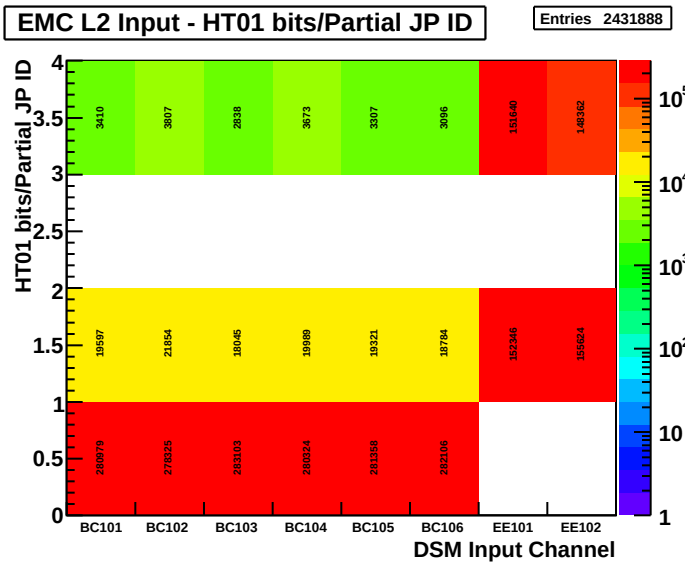

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

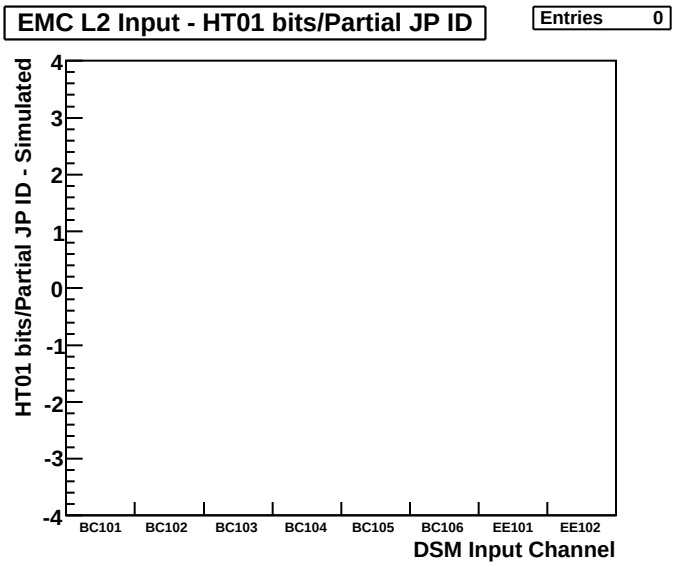

EMC L2 Input - JPZ/JPC bits
Entries 2431888
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
202855 208682 201677 203367 202283 203396 303986 303986
15165 14329 14137 14167 13707 14191
7939 7718 7444 7634 7071 7611
78027 73357 80728 72818 80925 78788

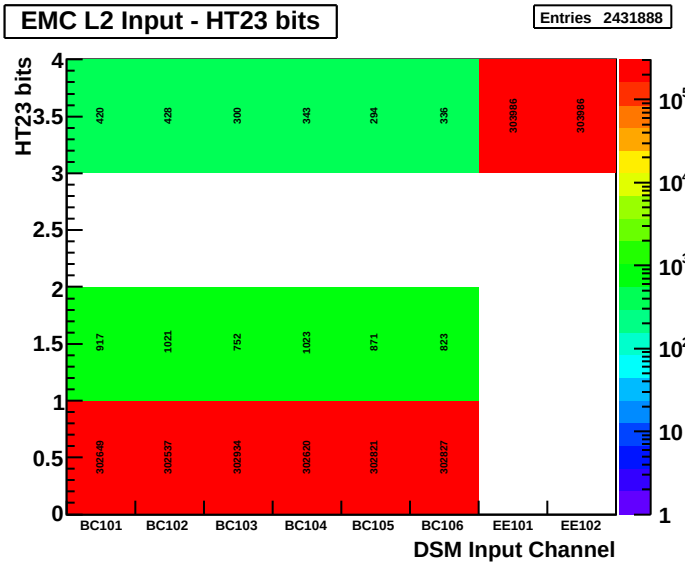

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

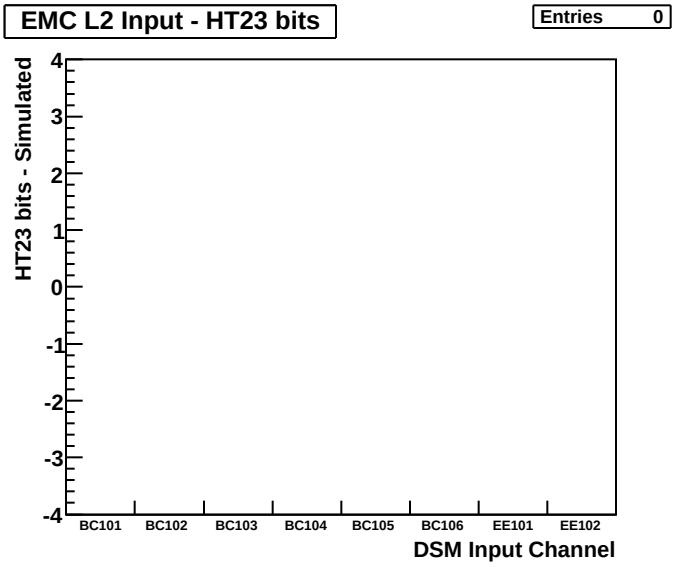

EMC L2 Input - Partial JP Sum
Entries 2431888
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

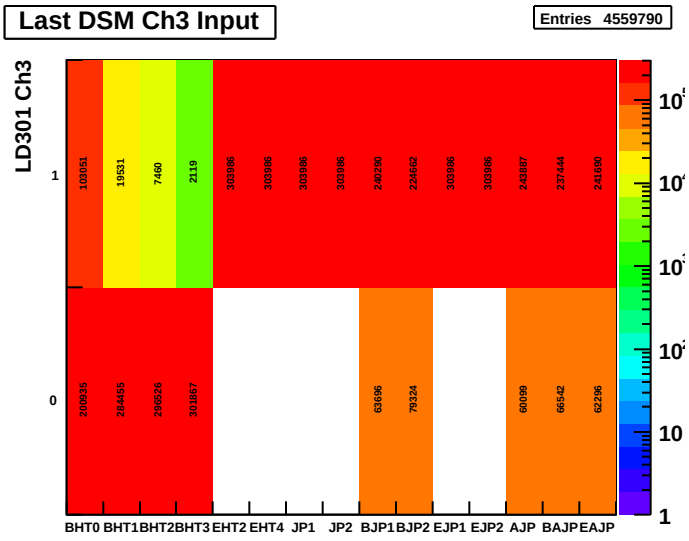

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

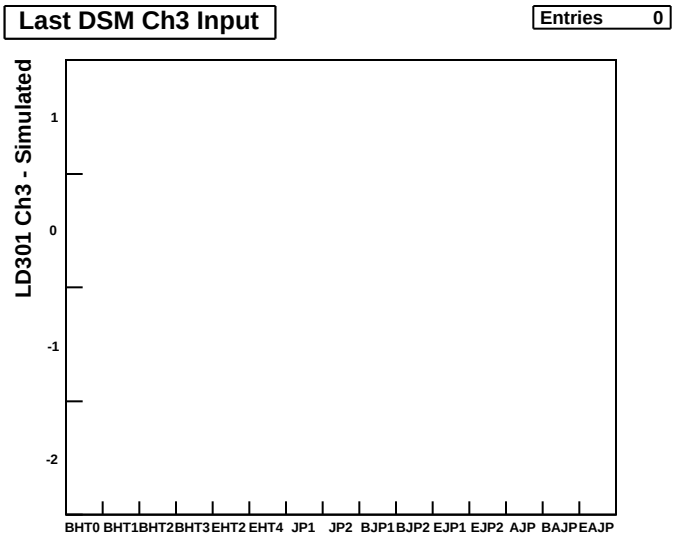

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2431888
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

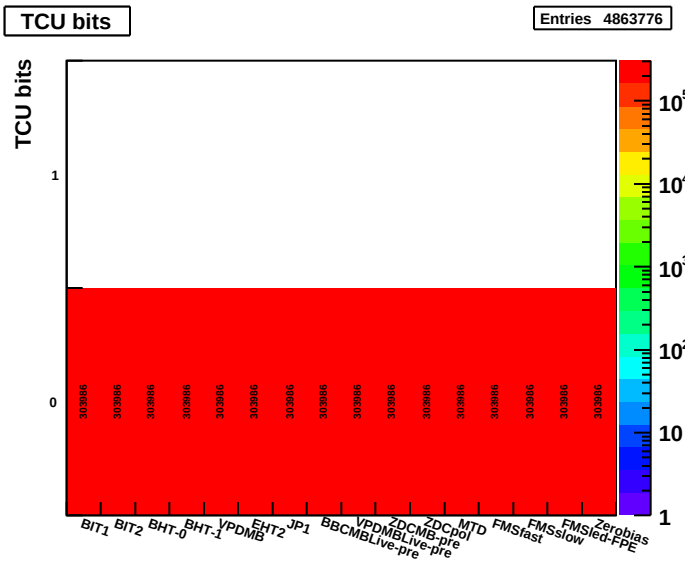

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

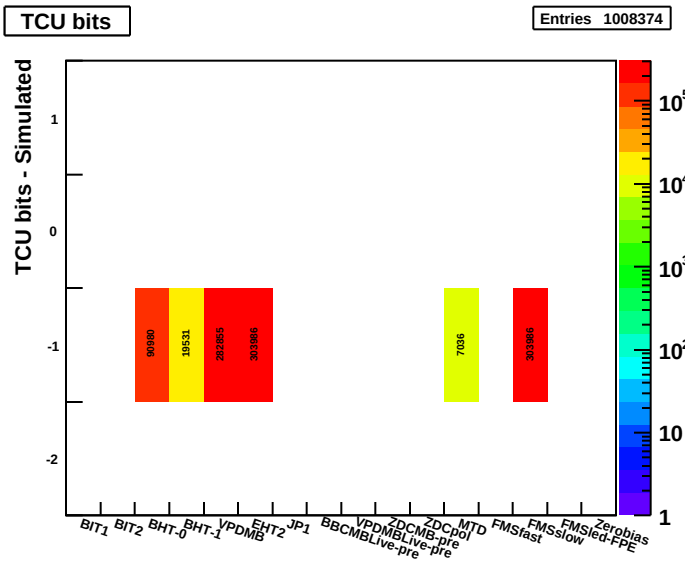

EMC L2 Input - HT23 bits
Entries 2431888
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

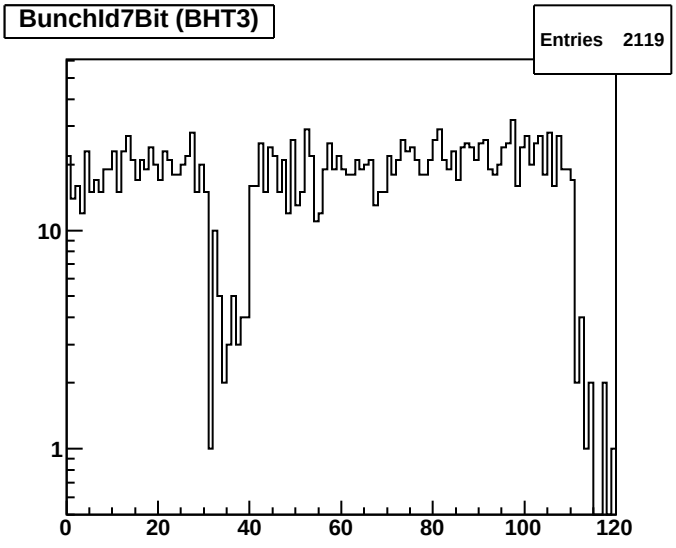

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

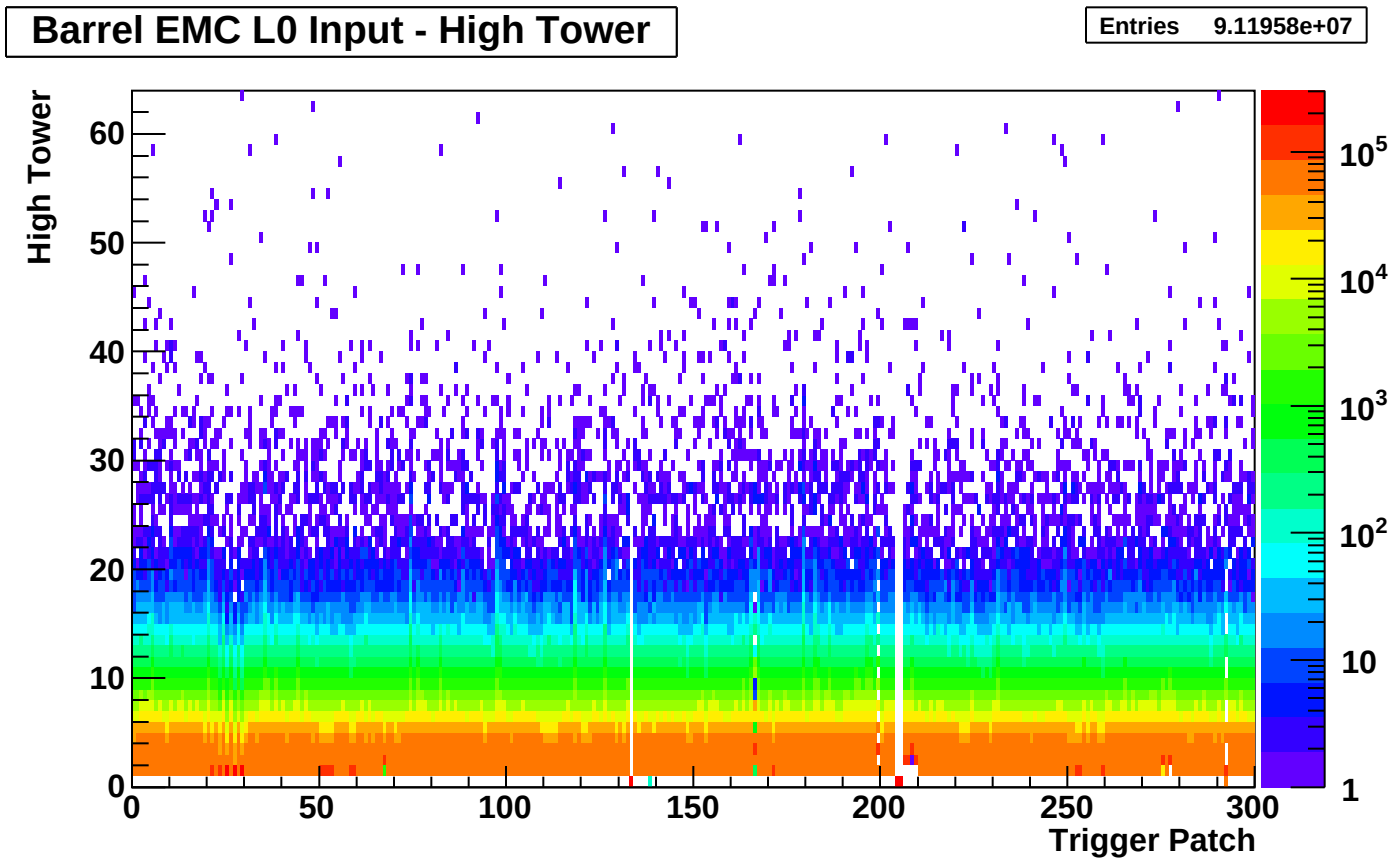

Last DSM Ch3 Input
Entries 4559790
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

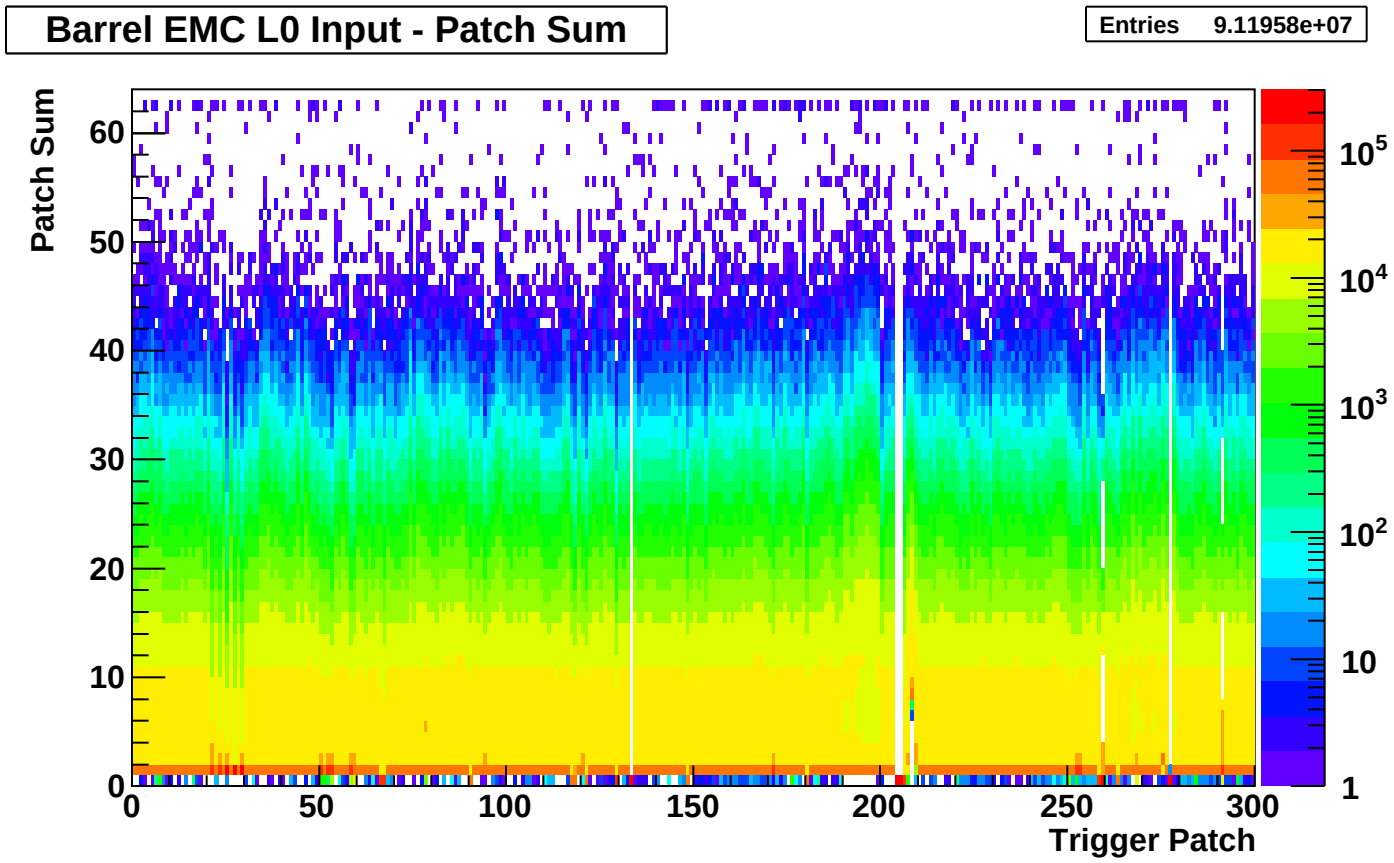

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

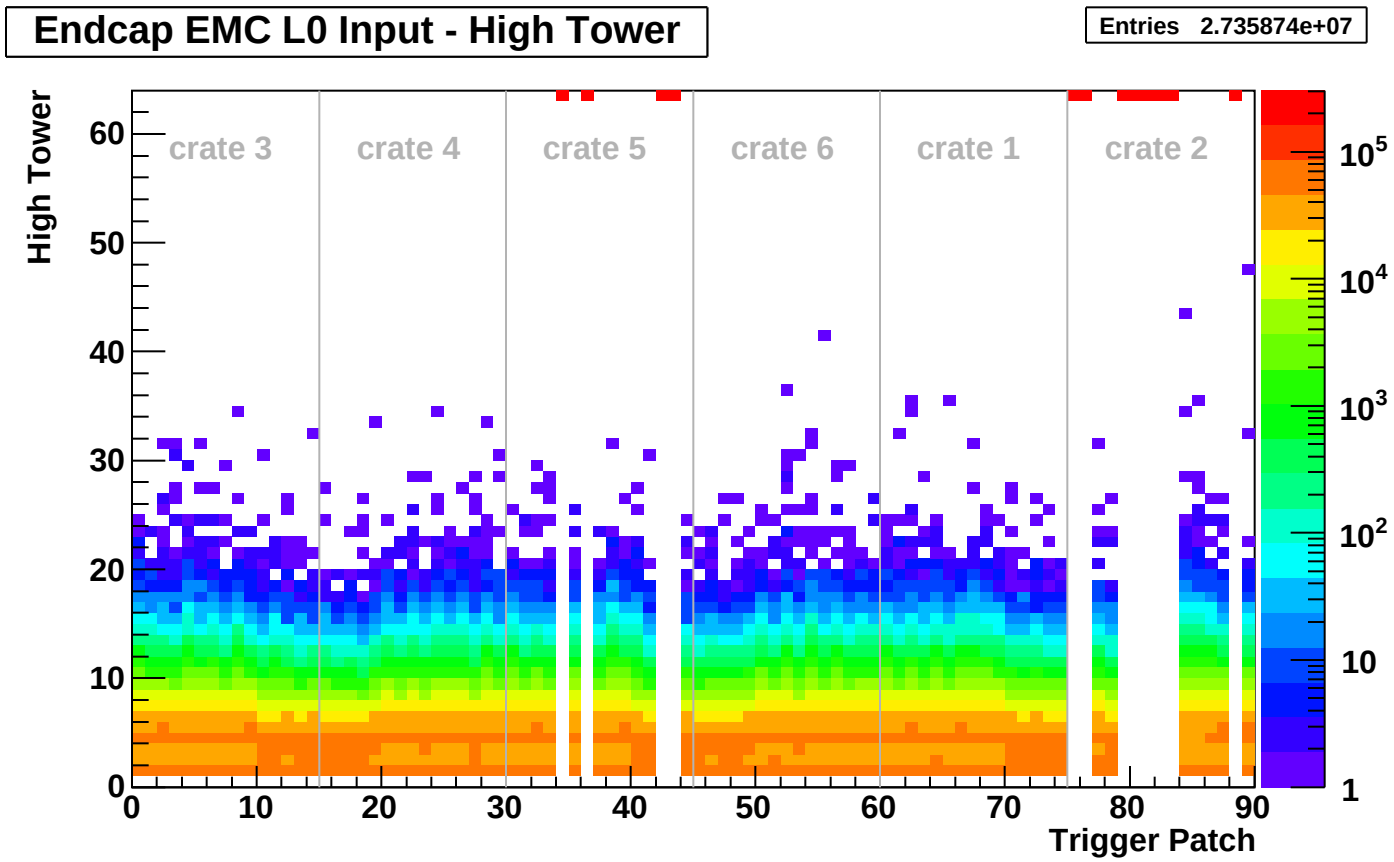

TCU bits
Entries 4863776
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

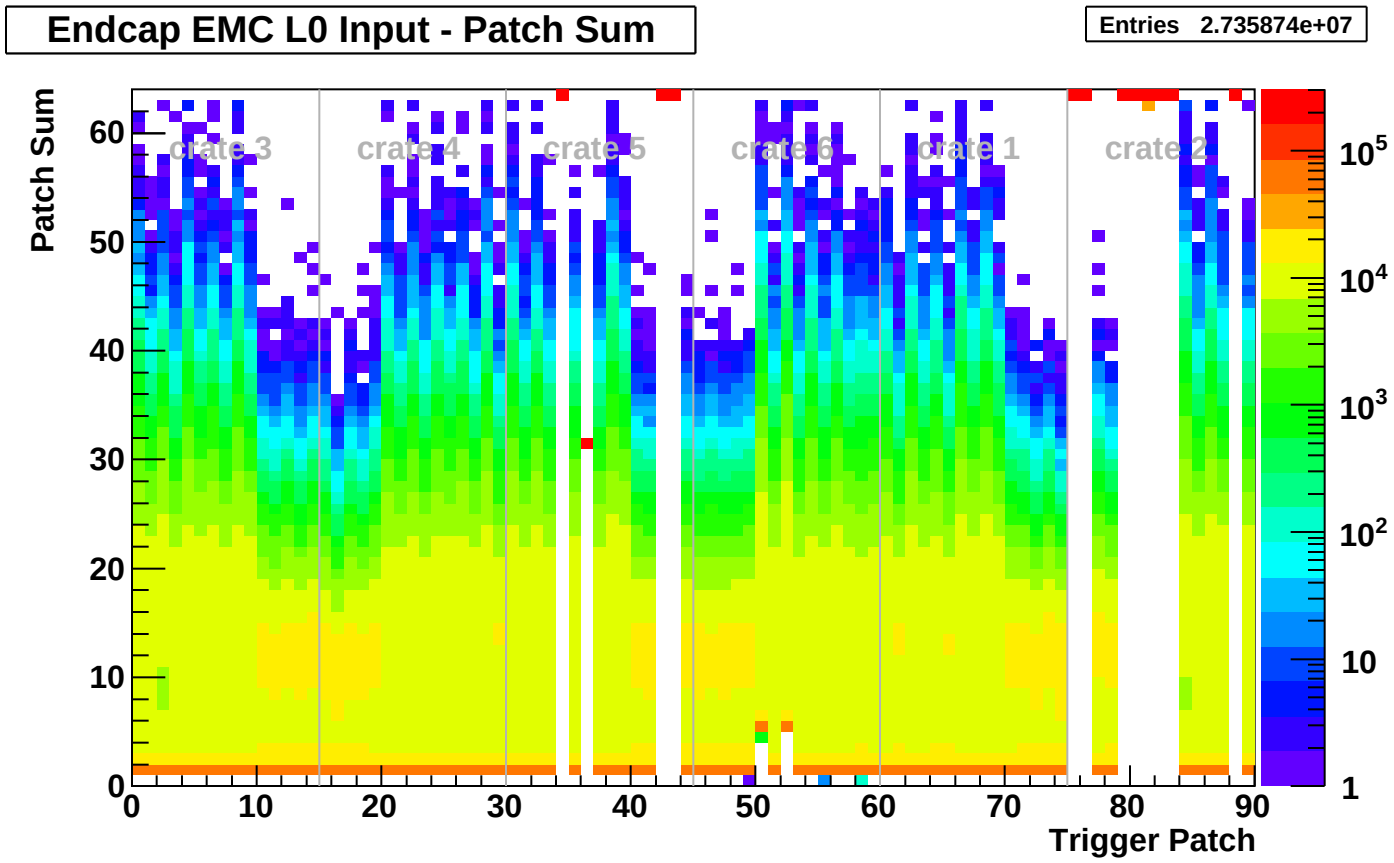

TCU bits
Entries 1008374
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

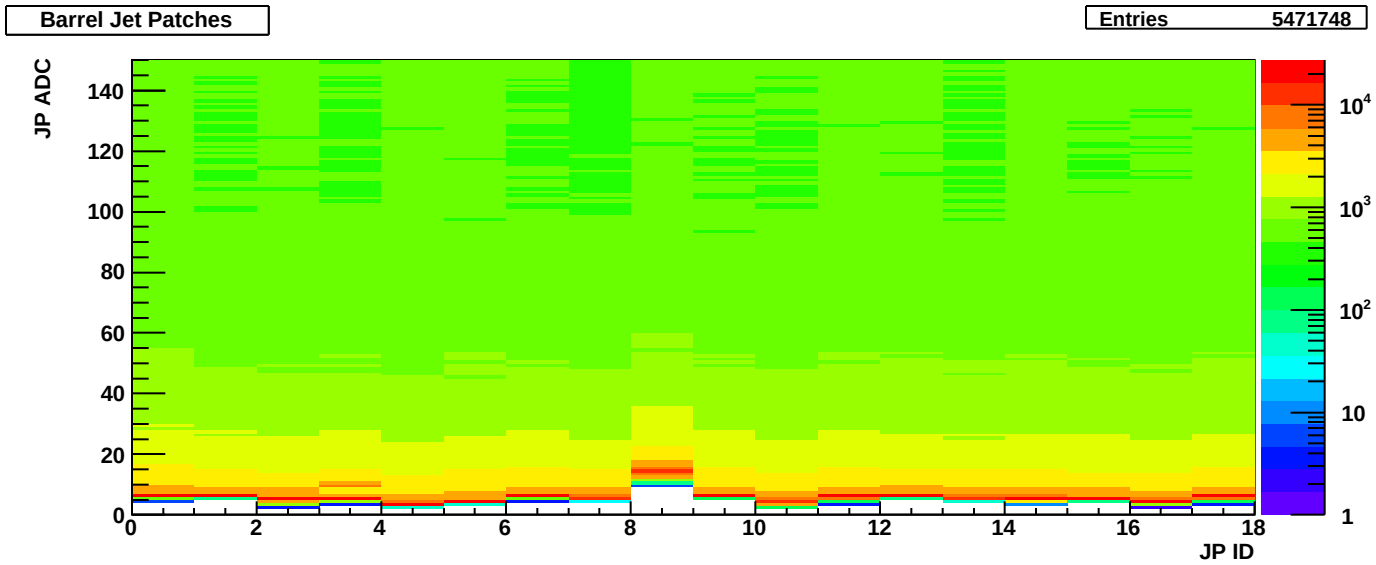

BunchId7Bit (BHT3)
Entries 2119

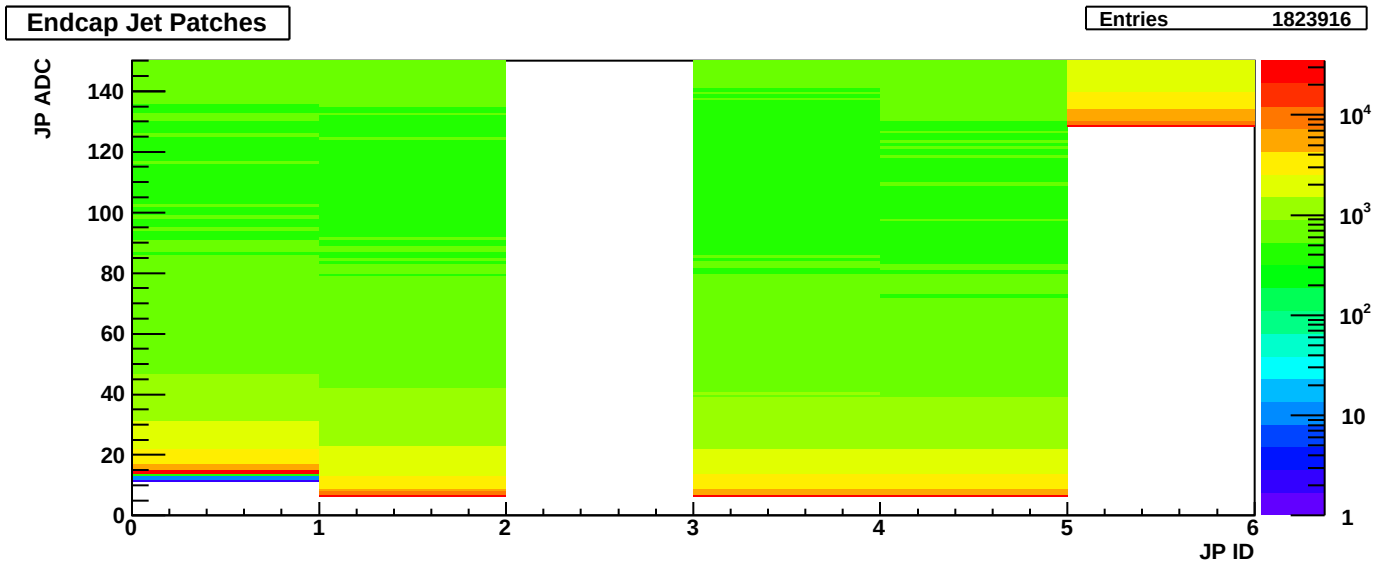

Barrel EMC L0 Input - High Tower
Entries 9.11958e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$
$10^5$

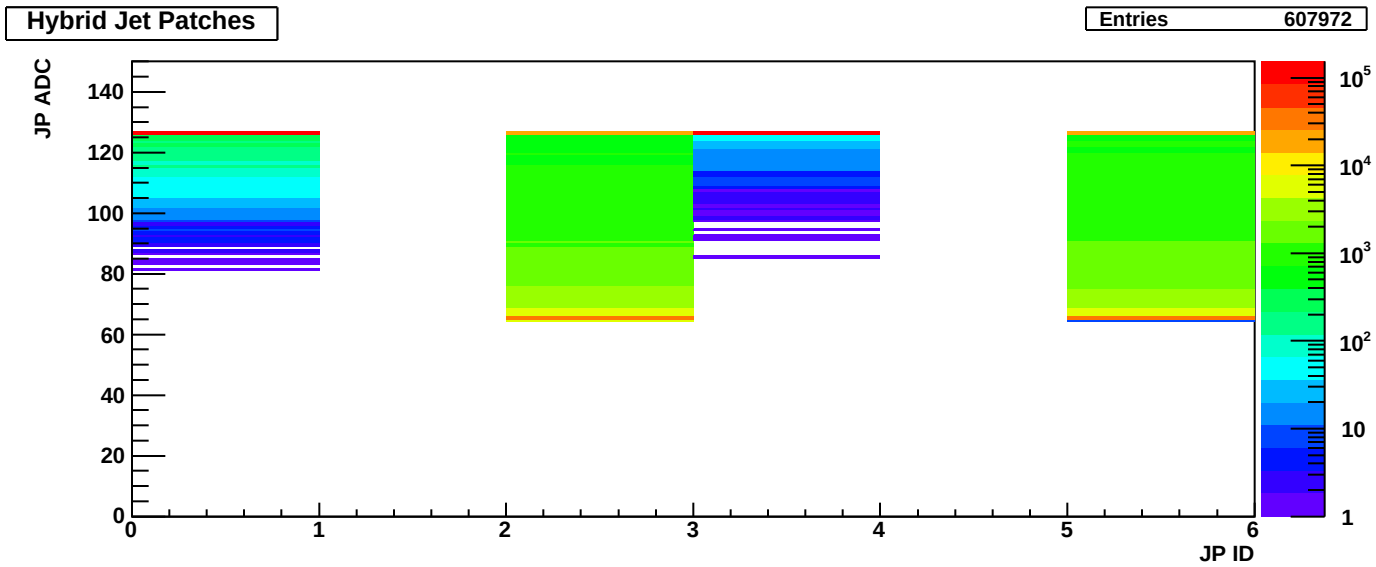

Barrel EMC L0 Input - Patch Sum
Entries 9.11958e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$
$10^5$

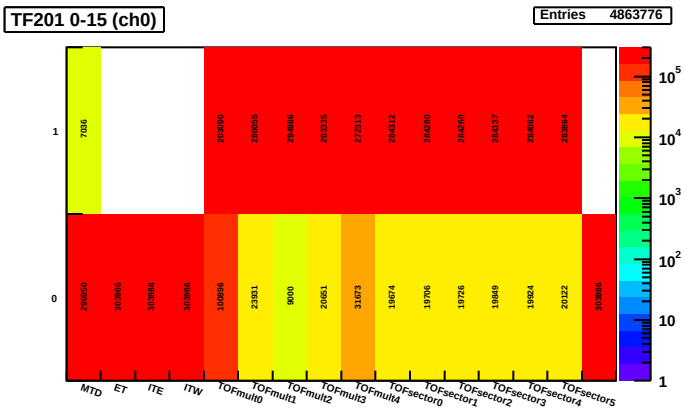

Endcap EMC L0 Input - High Tower
Entries 2.735874e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

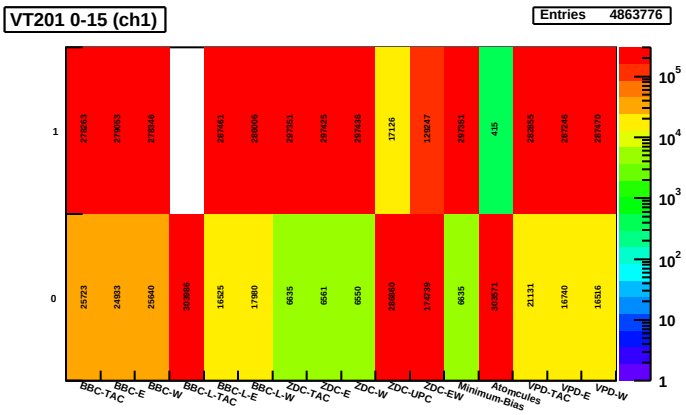

Endcap EMC L0 Input - Patch Sum
Entries 2.735874e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

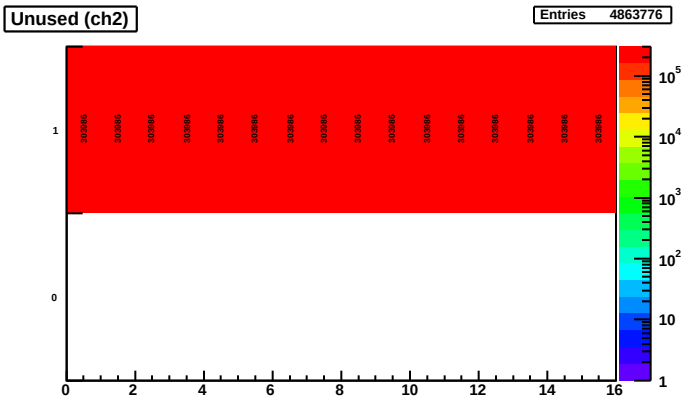

Barrel Jet Patches
Entries 5471748
JP ADC
JP ID

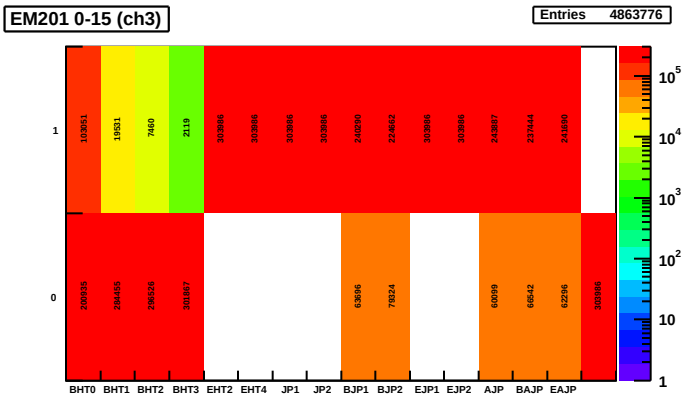

Endcap Jet Patches
Entries 1823916
JP ADC
JP ID

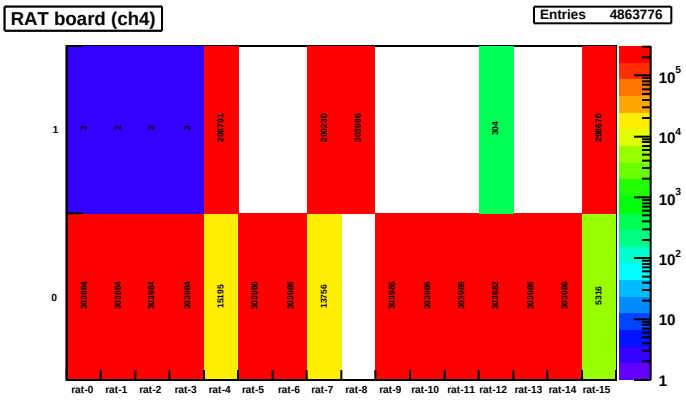

Hybrid Jet Patches
Entries 607972
JP ADC
JP ID

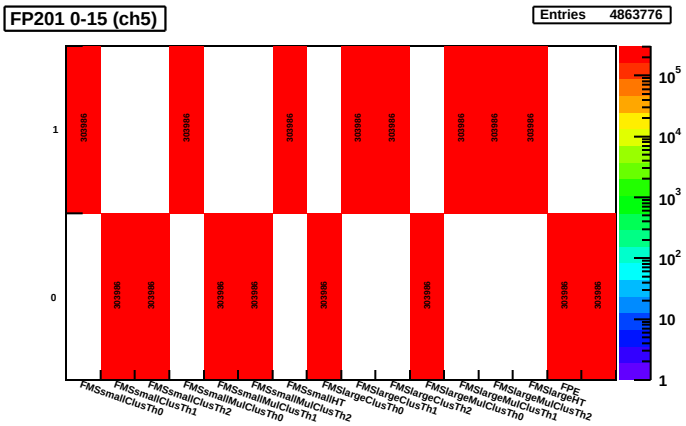

TF201 0-15 (ch0)
Entries 4863776
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

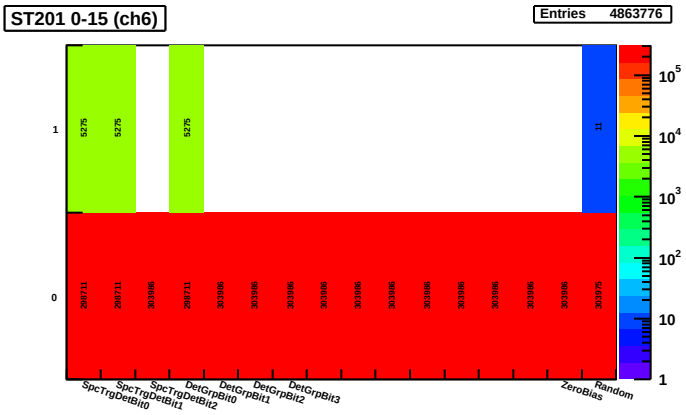

VT201 0-15 (ch1)
Entries 4863776
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

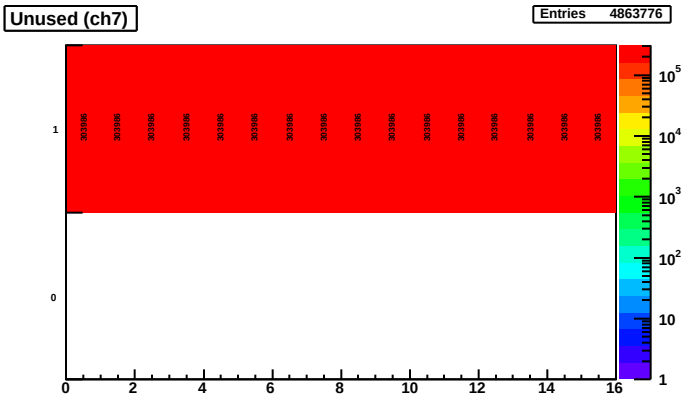

Unused (ch2)
Entries 4863776

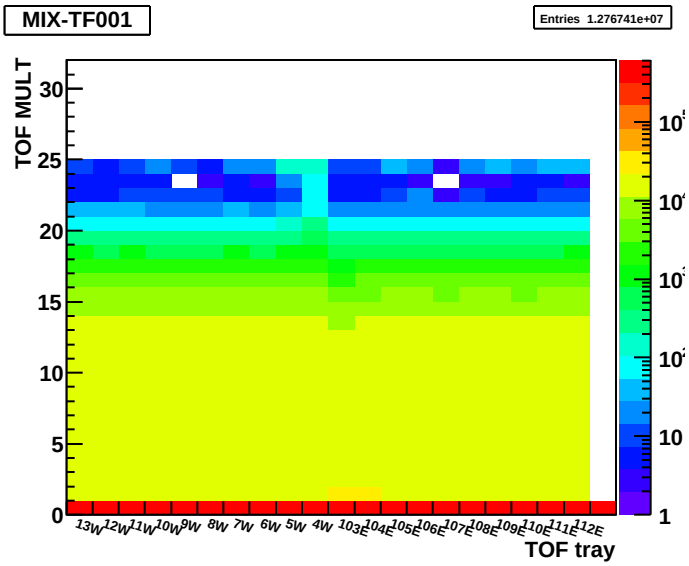

EM201 0-15 (ch3)
Entries 4863776
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

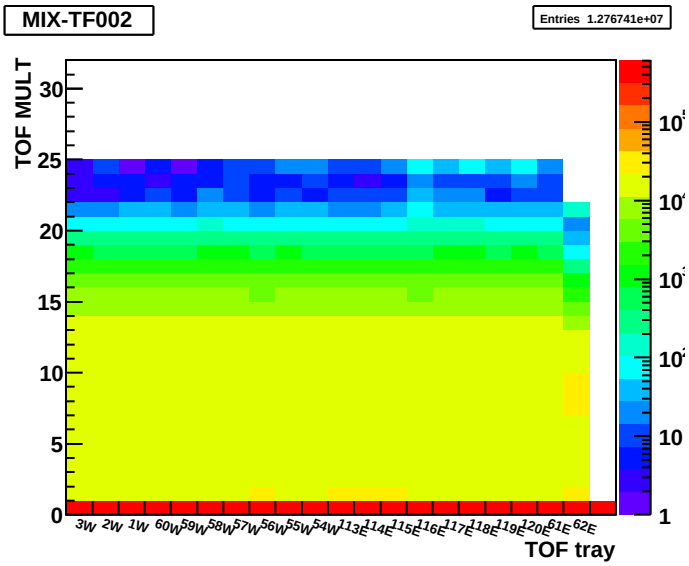

RAT board (ch4)
Entries 4863776
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

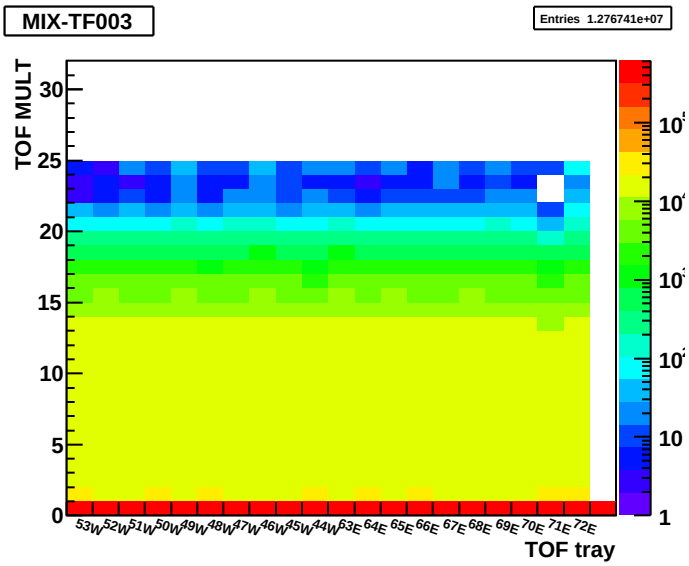

FP201 0-15 (ch5)
Entries 4863776
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

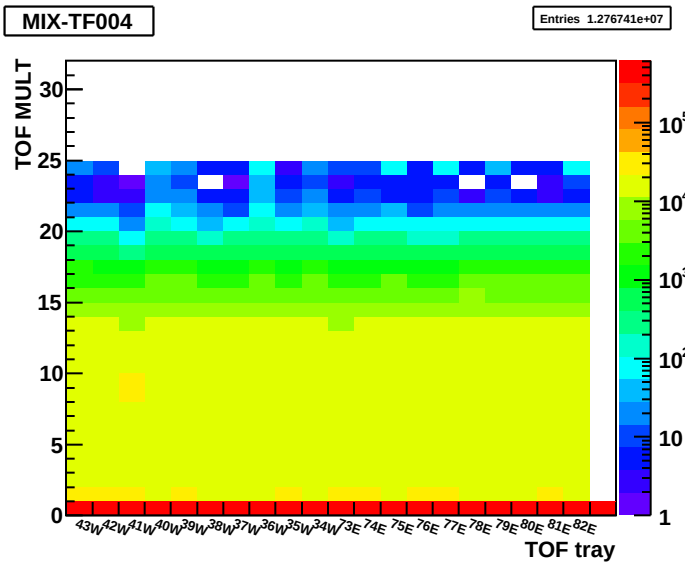

ST201 0-15 (ch6)
Entries 4863776
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

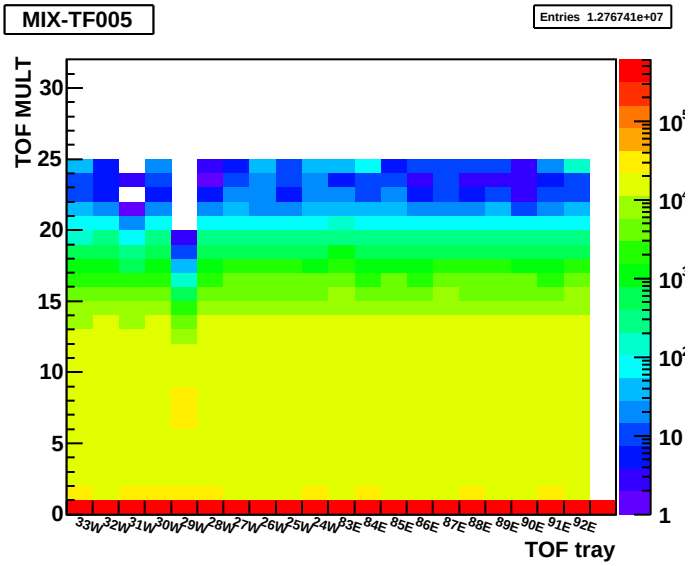

Unused (ch7)
Entries 4863776

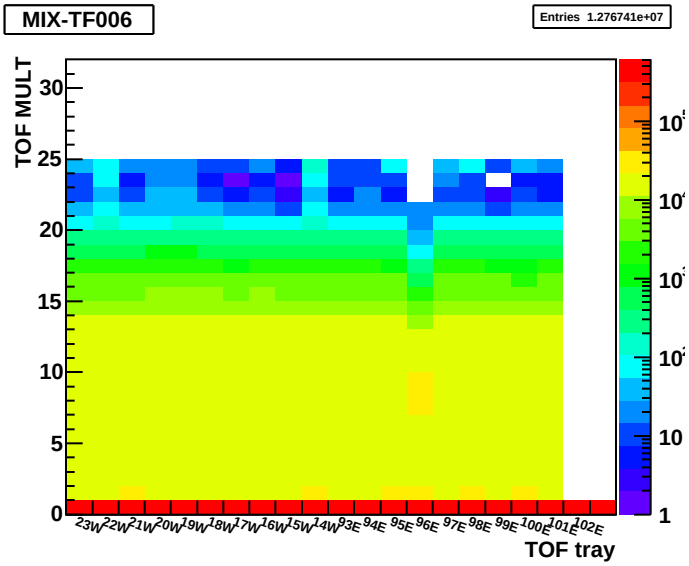

MIX-TF001
Entries 1.276741e+07
TOF MULT
TOF tray

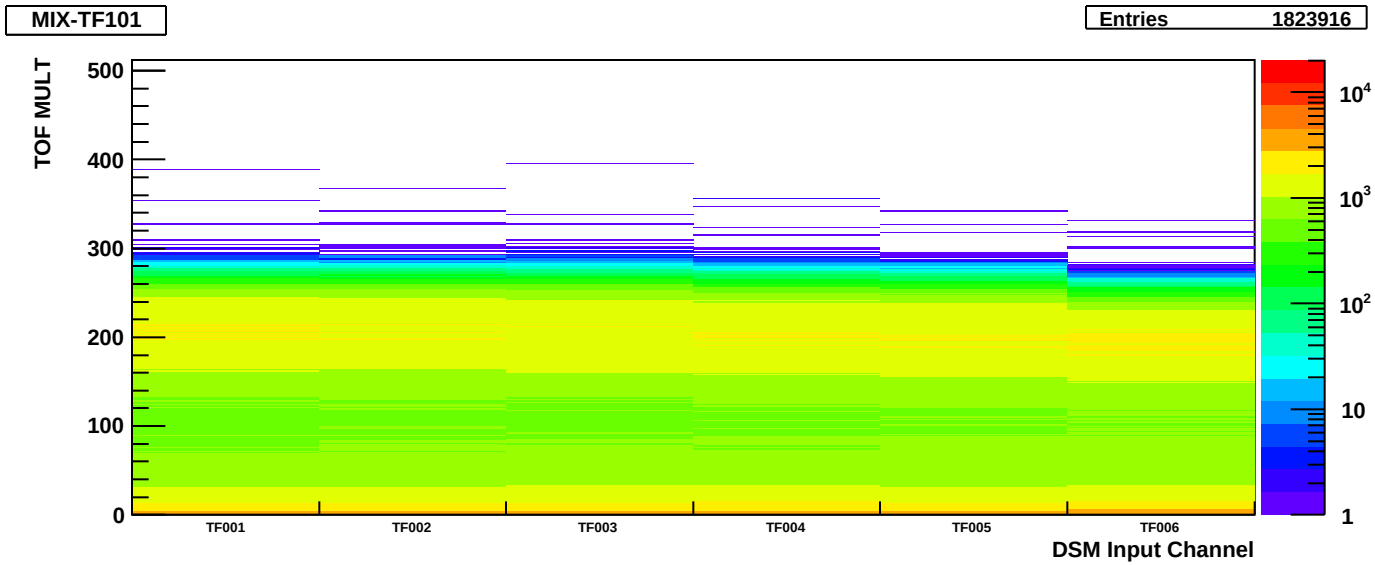

MIX-TF002
Entries 1.276741e+07
TOF MULT
TOF tray

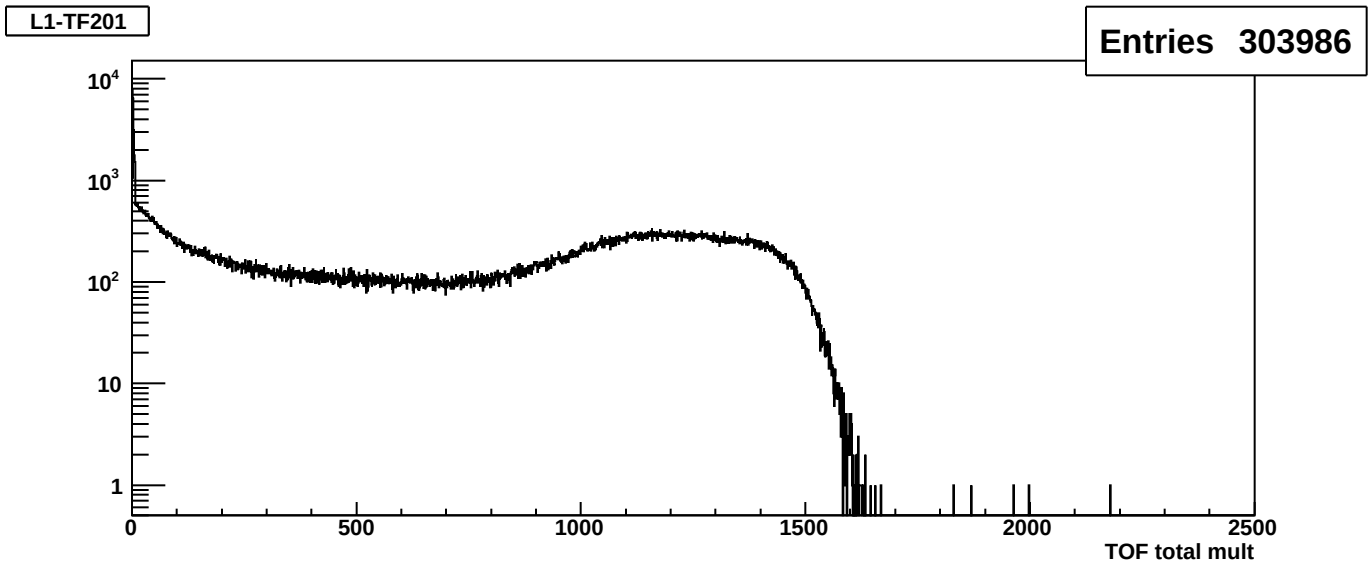

MIX-TF003
Entries 1.276741e+07
TOF MULT
TOF tray

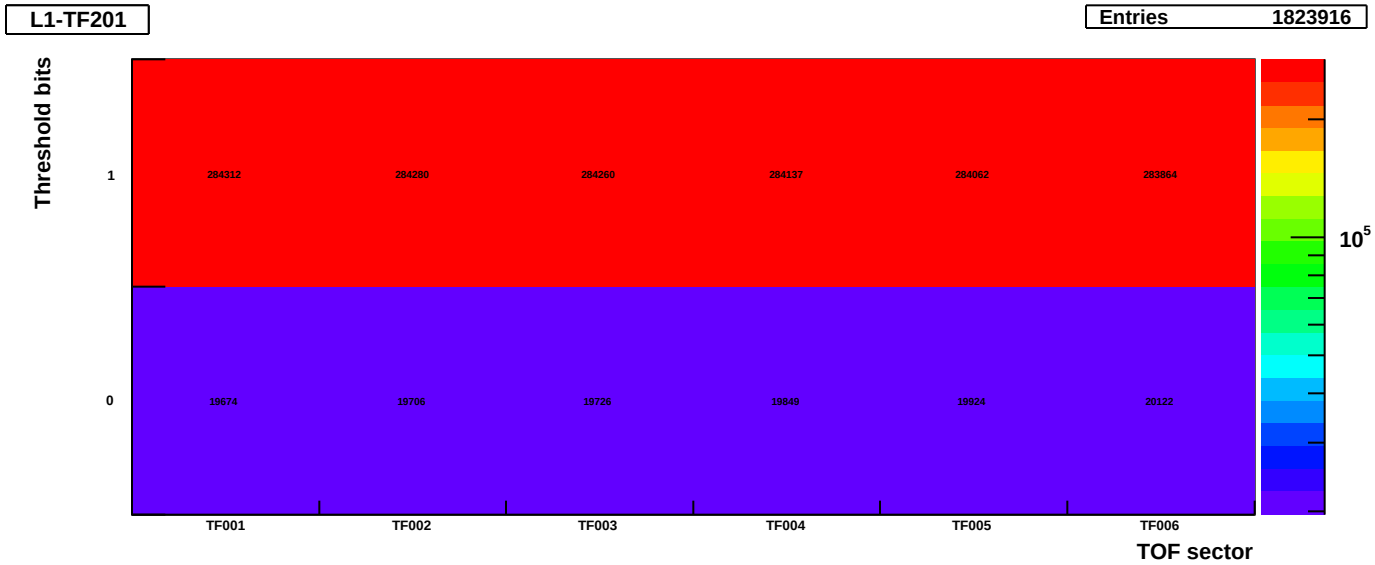

MIX-TF004
Entries 1.276741e+07
TOF MULT
TOF tray

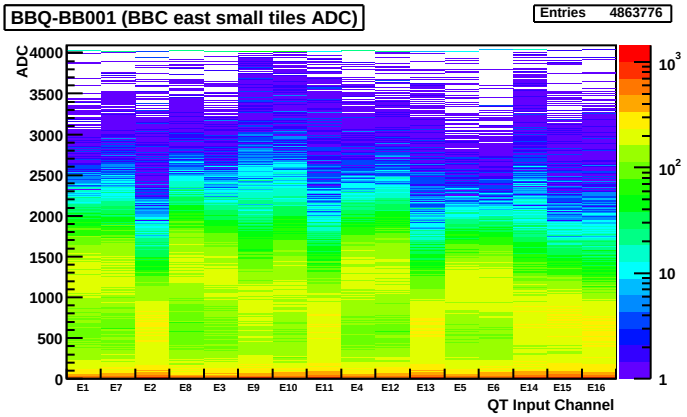

MIX-TF005
Entries 1.276741e+07
TOF MULT
TOF tray

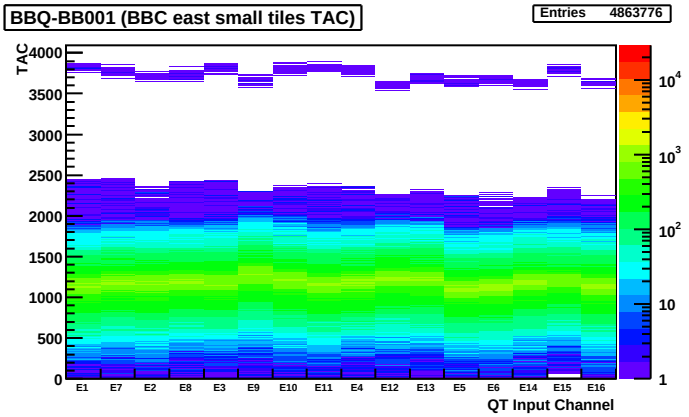

MIX-TF006
Entries 1.276741e+07
TOF MULT
TOF tray

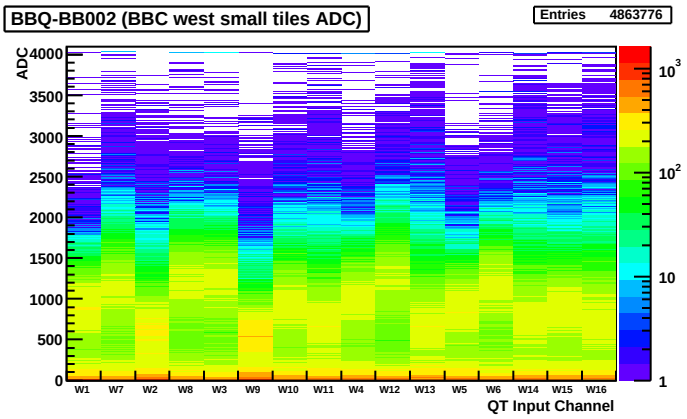

MIX-TF101
Entries 1823916
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
$10^4$
$10^3$
$10^2$
10
1

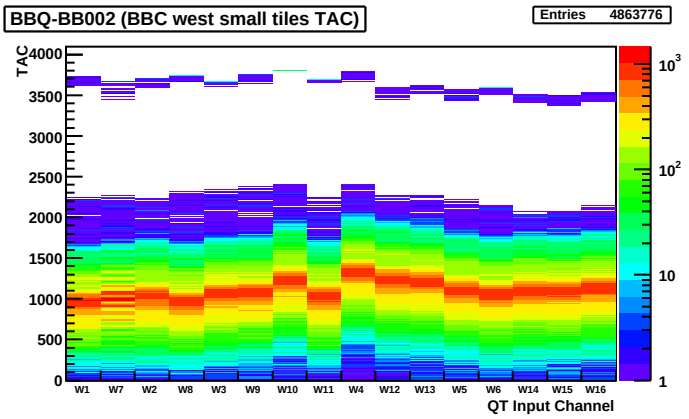

L1-TF201
Entries 303986
$10^4$
$10^3$
$10^2$
10
1
0
500
1000
1500
2000
2500
TOF total mult

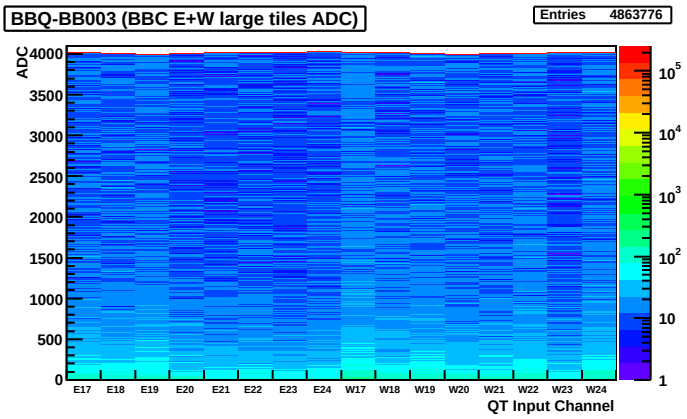

L1-TF201
Entries 1823916
Threshold bits
1
0
284312
284280
284260
284137
284062
283864
19674
19706
19726
19849
19924
20122
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
$10^5$

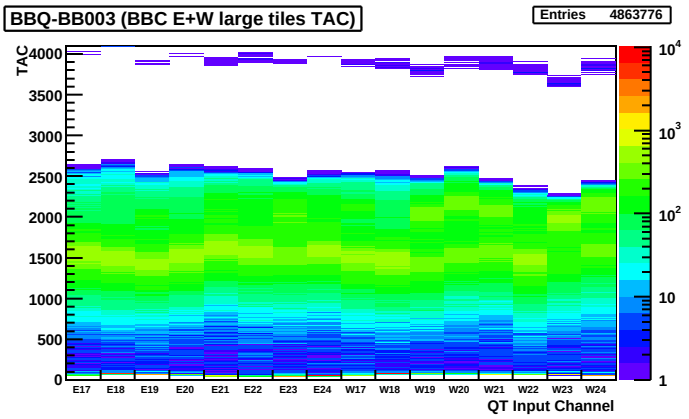

BBQ-BB001 (BBC east small tiles ADC)
Entries 4863776
ADC
QT Input Channel

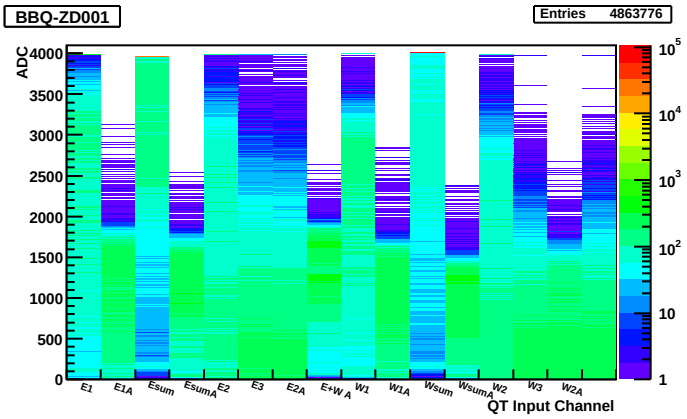

BBQ-BB001 (BBC east small tiles TAC)
Entries 4863776
TAC
QT Input Channel

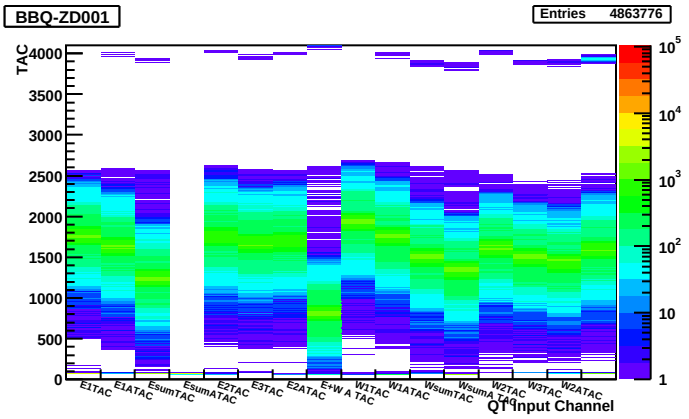

BBQ-BB002 (BBC west small tiles ADC)
Entries 4863776
ADC
QT Input Channel

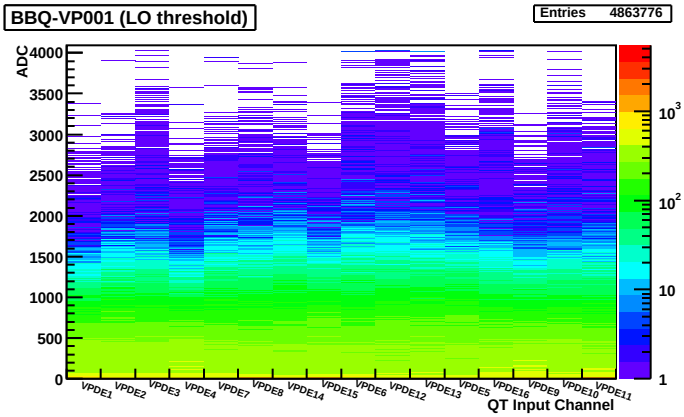

BBQ-BB002 (BBC west small tiles TAC)
Entries 4863776
TAC
QT Input Channel

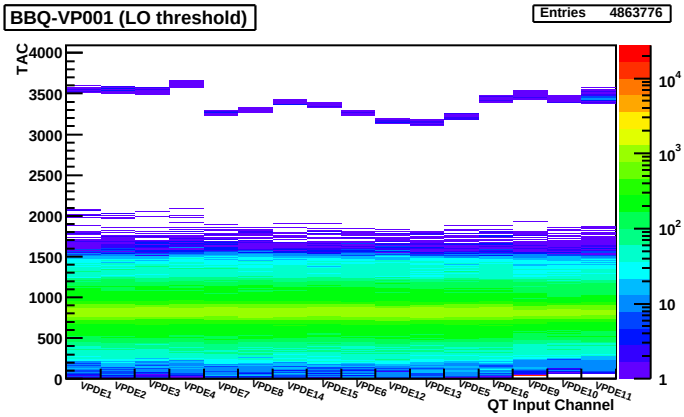

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4863776
ADC
QT Input Channel

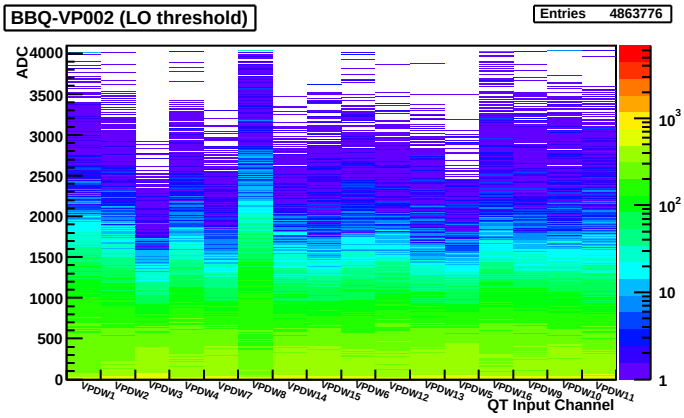

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4863776
TAC
QT Input Channel

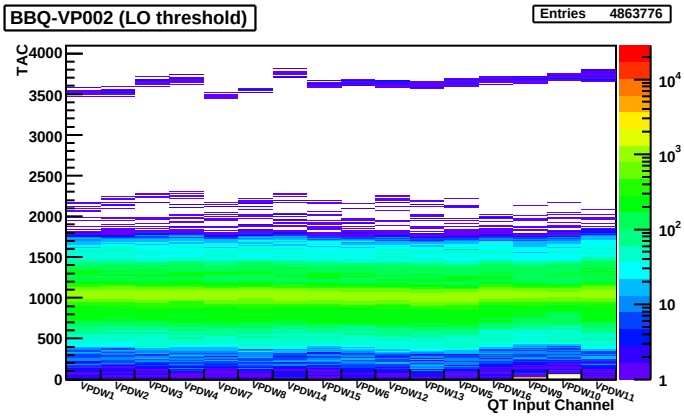

BBQ-ZD001
Entries 4863776
ADC
QT Input Channel

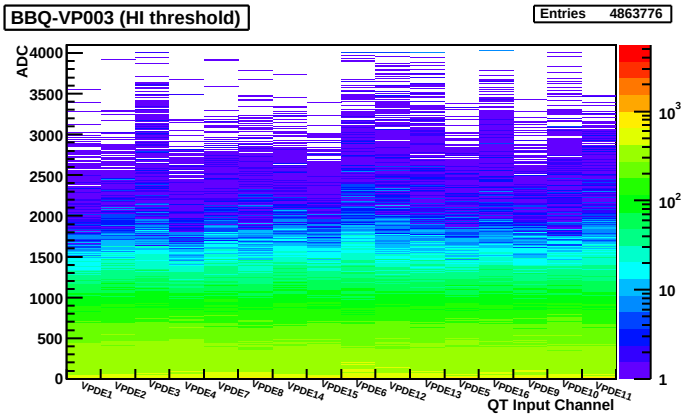

BBQ-ZD001
Entries 4863776
TAC
QT Input Channel

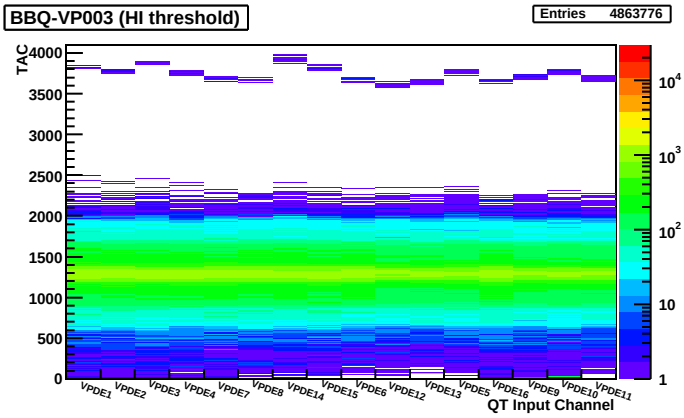

BBQ-VP001 (LO threshold)
Entries 4863776
ADC
QT Input Channel

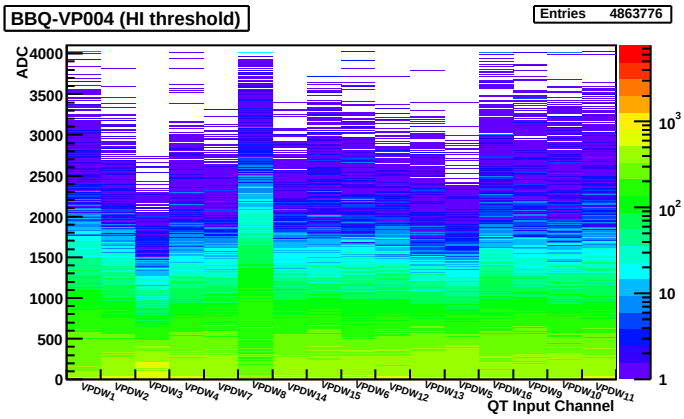

BBQ-VP001 (LO threshold)
Entries 4863776
TAC
QT Input Channel

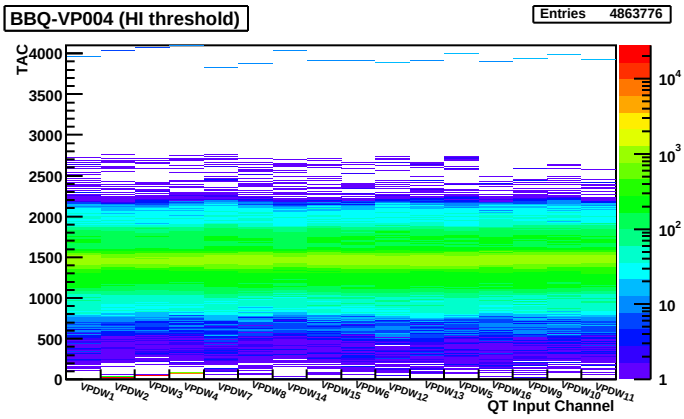

BBQ-VP002 (LO threshold)
Entries 4863776
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 4863776
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 4863776
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 4863776
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 4863776
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 4863776
TAC
QT Input Channel