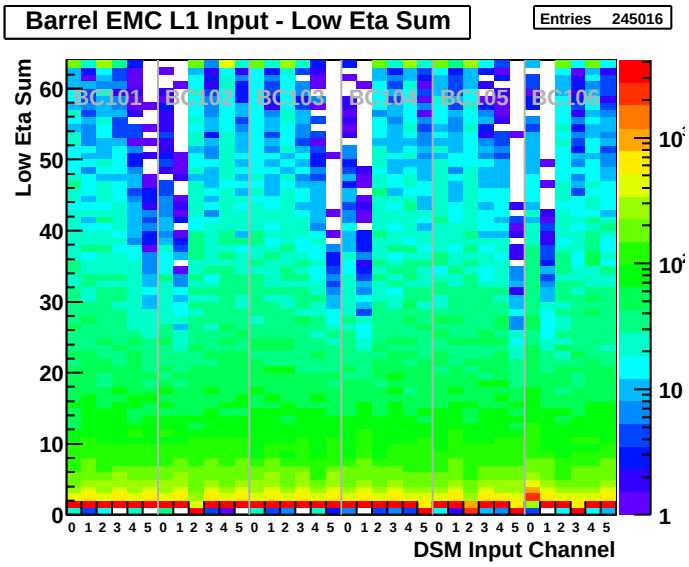
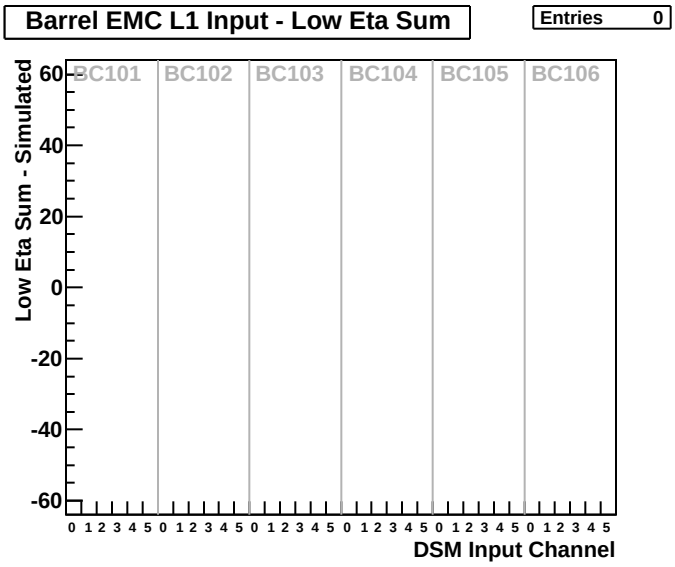
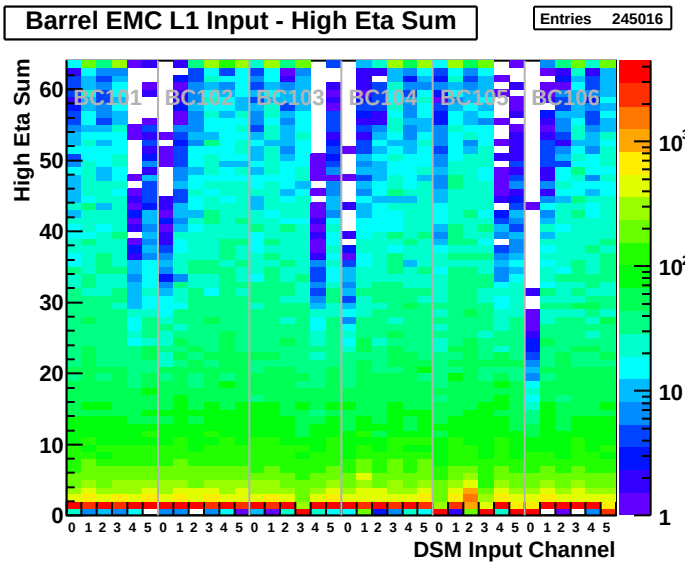
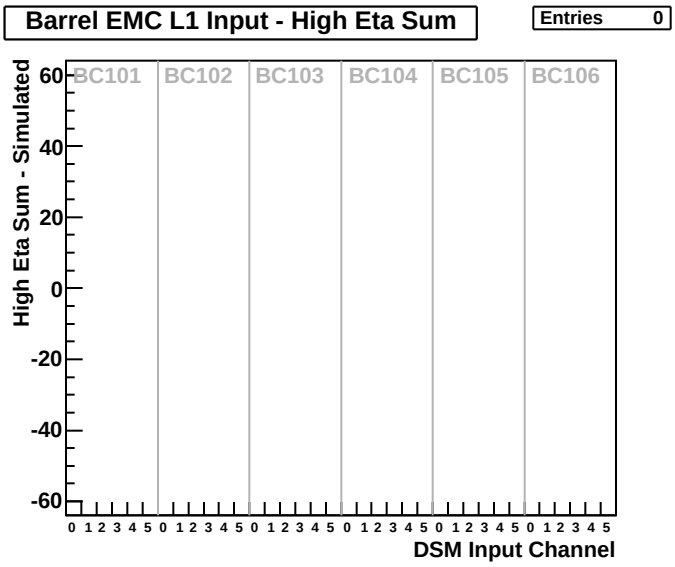
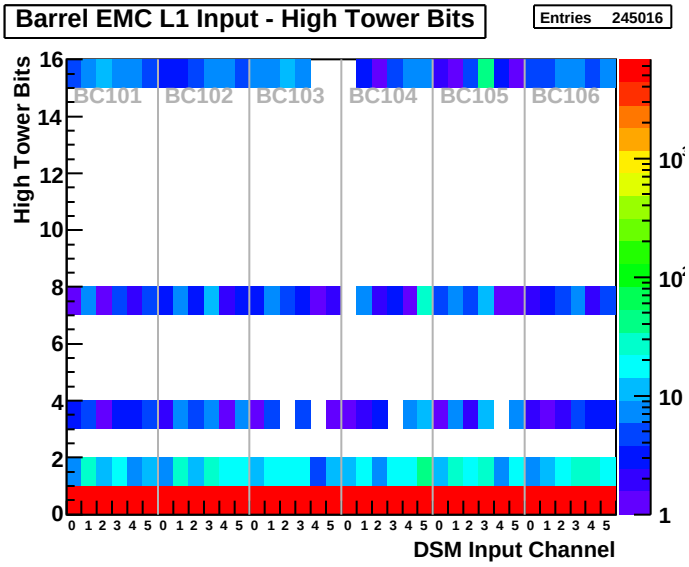
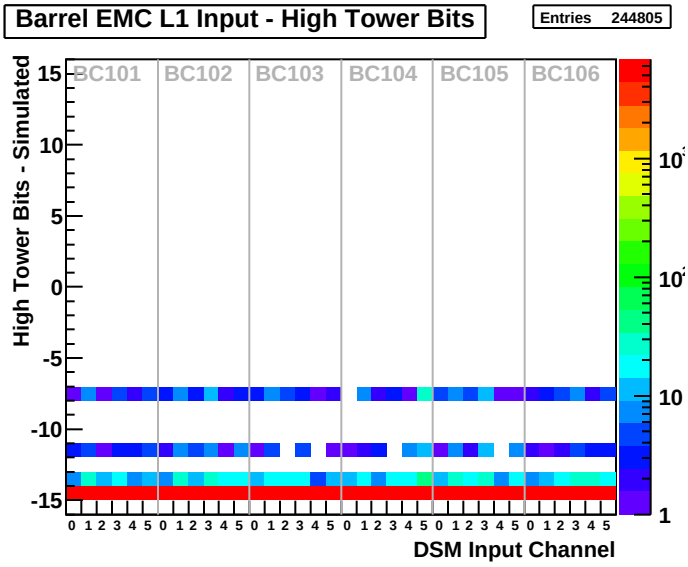

Barrel EMC L1 Input - Low Eta Sum
Entries 245016
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

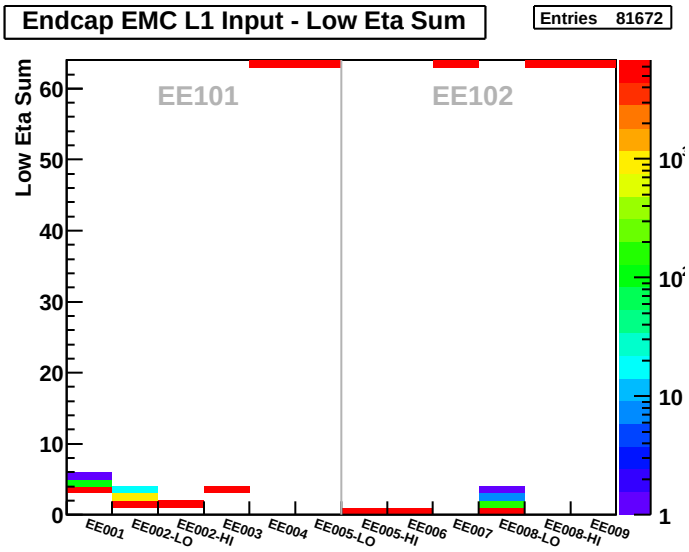

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

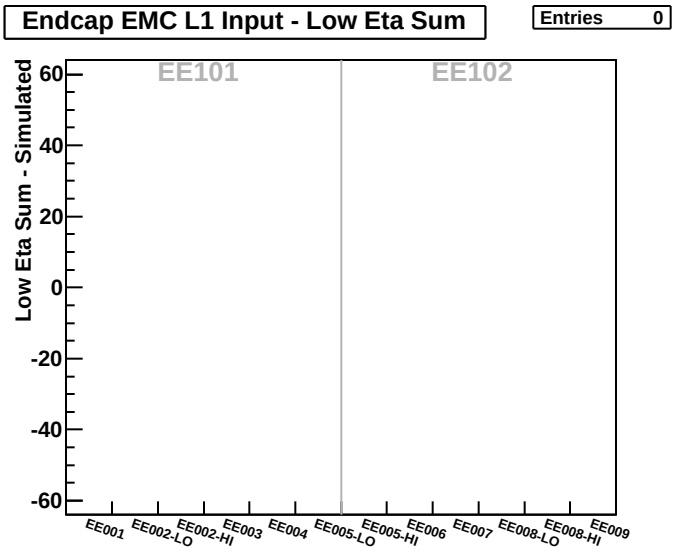

Barrel EMC L1 Input - High Eta Sum
Entries 245016
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

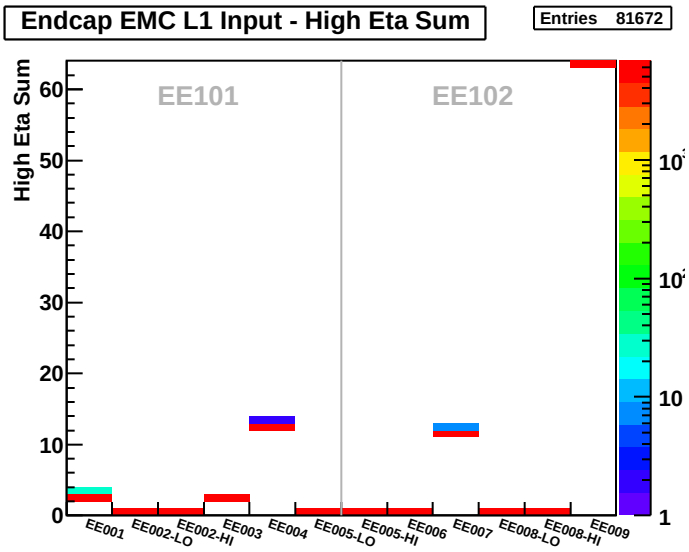

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

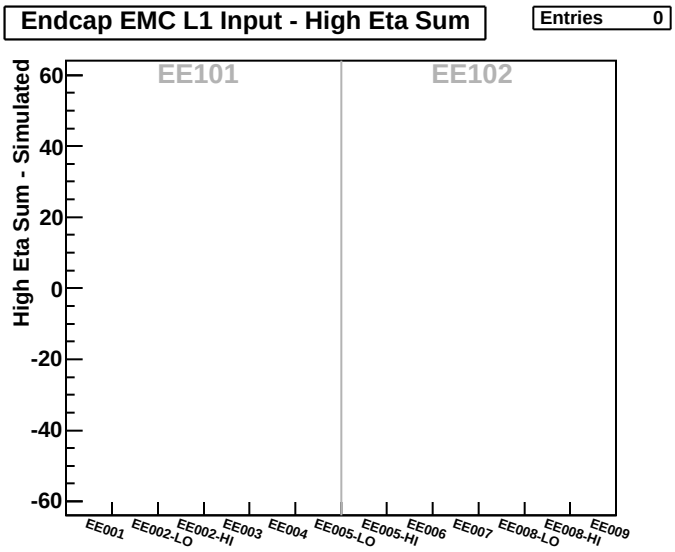

Barrel EMC L1 Input - High Tower Bits
Entries 245016
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

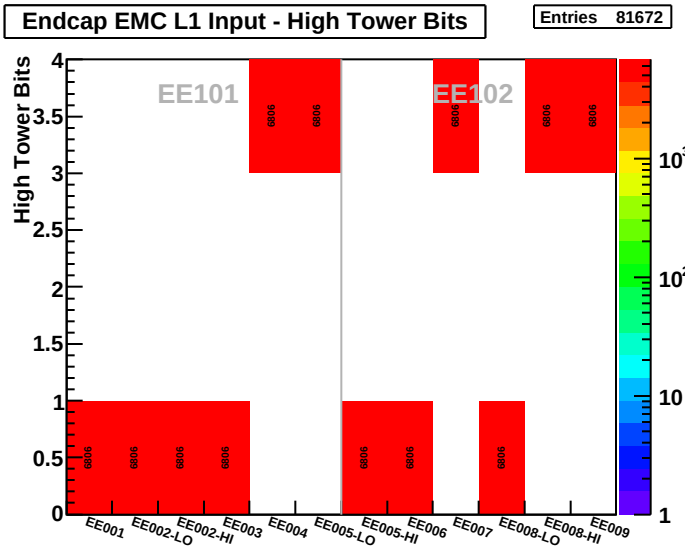

Barrel EMC L1 Input - High Tower Bits
Entries 244805
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

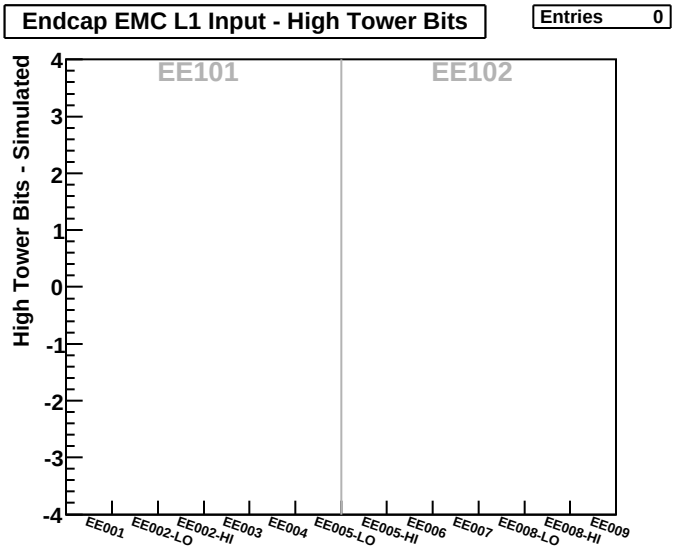

Endcap EMC L1 Input - Low Eta Sum
Entries 81672
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

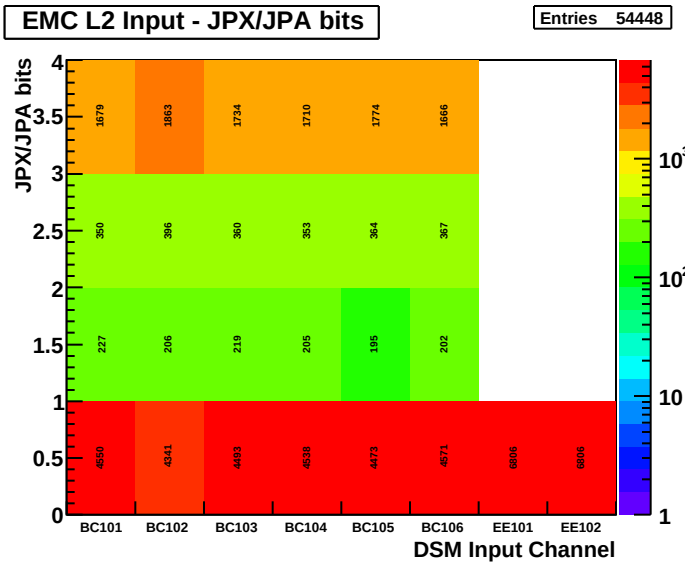

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

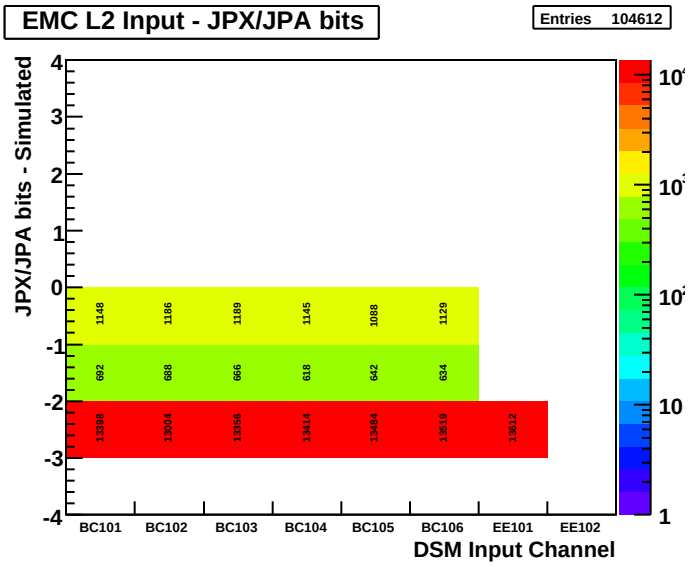

Endcap EMC L1 Input - High Eta Sum
Entries 81672
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

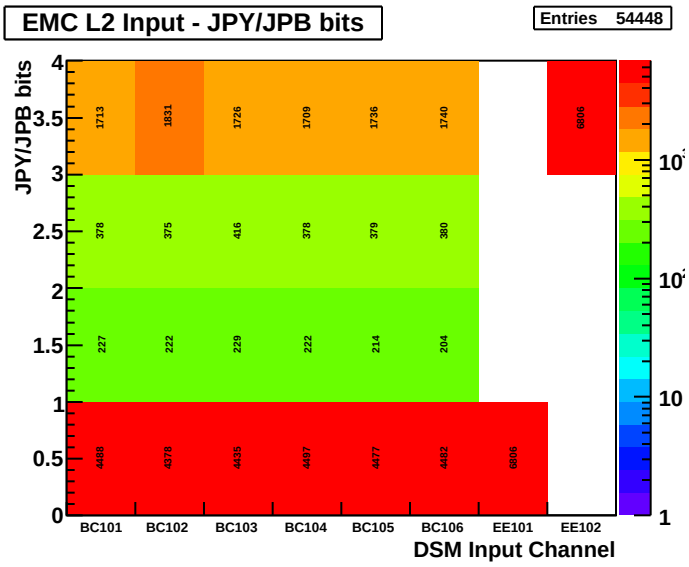

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

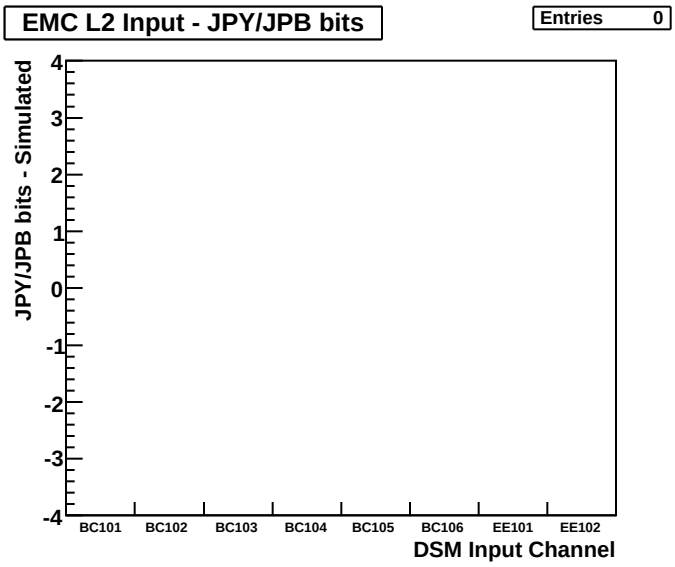

Endcap EMC L1 Input - High Tower Bits
Entries 81672
High Tower Bits
EE101
EE102
6806
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

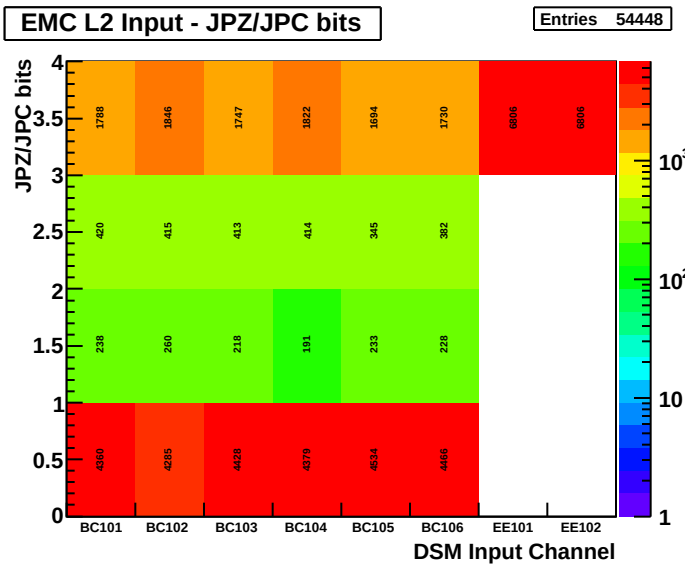

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

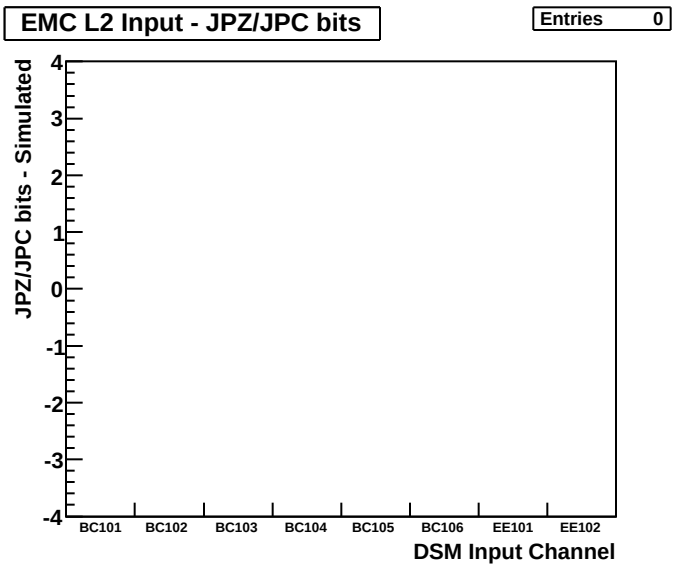

EMC L2 Input - JPX/JPA bits
Entries 54448
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1679 1863 1734 1710 1774 1666
350 396 360 353 364 367
227 206 219 205 195 202
4550 4341 4493 4538 4473 4571 6806 6806

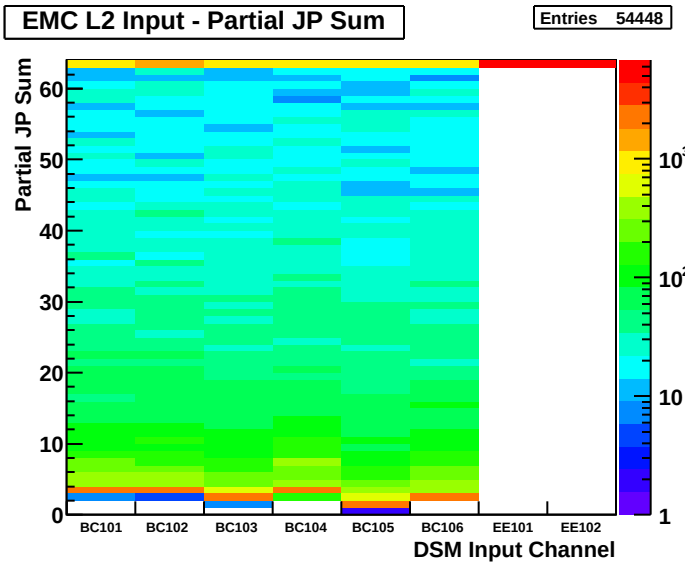

EMC L2 Input - JPX/JPA bits
Entries 104612
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1148 1186 1189 1145 1088 1129
692 688 666 618 642 634
13398 13004 13356 13414 13484 13519 13612

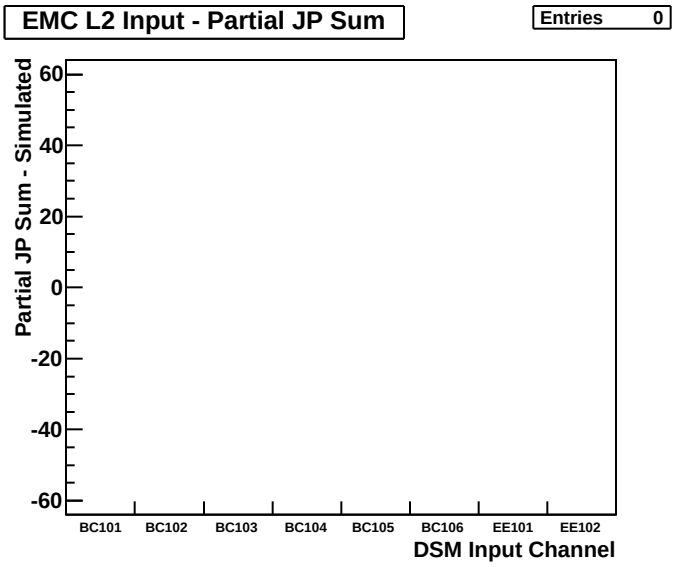

EMC L2 Input - JPY/JPB bits
Entries 54448
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1713 1831 1726 1709 1736 1740 6806
378 375 416 378 379 380
227 222 229 222 214 204
4488 4378 4435 4497 4477 4482 6806

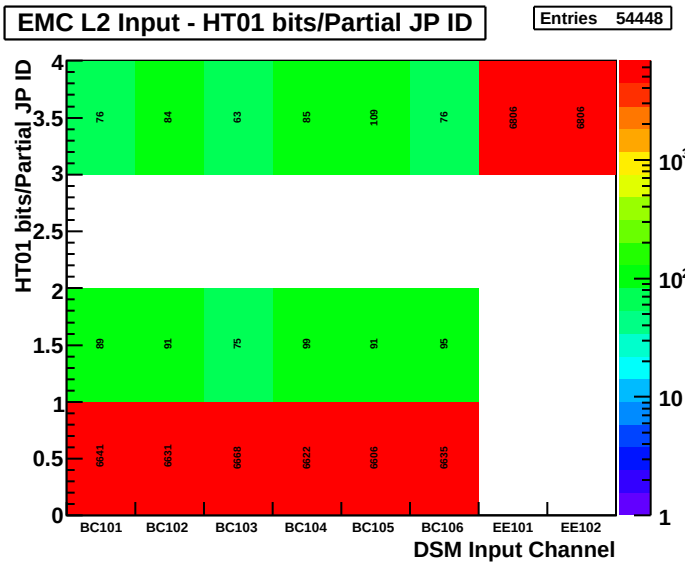

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

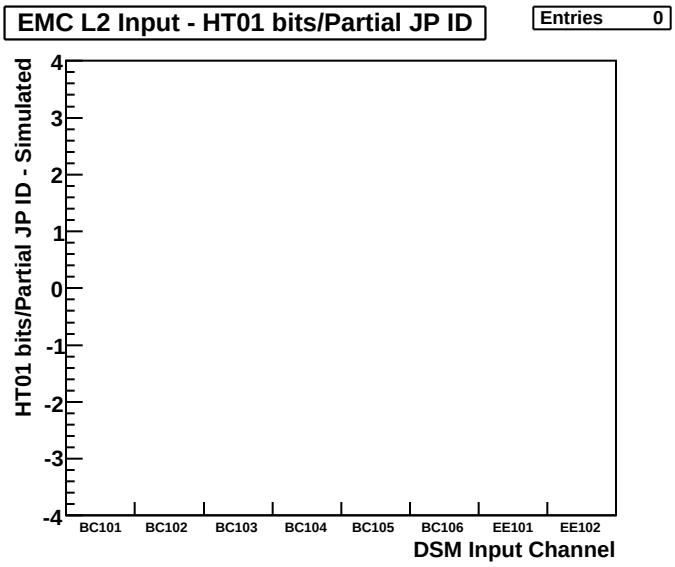

EMC L2 Input - JPZ/JPC bits
Entries 54448
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1788 1846 1747 1822 1694 1730 6806 6806
420 415 413 414 345 382
238 260 218 191 233 228
4360 4285 4428 4379 4534 4466

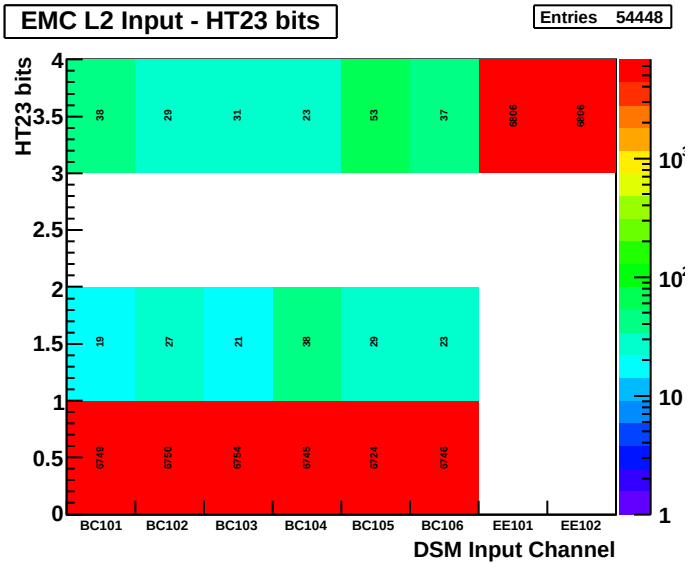

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

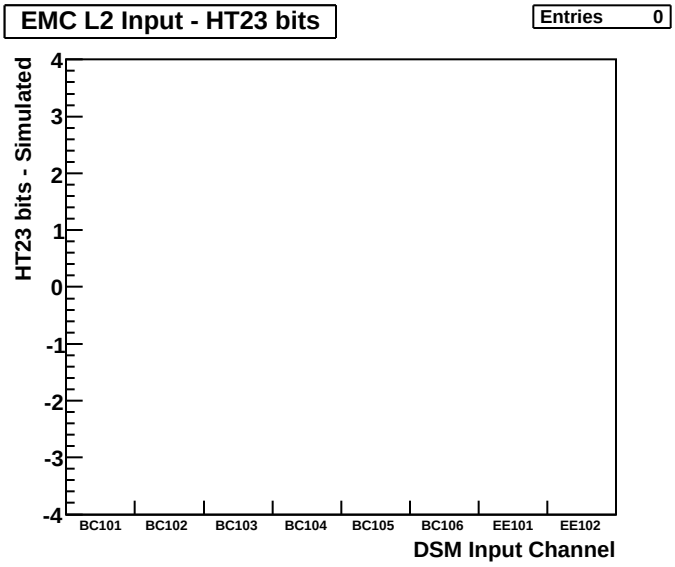

EMC L2 Input - Partial JP Sum
Entries 54448
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

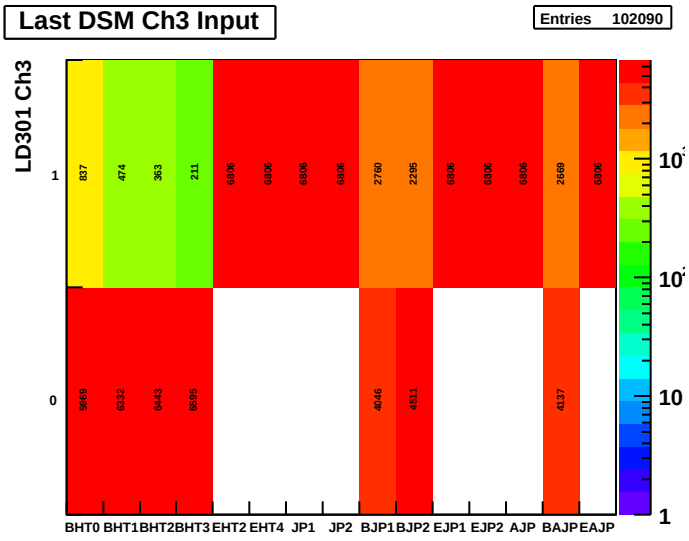

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

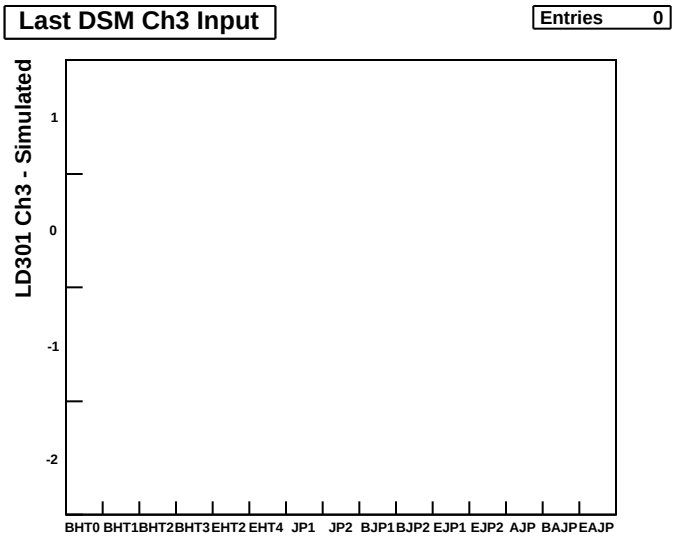

EMC L2 Input - HT01 bits/Partial JP ID
Entries 54448
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

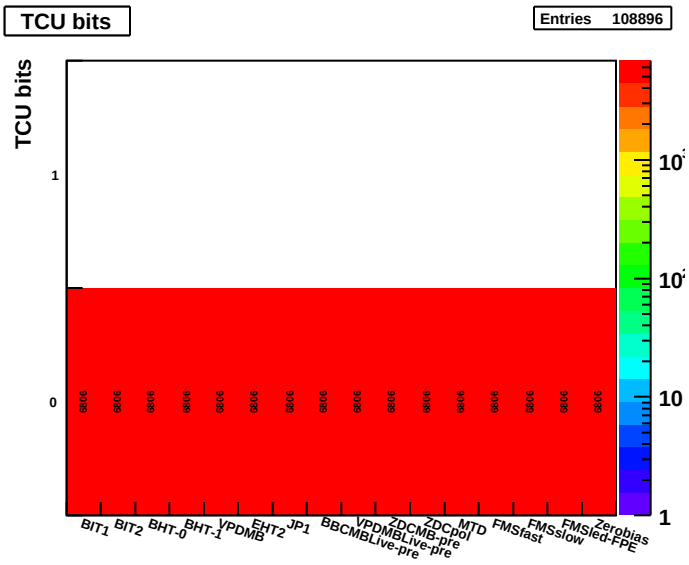

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

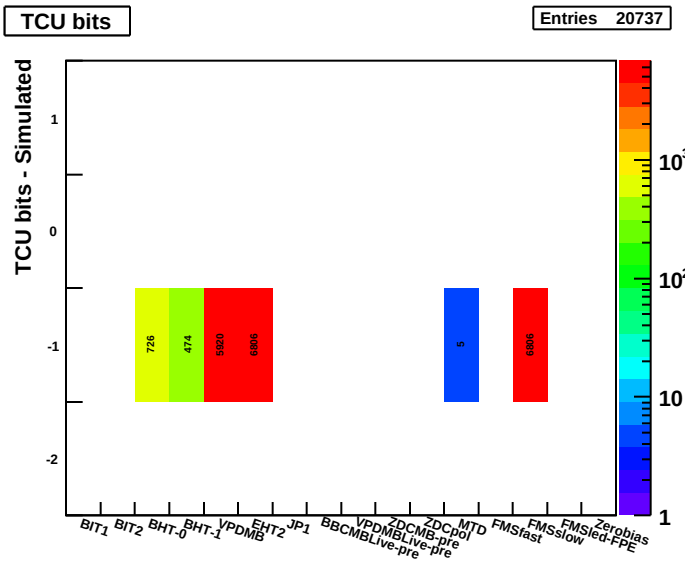

EMC L2 Input - HT23 bits
Entries 54448
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

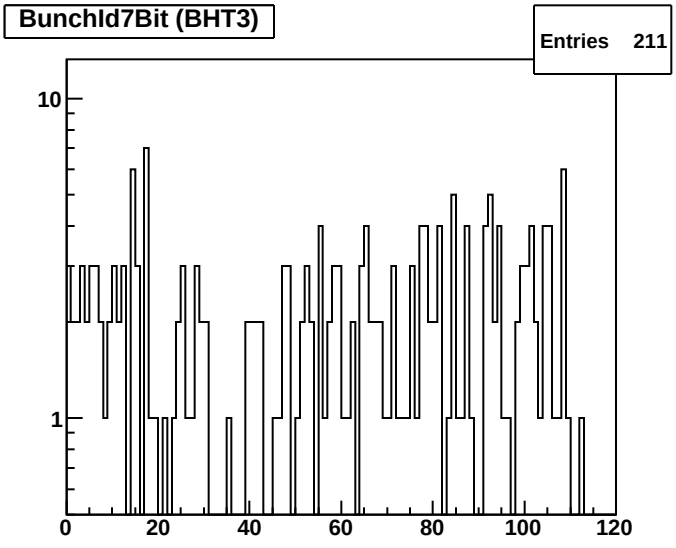

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

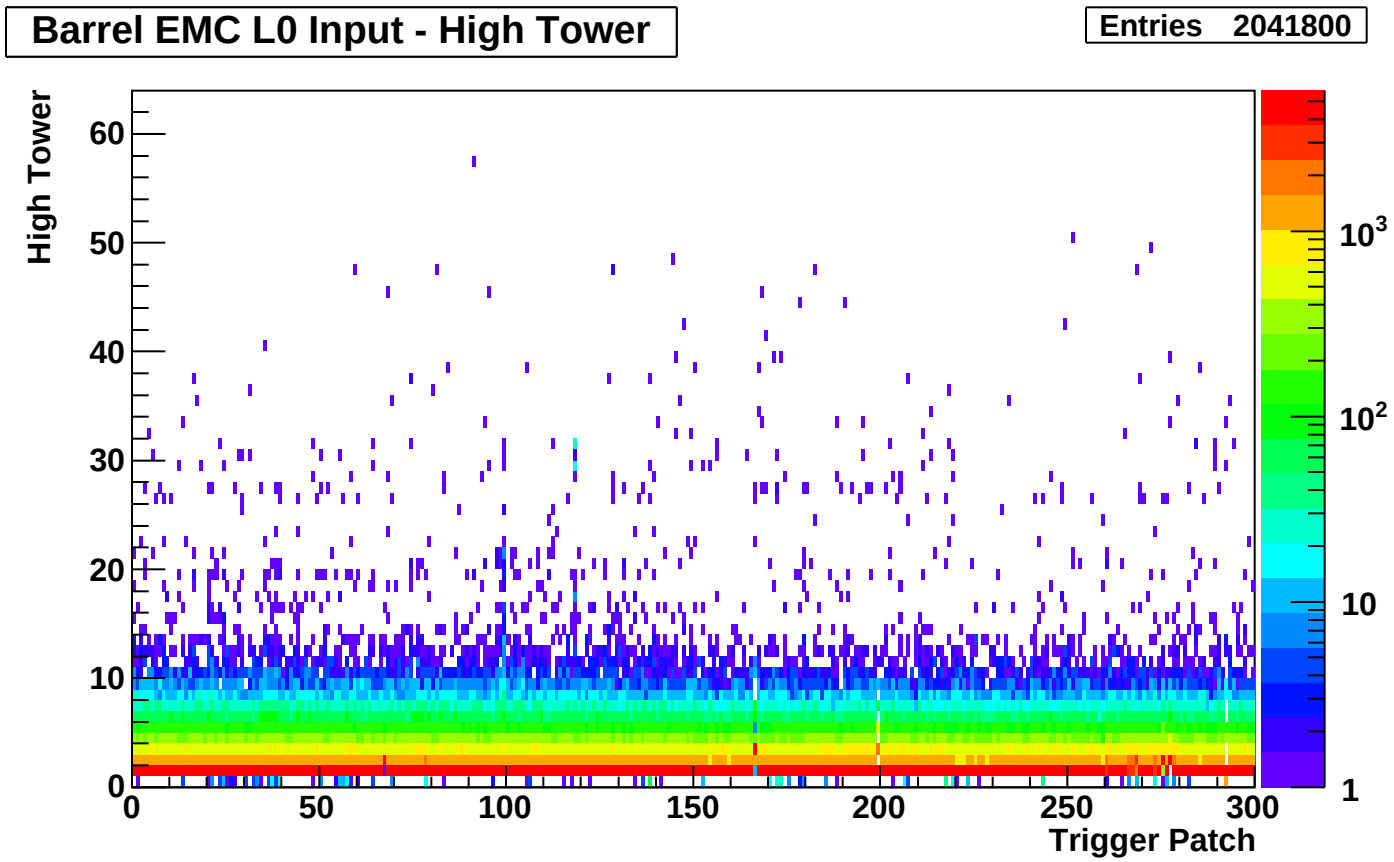

Last DSM Ch3 Input
Entries 102090
LD301 Ch3
1
0
837
474
363
211
6806
6806
6806
6806
2760
2295
6806
6806
6806
2669
6806
5969
6332
6443
6595
4046
4511
4137
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP
1

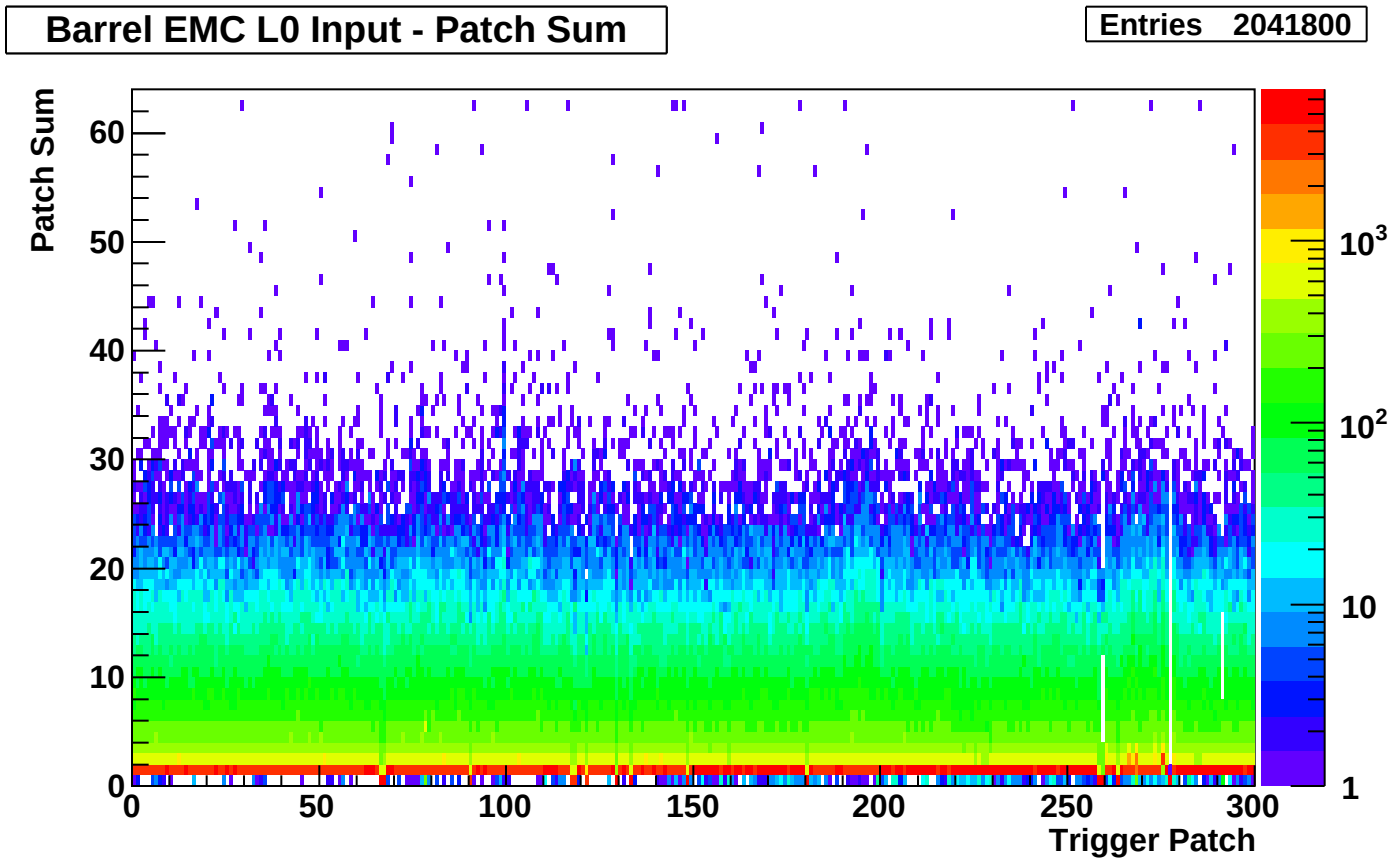

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
1
0
-1
-2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

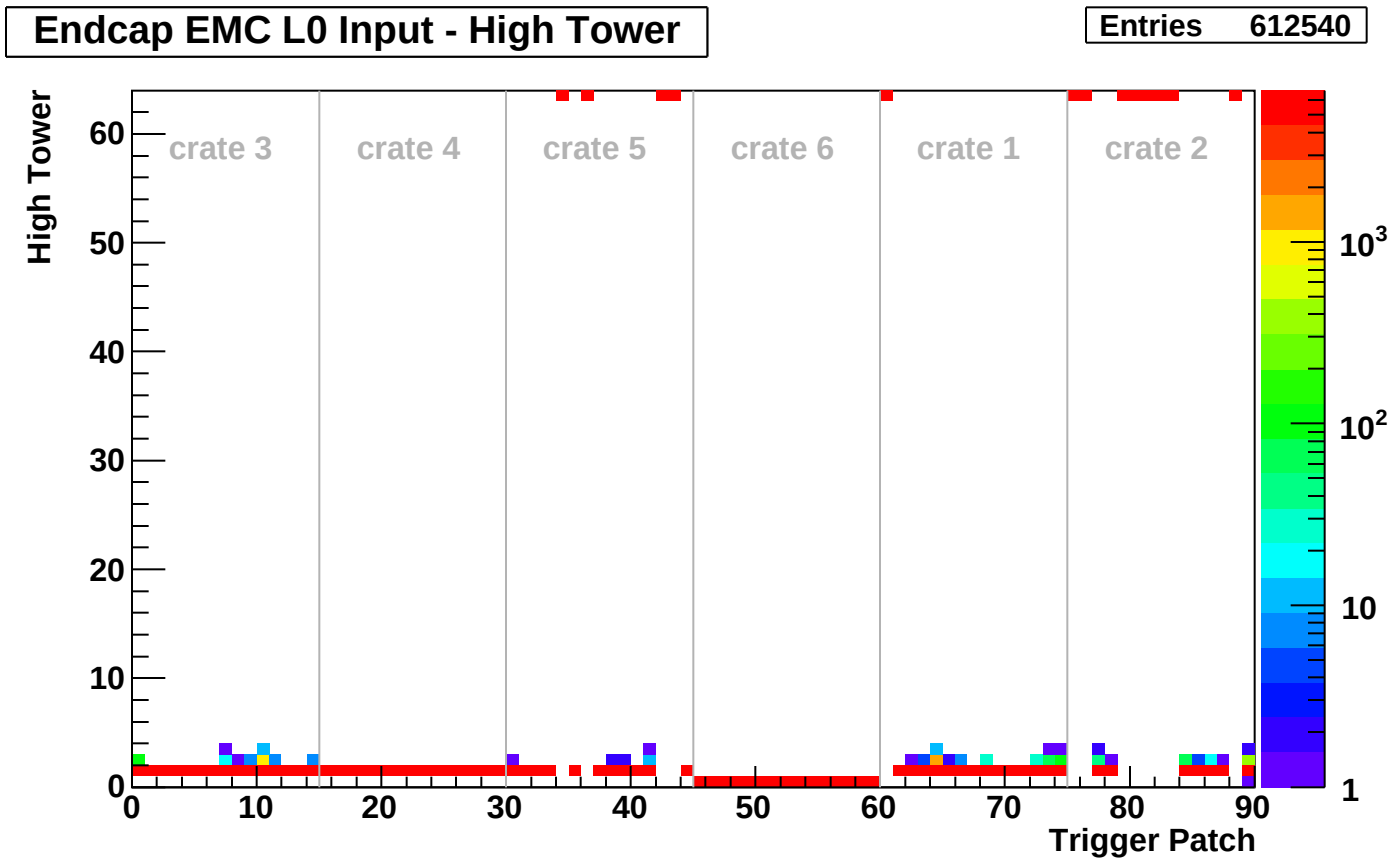

TCU bits
Entries 108896
TCU bits
1
0
6806
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias
1

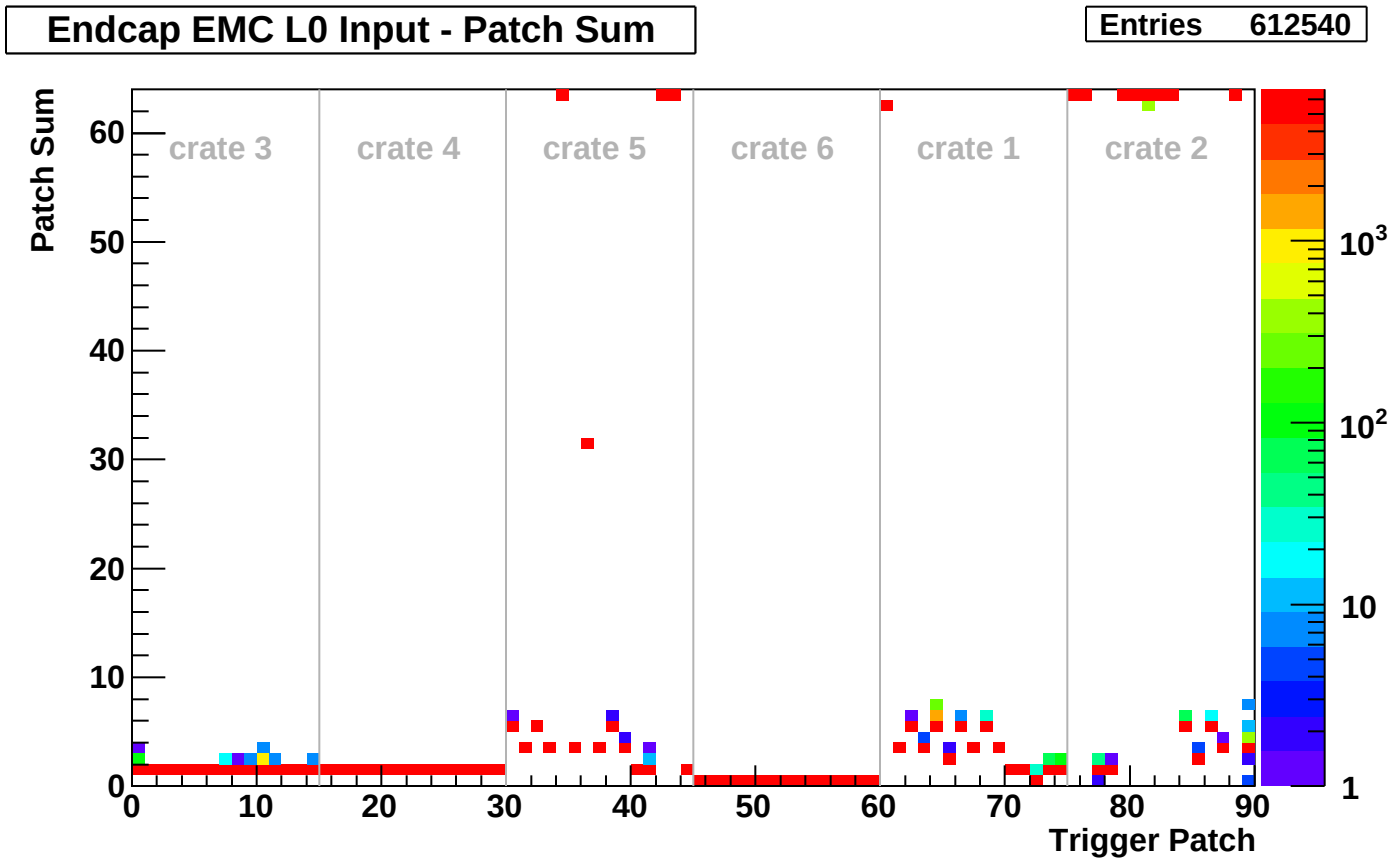

TCU bits
Entries 20737
TCU bits - Simulated
1
0
-1
-2
726
474
5920
6806
5
6806
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias
1

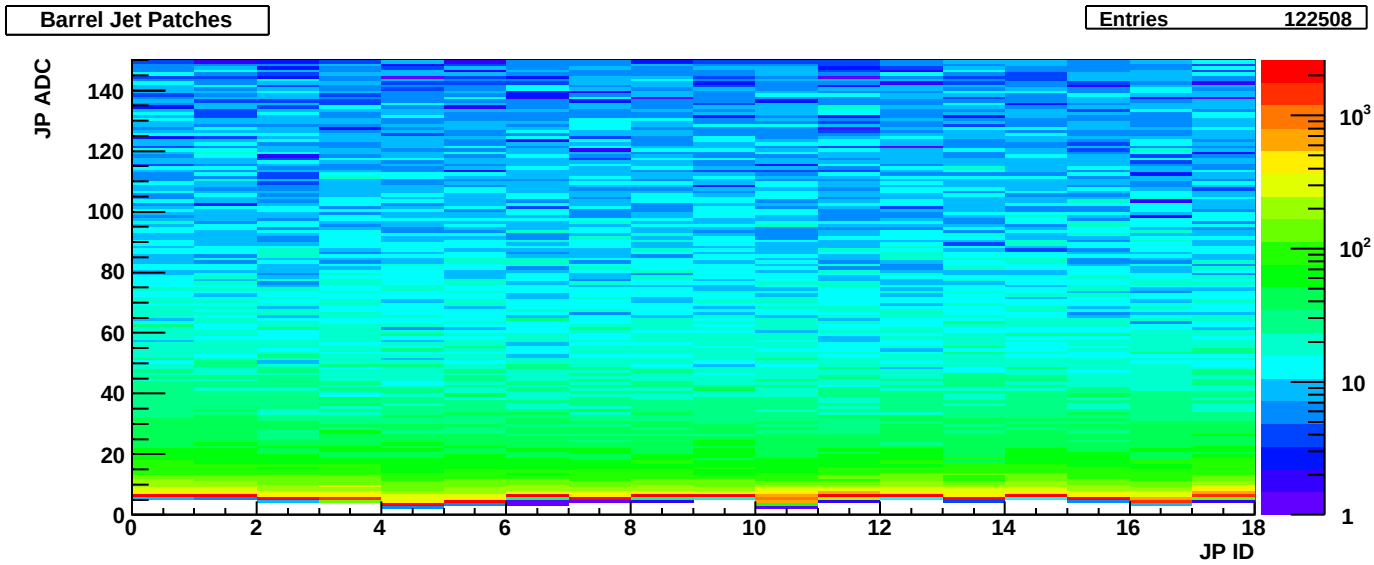

BunchId7Bit (BHT3)
Entries 211
10
1
0
20
40
60
80
100
120

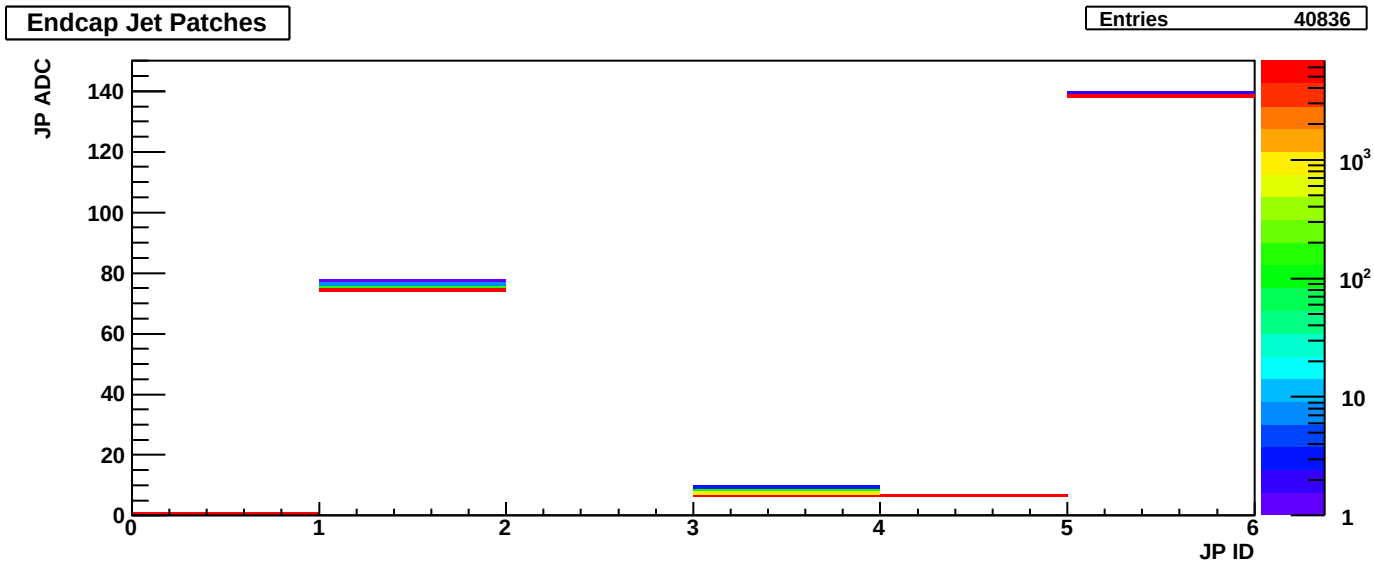

Barrel EMC L0 Input - High Tower
Entries 2041800
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$

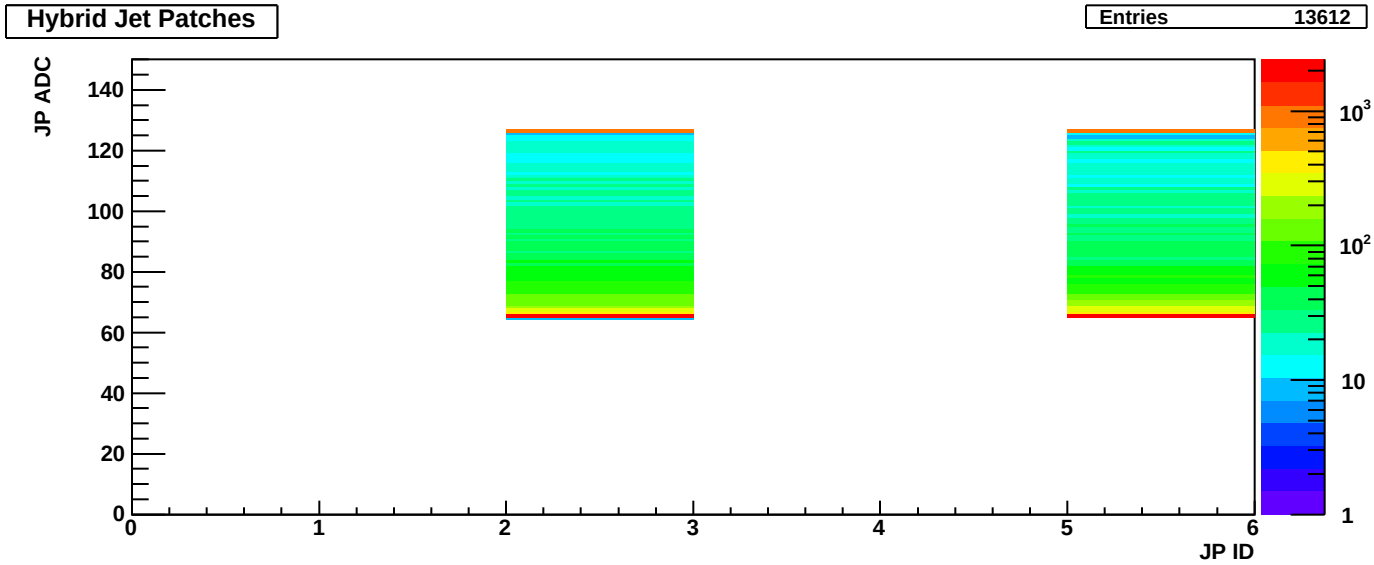

Barrel EMC L0 Input - Patch Sum
Entries 2041800
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$

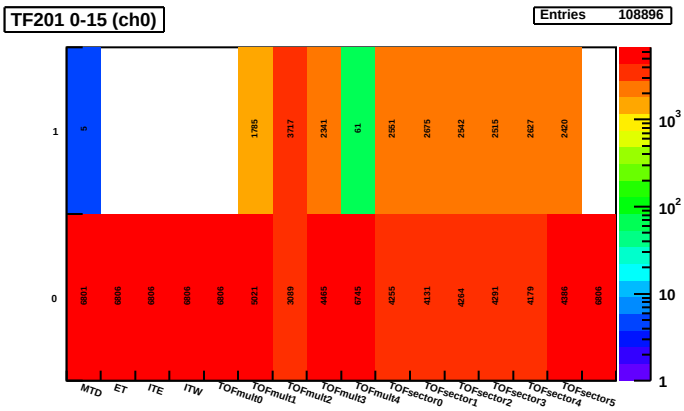

Endcap EMC L0 Input - High Tower
Entries 612540
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

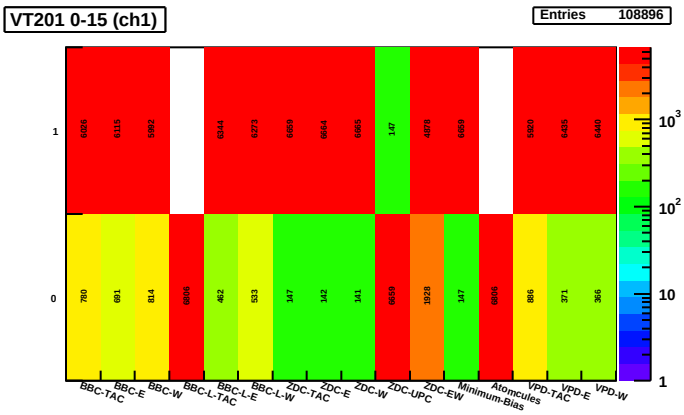

Endcap EMC L0 Input - Patch Sum
Entries 612540
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

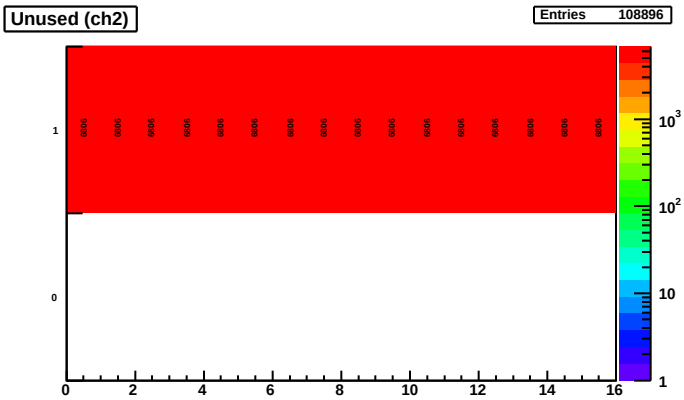

Barrel Jet Patches
Entries 122508
JP ADC
JP ID

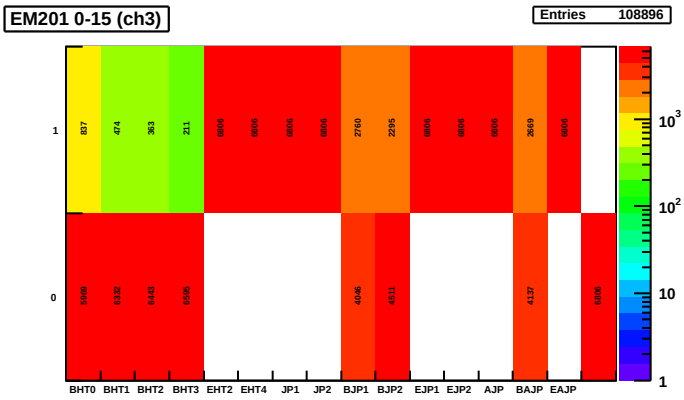

Endcap Jet Patches
Entries 40836
JP ADC
JP ID

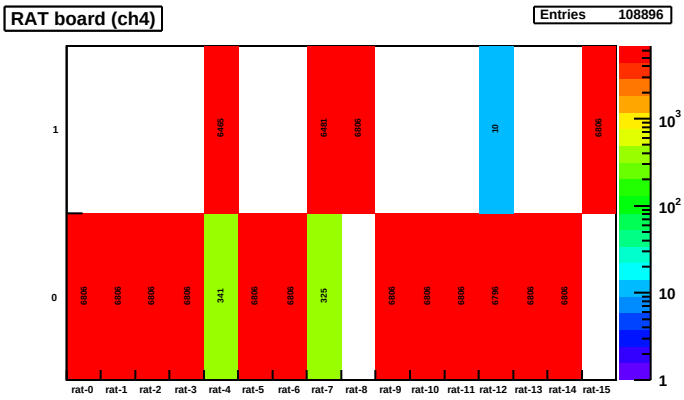

Hybrid Jet Patches
Entries 13612
JP ADC
JP ID

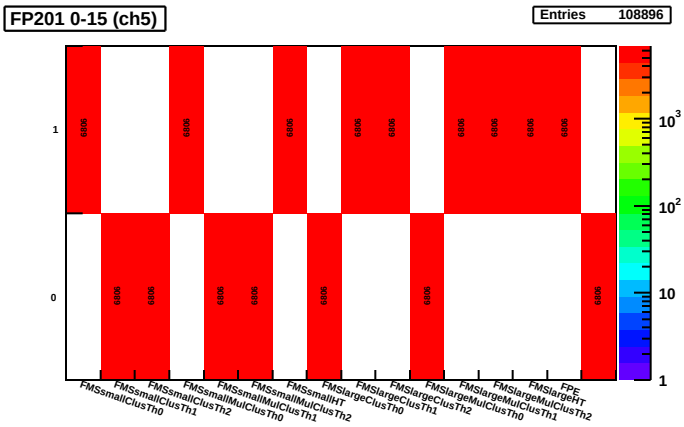

TF201 0-15 (ch0)
Entries 108896
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

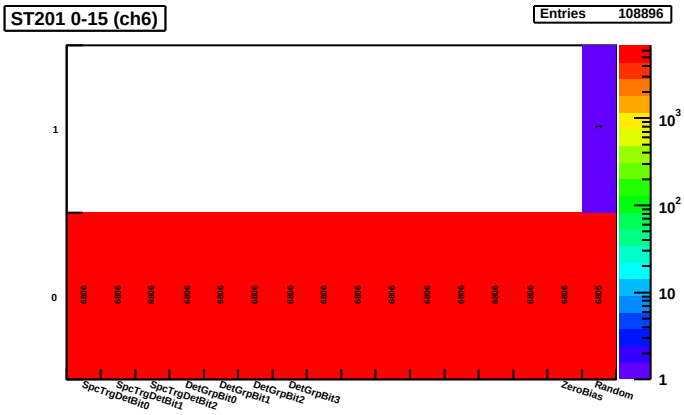

VT201 0-15 (ch1)
Entries 108896
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

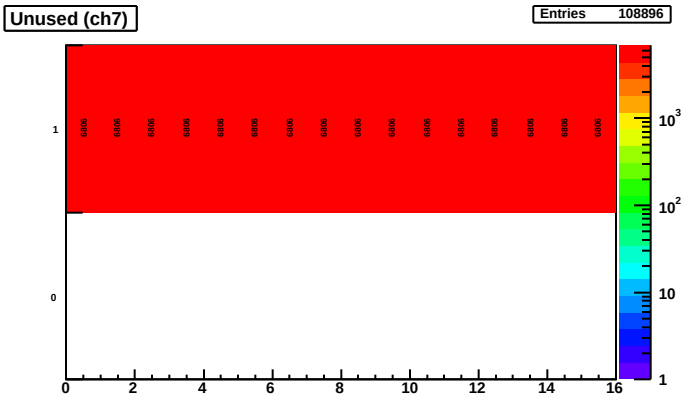

Unused (ch2)
Entries 108896

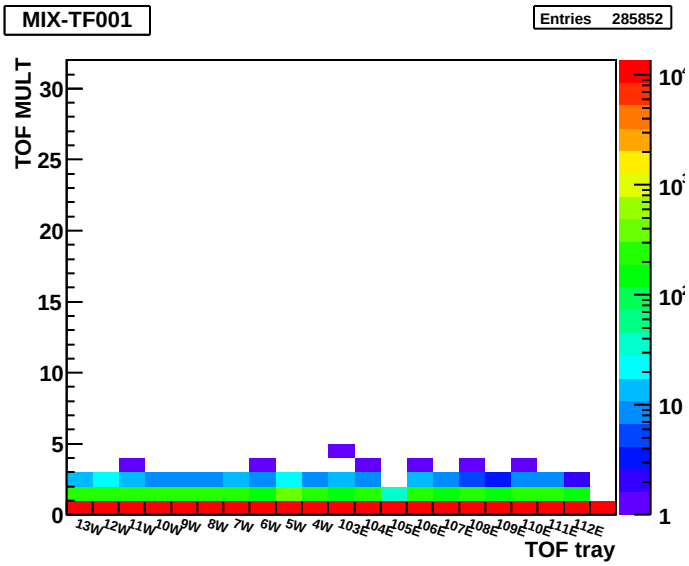

EM201 0-15 (ch3)
Entries 108896
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

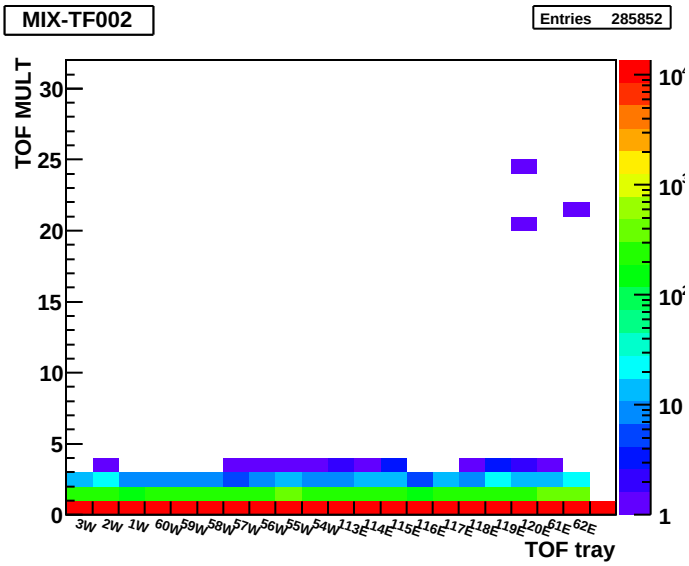

RAT board (ch4)
Entries 108896
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

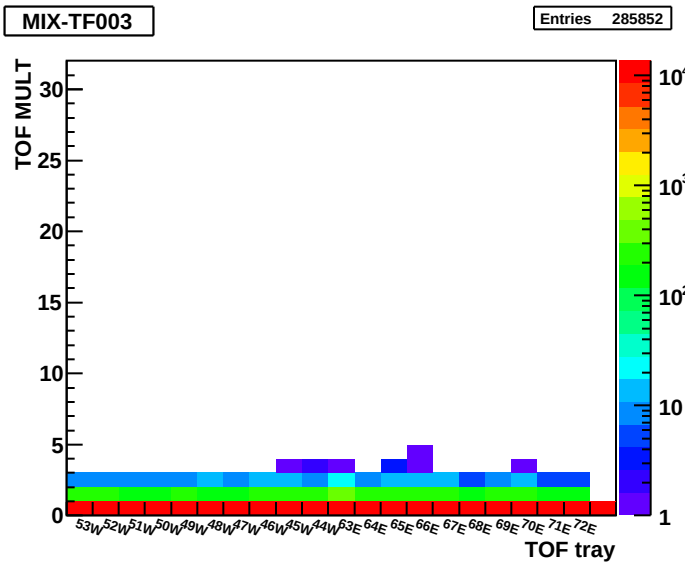

FP201 0-15 (ch5)
Entries 108896
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

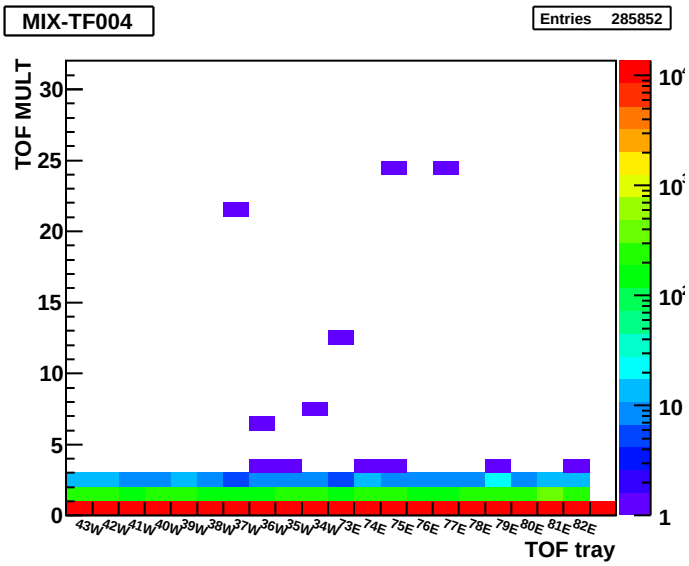

ST201 0-15 (ch6)
Entries 108896
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

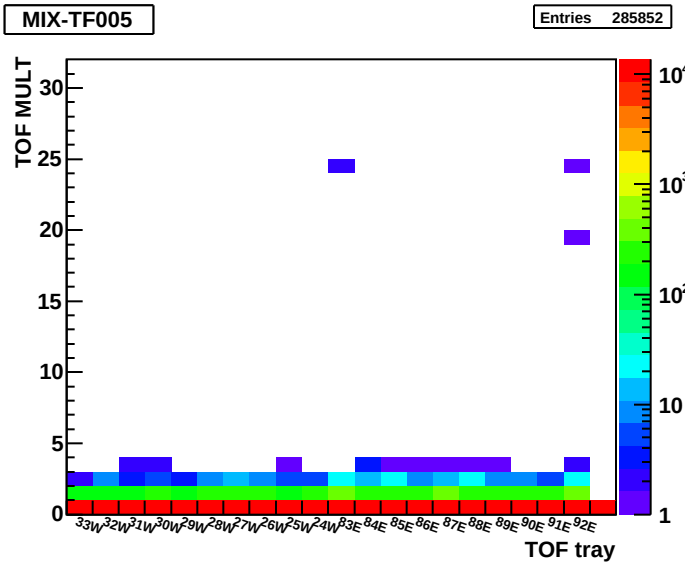

Unused (ch7)
Entries 108896

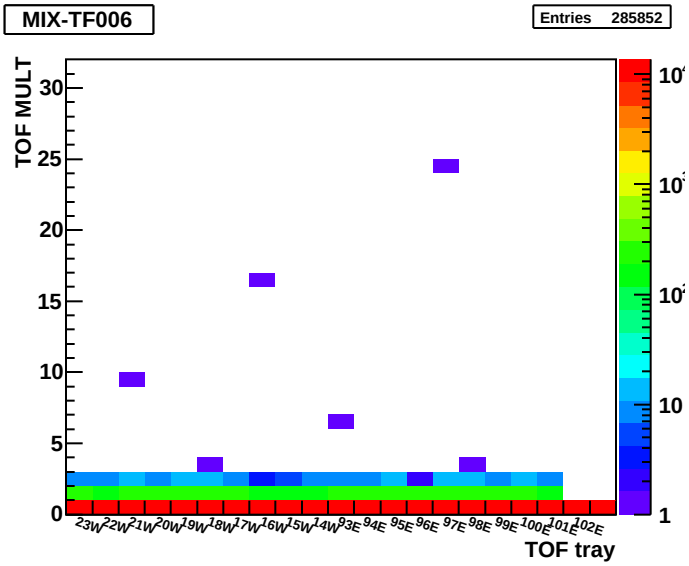

MIX-TF001
Entries 285852
TOF MULT
TOF tray

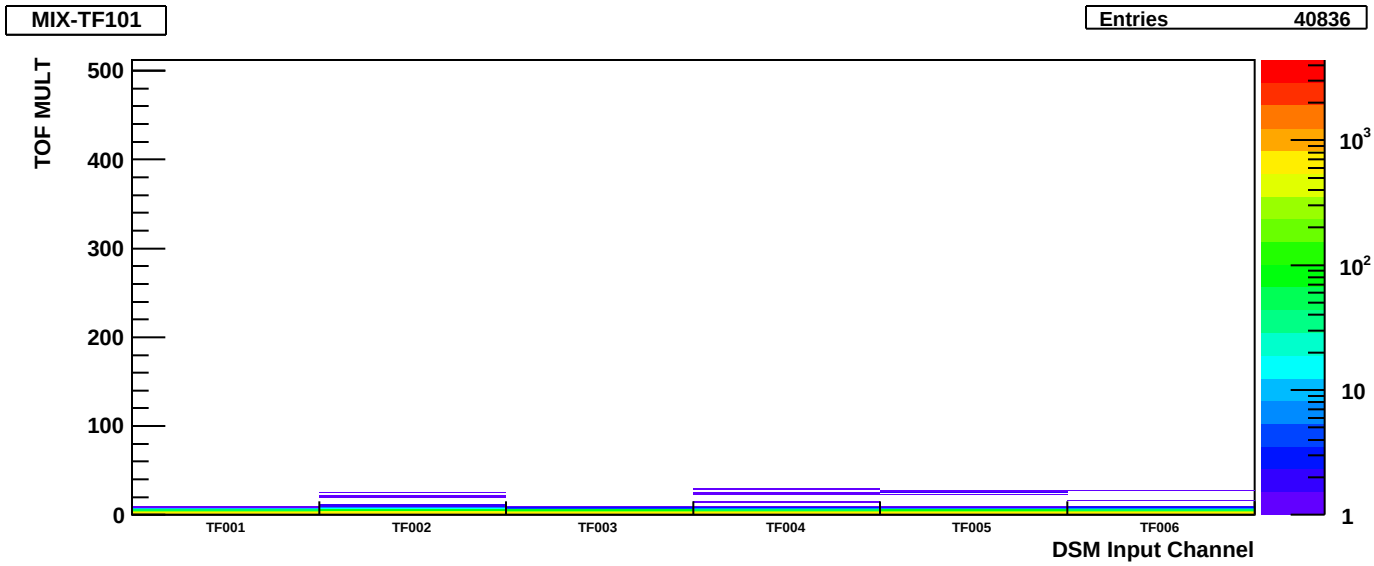

MIX-TF002
Entries 285852
TOF MULT
TOF tray

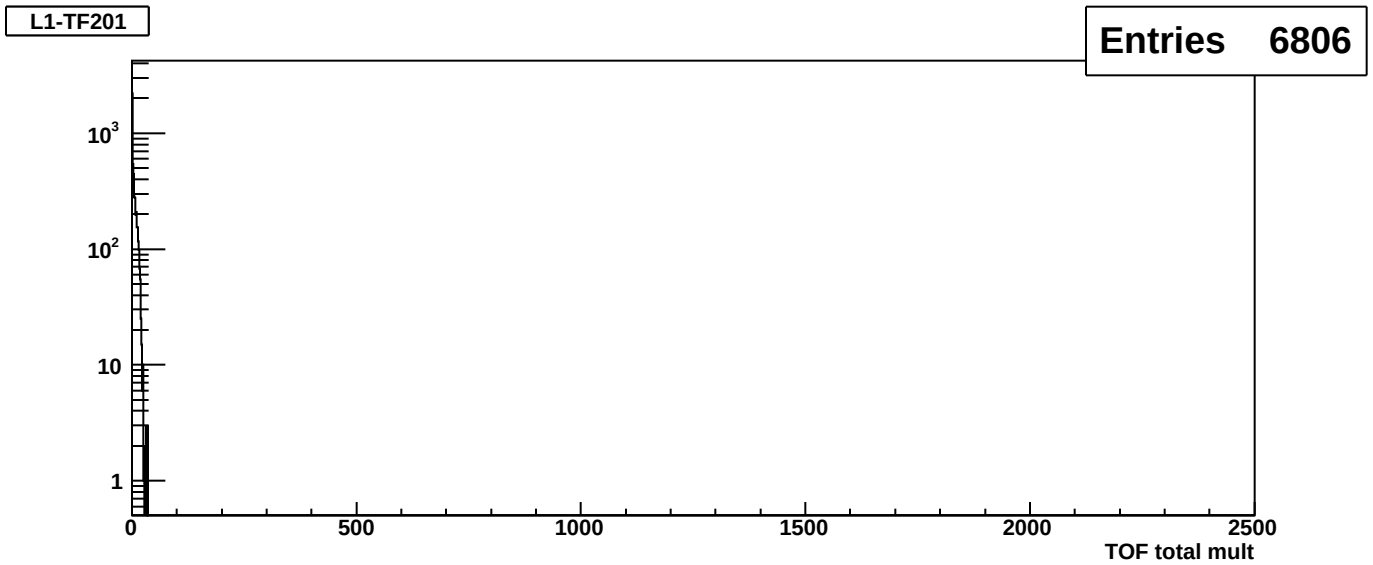

MIX-TF003
Entries 285852
TOF MULT
TOF tray

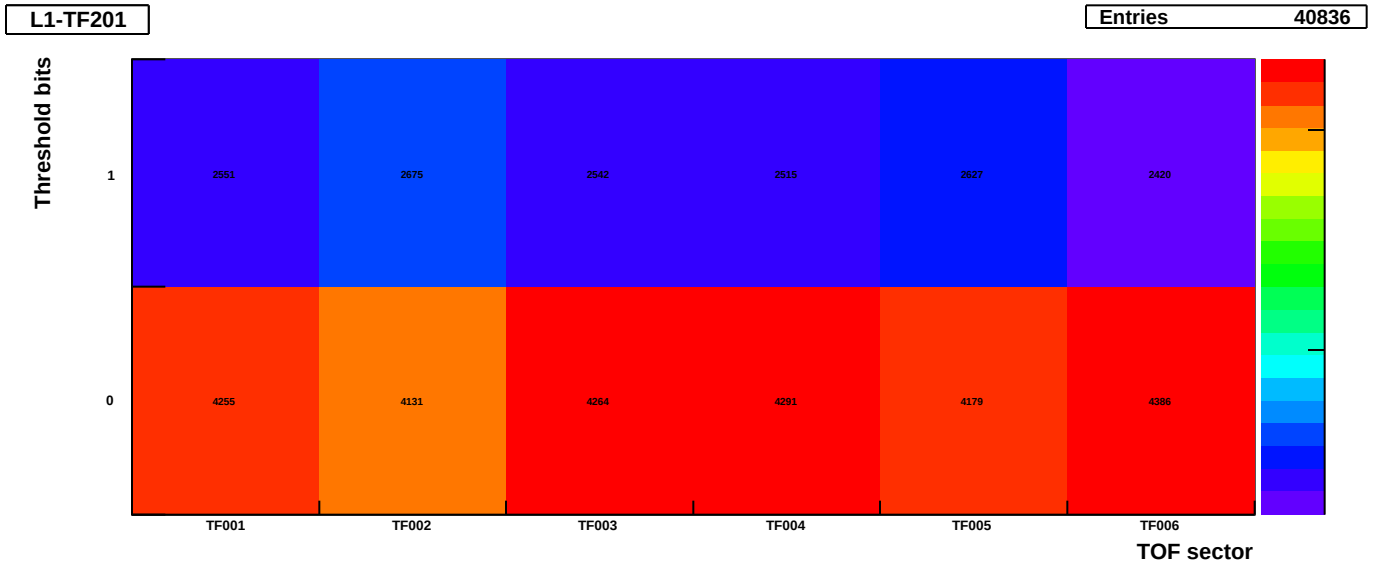

MIX-TF004
Entries 285852
TOF MULT
TOF tray

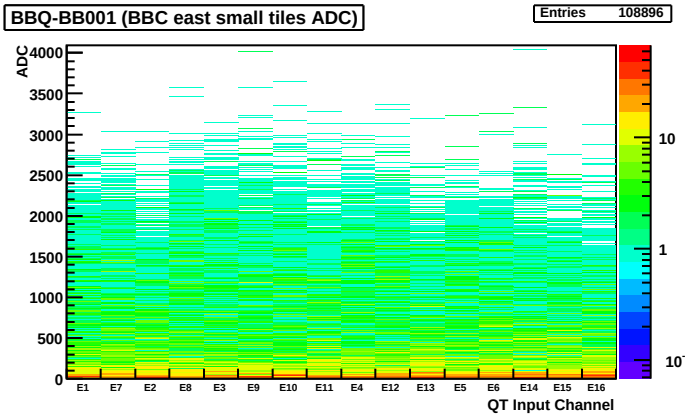

MIX-TF005
Entries 285852
TOF MULT
TOF tray

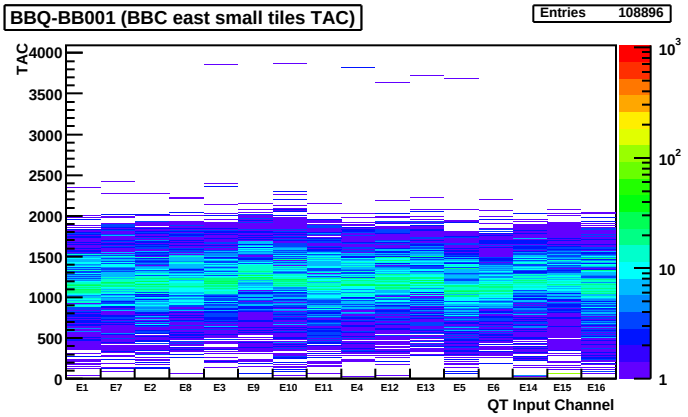

MIX-TF006
Entries 285852
TOF MULT
TOF tray

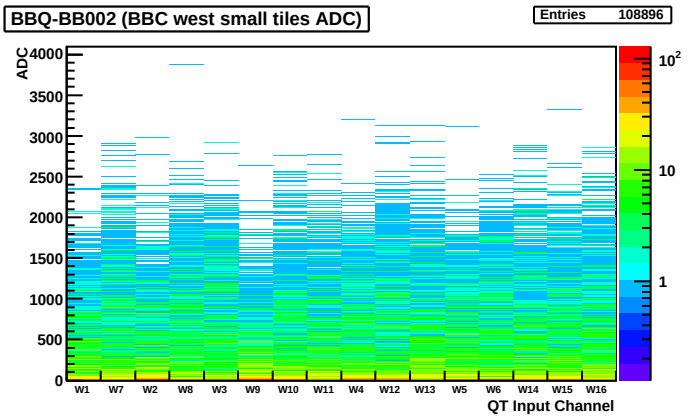

MIX-TF101
Entries 40836
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
$10^3$
$10^2$
10
1

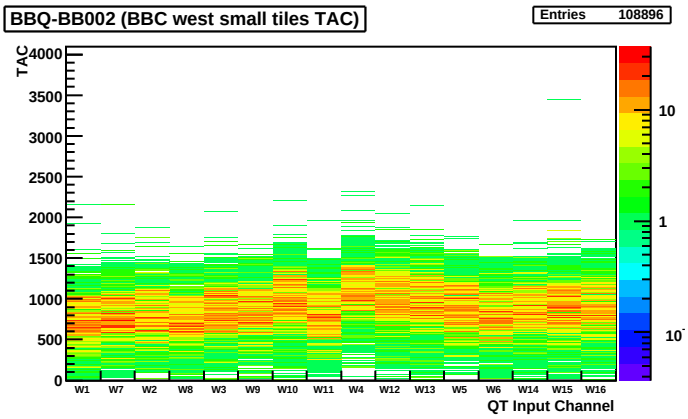

L1-TF201
Entries 6806
$10^3$
$10^2$
10
1
0
500
1000
1500
2000
2500
TOF total mult

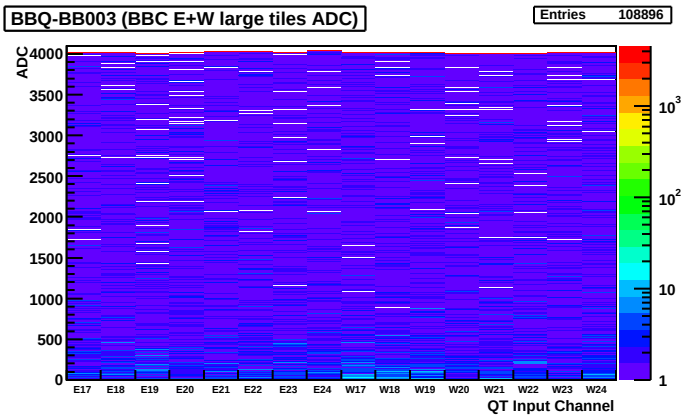

L1-TF201
Entries 40836
Threshold bits
1
0
2551
2675
2542
2515
2627
2420
4255
4131
4264
4291
4179
4386
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

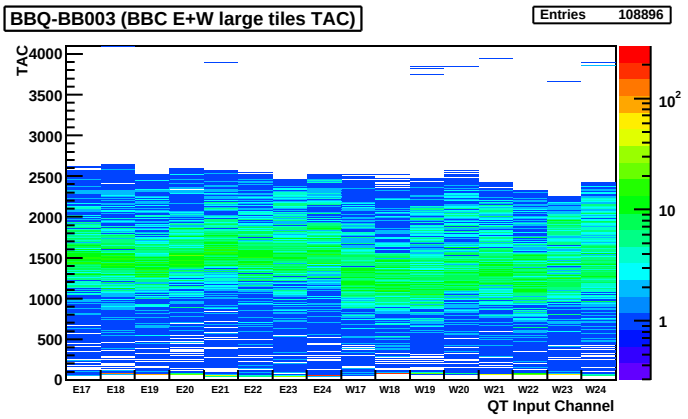

BBQ-BB001 (BBC east small tiles ADC)
Entries 108896
ADC
QT Input Channel

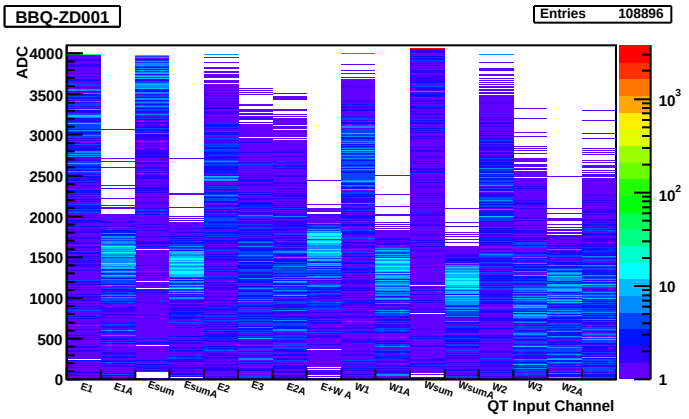

BBQ-BB001 (BBC east small tiles TAC)
Entries 108896
TAC
QT Input Channel

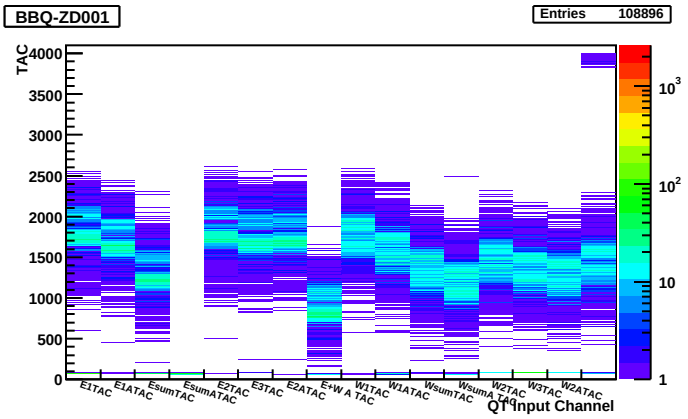

BBQ-BB002 (BBC west small tiles ADC)
Entries 108896
ADC
QT Input Channel

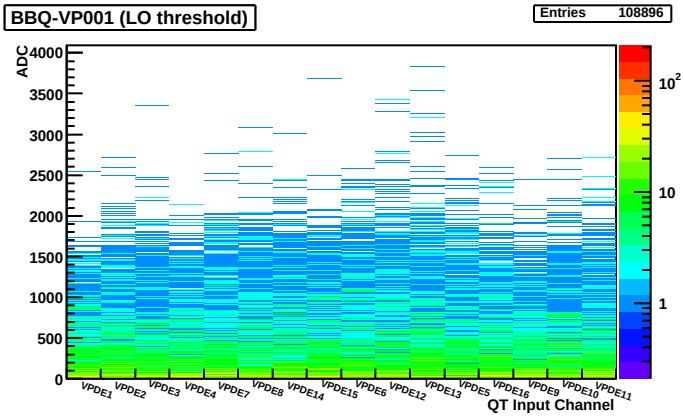

BBQ-BB002 (BBC west small tiles TAC)
Entries 108896
TAC
QT Input Channel

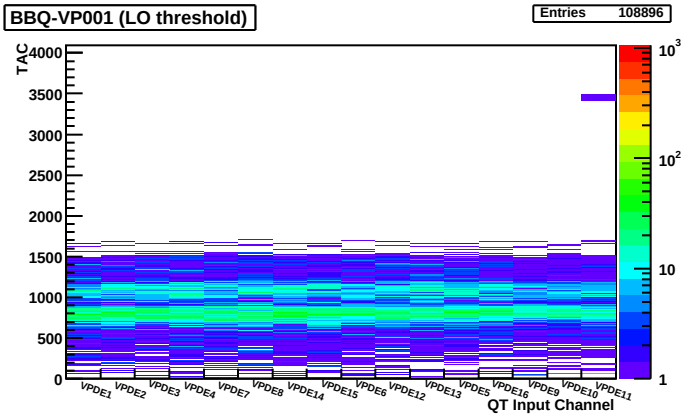

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 108896
ADC
QT Input Channel

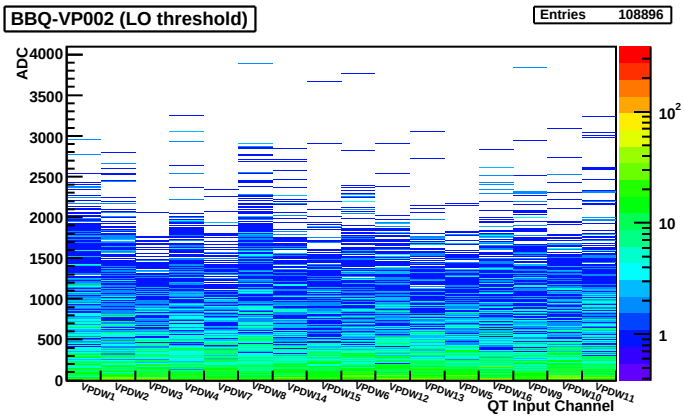

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 108896
TAC
QT Input Channel

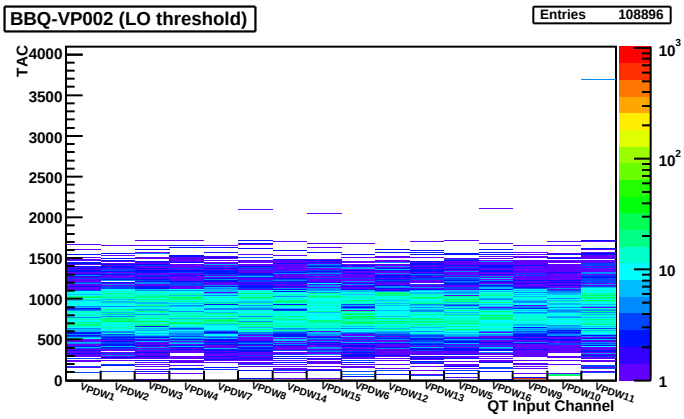

BBQ-ZD001
Entries 108896
ADC
QT Input Channel

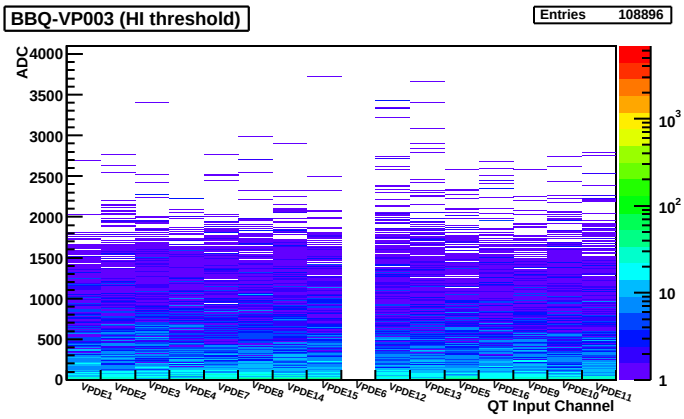

BBQ-ZD001
Entries 108896
TAC
QT Input Channel

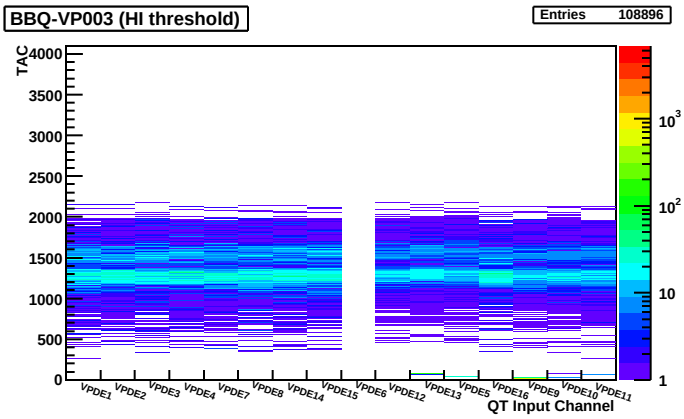

BBQ-VP001 (LO threshold)
Entries 108896
ADC
QT Input Channel

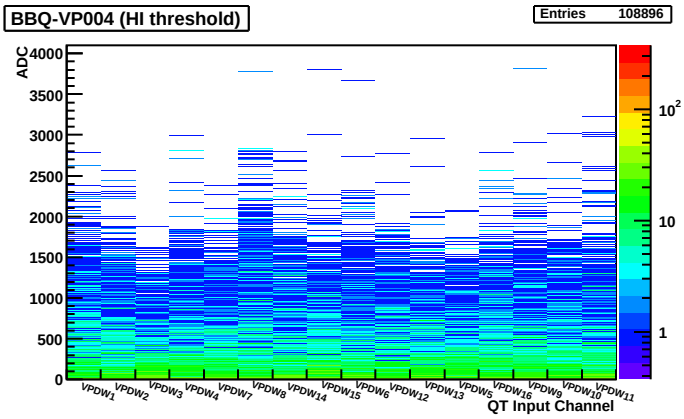

BBQ-VP001 (LO threshold)
Entries 108896
TAC
QT Input Channel

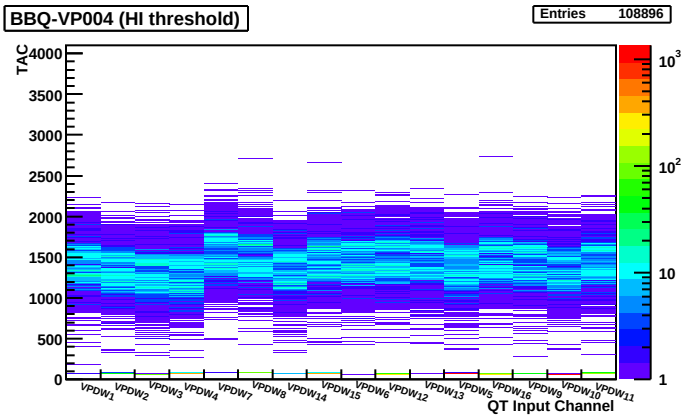

BBQ-VP002 (LO threshold)
Entries 108896
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 108896
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 108896
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 108896
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 108896
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 108896
TAC
QT Input Channel