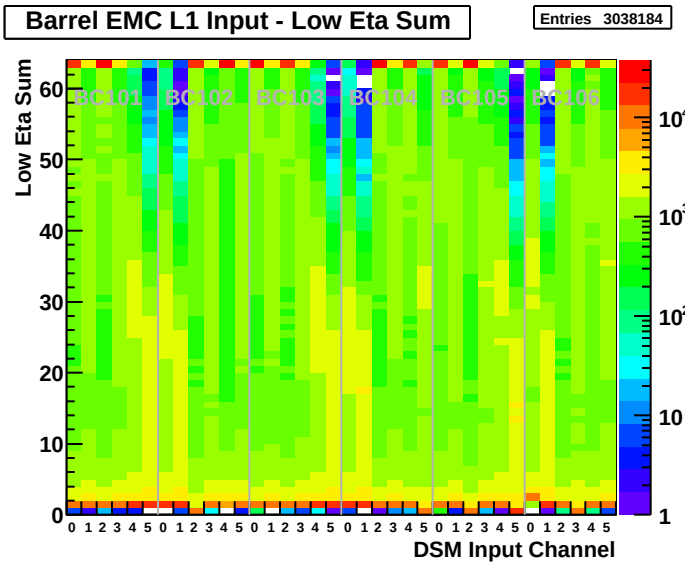
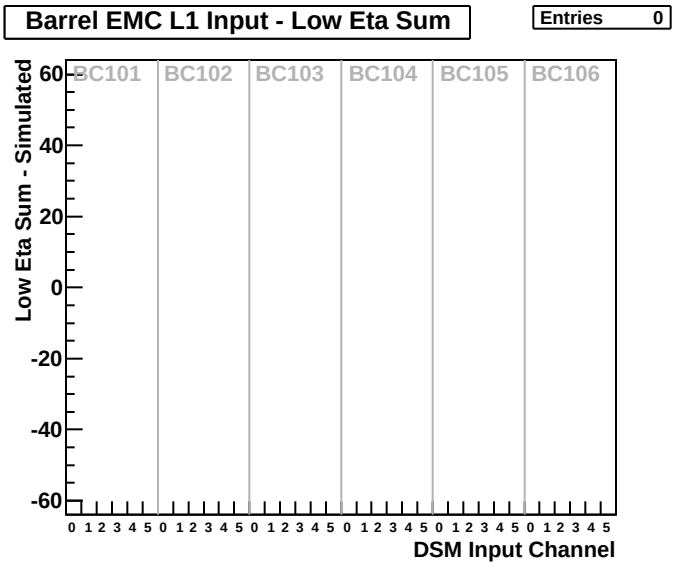
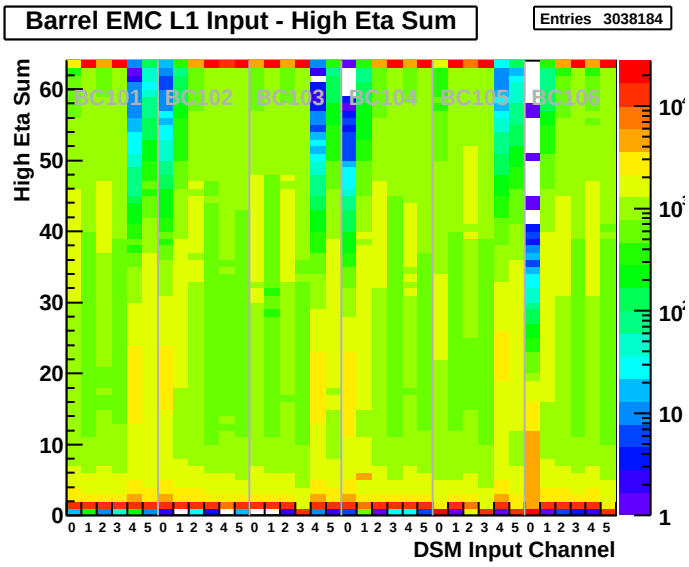
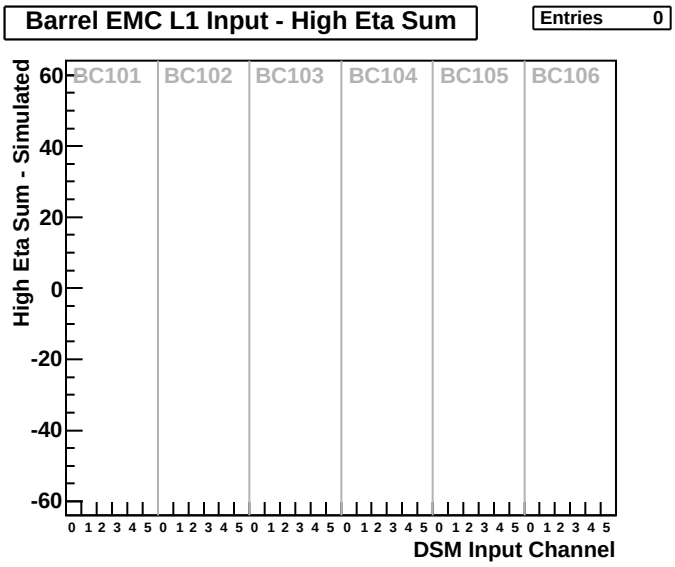
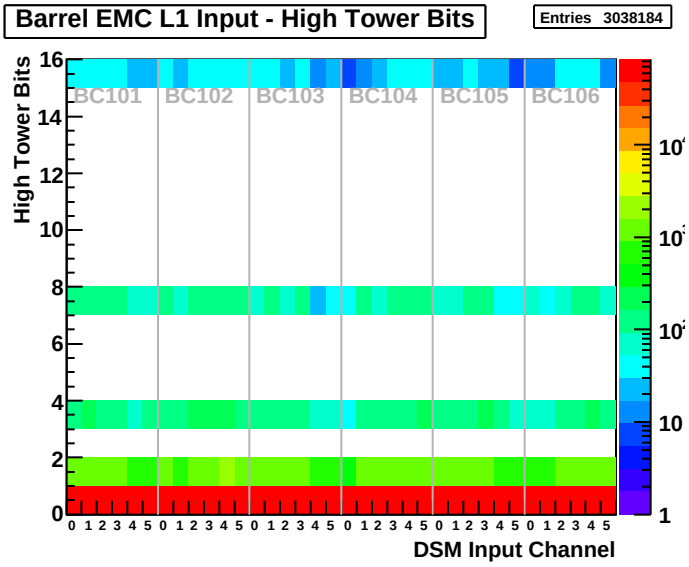
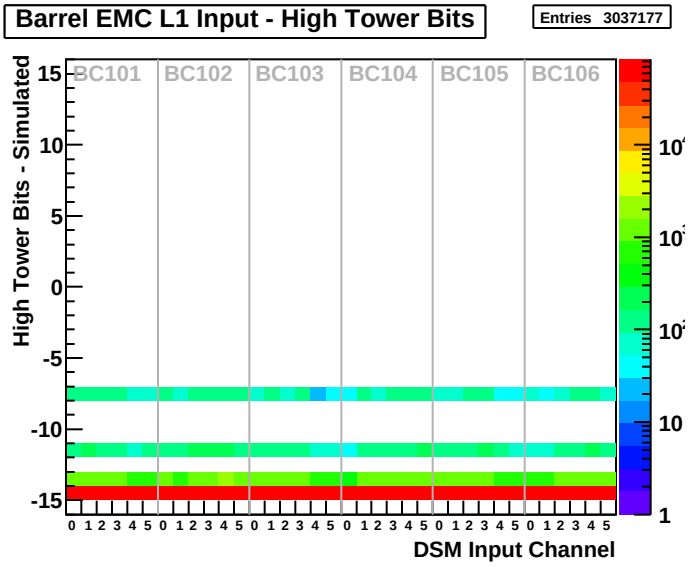

Barrel EMC L1 Input - Low Eta Sum
Entries 3038184
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

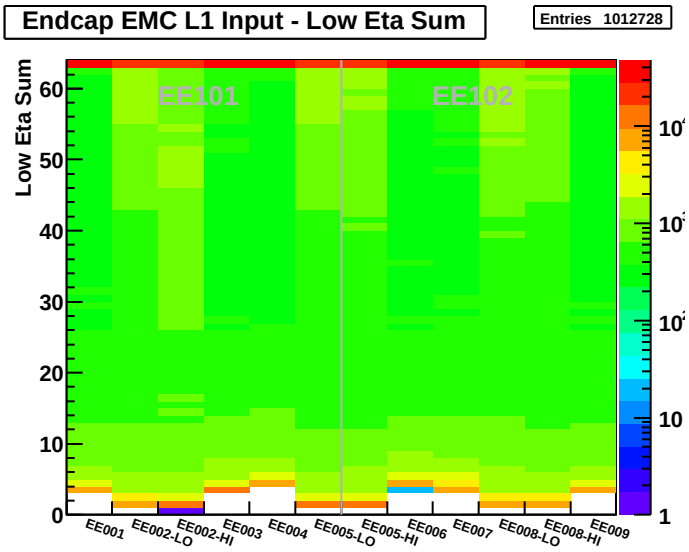

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

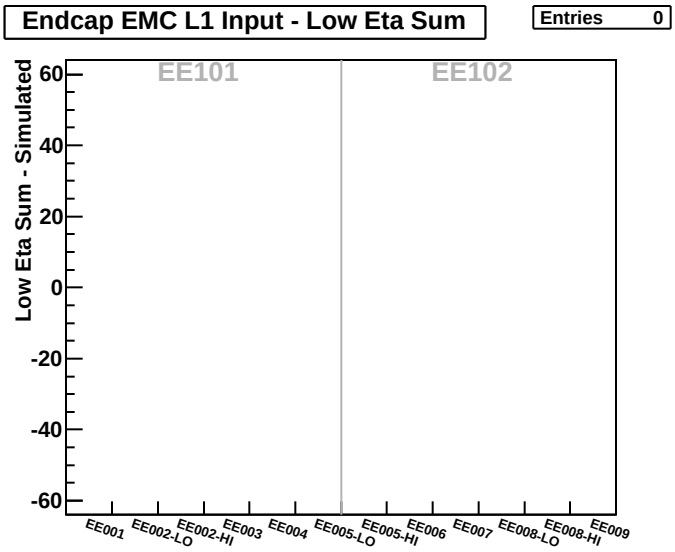

Barrel EMC L1 Input - High Eta Sum
Entries 3038184
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

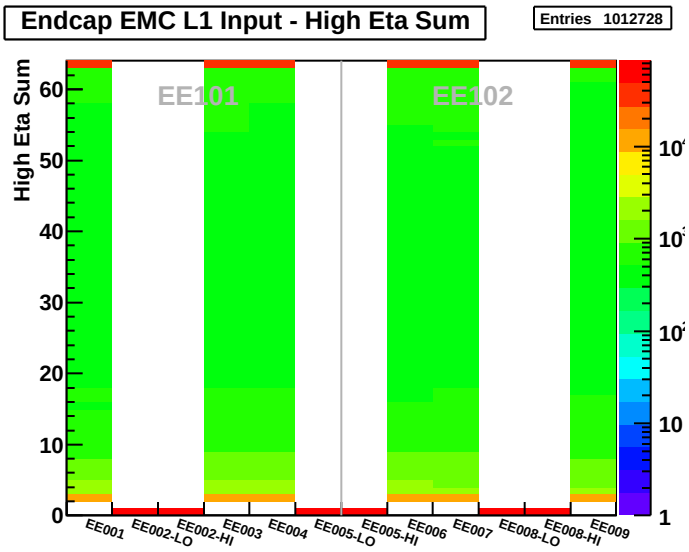

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

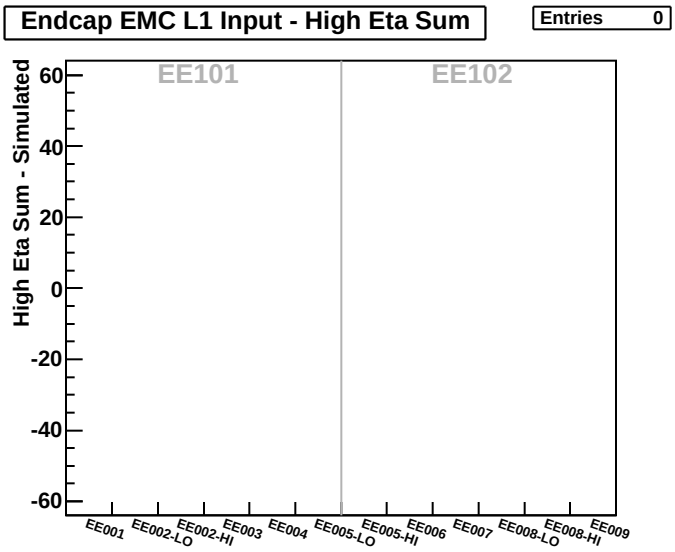

Barrel EMC L1 Input - High Tower Bits
Entries 3038184
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

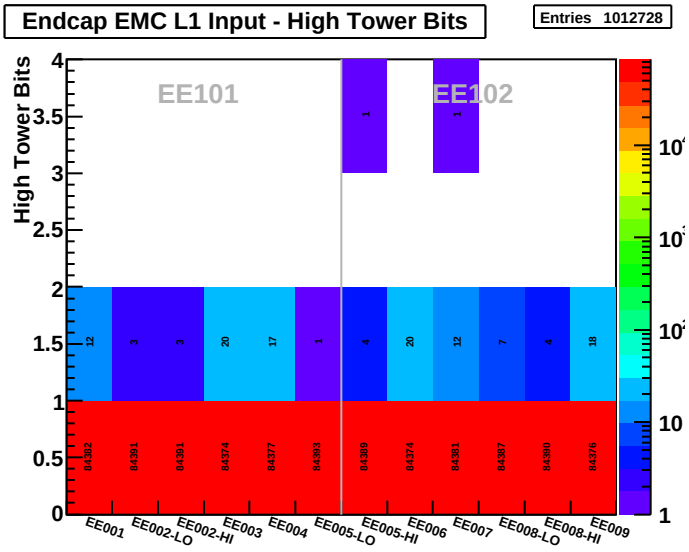

Barrel EMC L1 Input - High Tower Bits
Entries 3037177
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

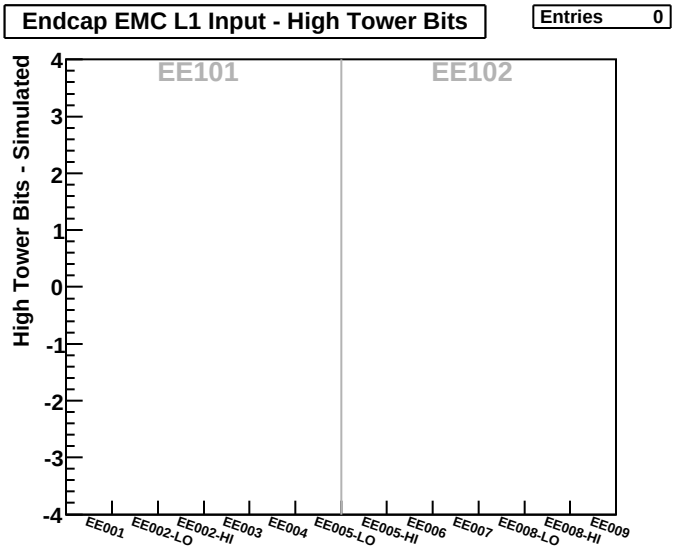

Endcap EMC L1 Input - Low Eta Sum
Entries 1012728
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

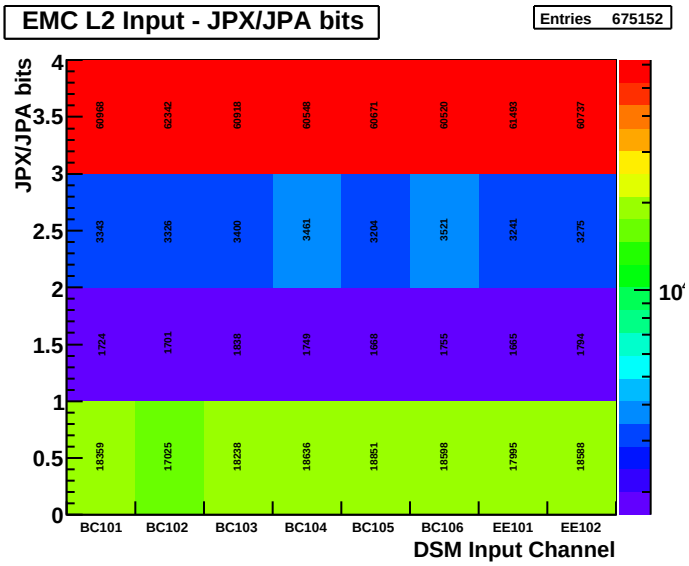

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

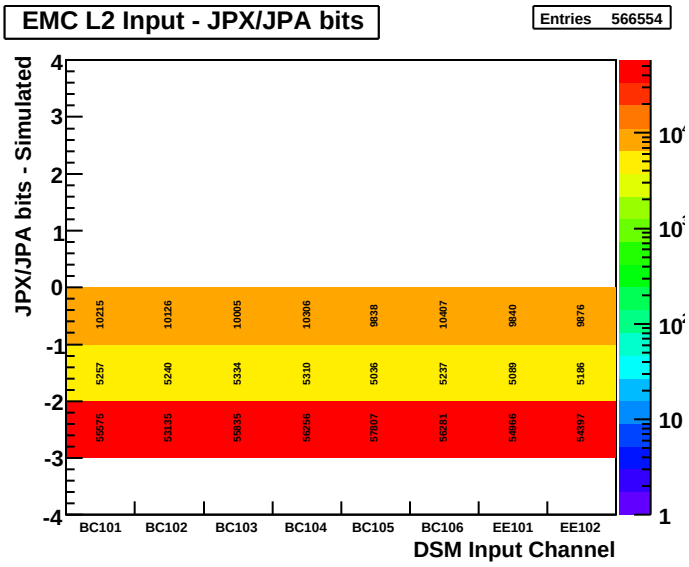

Endcap EMC L1 Input - High Eta Sum
Entries 1012728
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

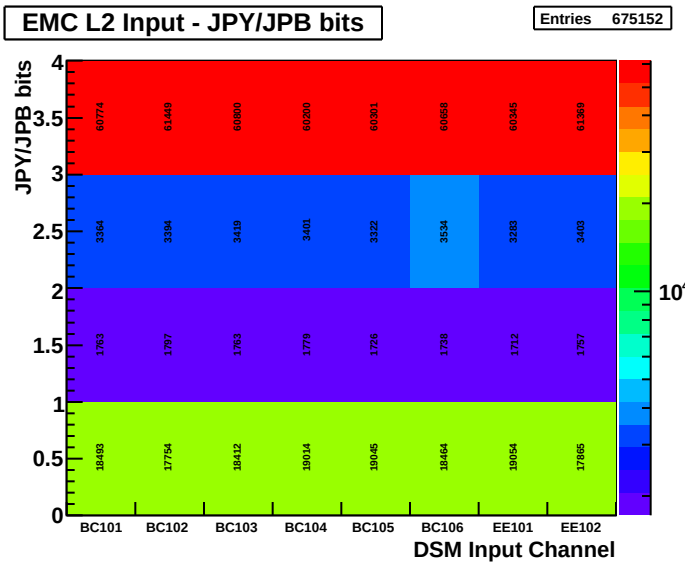

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

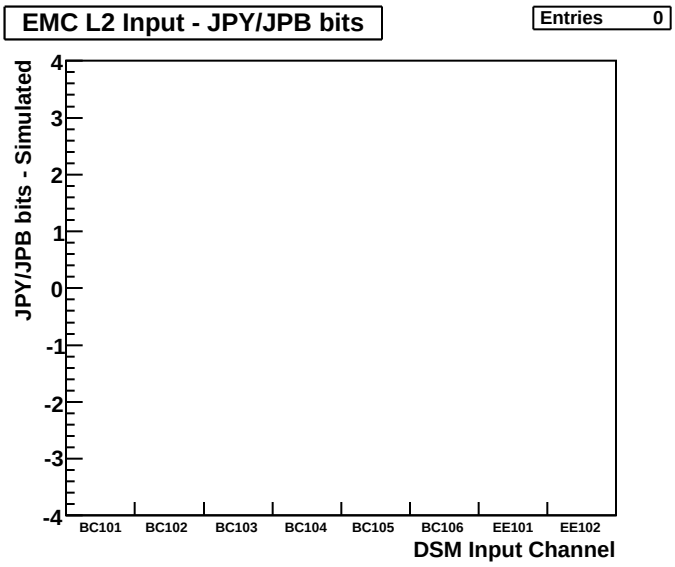

Endcap EMC L1 Input - High Tower Bits
Entries 1012728
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

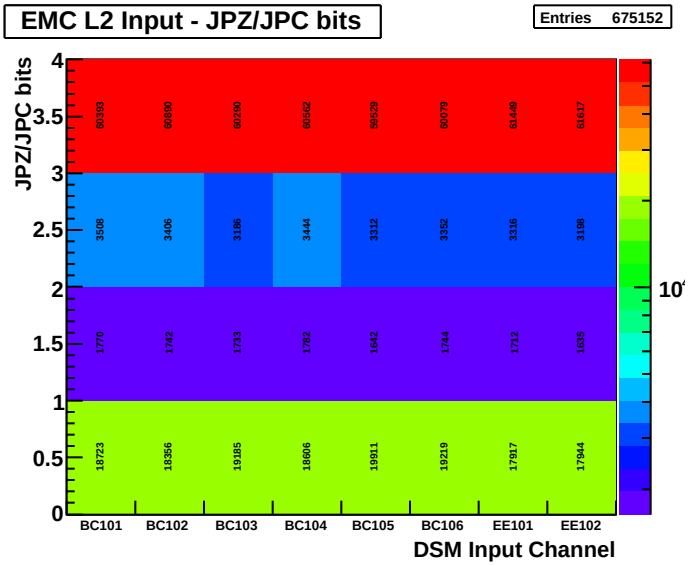

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

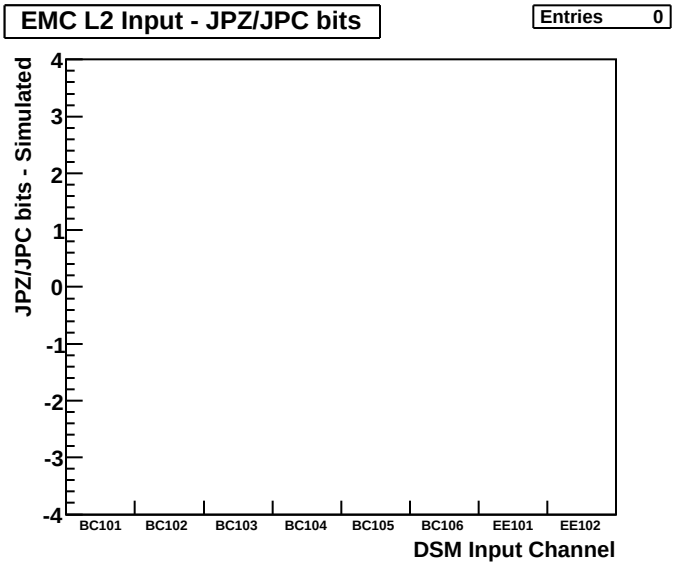

EMC L2 Input - JPX/JPA bits
Entries 675152
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
60968 62342 60918 60548 60671 60520 61493 60737
3343 3326 3400 3461 3204 3521 3241 3275
1724 1701 1638 1749 1668 1755 1665 1794
18359 17025 18238 18636 18851 18598 17995 18588

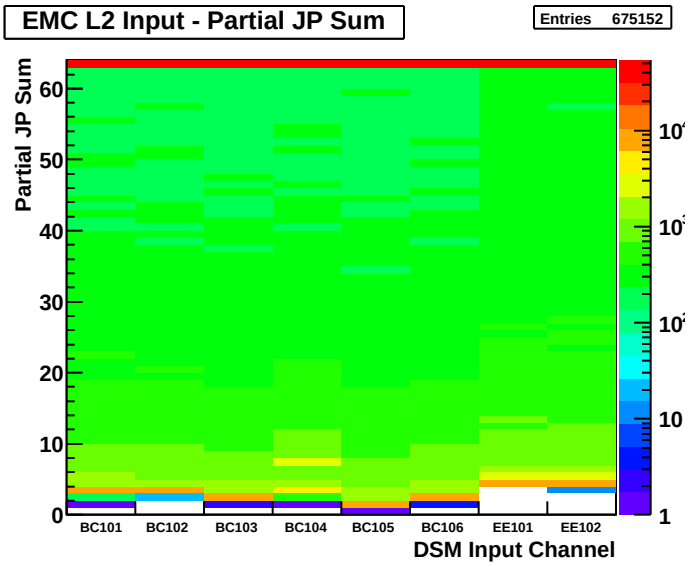

EMC L2 Input - JPX/JPA bits
Entries 566554
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
10215 10126 10005 10306 9838 10407 9840 9876
5257 5240 5334 5310 5036 5237 5089 5186
55575 53135 53935 56256 57807 56281 54966 54397

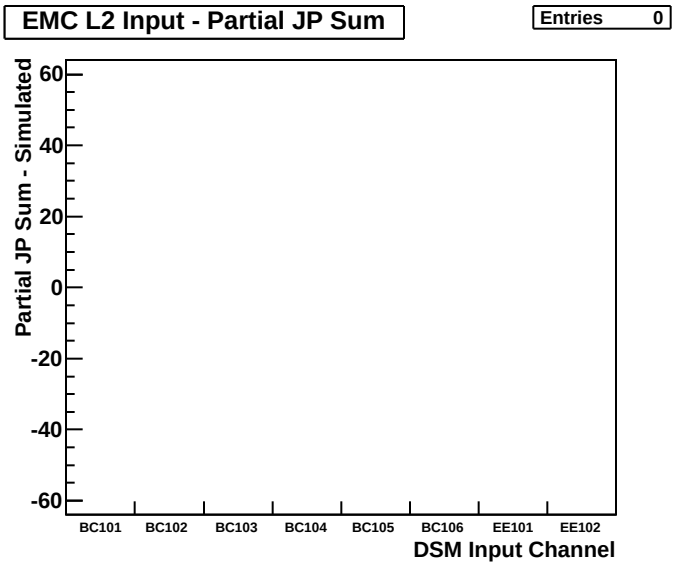

EMC L2 Input - JPY/JPB bits
Entries 675152
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
60774 61449 60800 60200 60301 60658 60345 61369
3364 3394 3419 3401 3322 3534 3283 3403
1763 1797 1763 1779 1726 1738 1712 1757
18493 17754 18412 19014 19045 18464 19054 17865

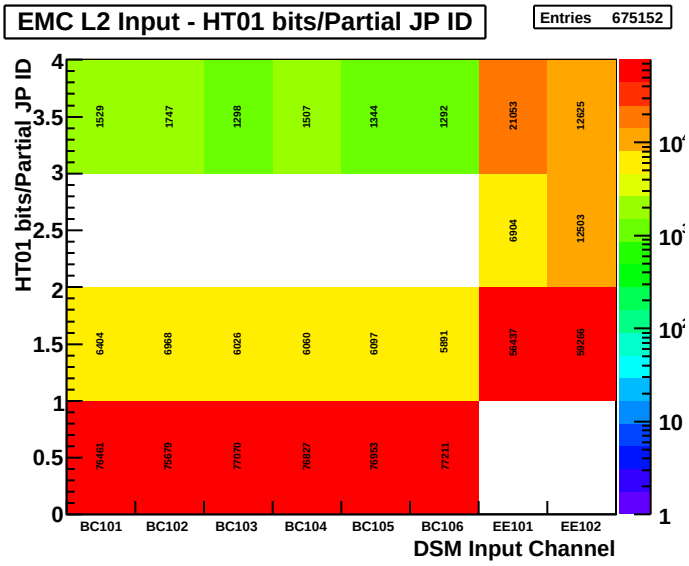

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

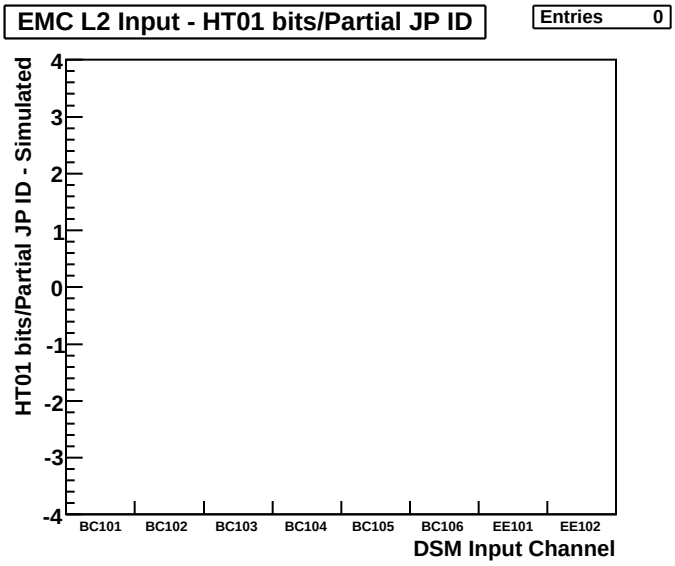

EMC L2 Input - JPZ/JPC bits
Entries 675152
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
60393 60890 60230 60562 59529 60079 61449 61617
3508 3406 3186 3444 3312 3352 3316 3198
1770 1742 1733 1782 1642 1744 1712 1635
18723 18356 19185 18606 19911 19219 17917 17944

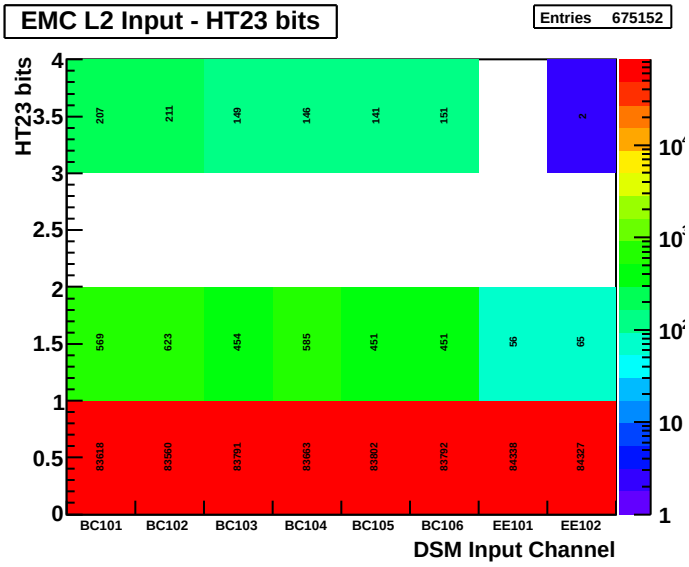

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

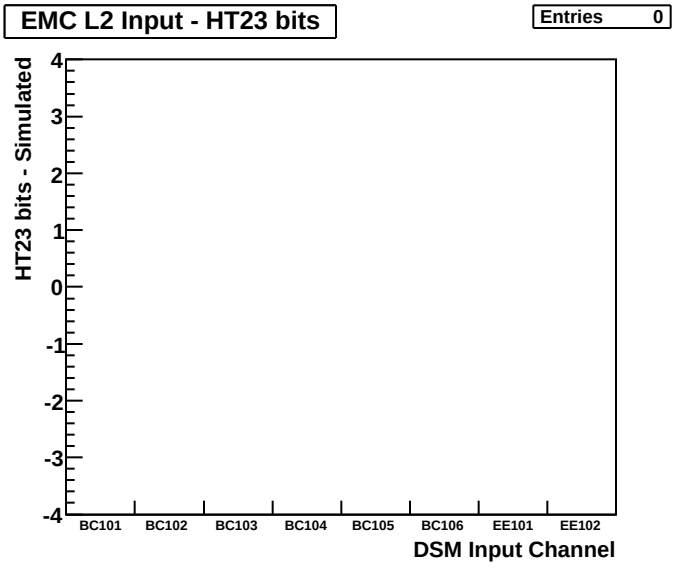

EMC L2 Input - Partial JP Sum
Entries 675152
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

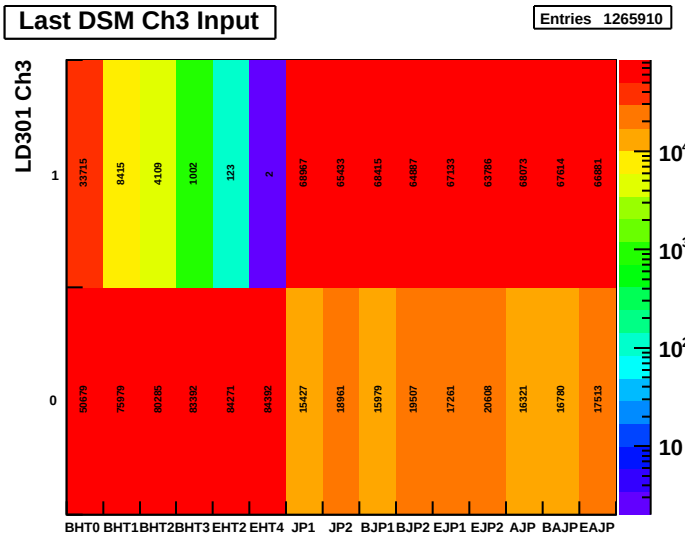

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

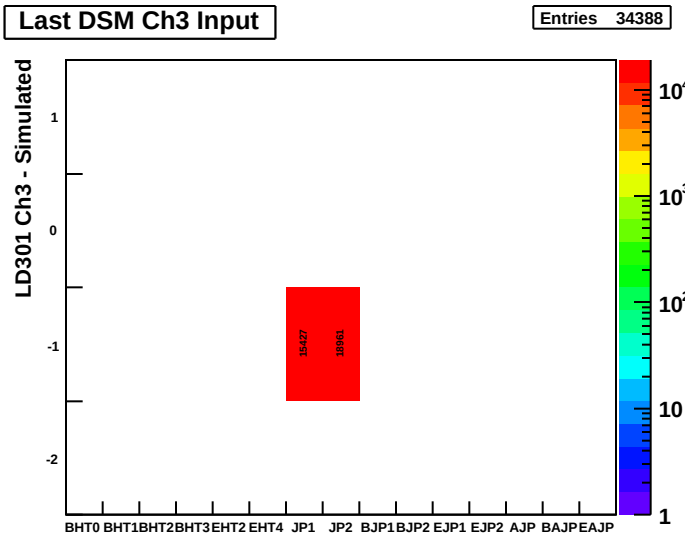

EMC L2 Input - HT01 bits/Partial JP ID
Entries 675152
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

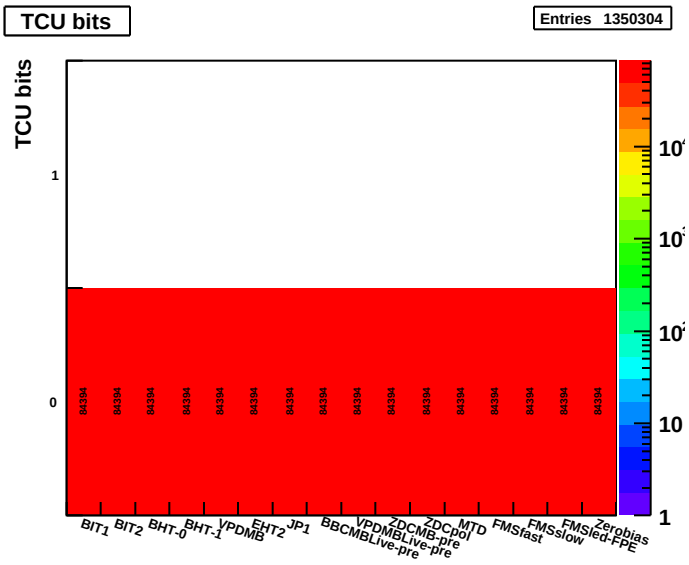

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

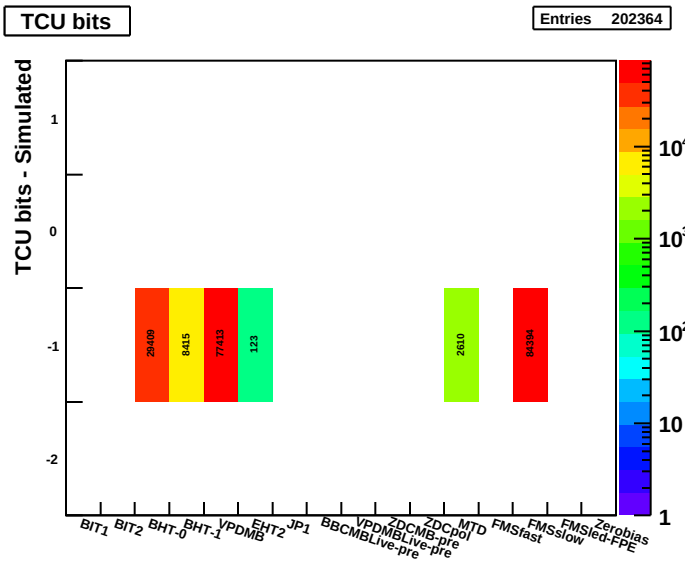

EMC L2 Input - HT23 bits
Entries 675152
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

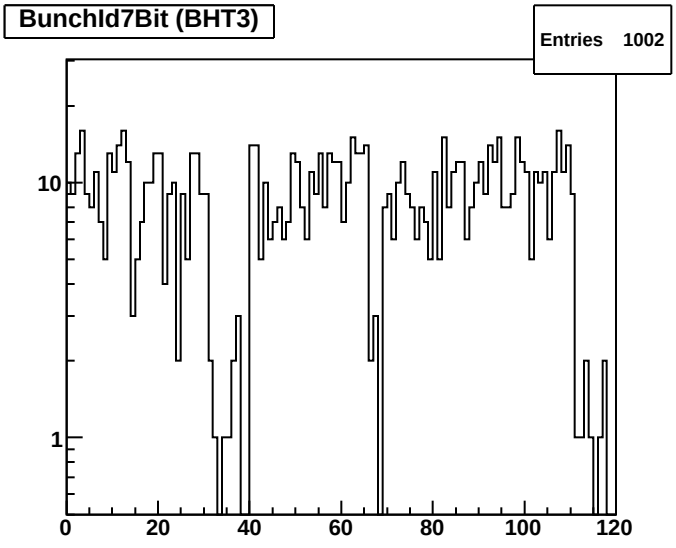

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

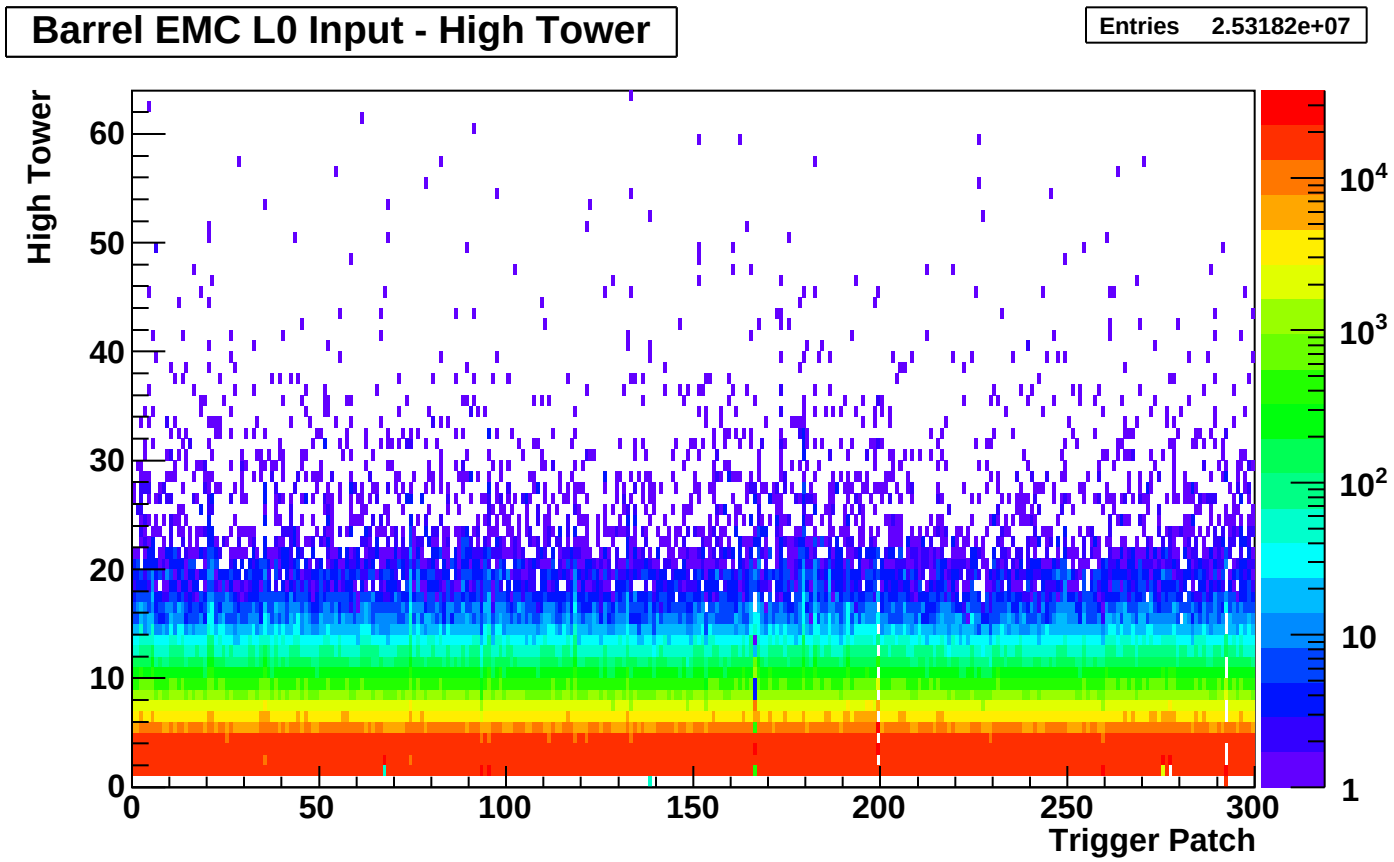

Last DSM Ch3 Input
Entries 1265910
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

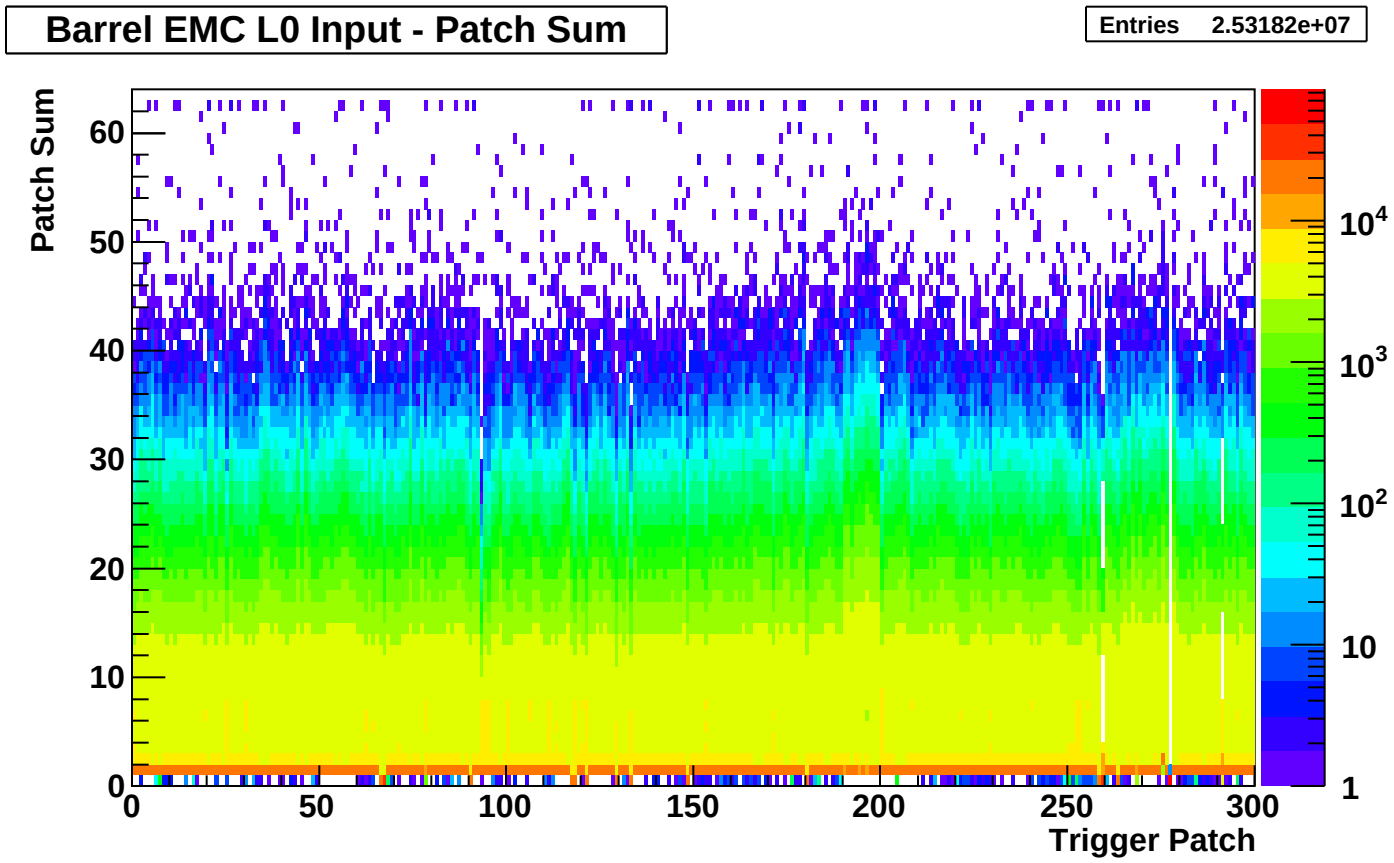

Last DSM Ch3 Input
Entries 34388
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

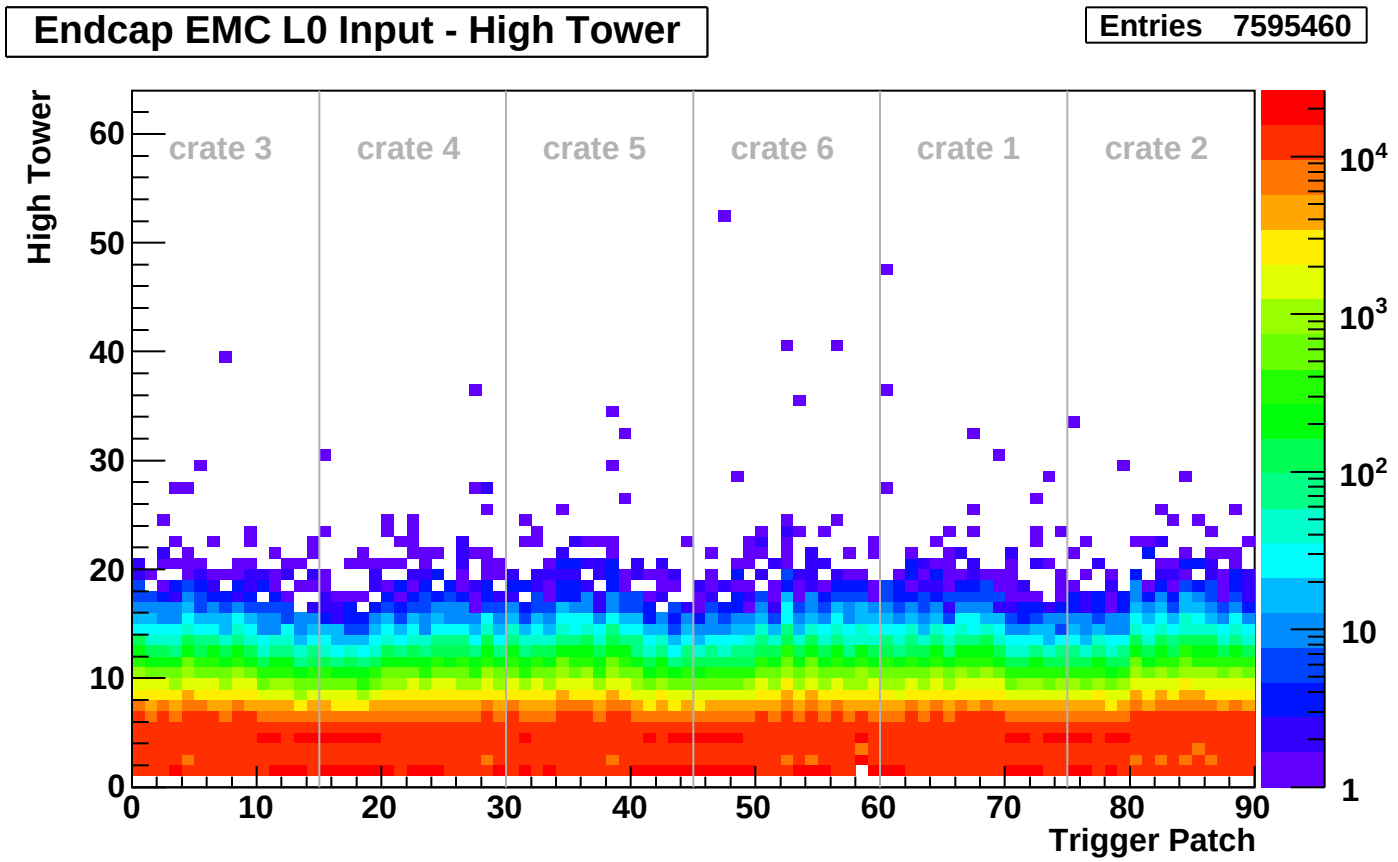

TCU bits
Entries 1350304
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

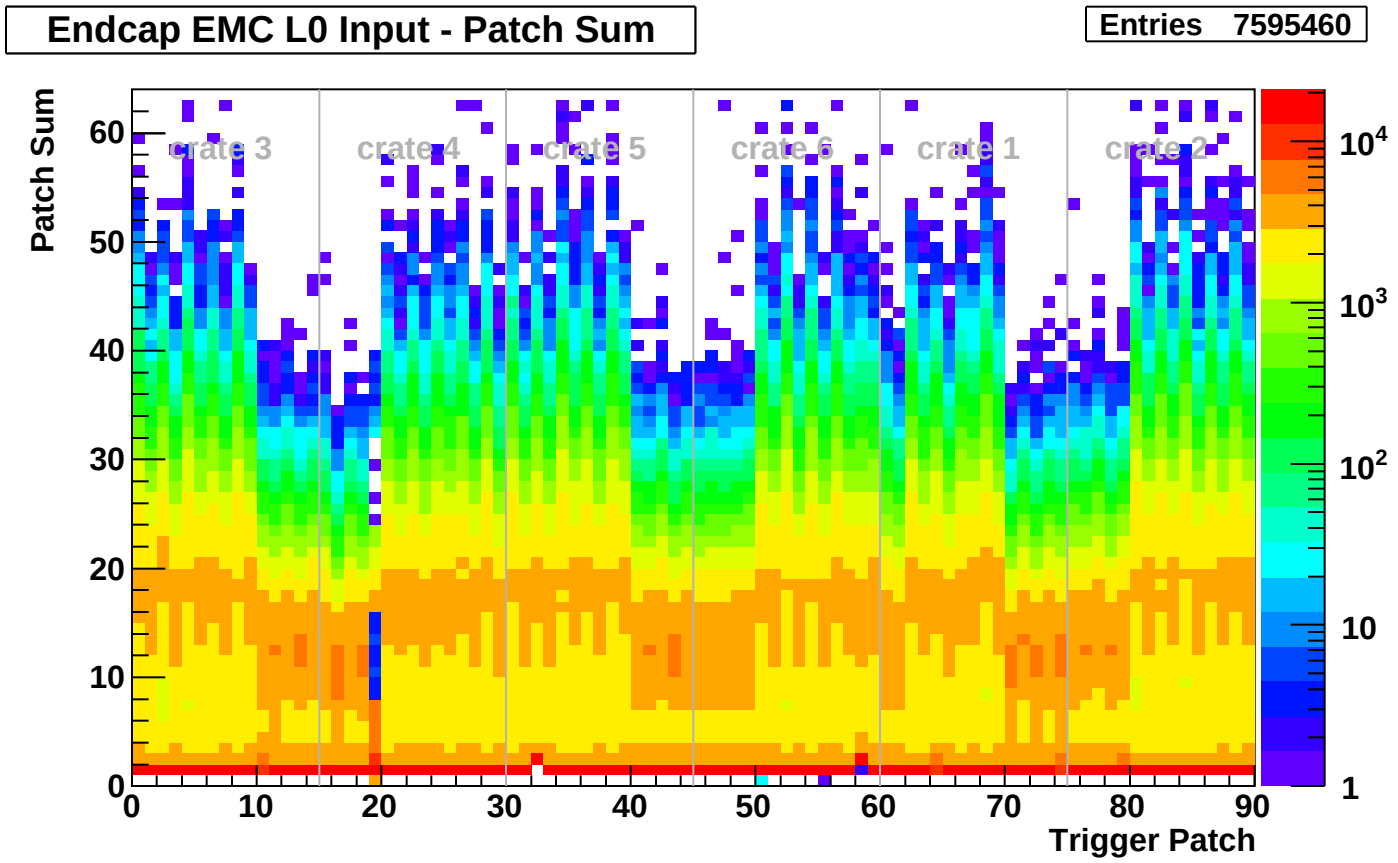

TCU bits
Entries 202364
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

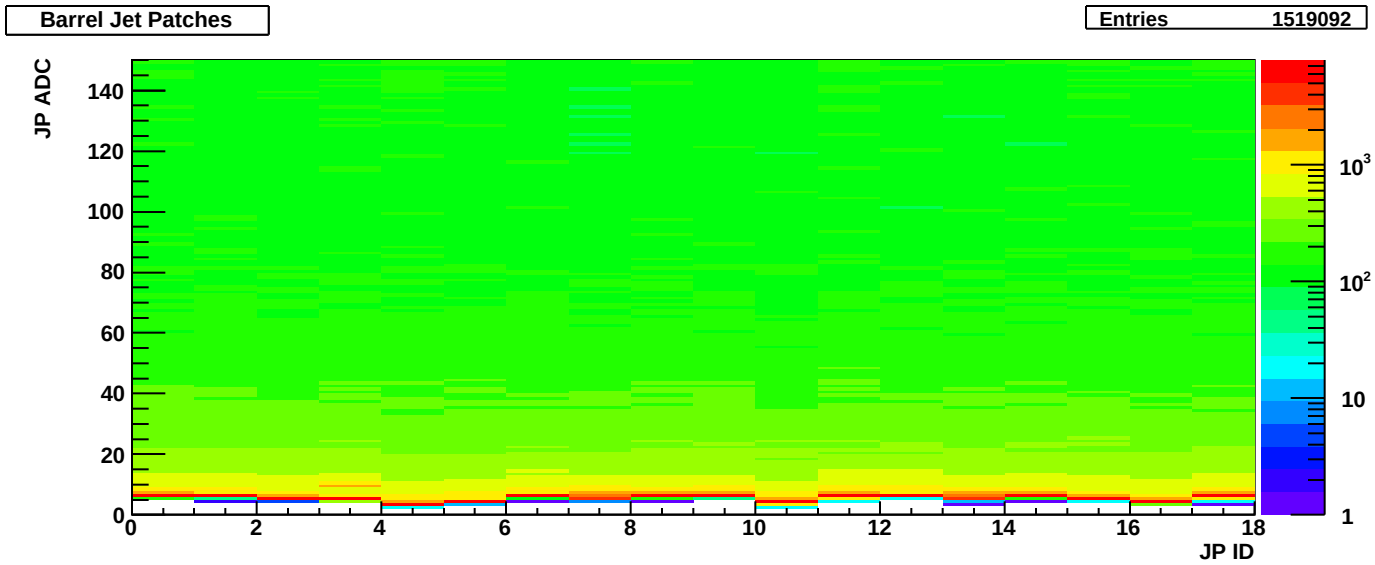

BunchId7Bit (BHT3)
Entries 1002

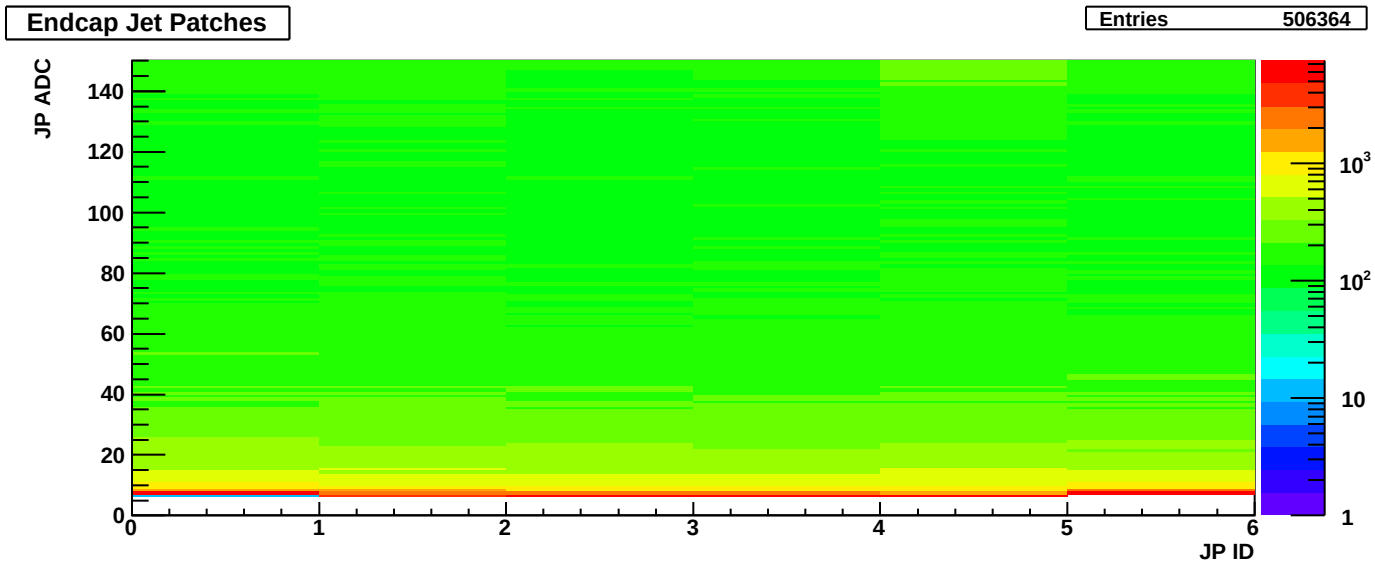

Barrel EMC L0 Input - High Tower
Entries 2.53182e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

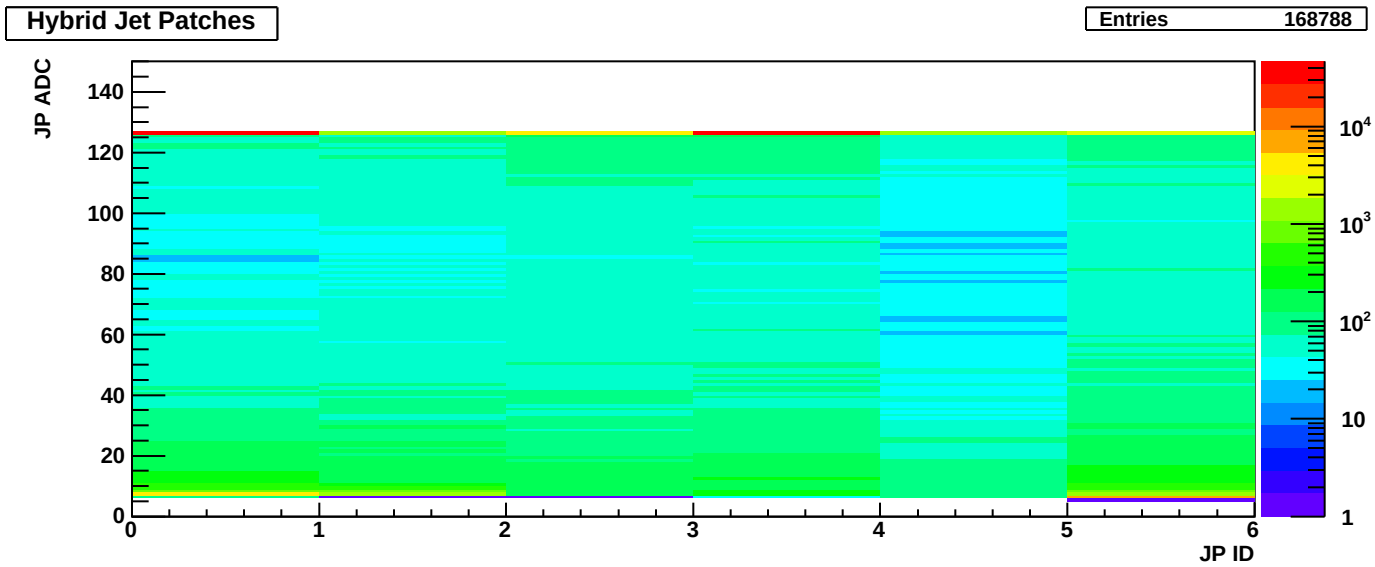

Barrel EMC L0 Input - Patch Sum
Entries 2.53182e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

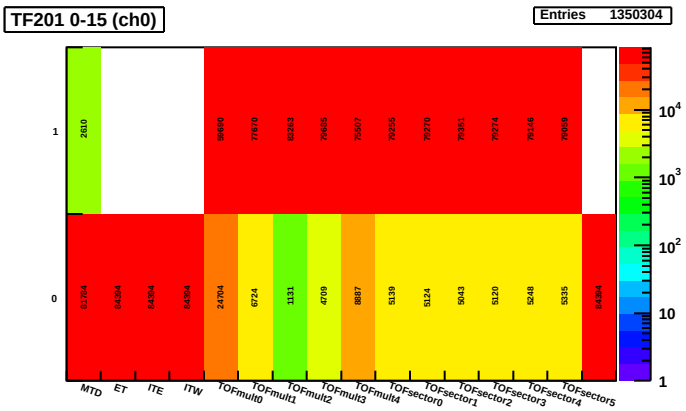

Endcap EMC L0 Input - High Tower
Entries 7595460
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

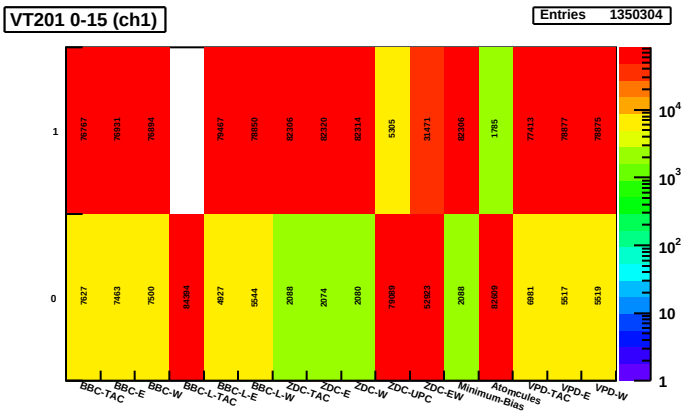

Endcap EMC L0 Input - Patch Sum
Entries 7595460
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

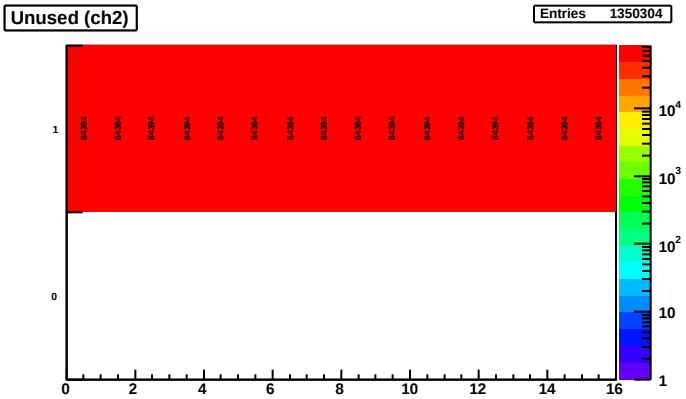

Barrel Jet Patches
Entries 1519092
JP ADC
JP ID

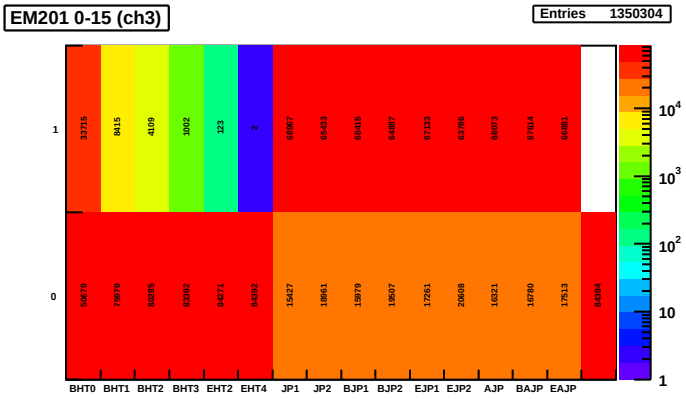

Endcap Jet Patches
Entries 506364
JP ADC
JP ID

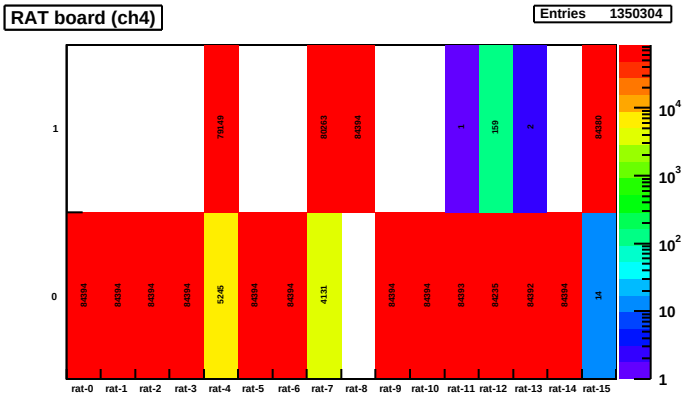

Hybrid Jet Patches
Entries 168788
JP ADC
JP ID

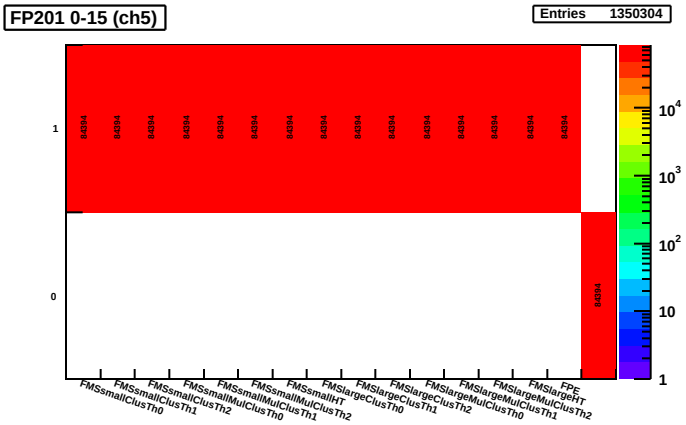

TF201 0-15 (ch0)
Entries 1350304
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

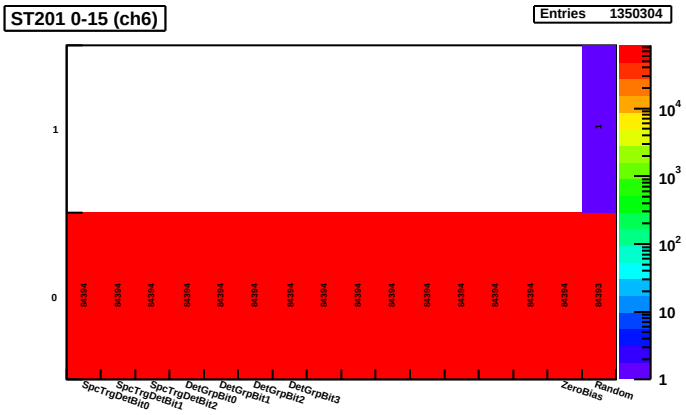

VT201 0-15 (ch1)
Entries 1350304
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

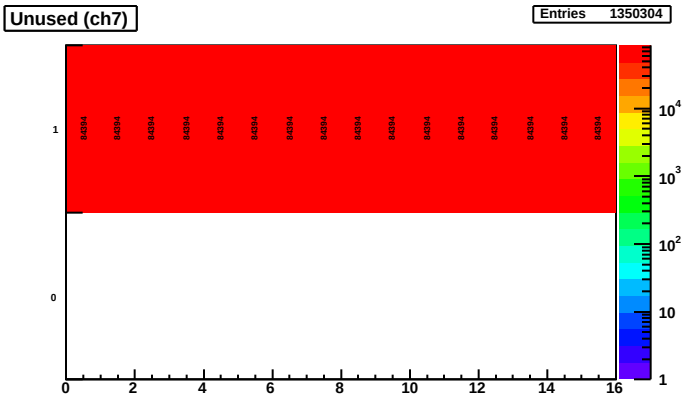

Unused (ch2)
Entries 1350304

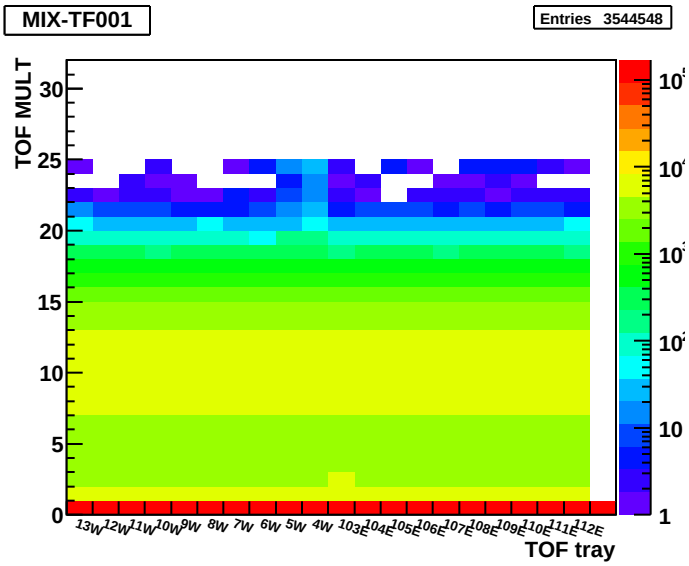

EM201 0-15 (ch3)
Entries 1350304
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

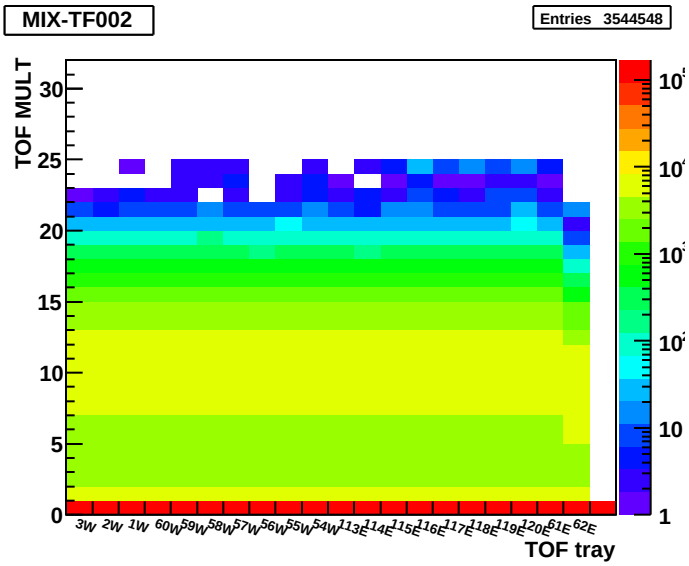

RAT board (ch4)
Entries 1350304
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

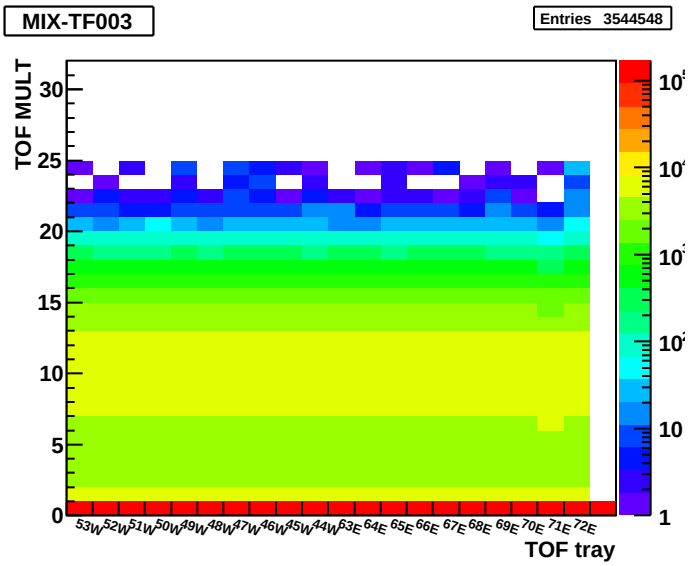

FP201 0-15 (ch5)
Entries 1350304
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

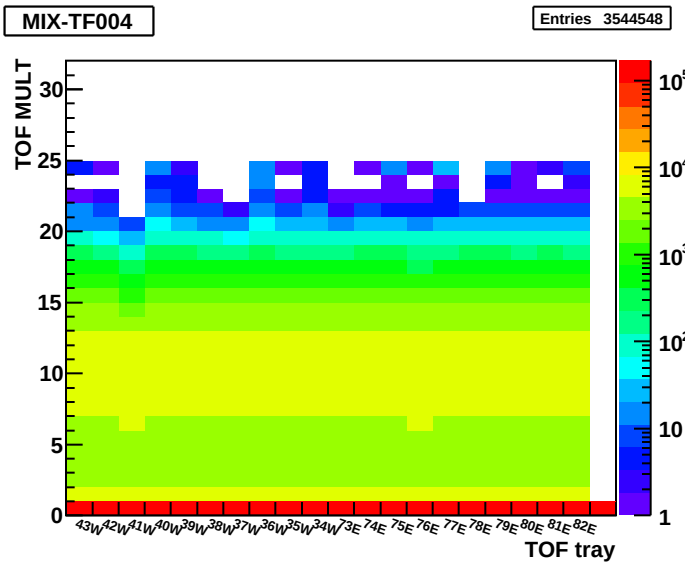

ST201 0-15 (ch6)
Entries 1350304
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

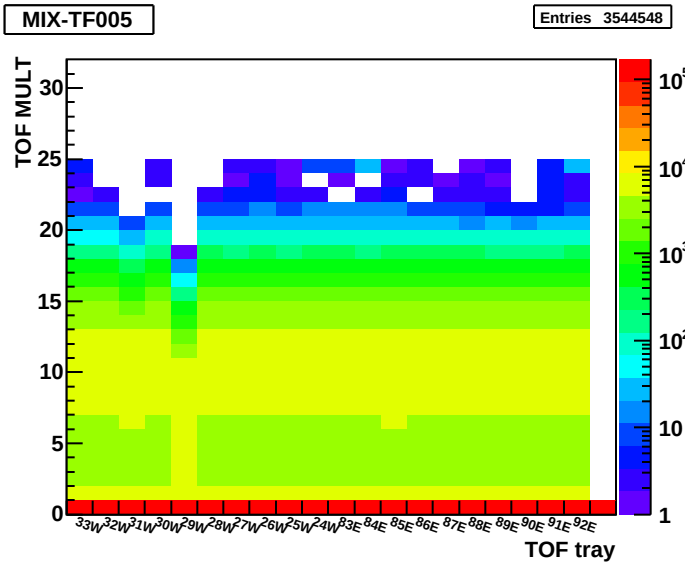

Unused (ch7)
Entries 1350304

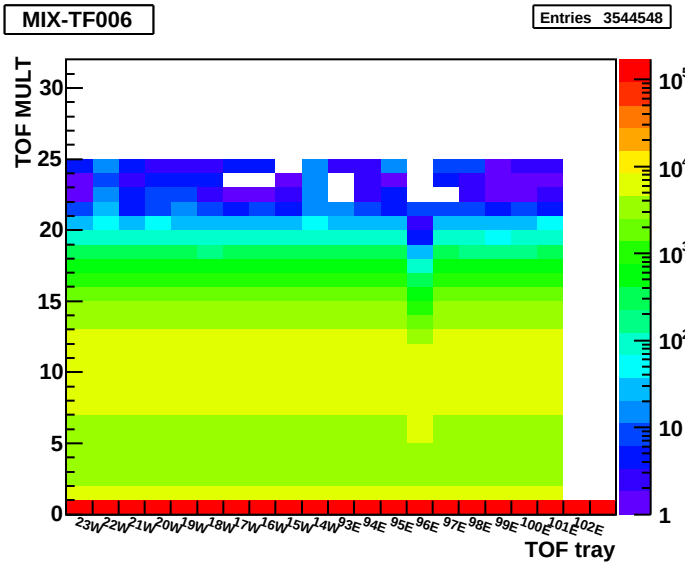

MIX-TF001
Entries 3544548
TOF MULT
TOF tray

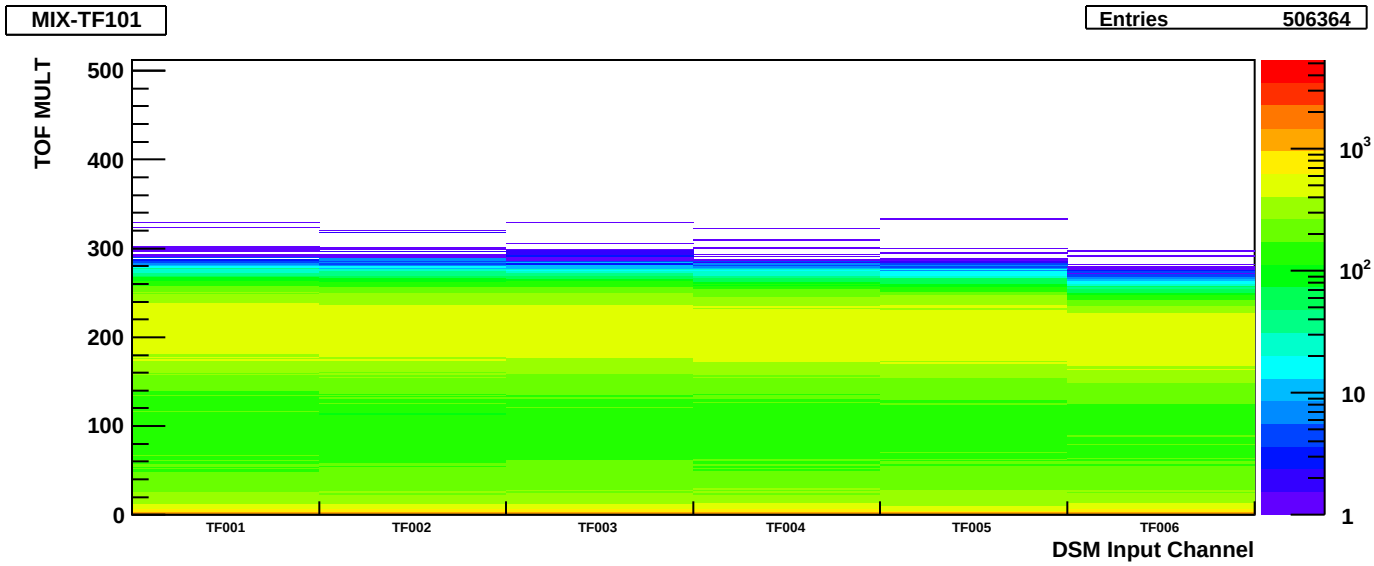

MIX-TF002
Entries 3544548
TOF MULT
TOF tray

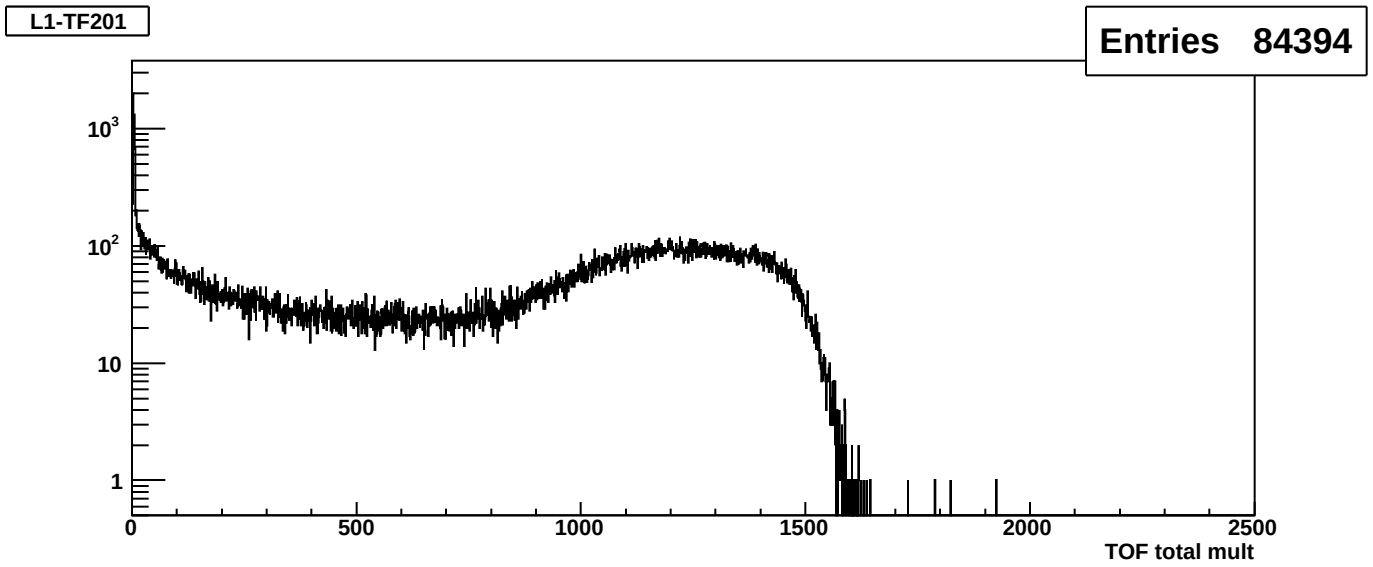

MIX-TF003
Entries 3544548
TOF MULT
TOF tray

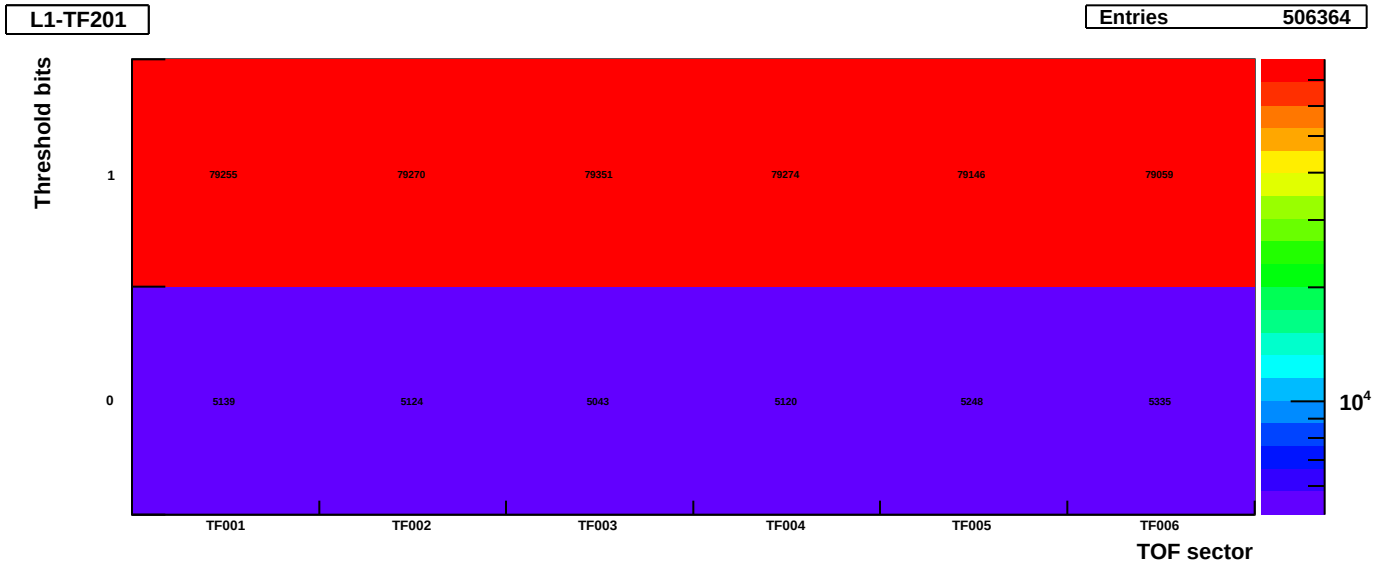

MIX-TF004
Entries 3544548
TOF MULT
TOF tray

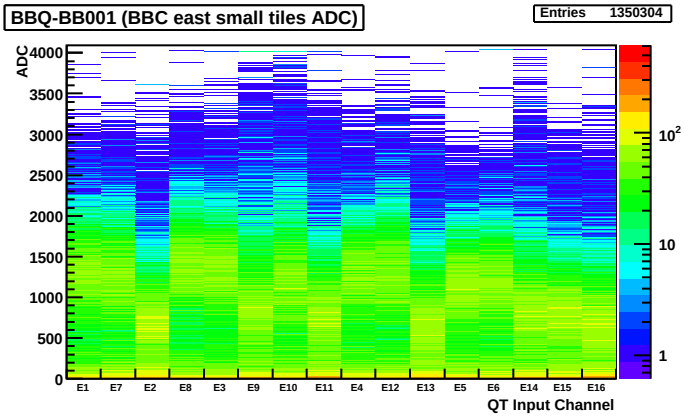

MIX-TF005
Entries 3544548
TOF MULT
TOF tray

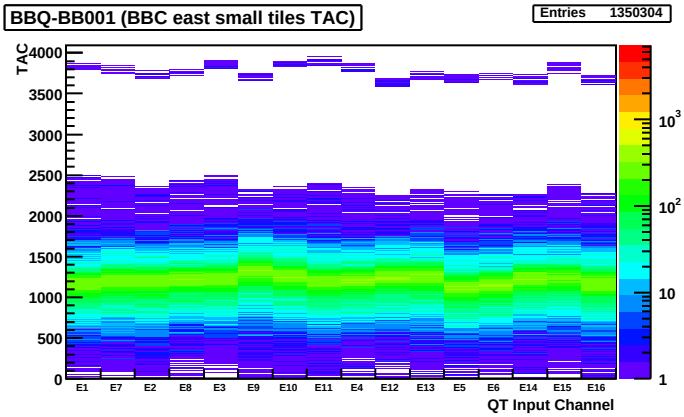

MIX-TF006
Entries 3544548
TOF MULT
TOF tray

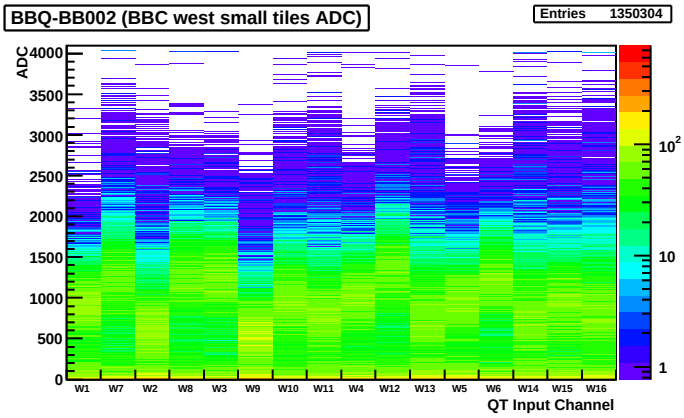

MIX-TF101
Entries 506364
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
$10^3$
$10^2$
10
1

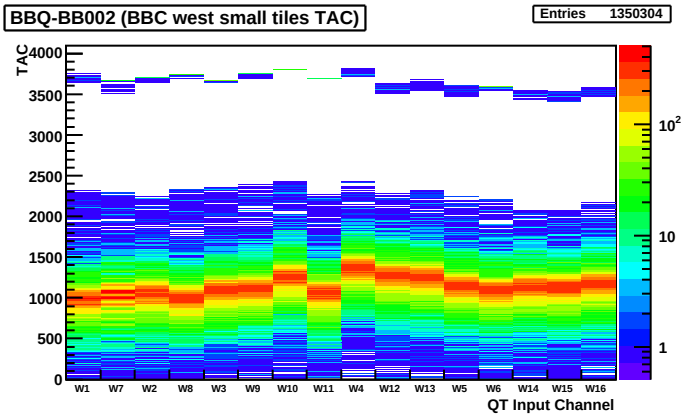

L1-TF201
Entries 84394
$10^3$
$10^2$
10
1
0
500
1000
1500
2000
2500
TOF total mult

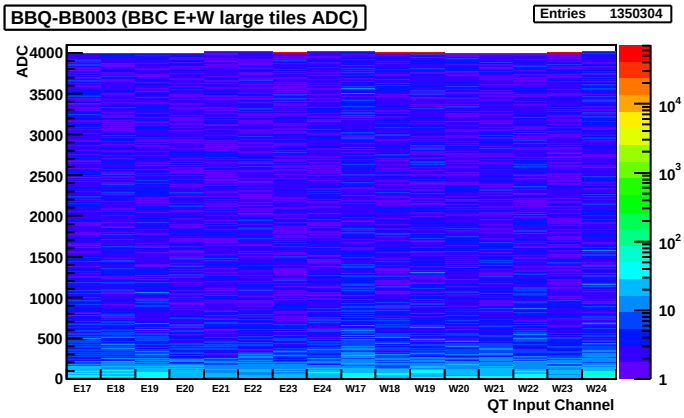

L1-TF201
Entries 506364
Threshold bits
1
0
79255
79270
79351
79274
79146
79059
5139
5124
5043
5120
5248
5335
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
$10^4$

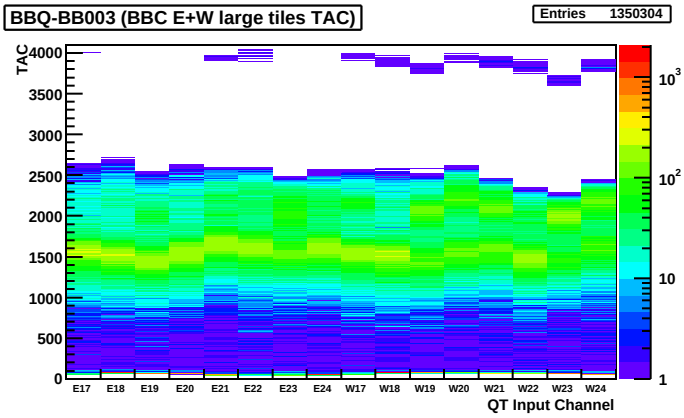

BBQ-BB001 (BBC east small tiles ADC)
Entries 1350304
ADC
QT Input Channel

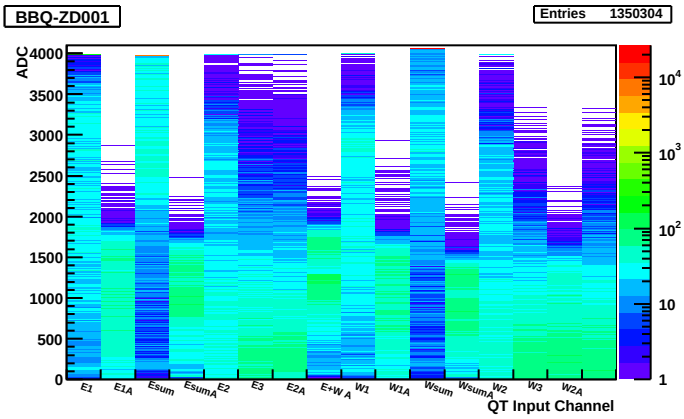

BBQ-BB001 (BBC east small tiles TAC)
Entries 1350304
TAC
QT Input Channel

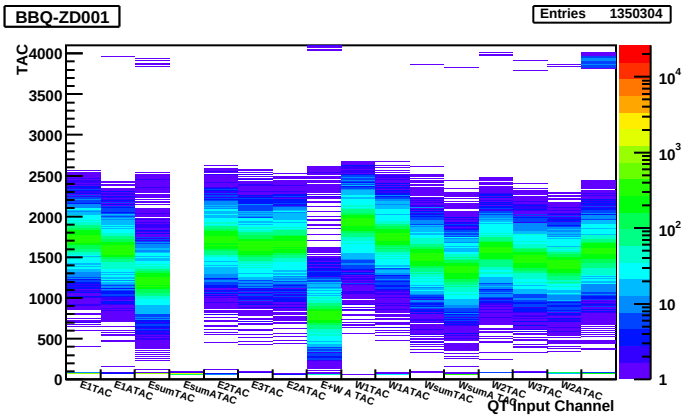

BBQ-BB002 (BBC west small tiles ADC)
Entries 1350304
ADC
QT Input Channel

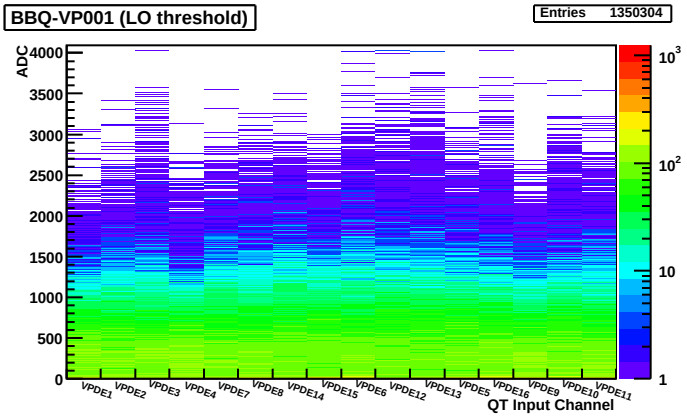

BBQ-BB002 (BBC west small tiles TAC)
Entries 1350304
TAC
QT Input Channel

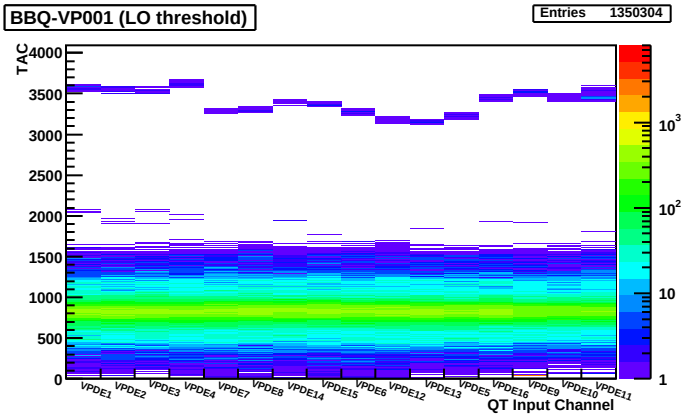

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1350304
ADC
QT Input Channel

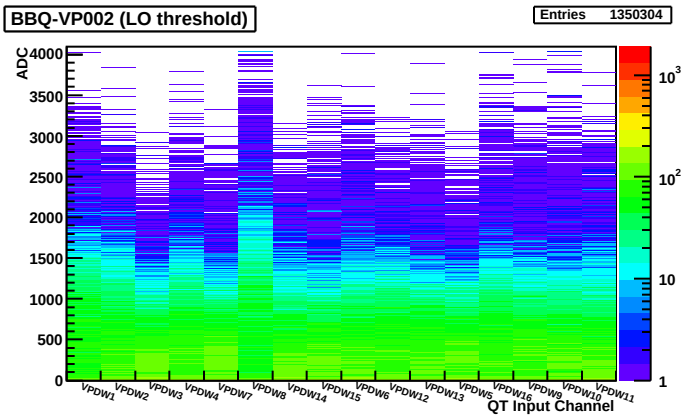

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1350304
TAC
QT Input Channel

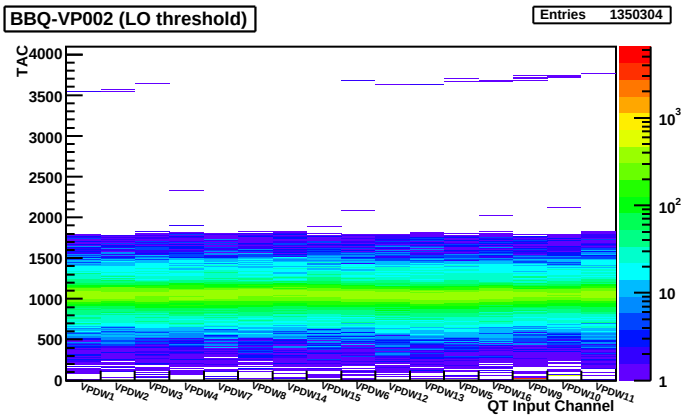

BBQ-ZD001
Entries 1350304
ADC
QT Input Channel

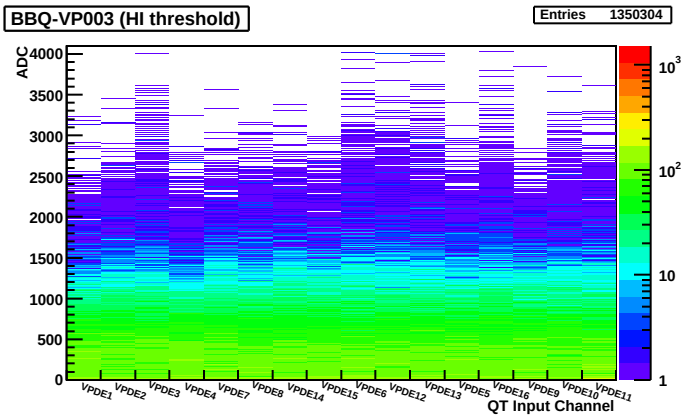

BBQ-ZD001
Entries 1350304
TAC
QT Input Channel

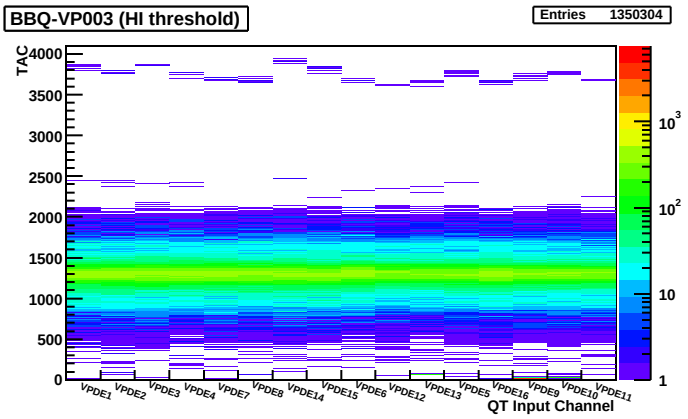

BBQ-VP001 (LO threshold)
Entries 1350304
ADC
QT Input Channel

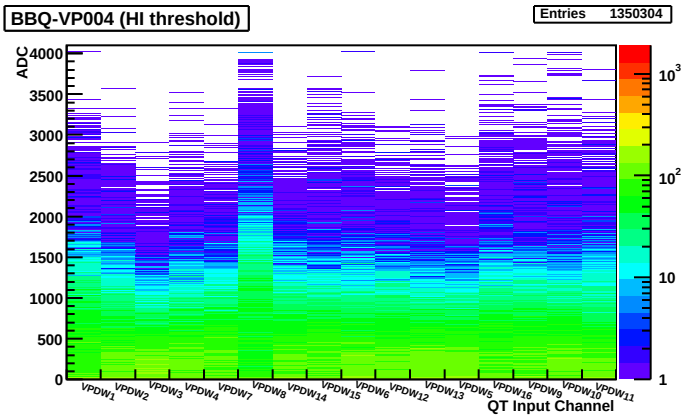

BBQ-VP001 (LO threshold)
Entries 1350304
TAC
QT Input Channel

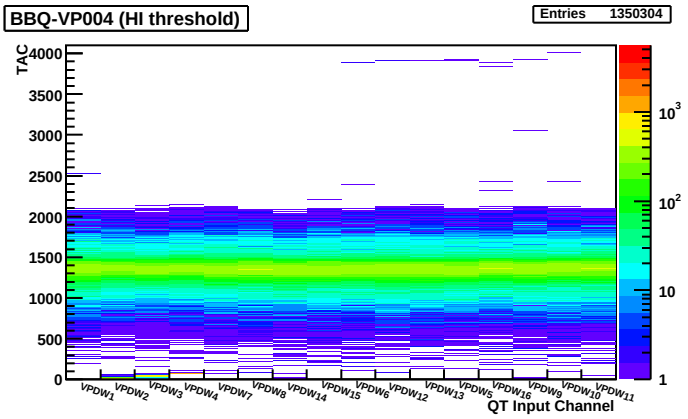

BBQ-VP002 (LO threshold)
Entries 1350304
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 1350304
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 1350304
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 1350304
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 1350304
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 1350304
TAC
QT Input Channel