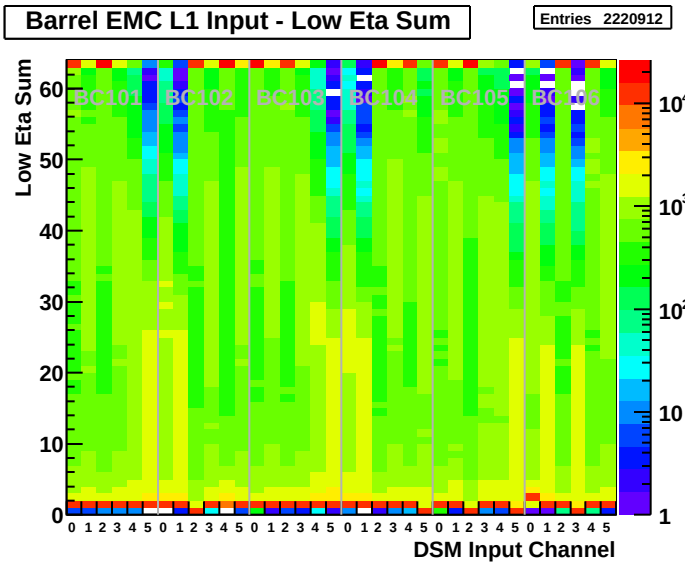
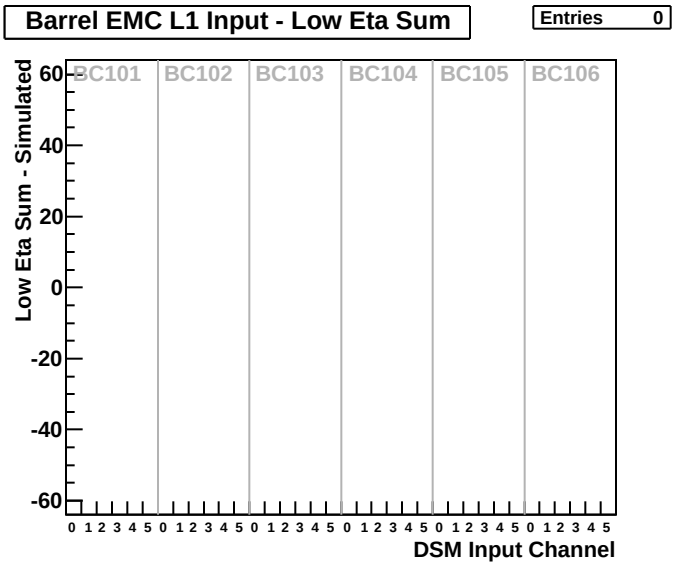
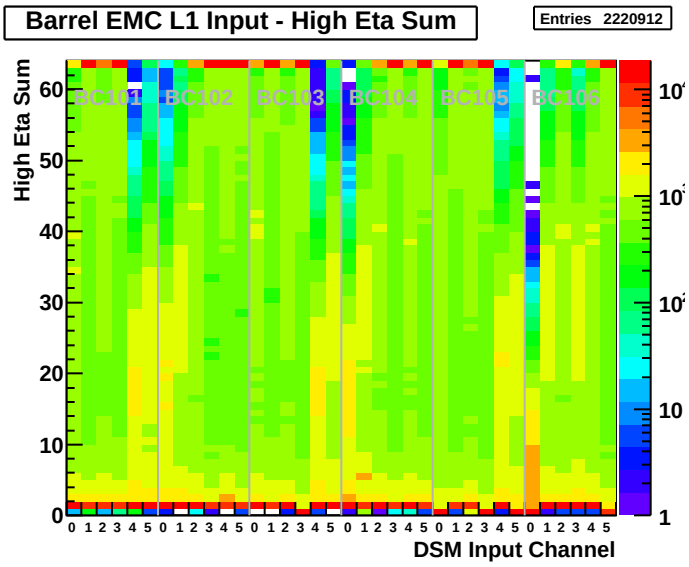
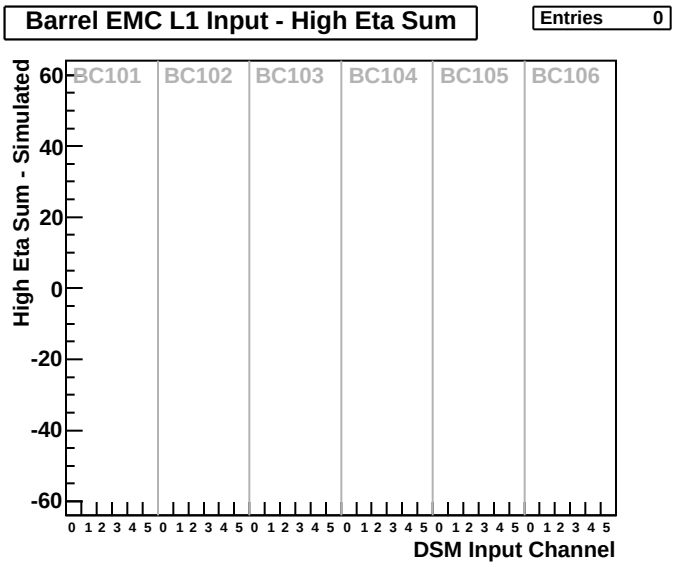
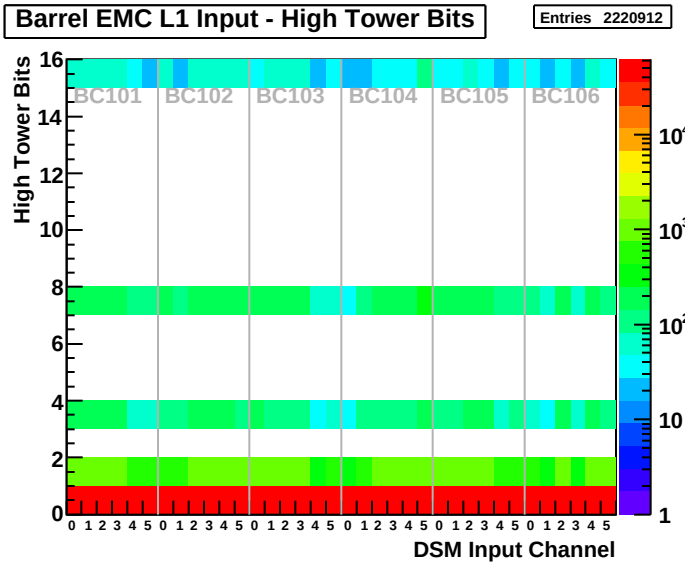
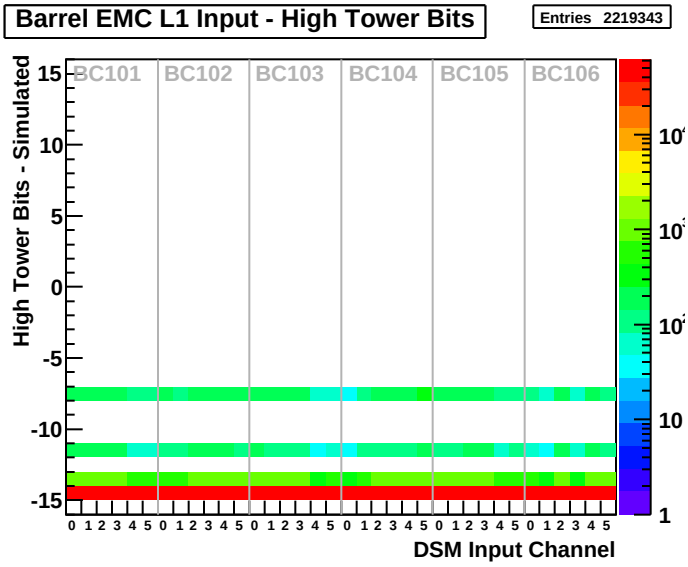

Barrel EMC L1 Input - Low Eta Sum
Entries 2220912
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

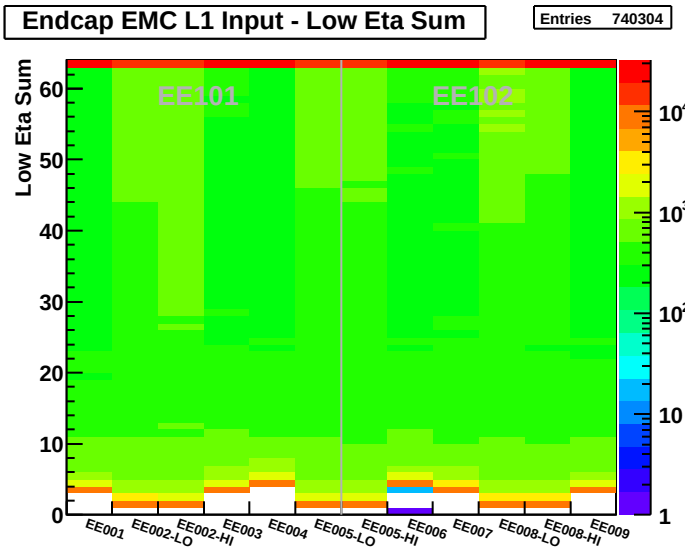

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

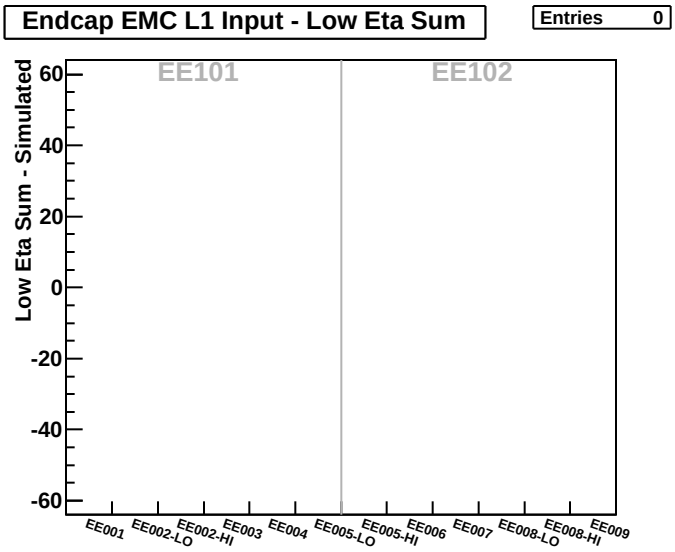

Barrel EMC L1 Input - High Eta Sum
Entries 2220912
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

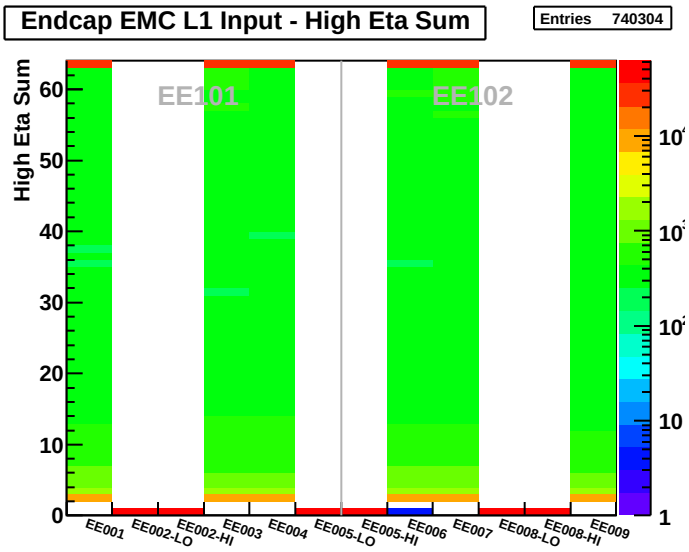

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

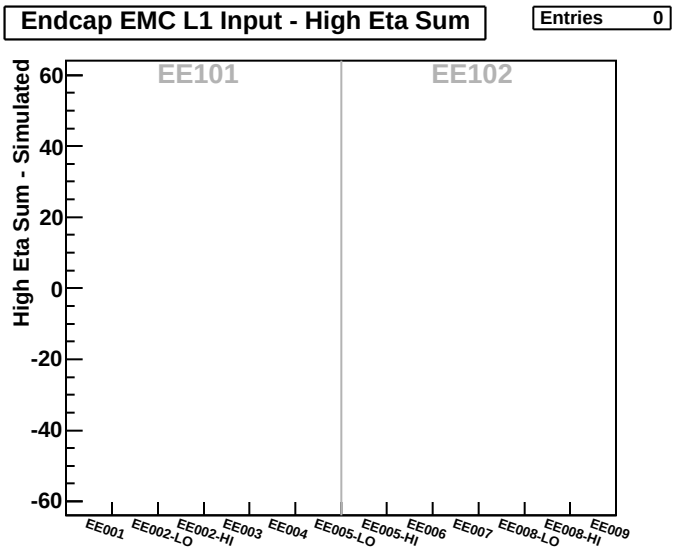

Barrel EMC L1 Input - High Tower Bits
Entries 2220912
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

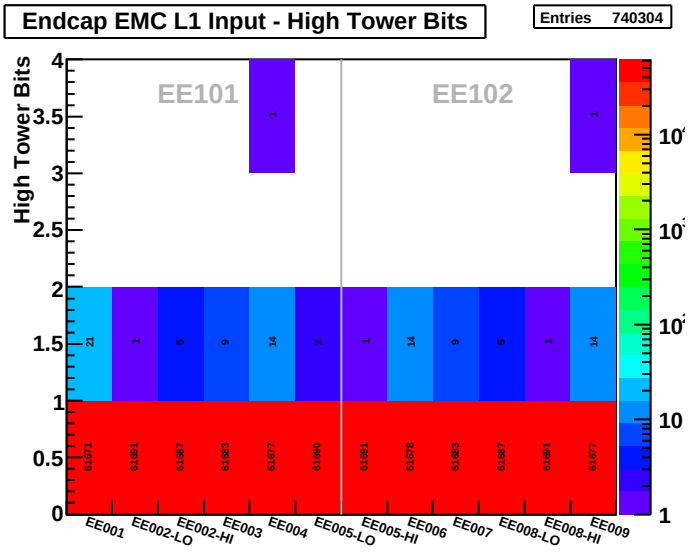

Barrel EMC L1 Input - High Tower Bits
Entries 2219343
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

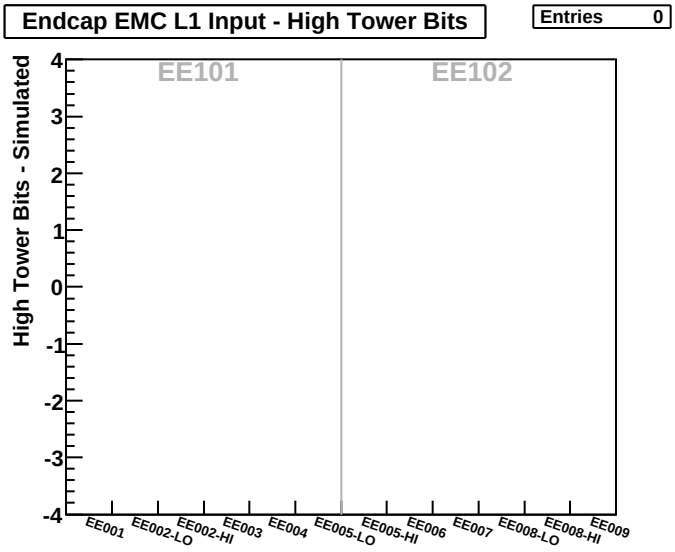

Endcap EMC L1 Input - Low Eta Sum
Entries 740304
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

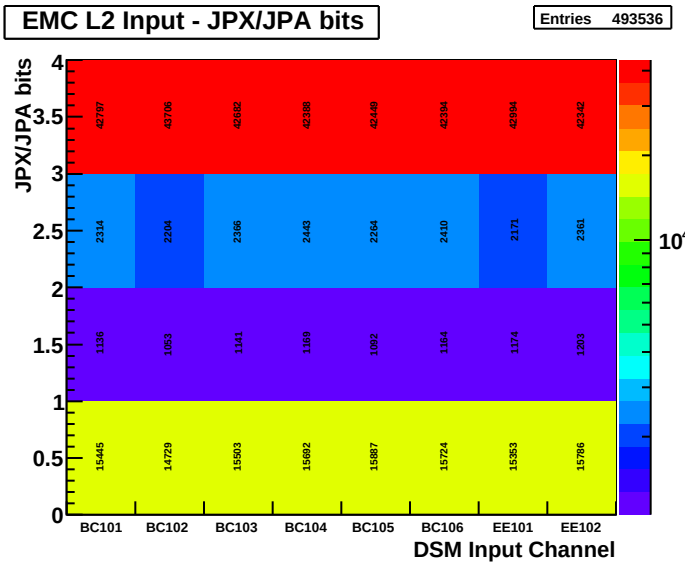

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

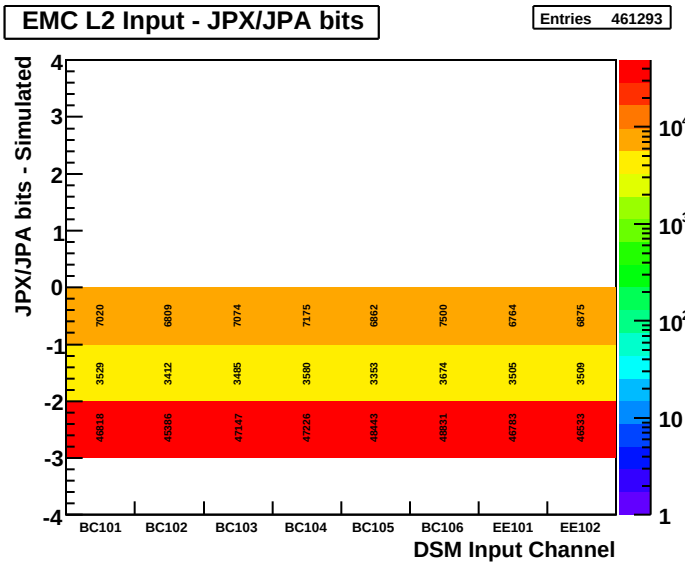

Endcap EMC L1 Input - High Eta Sum
Entries 740304
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

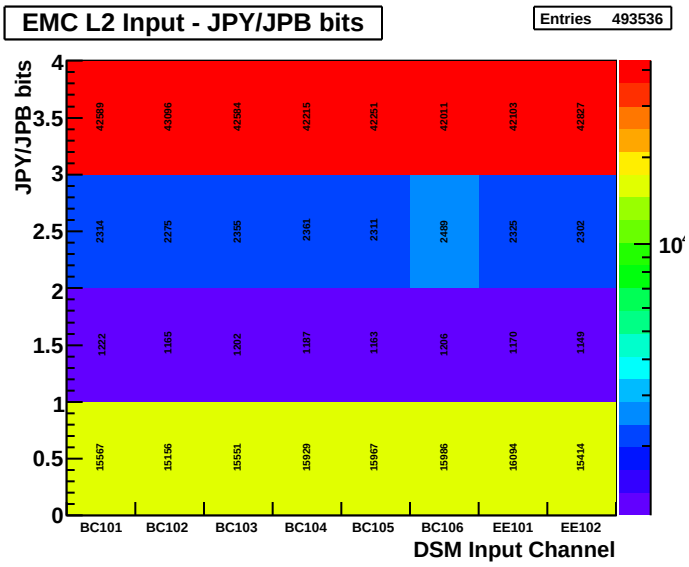

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

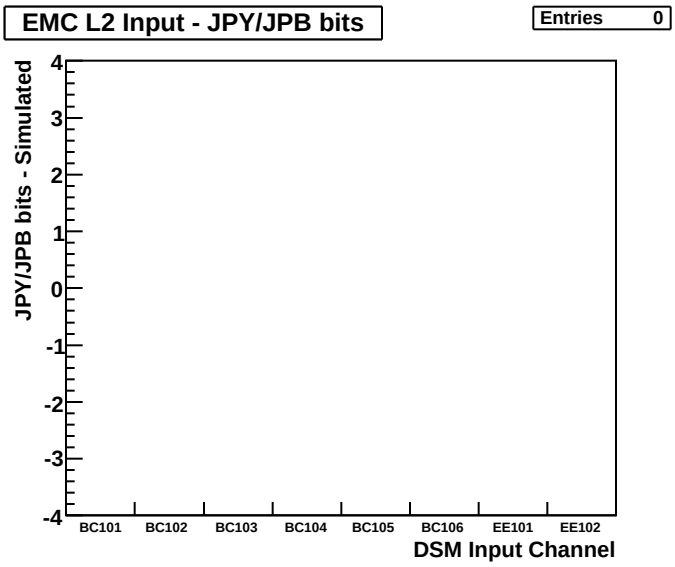

Endcap EMC L1 Input - High Tower Bits
Entries 740304
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

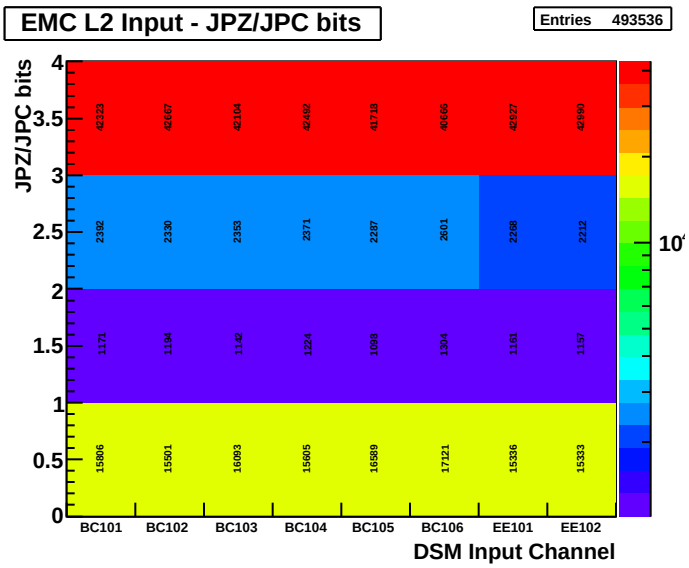

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

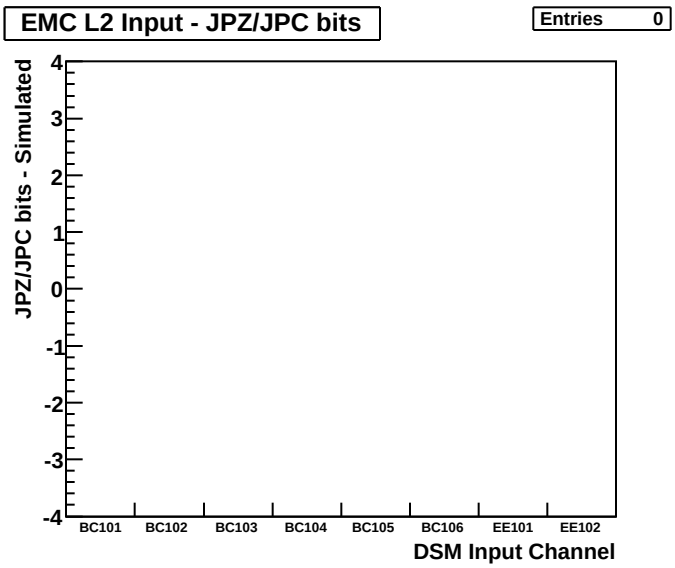

EMC L2 Input - JPX/JPA bits
Entries 493536
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
42797 43706 42682 42388 42449 42394 42994 42342
2314 2204 2366 2443 2264 2410 2171 2361
1136 1053 1141 1169 1092 1164 1174 1203
15445 14729 15503 15692 15887 15724 15353 15786

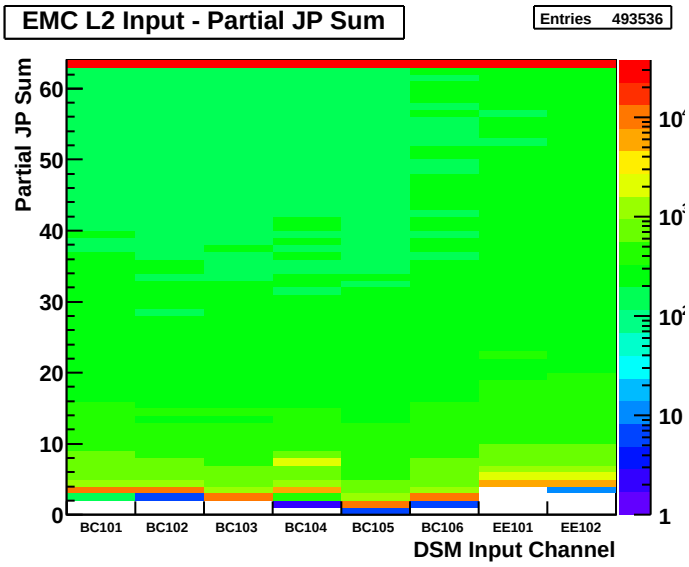

EMC L2 Input - JPX/JPA bits
Entries 461293
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
7020 6809 7074 7175 6862 7500 6764 6875
3529 3412 3485 3580 3353 3674 3505 3509
46818 45396 47147 47226 46443 48831 46783 46533

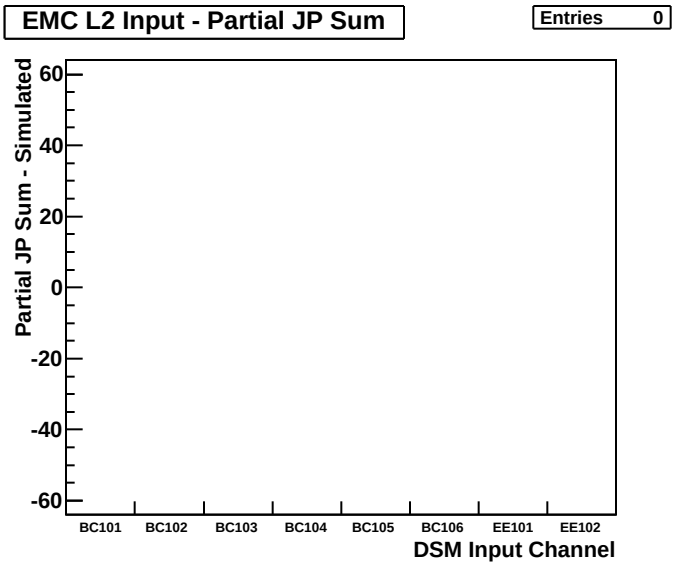

EMC L2 Input - JPY/JPB bits
Entries 493536
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
42589 43086 42584 42215 42251 42011 42103 42827
2314 2275 2355 2361 2311 2489 2325 2302
1222 1165 1202 1187 1163 1206 1170 1149
15567 15156 15551 15929 15967 15986 16094 15414

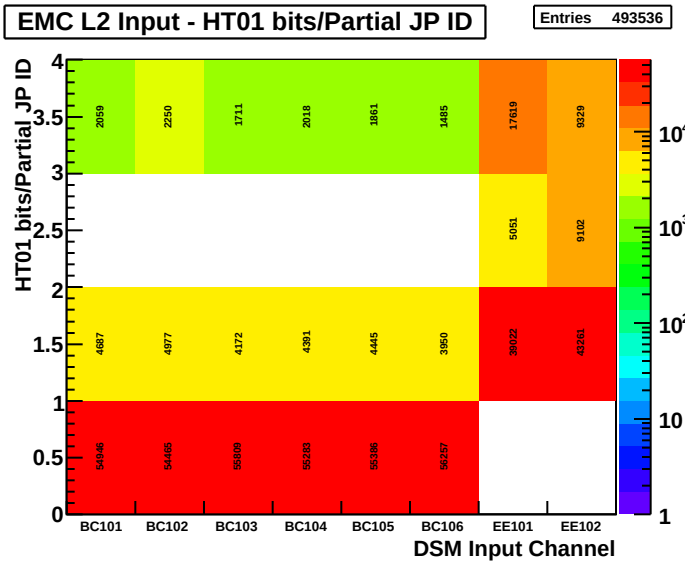

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

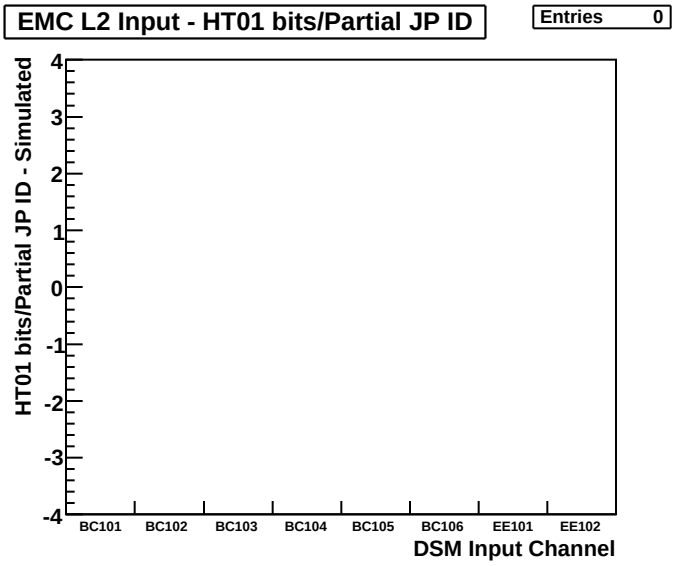

EMC L2 Input - JPZ/JPC bits
Entries 493536
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
42323 42667 42104 42492 41718 40666 42927 42990
2392 2330 2353 2371 2287 2601 2268 2212
1171 1194 1142 1224 1098 1304 1161 1157
15806 15601 16093 15605 16589 17121 15336 15333

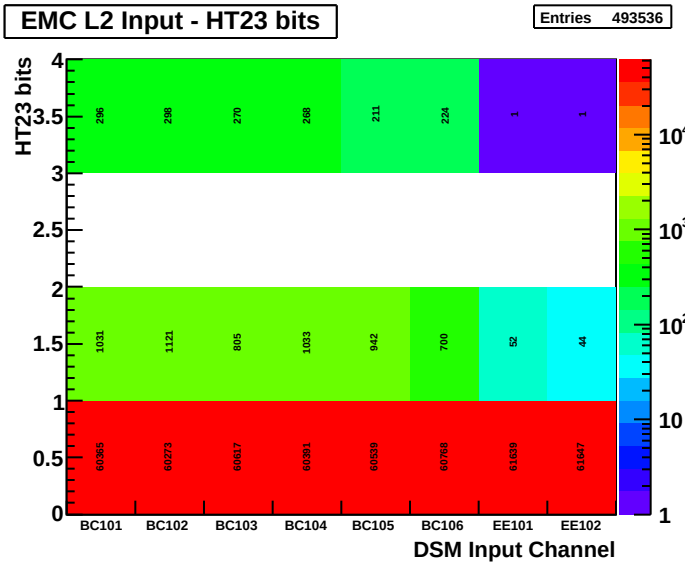

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

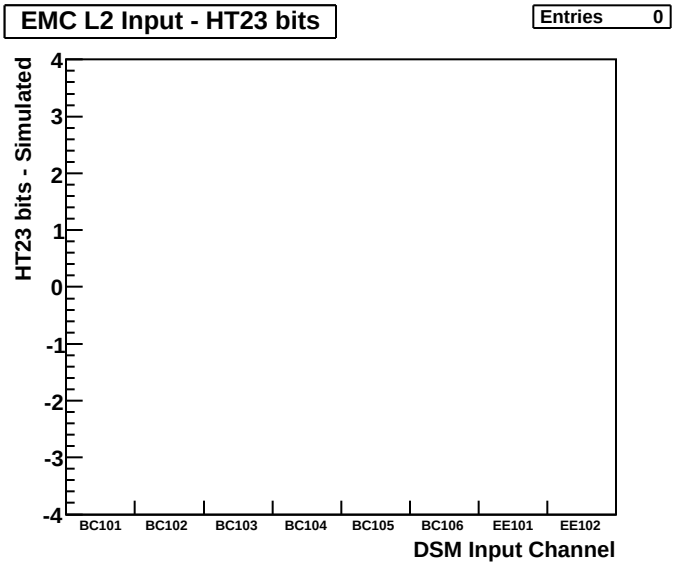

EMC L2 Input - Partial JP Sum
Entries 493536
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

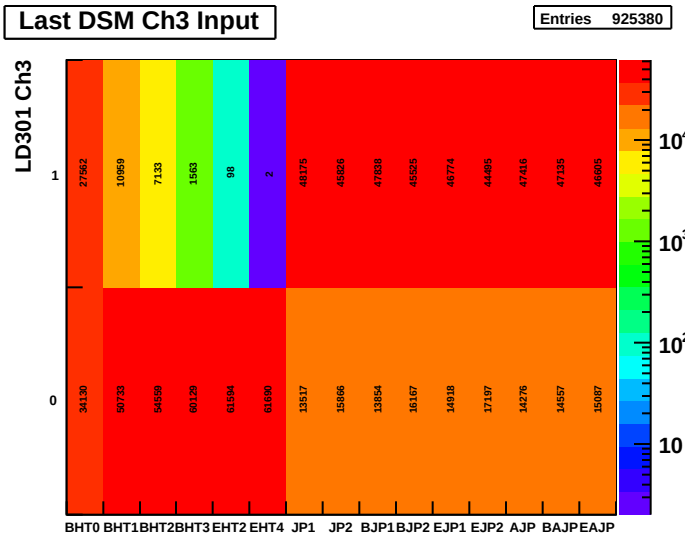

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

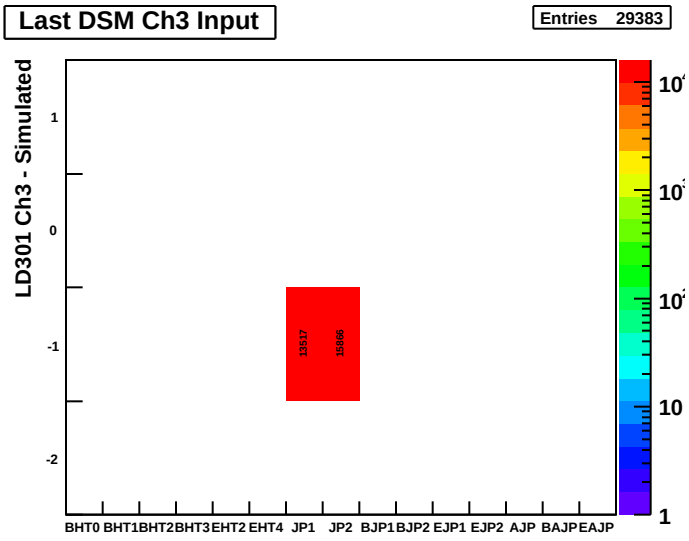

EMC L2 Input - HT01 bits/Partial JP ID
Entries 493536
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

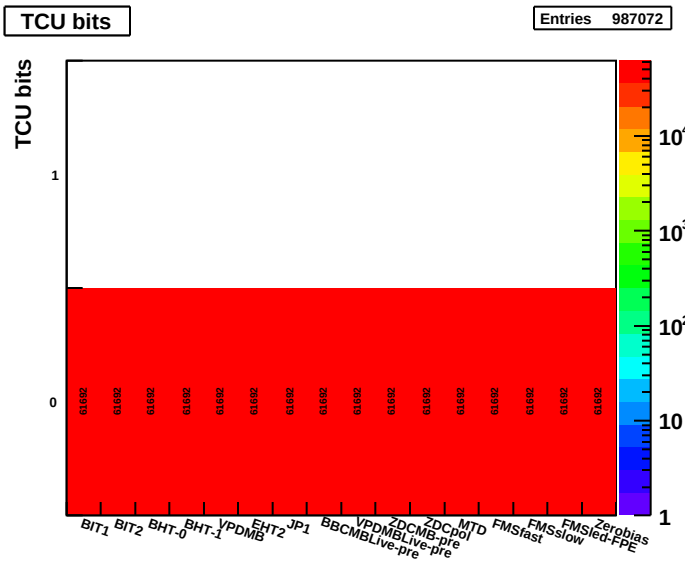

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

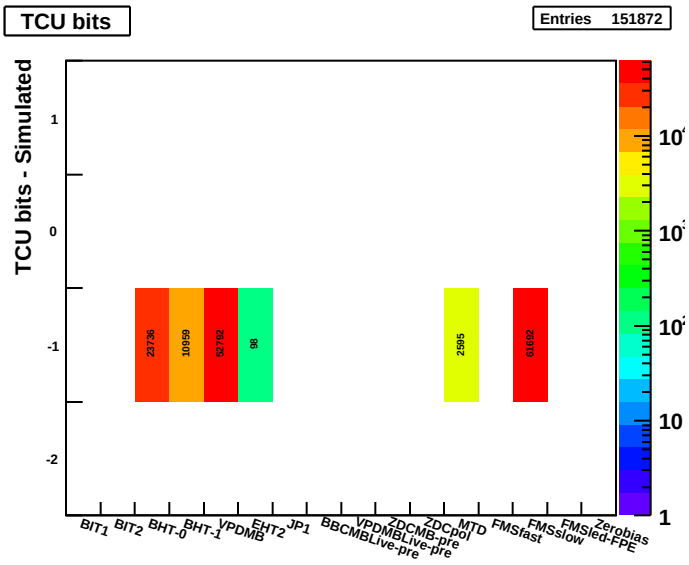

EMC L2 Input - HT23 bits
Entries 493536
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

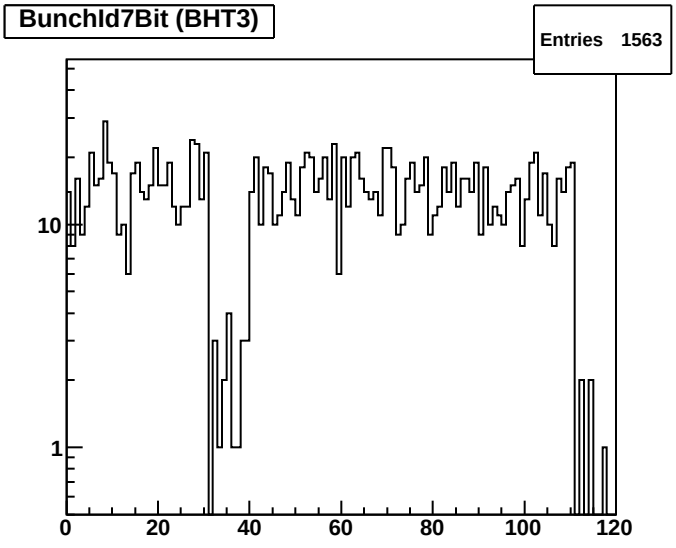

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

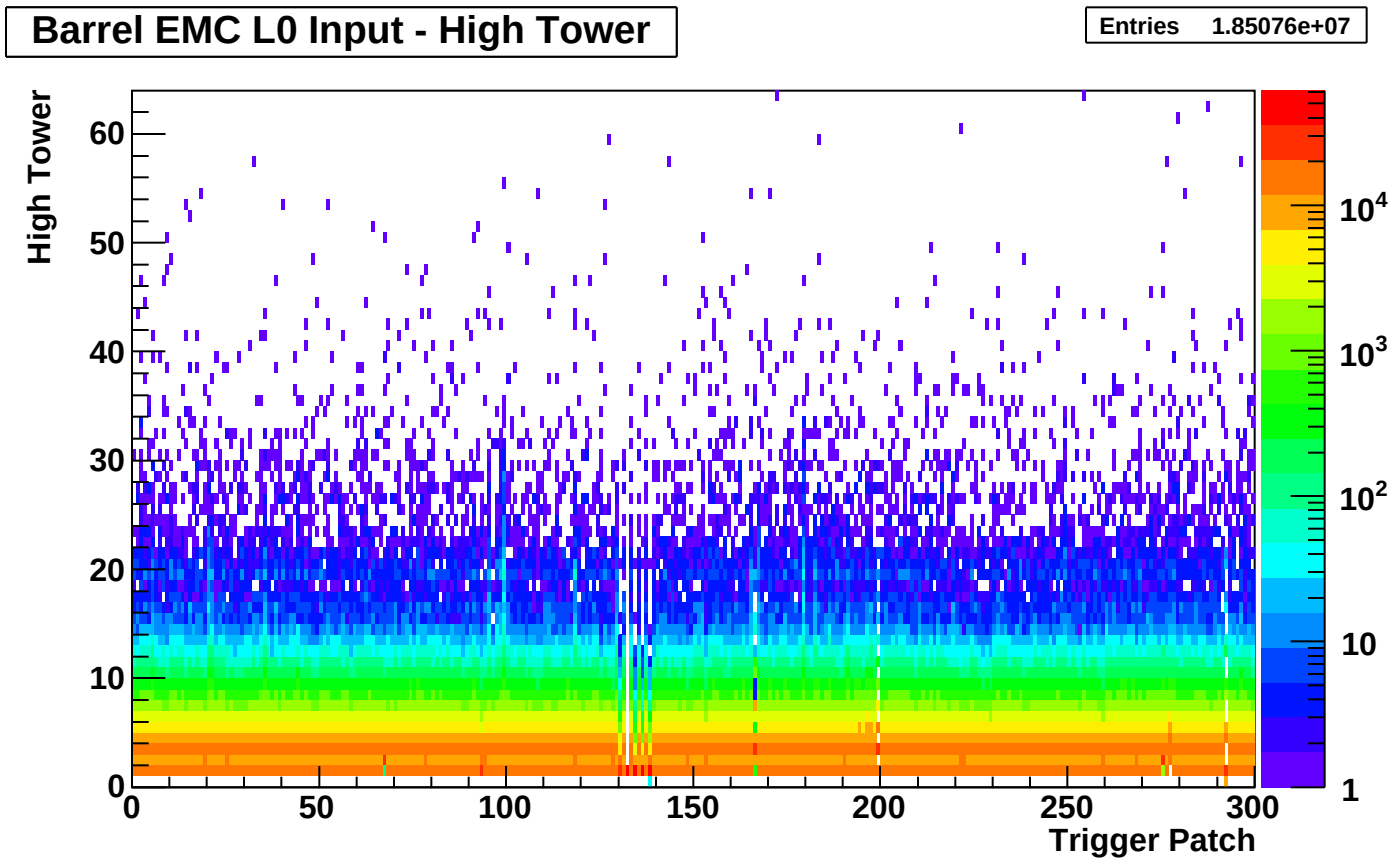

Last DSM Ch3 Input
Entries 925380
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

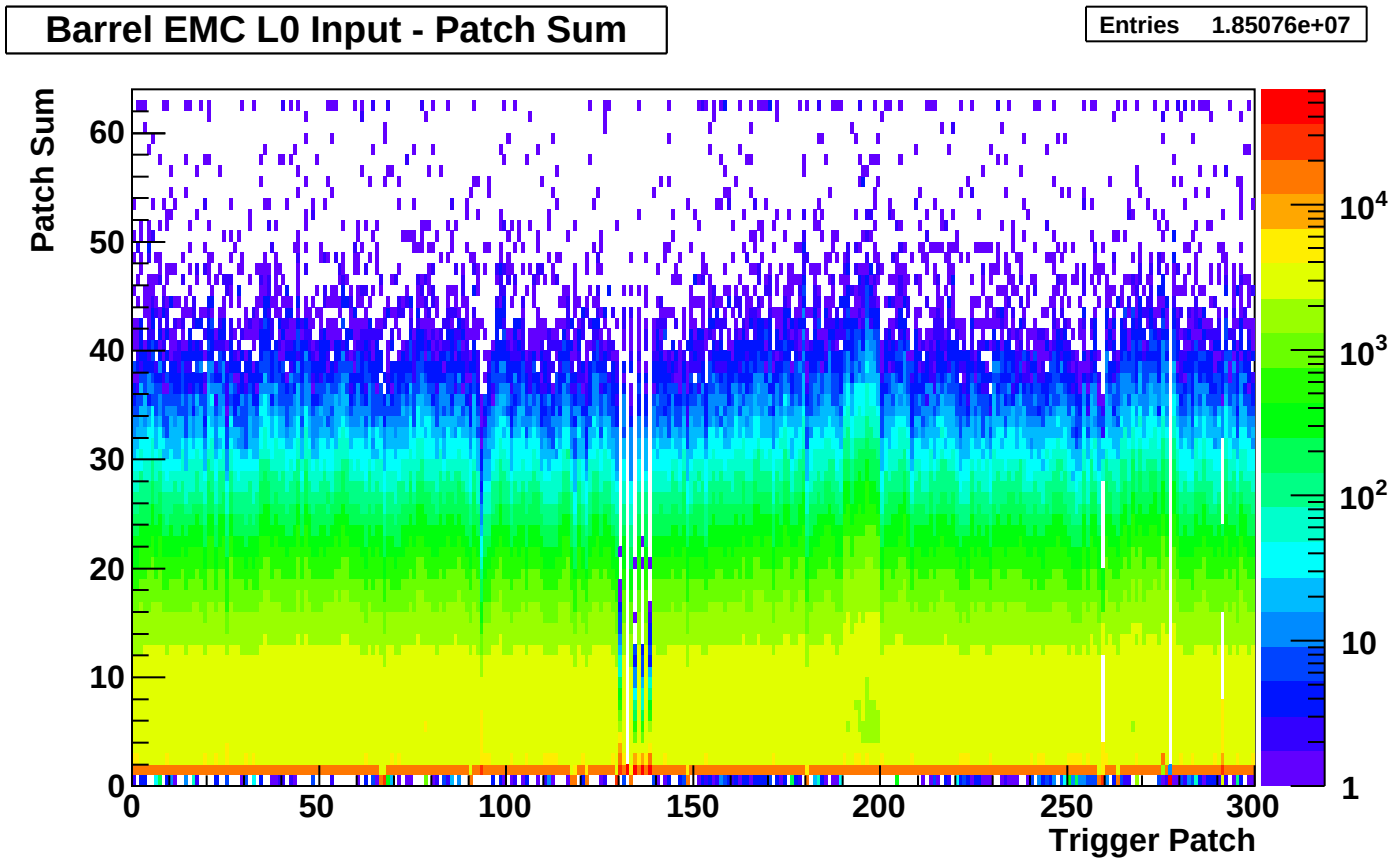

Last DSM Ch3 Input
Entries 29383
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

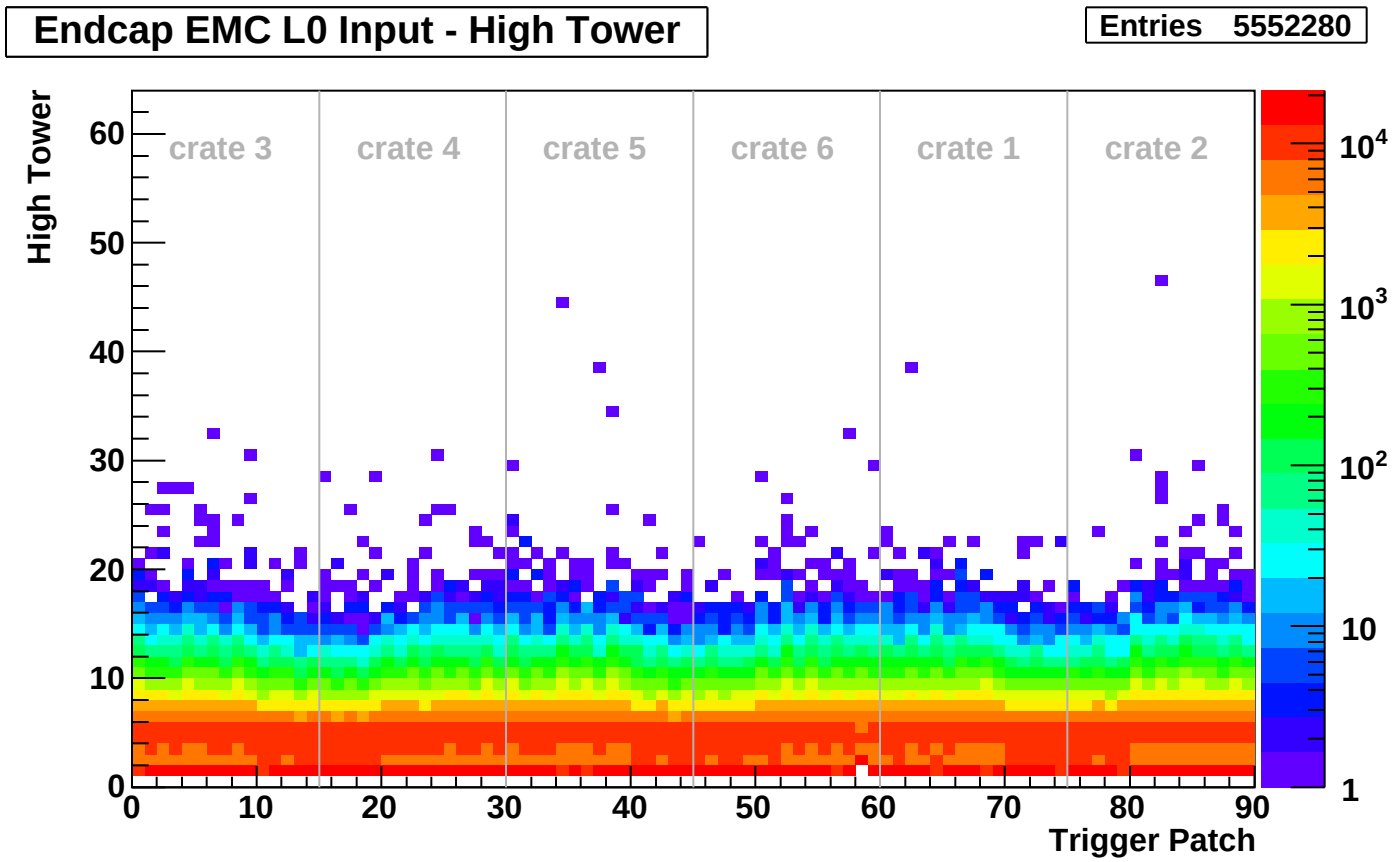

TCU bits
Entries 987072
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

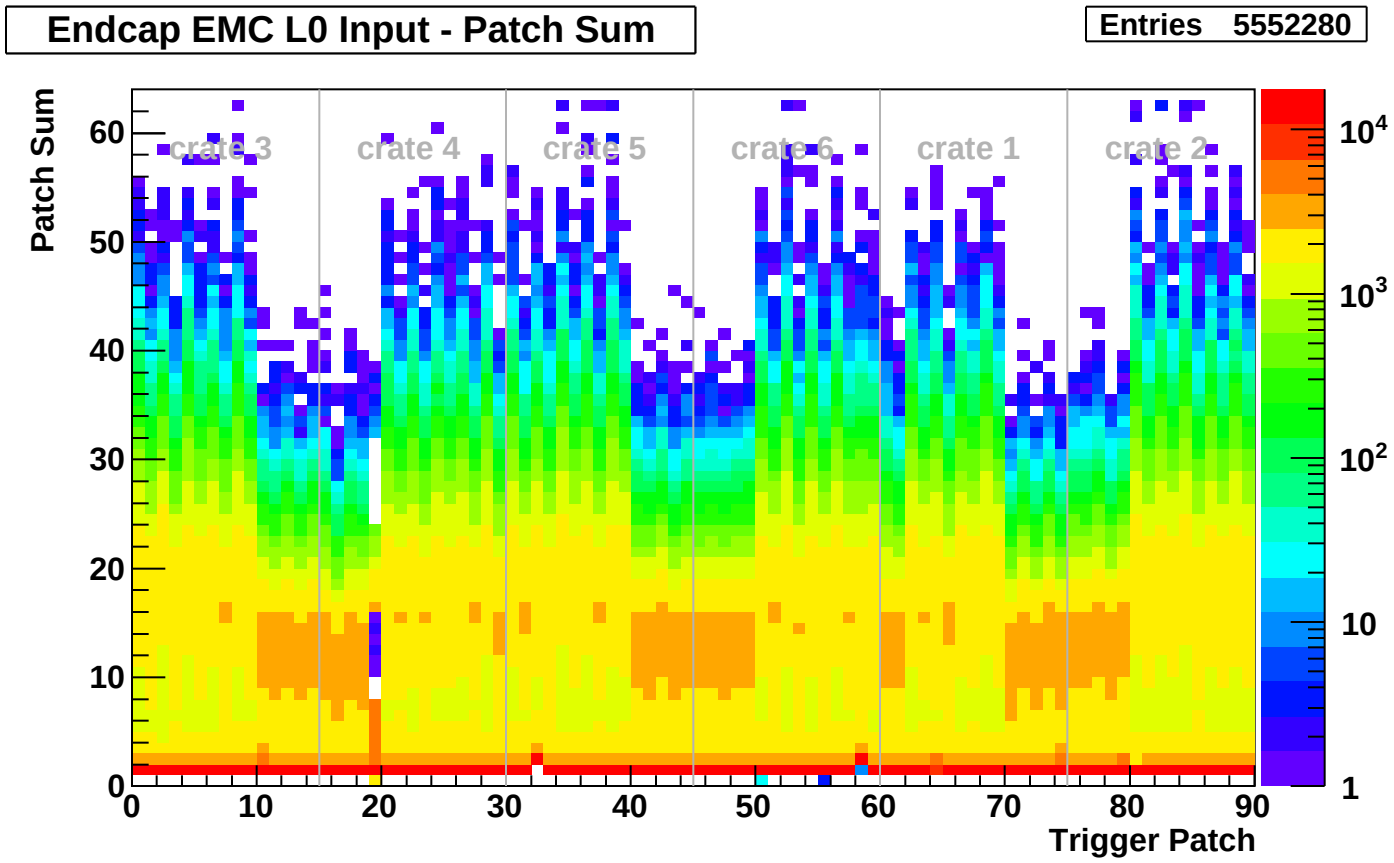

TCU bits
Entries 151872
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

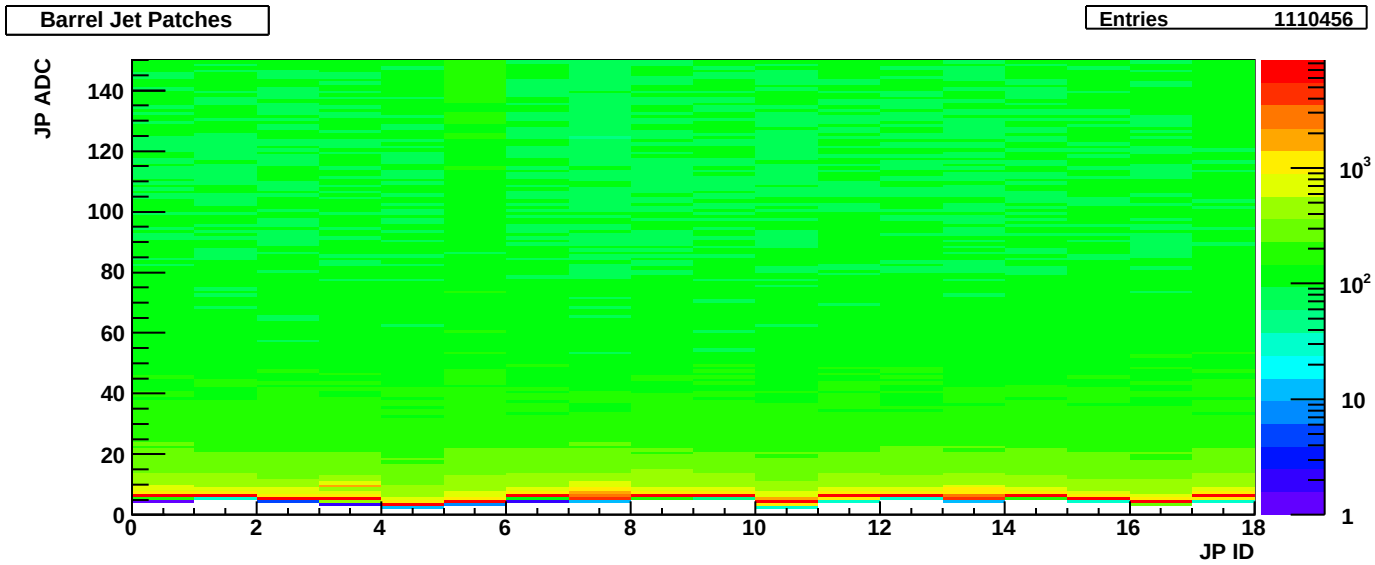

BunchId7Bit (BHT3)
Entries 1563

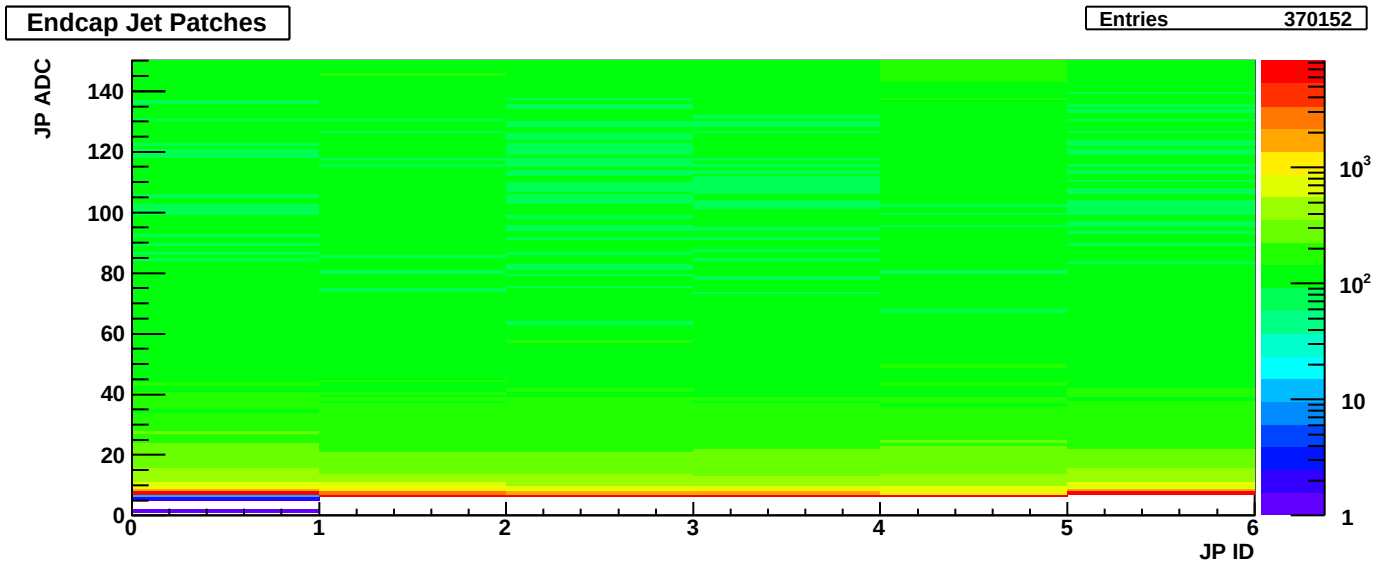

Barrel EMC L0 Input - High Tower
Entries 1.85076e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

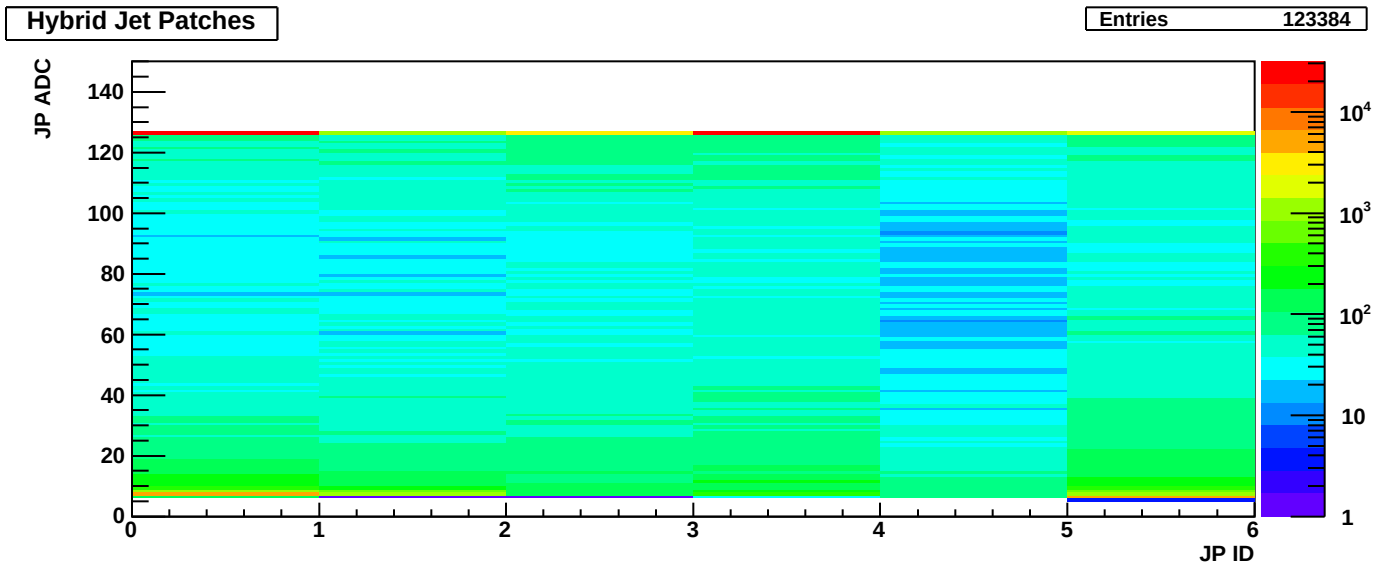

Barrel EMC L0 Input - Patch Sum
Entries 1.85076e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

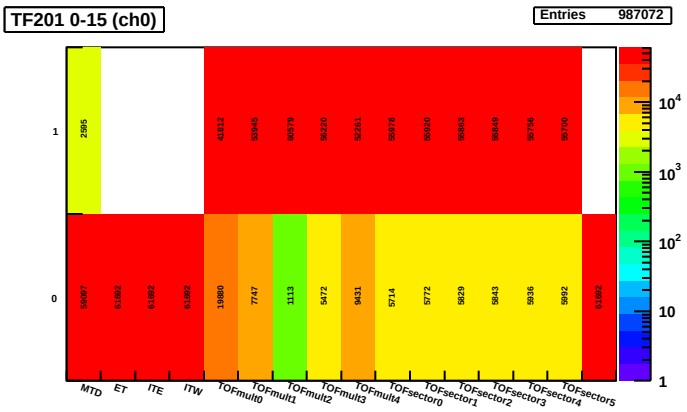

Endcap EMC L0 Input - High Tower
Entries 5552280
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

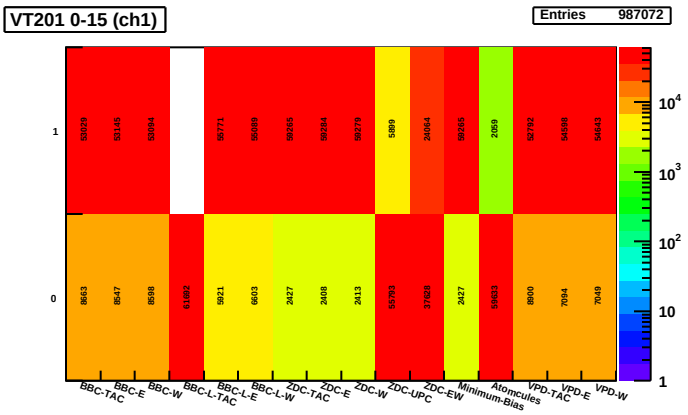

Endcap EMC L0 Input - Patch Sum
Entries 5552280
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

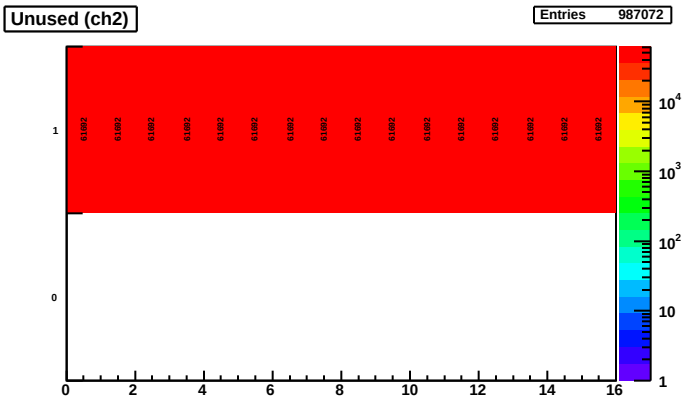

Barrel Jet Patches
Entries 1110456
JP ADC
JP ID

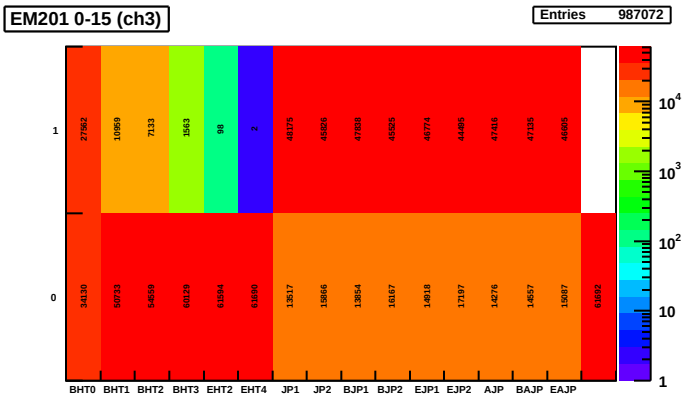

Endcap Jet Patches
Entries 370152
JP ADC
JP ID

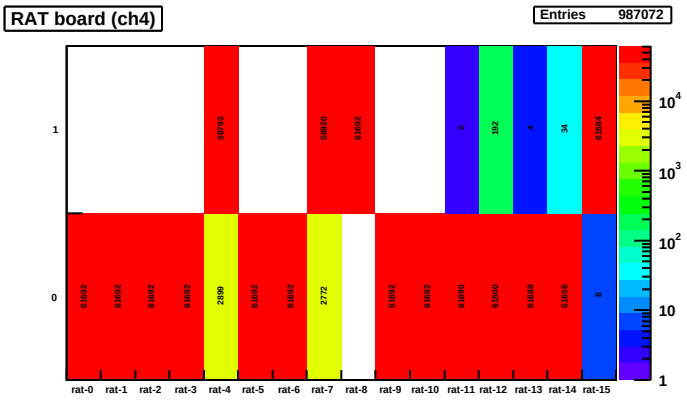

Hybrid Jet Patches
Entries 123384
JP ADC
JP ID

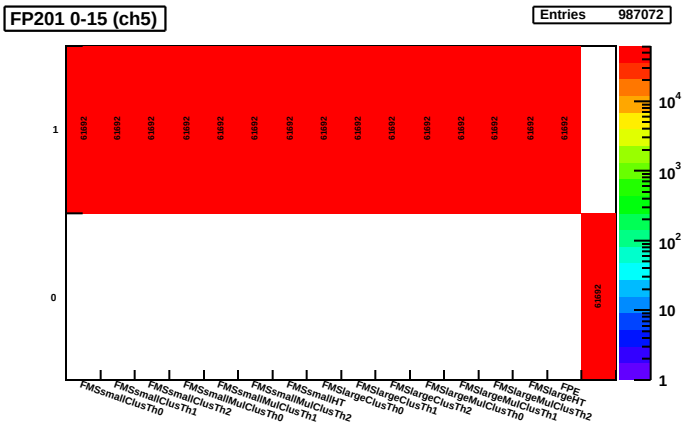

TF201 0-15 (ch0)
Entries 987072
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

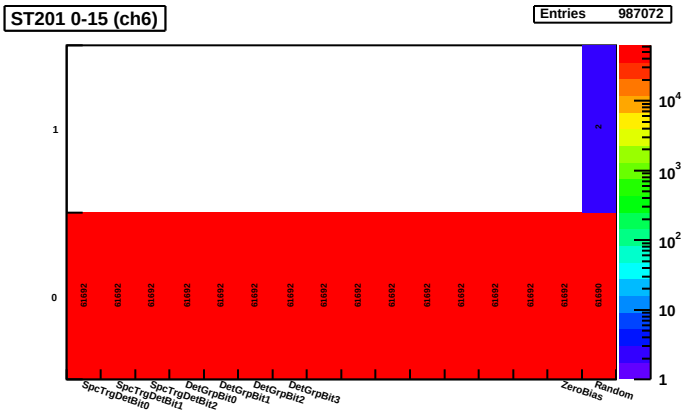

VT201 0-15 (ch1)
Entries 987072
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

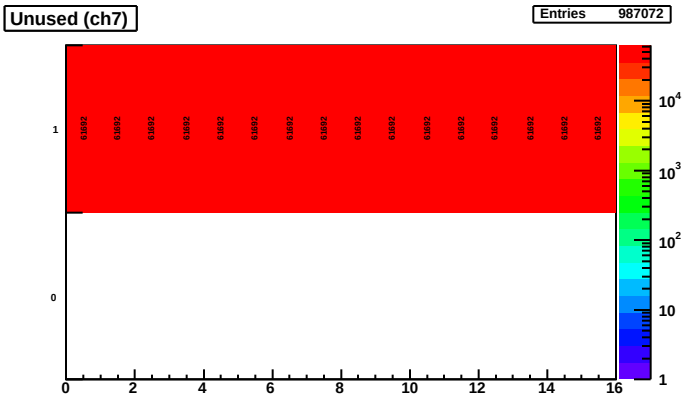

Unused (ch2)
Entries 987072

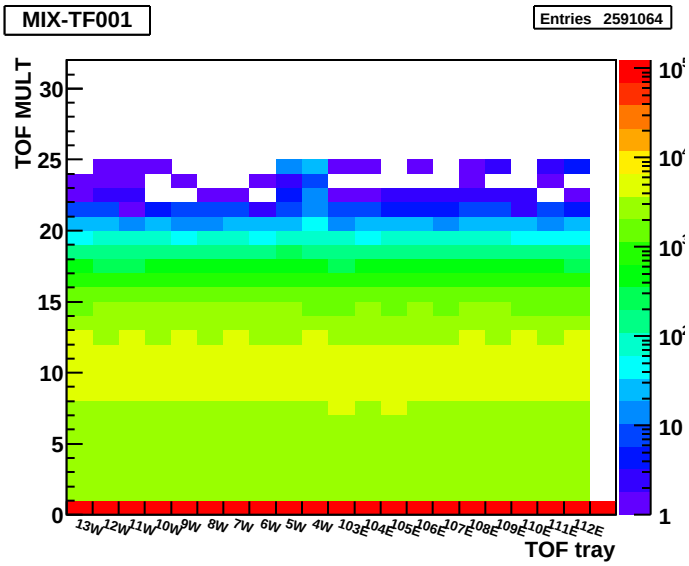

EM201 0-15 (ch3)
Entries 987072
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

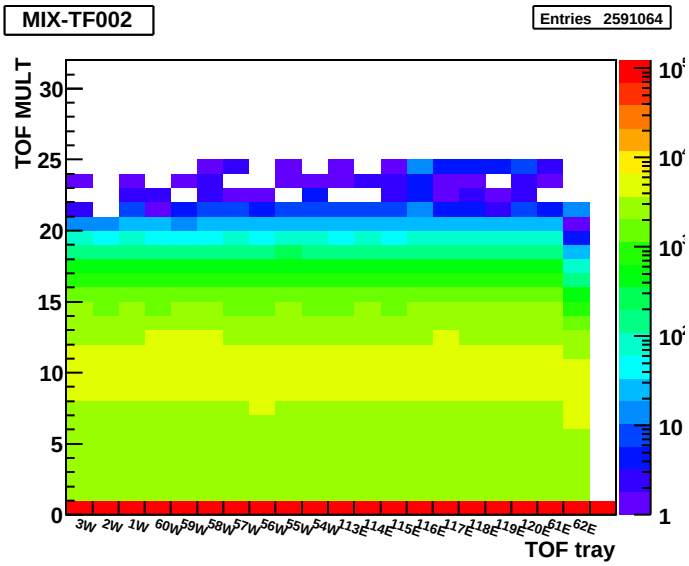

RAT board (ch4)
Entries 987072
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

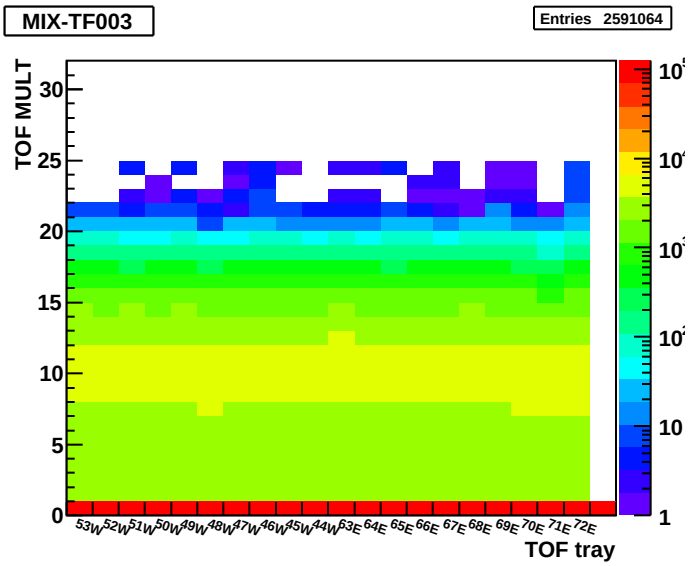

FP201 0-15 (ch5)
Entries 987072
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

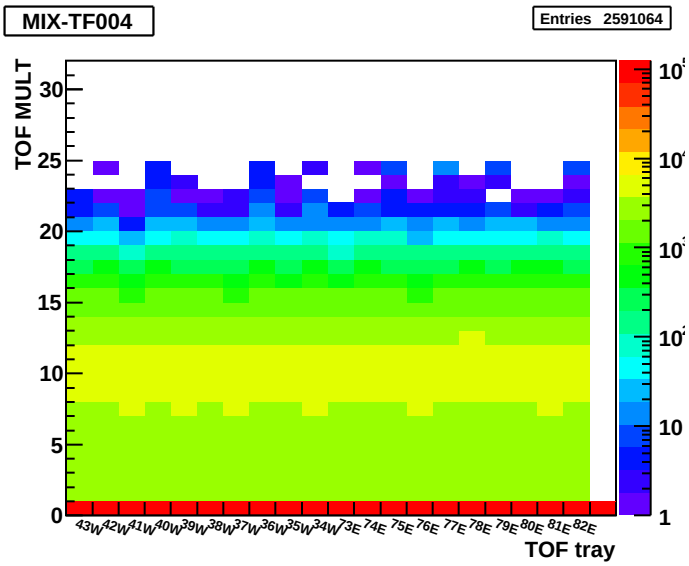

ST201 0-15 (ch6)
Entries 987072
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

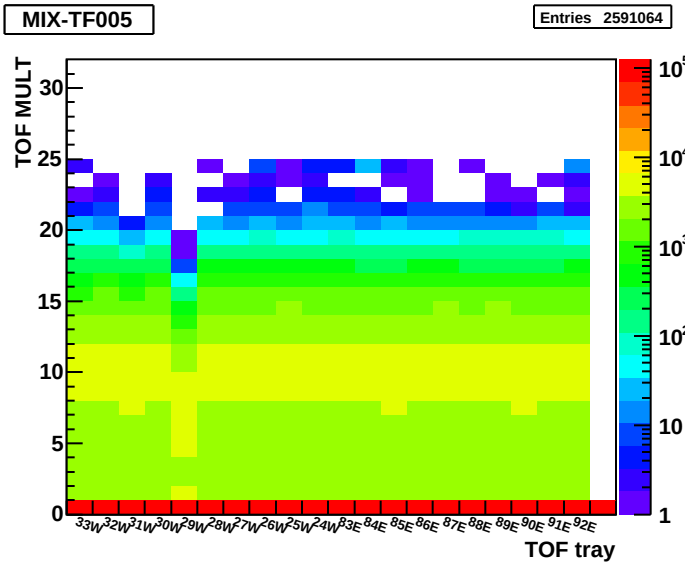

Unused (ch7)
Entries 987072

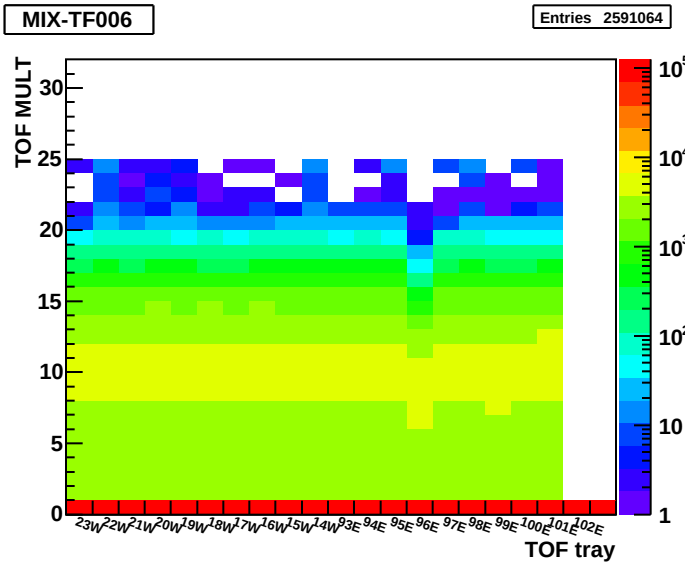

MIX-TF001
Entries 2591064
TOF MULT
TOF tray

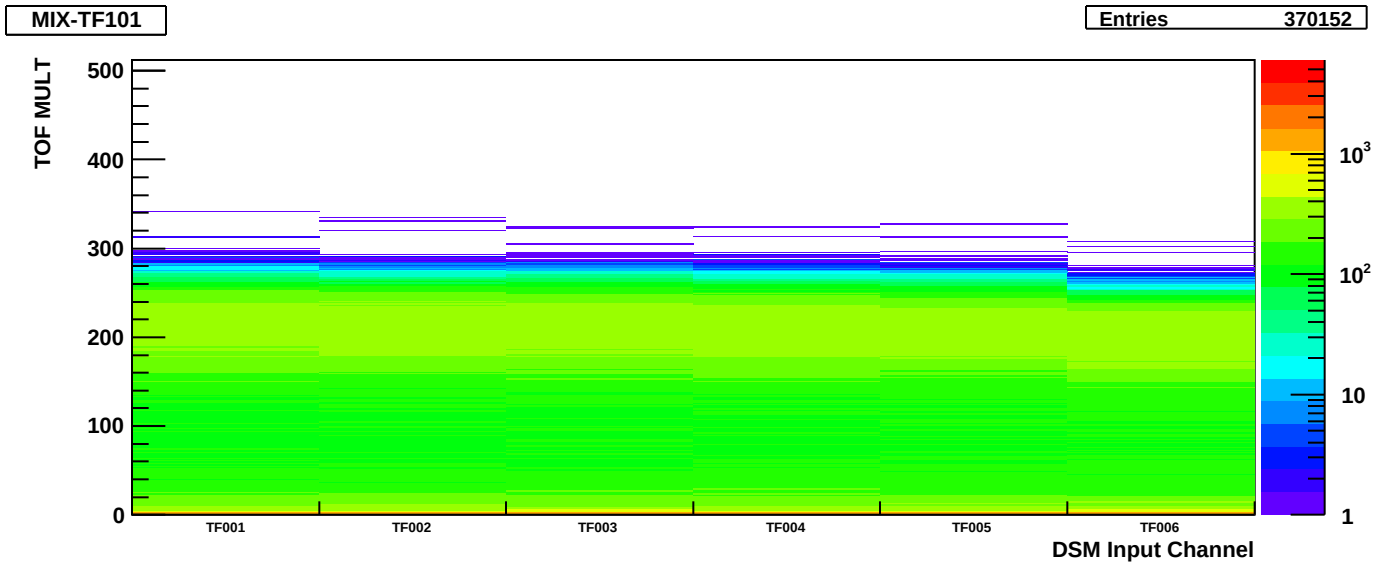

MIX-TF002
Entries 2591064
TOF MULT
TOF tray

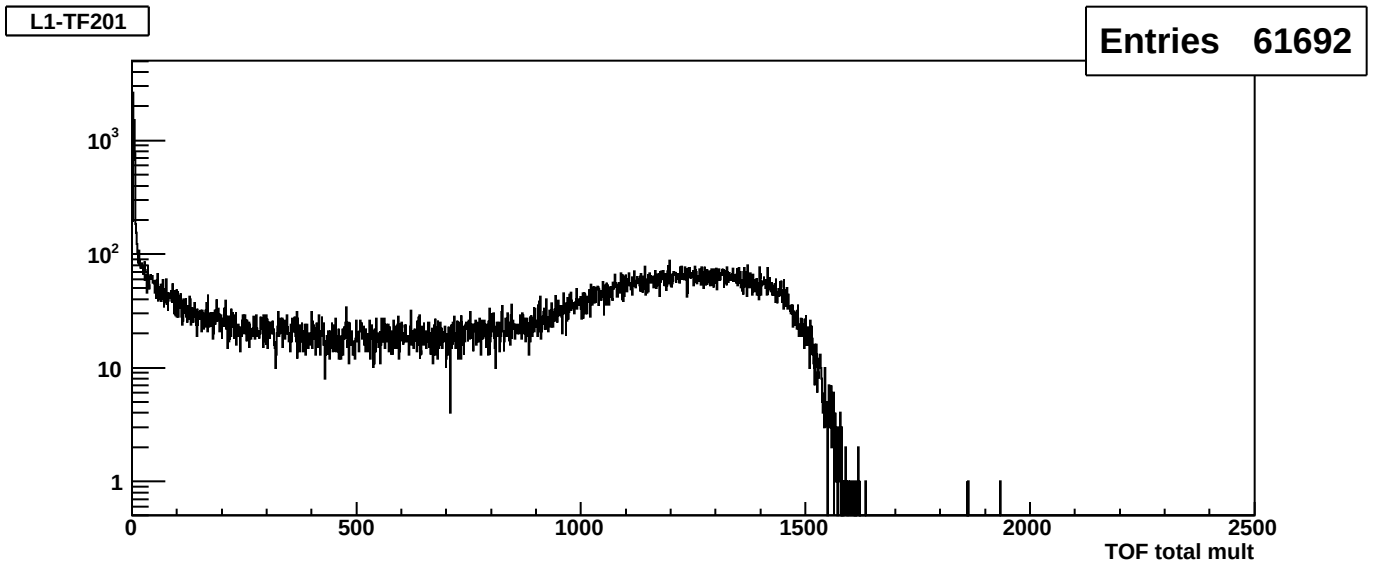

MIX-TF003
Entries 2591064
TOF MULT
TOF tray

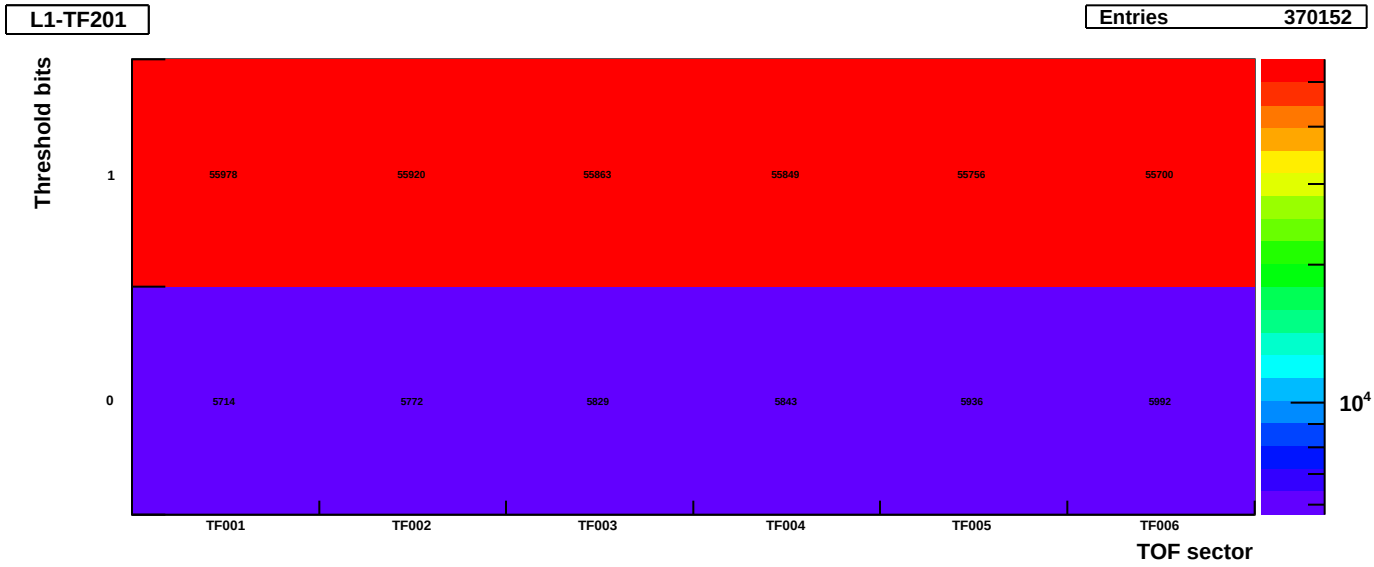

MIX-TF004
Entries 2591064
TOF MULT
TOF tray

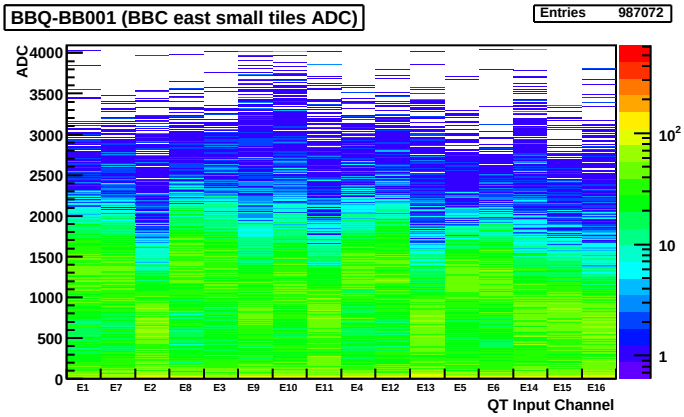

MIX-TF005
Entries 2591064
TOF MULT
TOF tray

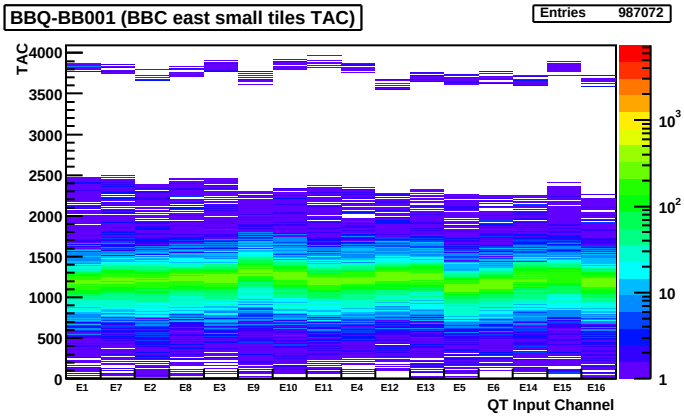

MIX-TF006
Entries 2591064
TOF MULT
TOF tray

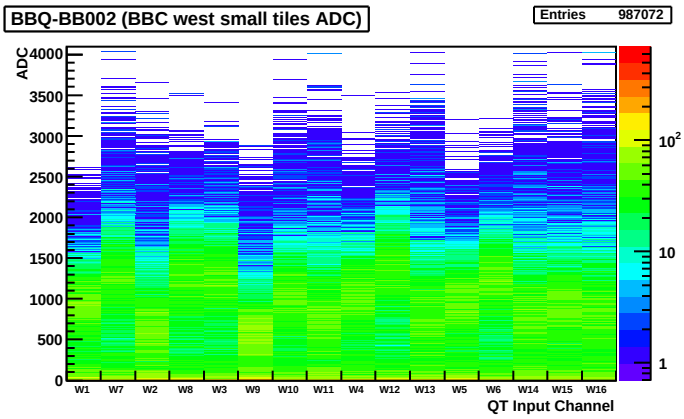

MIX-TF101
Entries 370152
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^3
10^2
10
1

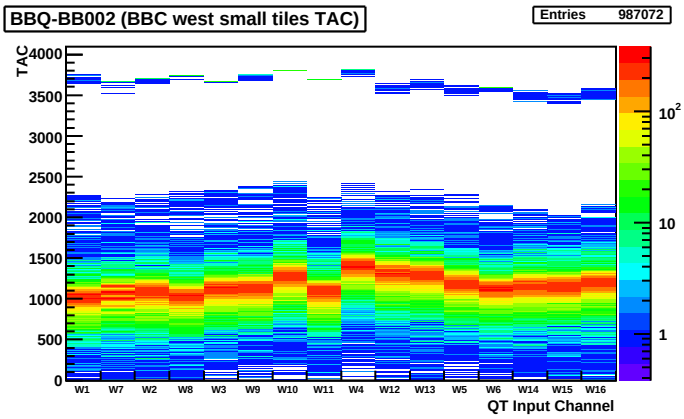

L1-TF201
Entries 61692
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

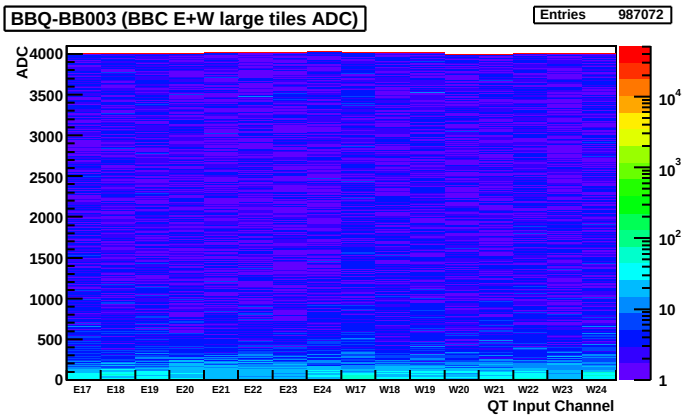

L1-TF201
Entries 370152
Threshold bits
1
0
55978
55920
55863
55849
55756
55700
5714
5772
5829
5843
5936
5992
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4

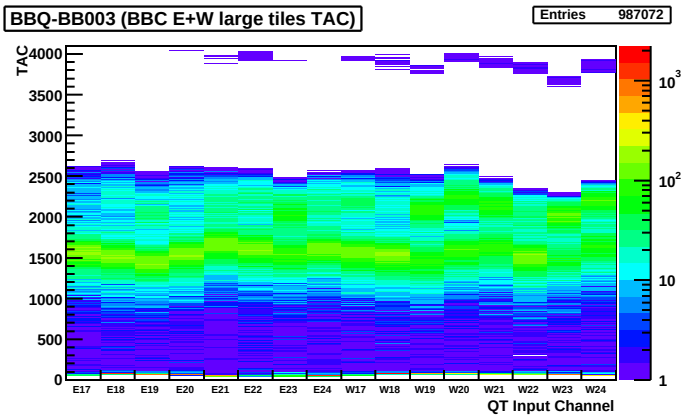

BBQ-BB001 (BBC east small tiles ADC)
Entries 987072
ADC
QT Input Channel

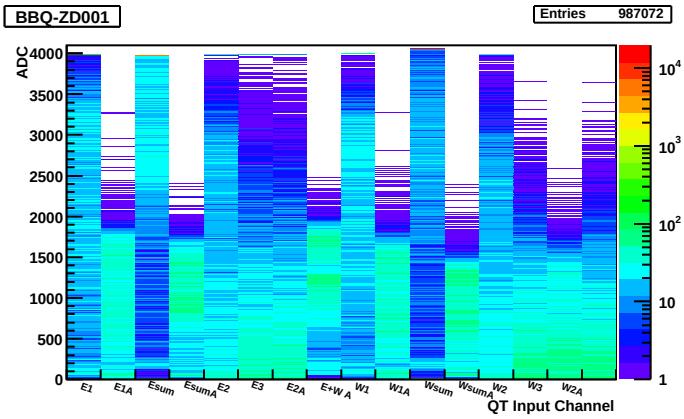

BBQ-BB001 (BBC east small tiles TAC)
Entries 987072
TAC
QT Input Channel

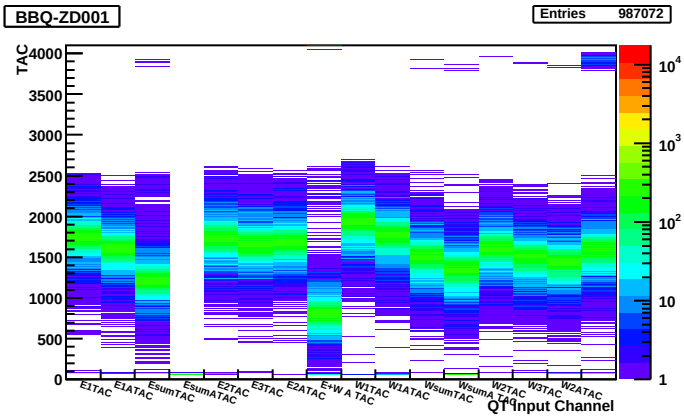

BBQ-BB002 (BBC west small tiles ADC)
Entries 987072
ADC
QT Input Channel

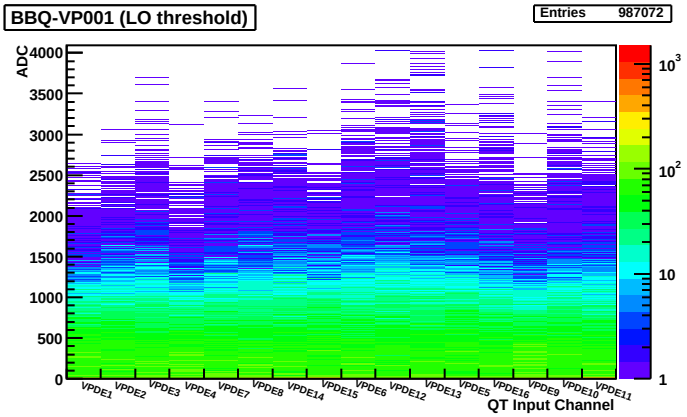

BBQ-BB002 (BBC west small tiles TAC)
Entries 987072
TAC
QT Input Channel

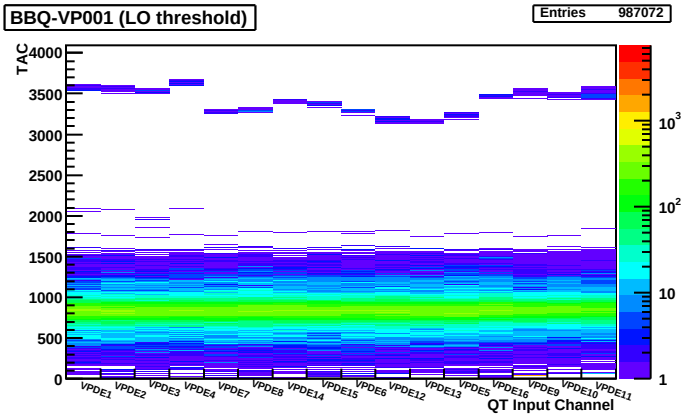

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 987072
ADC
QT Input Channel

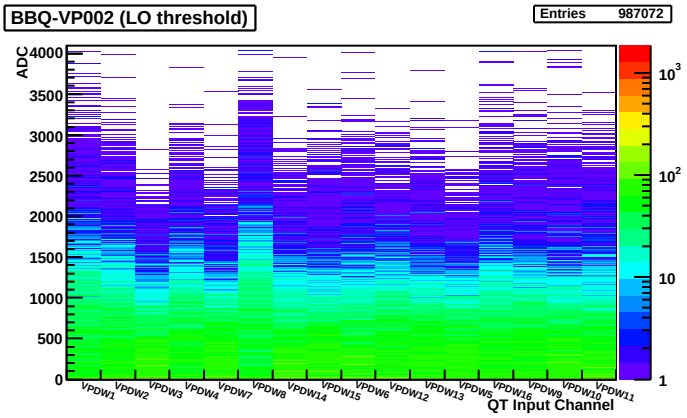

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 987072
TAC
QT Input Channel

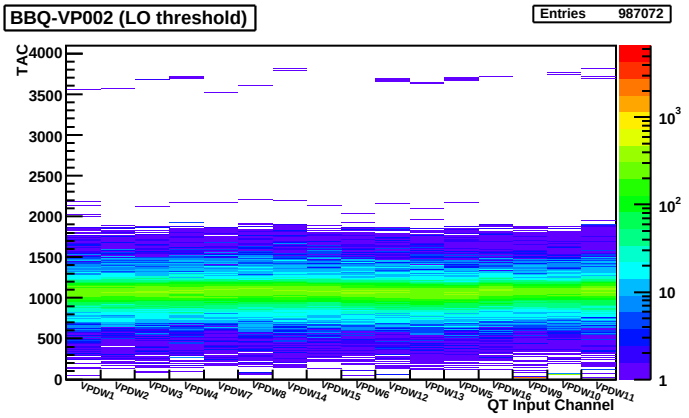

BBQ-ZD001
Entries 987072
ADC
QT Input Channel

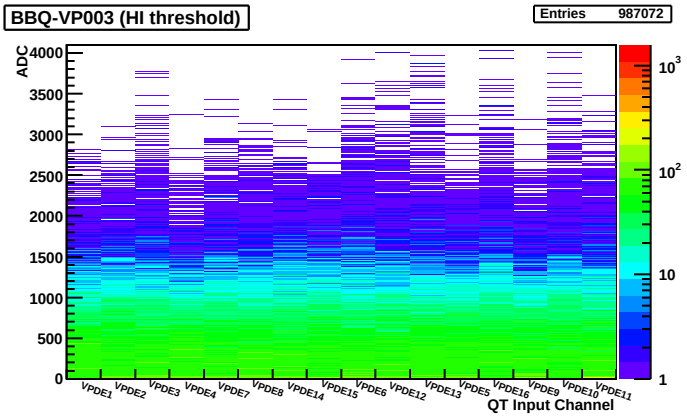

BBQ-ZD001
Entries 987072
TAC
QT Input Channel

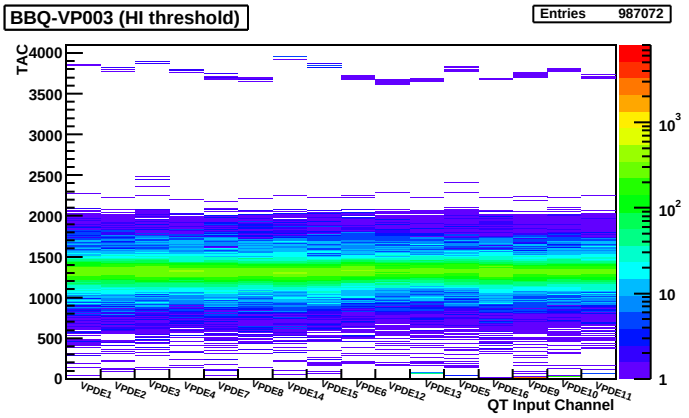

BBQ-VP001 (LO threshold)
Entries 987072
ADC
QT Input Channel

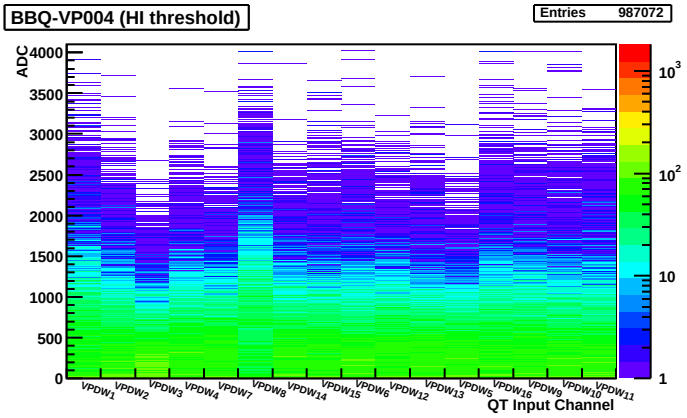

BBQ-VP001 (LO threshold)
Entries 987072
TAC
QT Input Channel

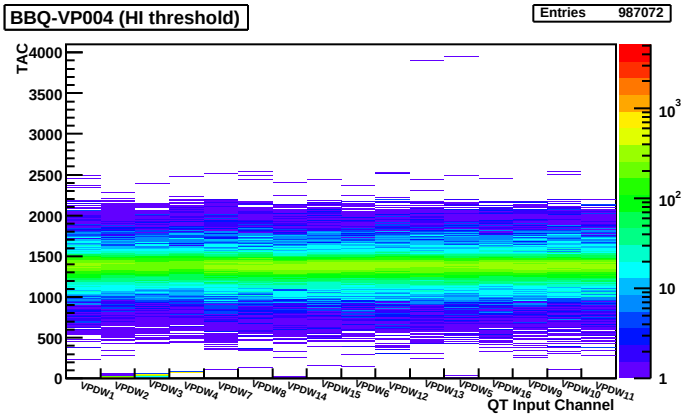

BBQ-VP002 (LO threshold)
Entries 987072
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 987072
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 987072
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 987072
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 987072
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 987072
TAC
QT Input Channel