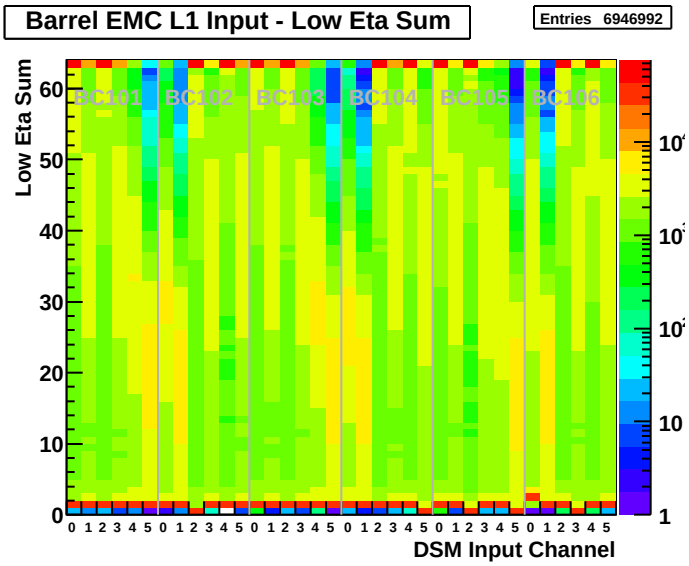
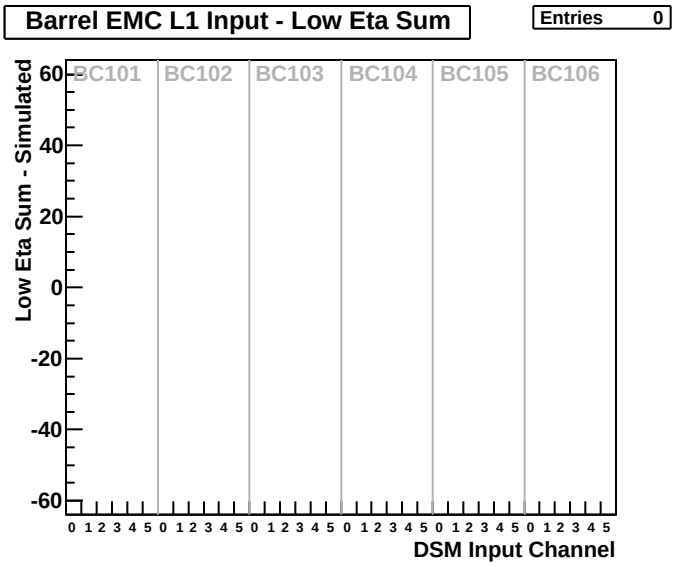
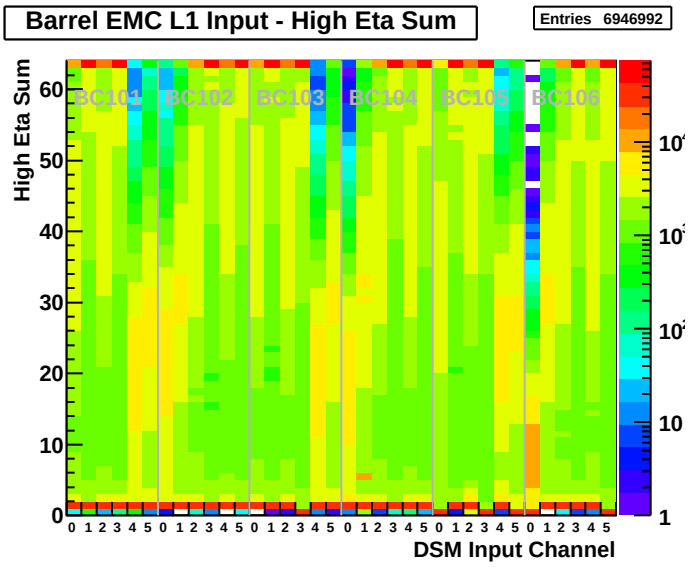
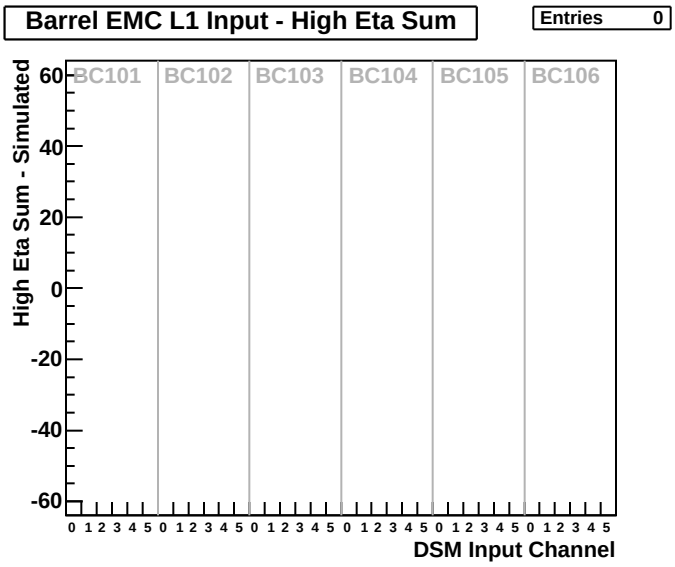
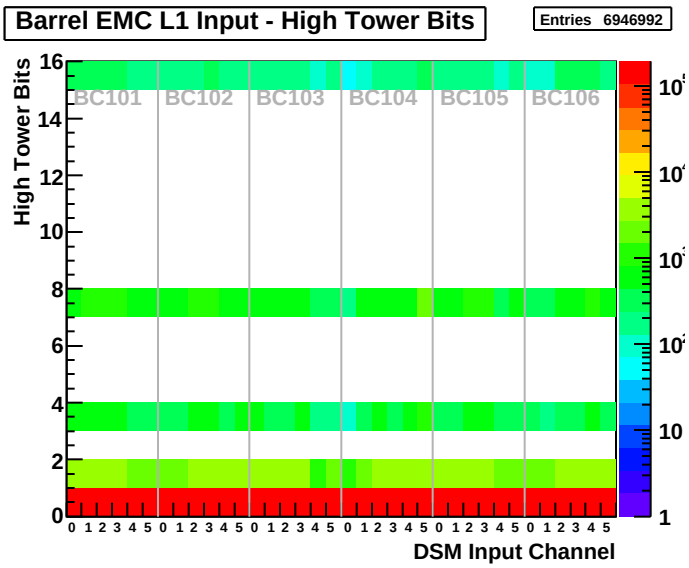
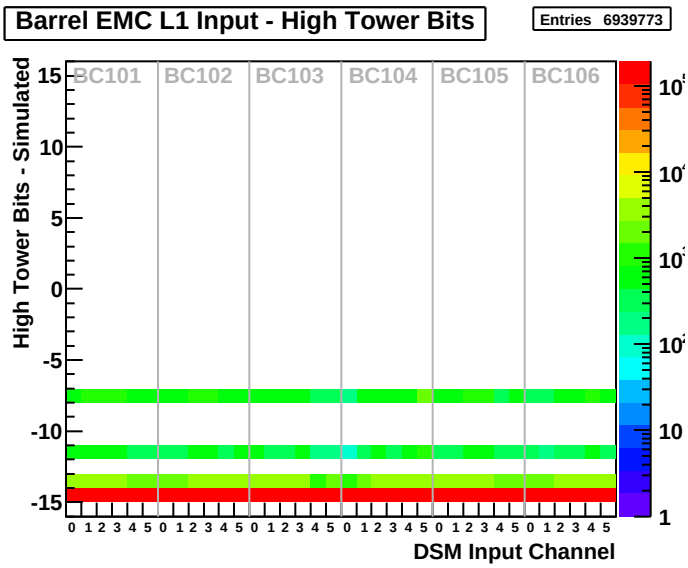

Barrel EMC L1 Input - Low Eta Sum
Entries 6946992
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

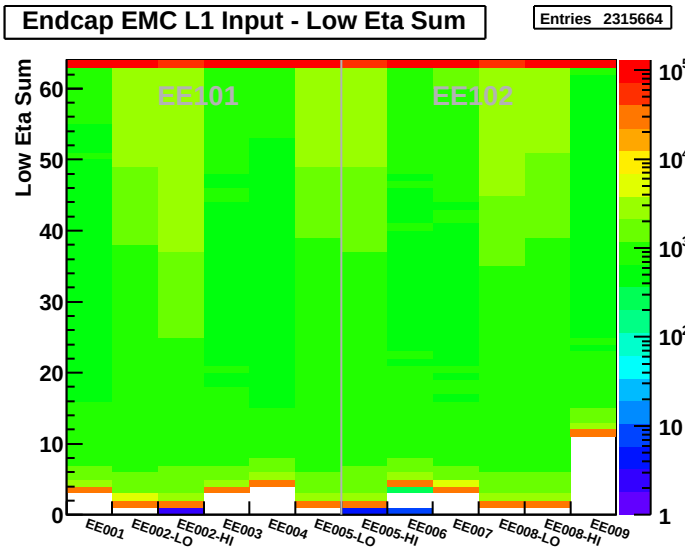

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

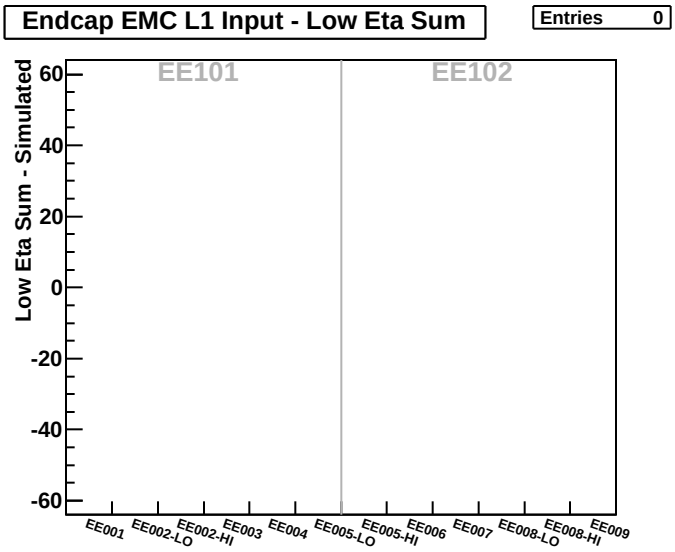

Barrel EMC L1 Input - High Eta Sum
Entries 6946992
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

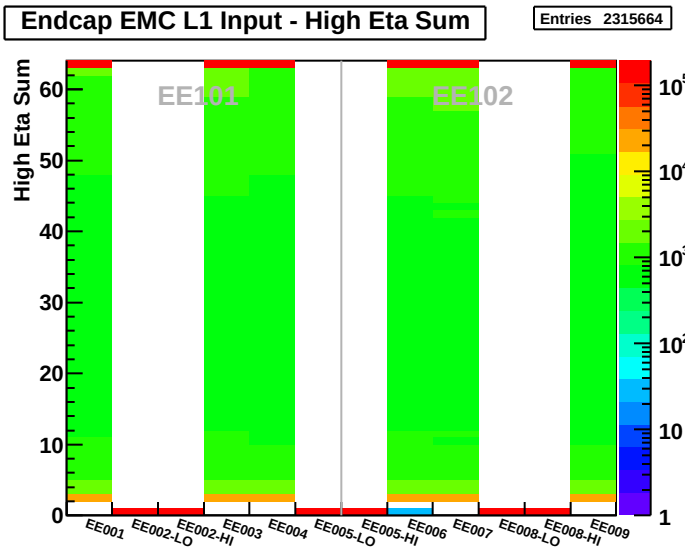

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

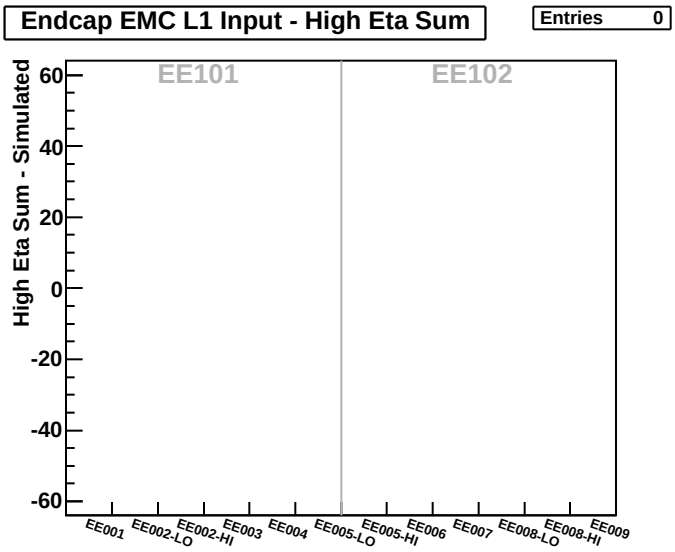

Barrel EMC L1 Input - High Tower Bits
Entries 6946992
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

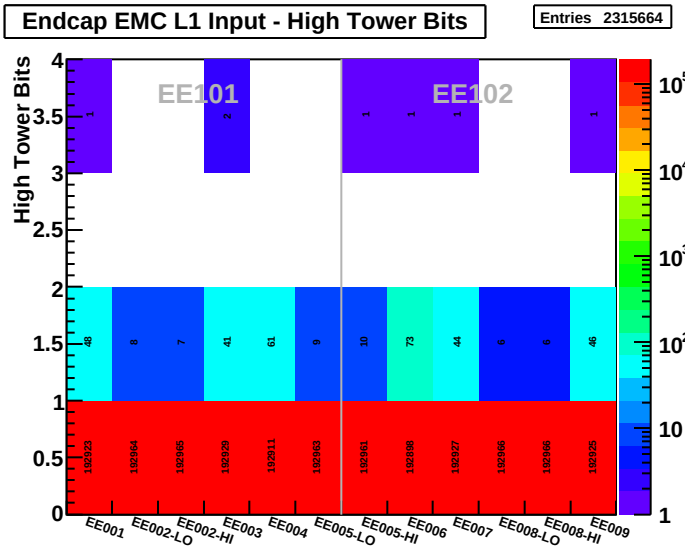

Barrel EMC L1 Input - High Tower Bits
Entries 6939773
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

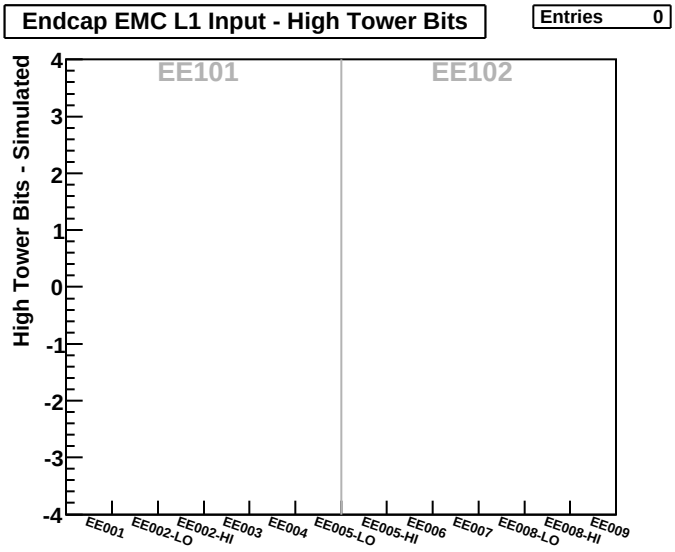

Endcap EMC L1 Input - Low Eta Sum
Entries 2315664
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

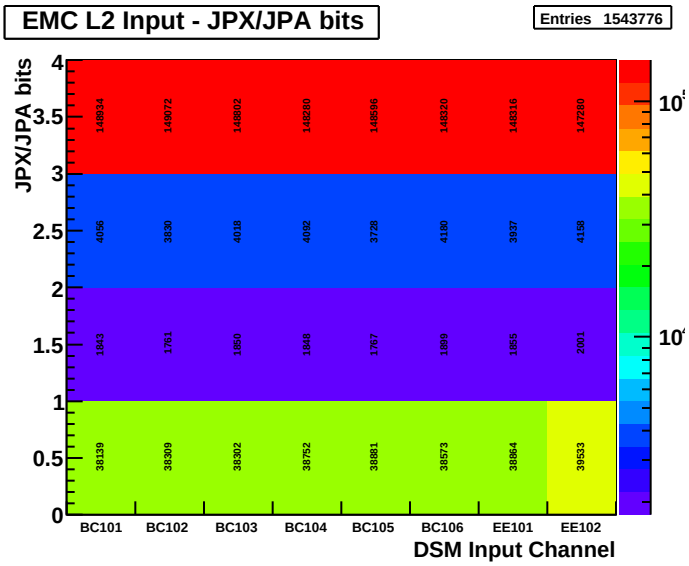

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

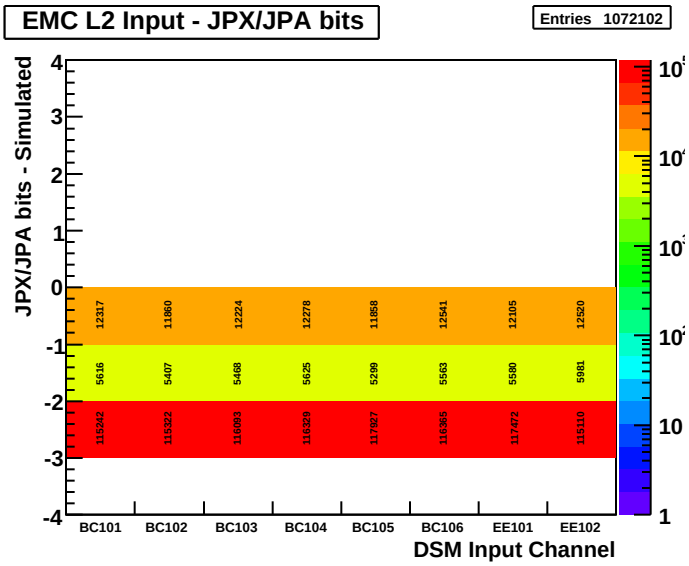

Endcap EMC L1 Input - High Eta Sum
Entries 2315664
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

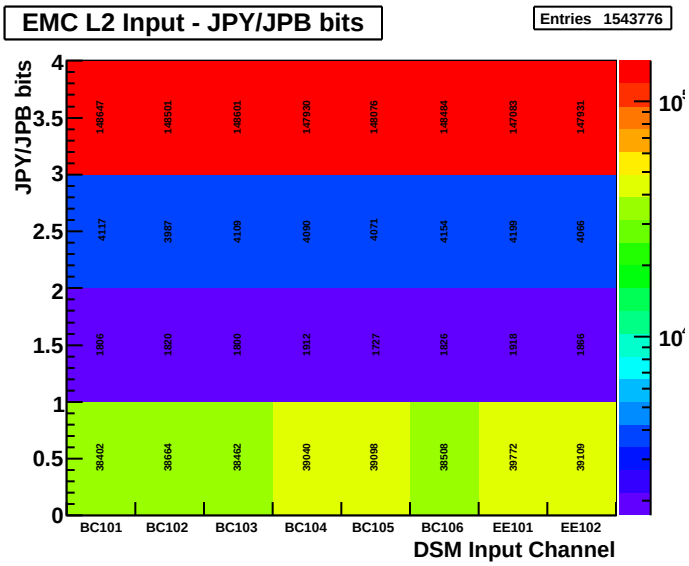

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

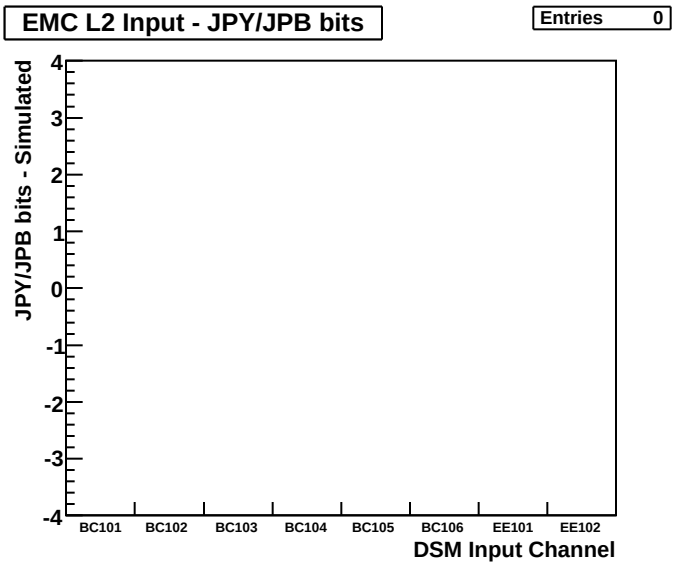

Endcap EMC L1 Input - High Tower Bits
Entries 2315664
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

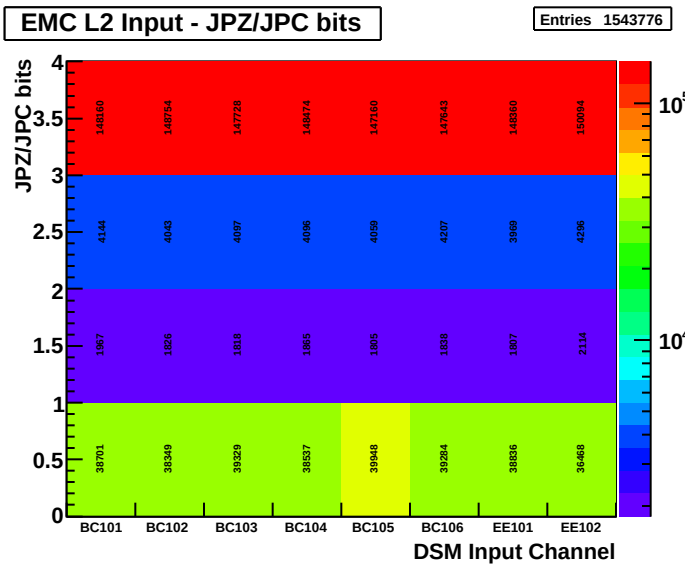

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

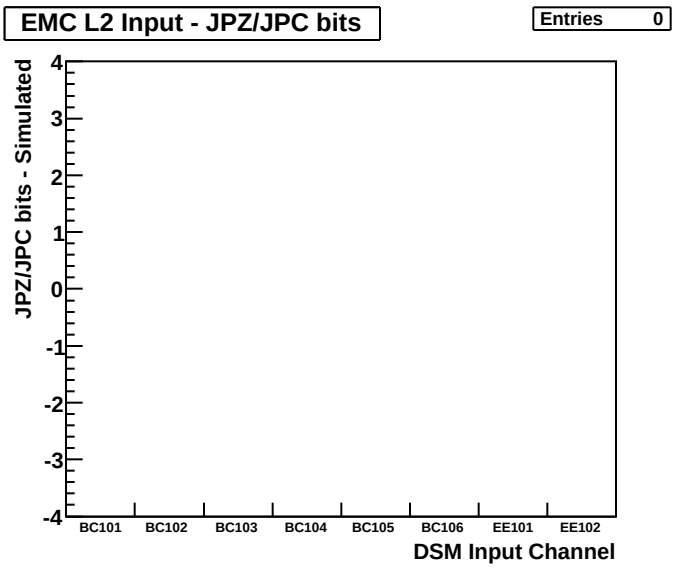

EMC L2 Input - JPX/JPA bits
Entries 1543776
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
148934 149072 148802 148280 148596 148320 148316 147280
4056 3830 4018 4092 3728 4180 3937 4158
1843 1761 1850 1848 1767 1899 1855 2001
38139 38309 38302 38752 38881 38573 38864 39533

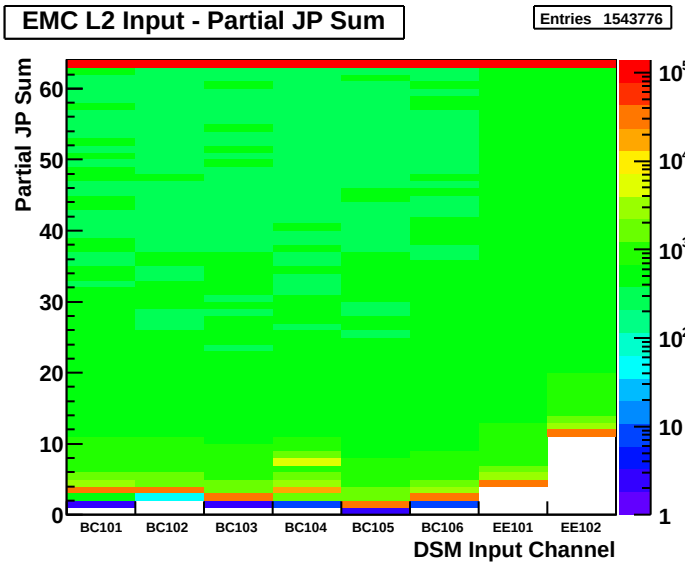

EMC L2 Input - JPX/JPA bits
Entries 1072102
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
12317 11860 12224 12278 11858 12541 12105 12520
5516 5407 5468 5625 5299 5563 5580 5981
115242 115322 116093 116329 117927 116365 117472 115110

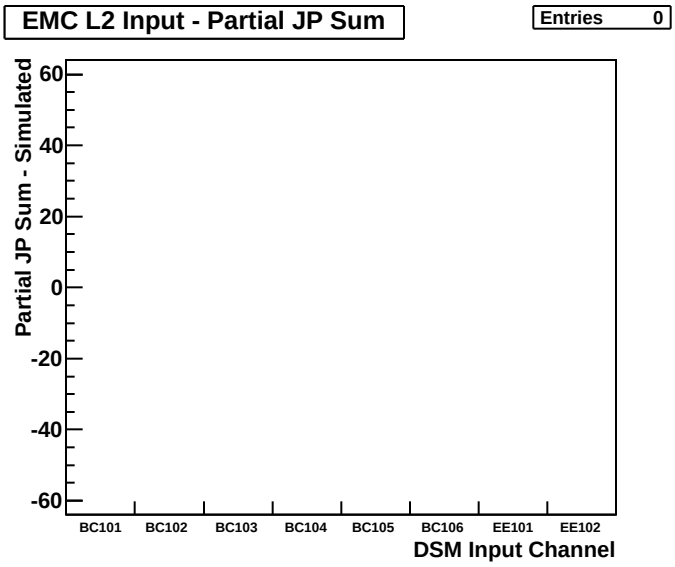

EMC L2 Input - JPY/JPB bits
Entries 1543776
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
148647 148501 148601 147930 148076 148484 147933 147931
4117 3987 4109 4090 4071 4154 4199 4066
1806 1820 1800 1812 1727 1826 1918 1866
38402 38664 38462 39040 39098 38508 39772 39109

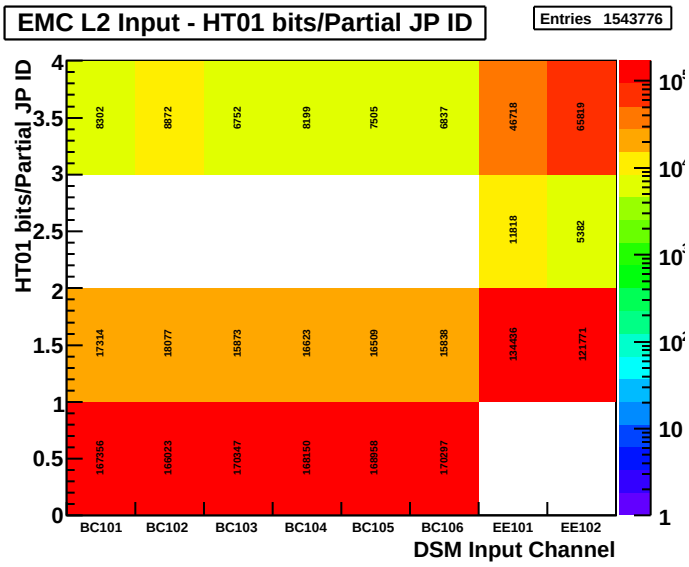

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

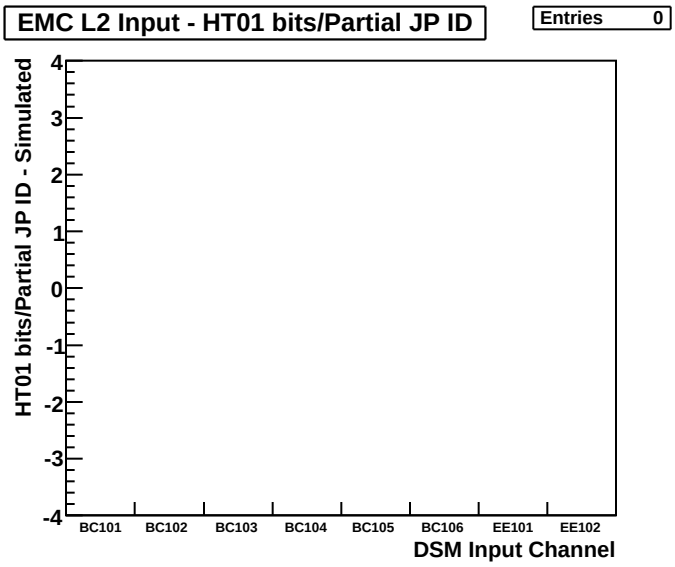

EMC L2 Input - JPZ/JPC bits
Entries 1543776
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
148160 148754 147728 148474 147160 147643 148360 150094
4144 4043 4097 4096 4099 4207 3969 4296
1867 1826 1818 1865 1805 1838 1807 2114
38701 38349 39329 38537 39948 39284 38836 36468

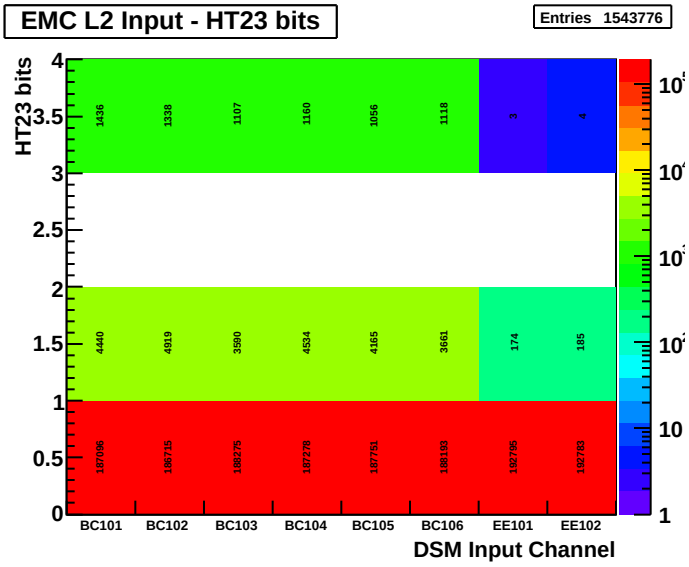

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

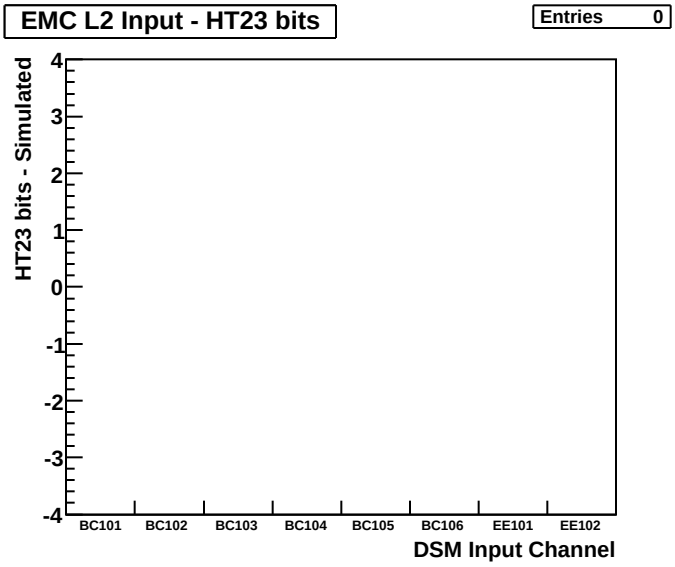

EMC L2 Input - Partial JP Sum
Entries 1543776
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

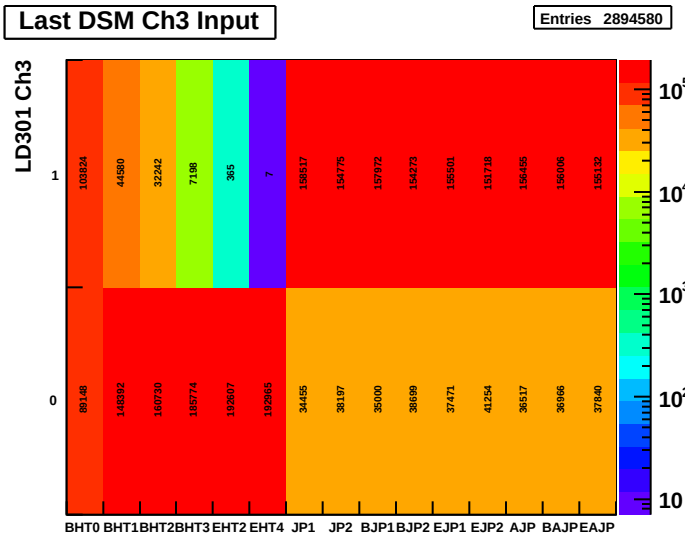

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

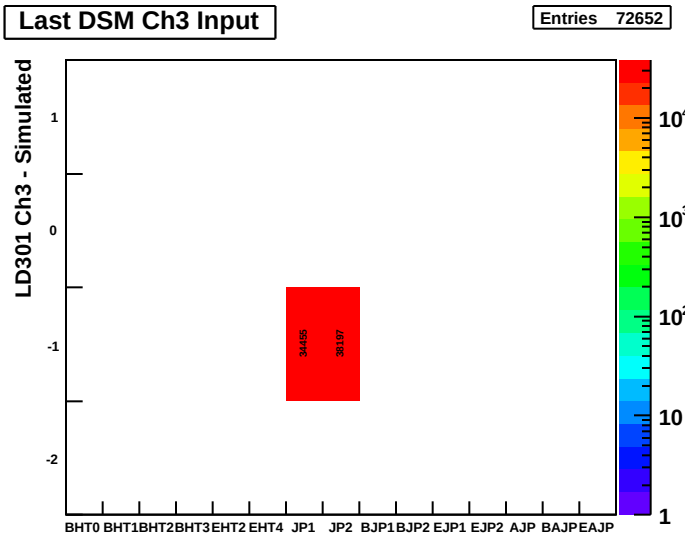

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1543776
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

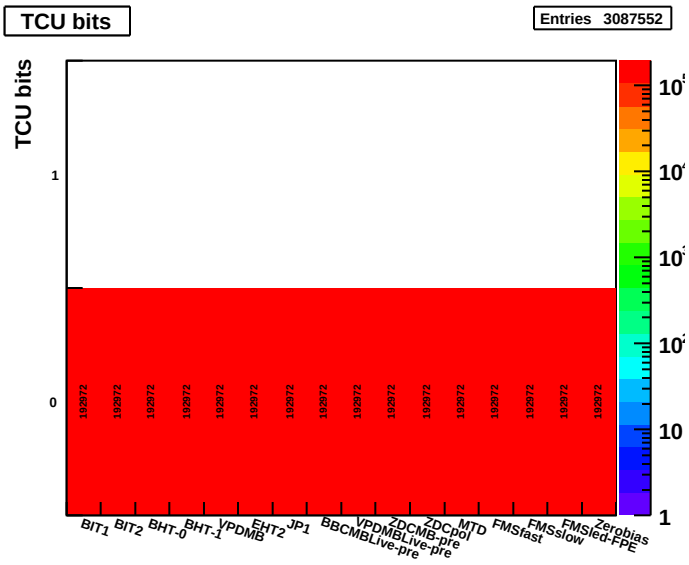

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

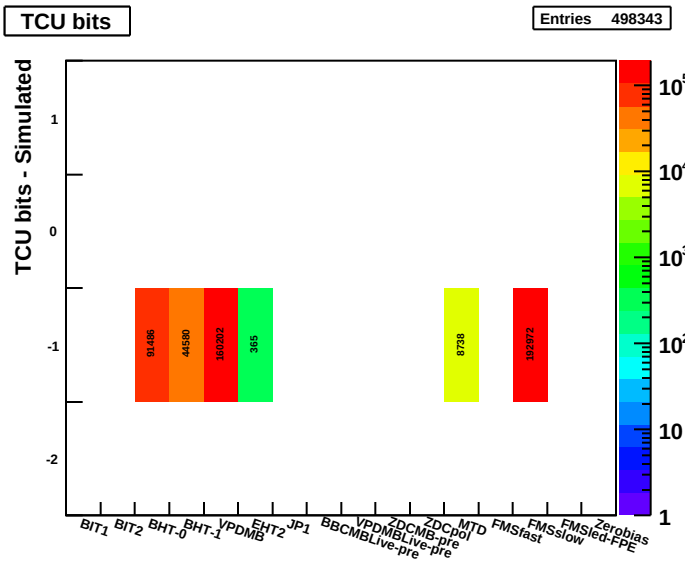

EMC L2 Input - HT23 bits
Entries 1543776
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

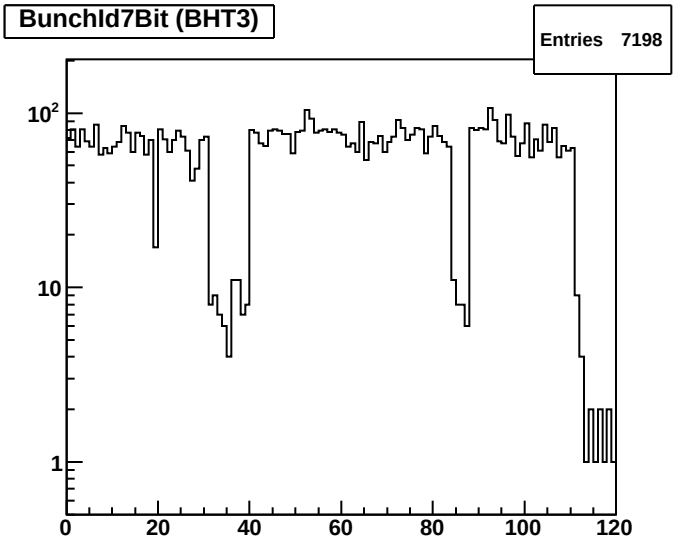

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

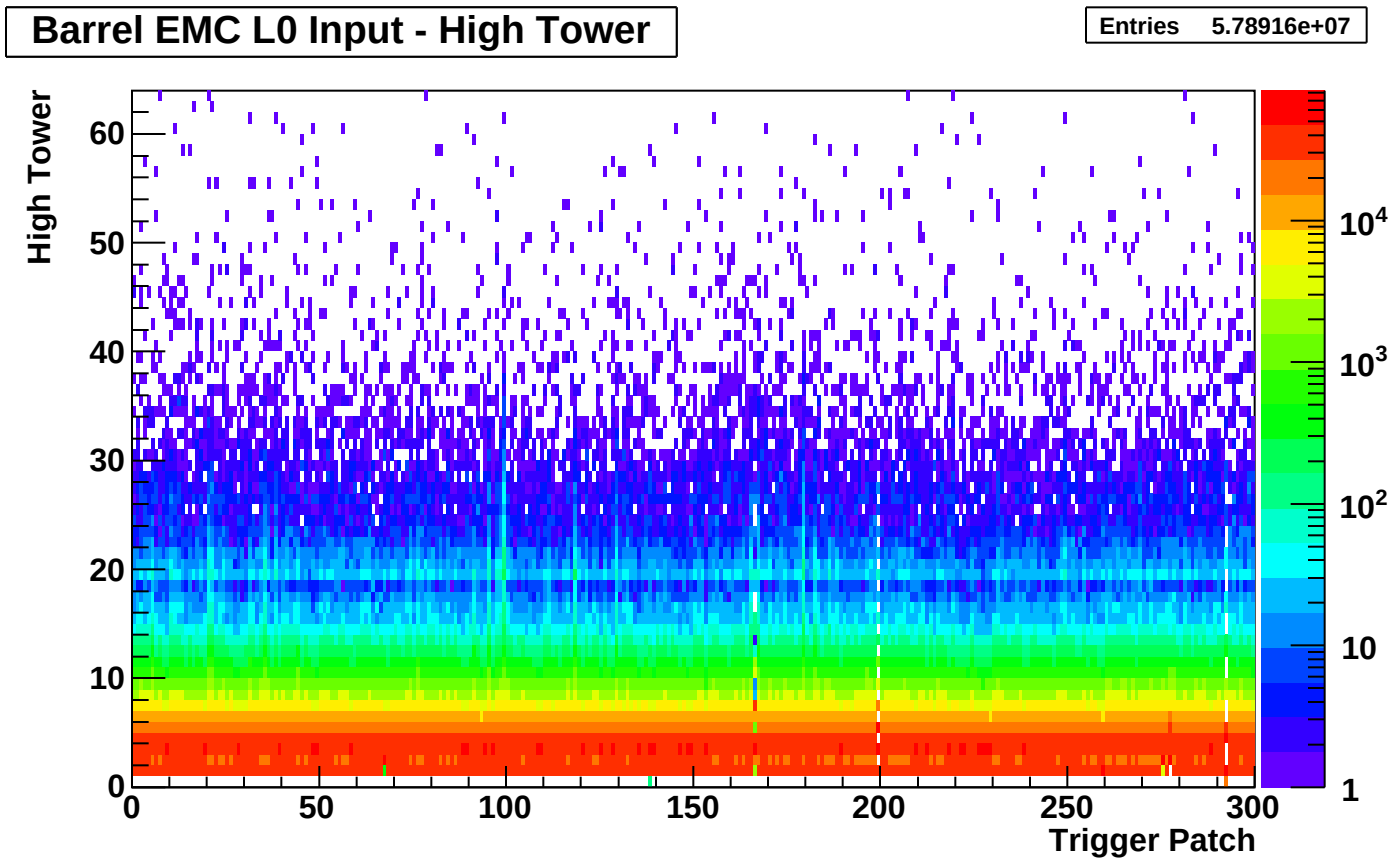

Last DSM Ch3 Input
Entries 2894580
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

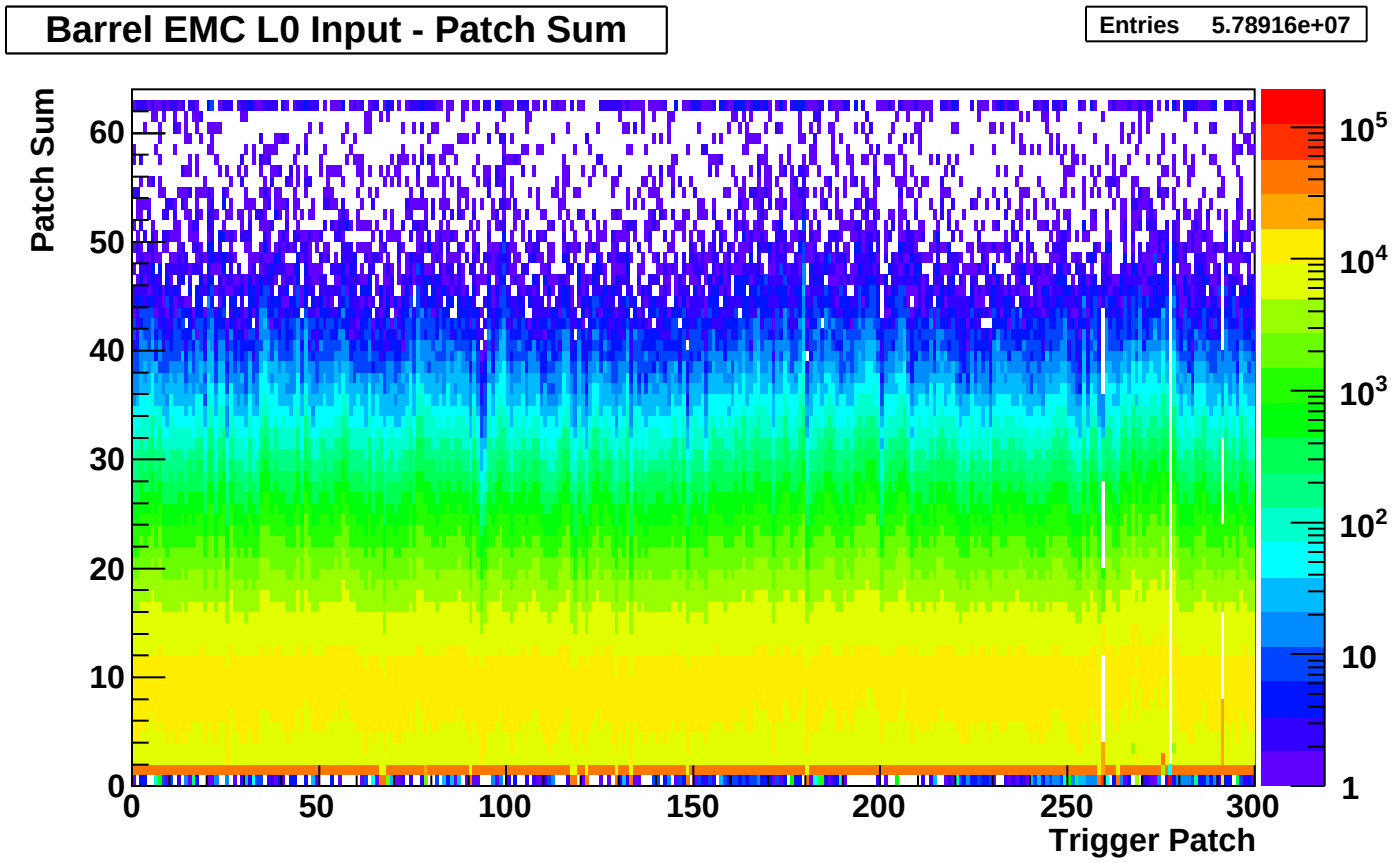

Last DSM Ch3 Input
Entries 72652
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

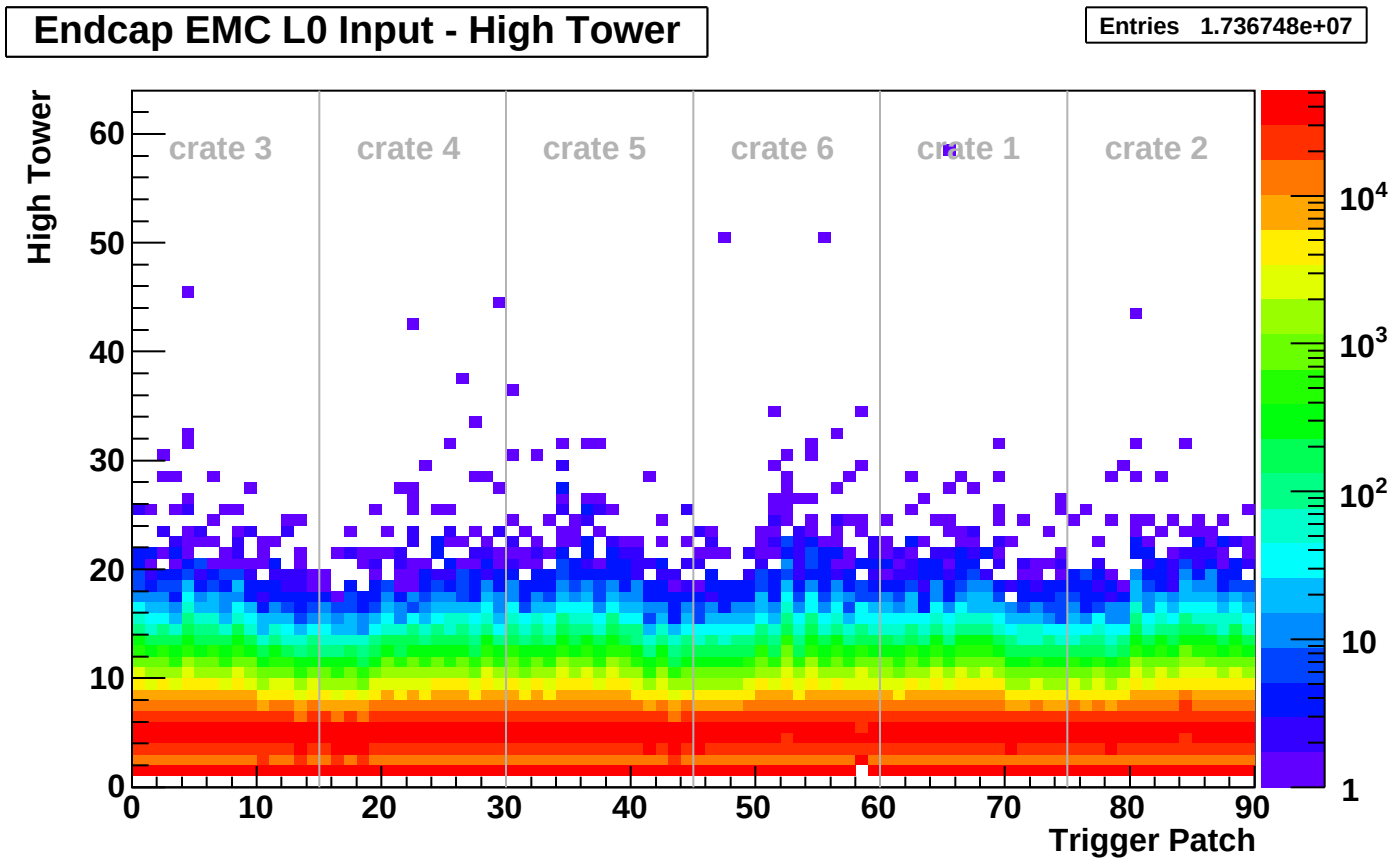

TCU bits
Entries 3087552
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

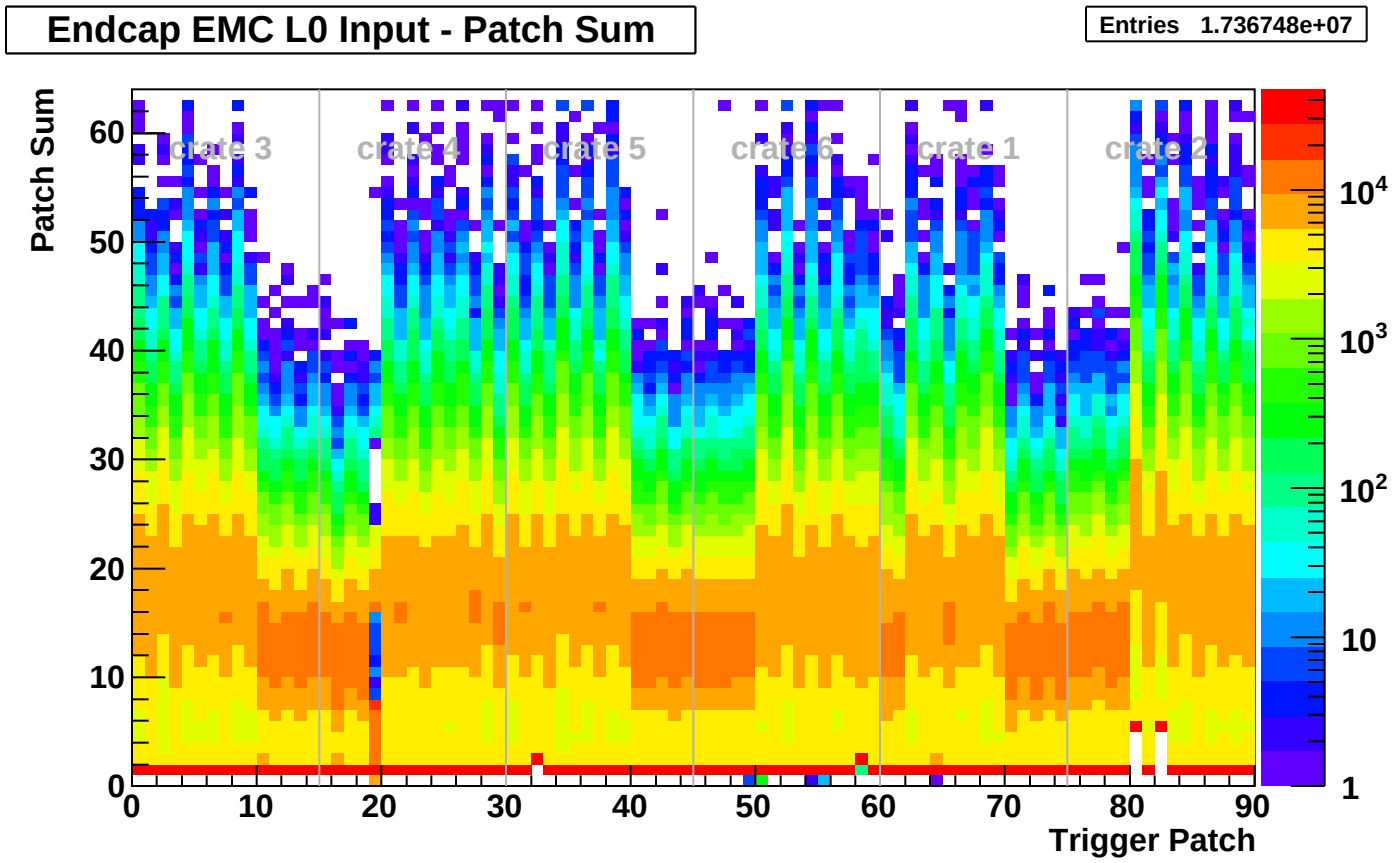

TCU bits
Entries 498343
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

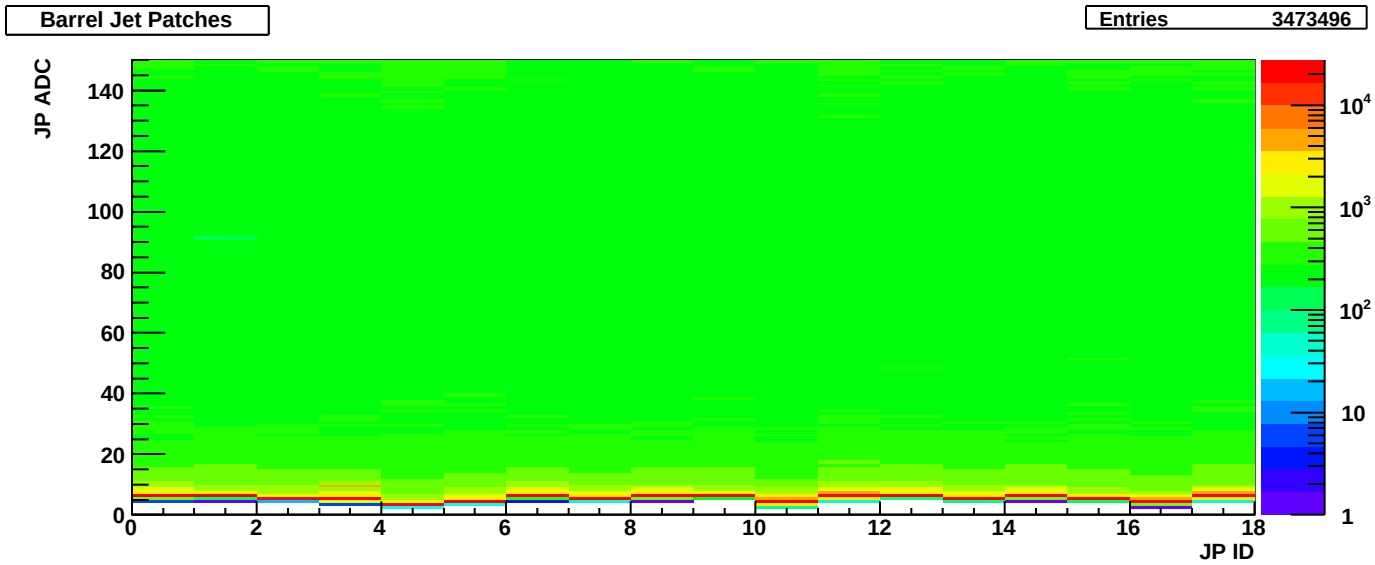

BunchId7Bit (BHT3)
Entries 7198

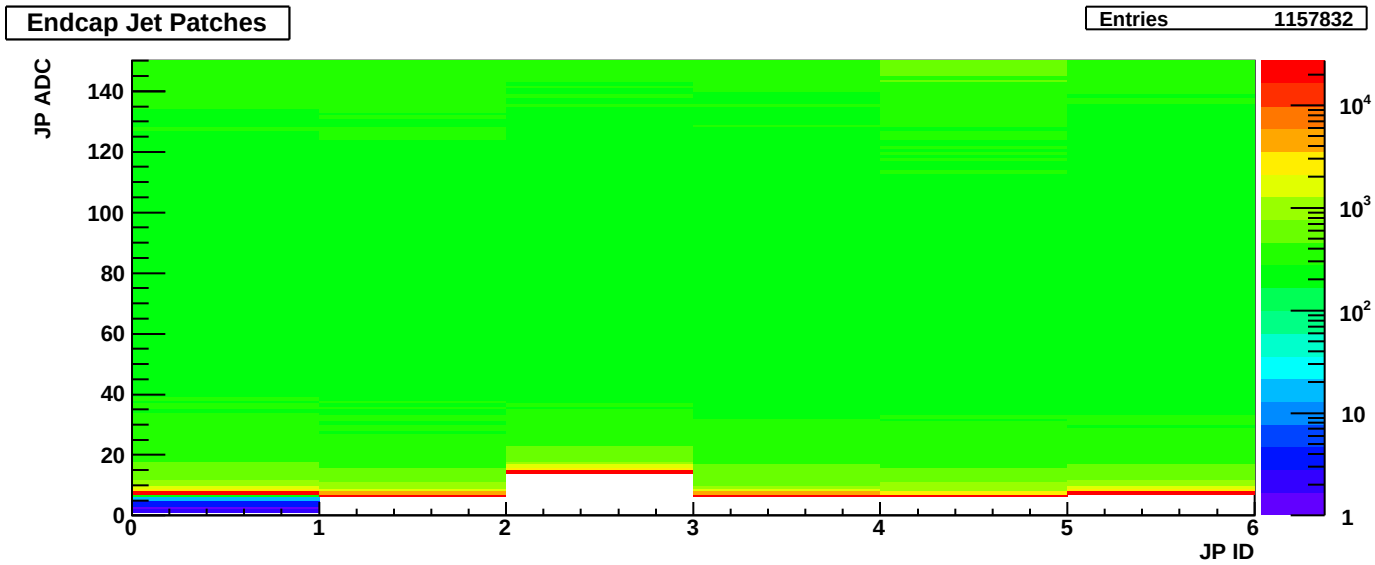

Barrel EMC L0 Input - High Tower
Entries 5.78916e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

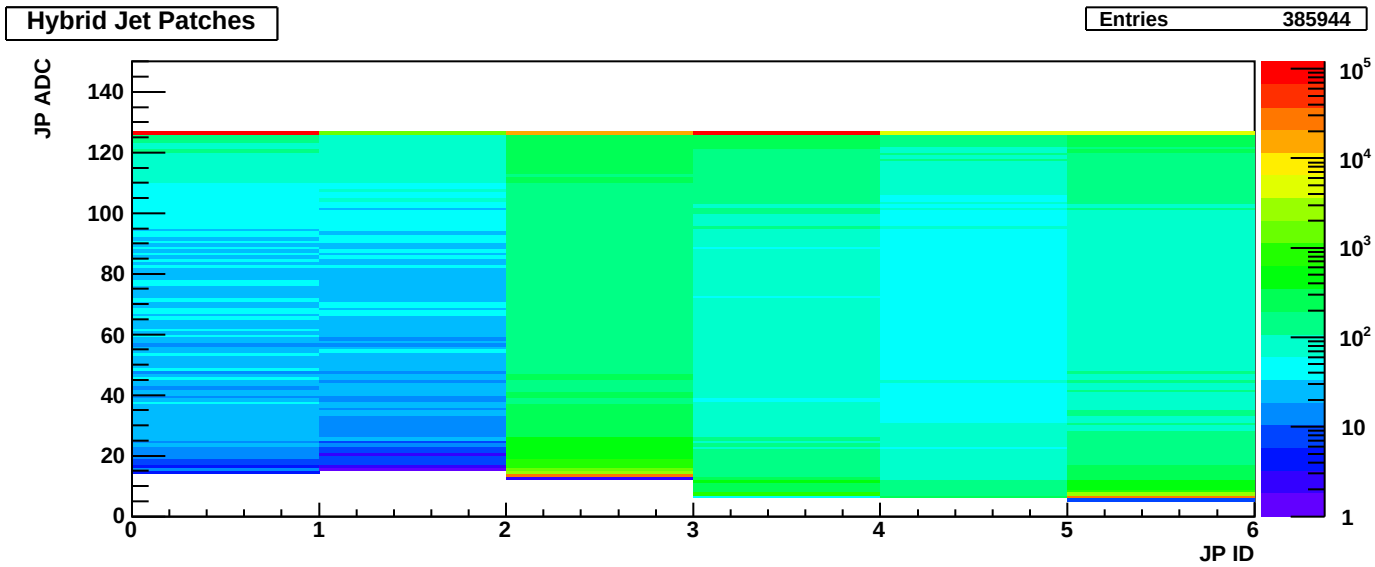

Barrel EMC L0 Input - Patch Sum
Entries 5.78916e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

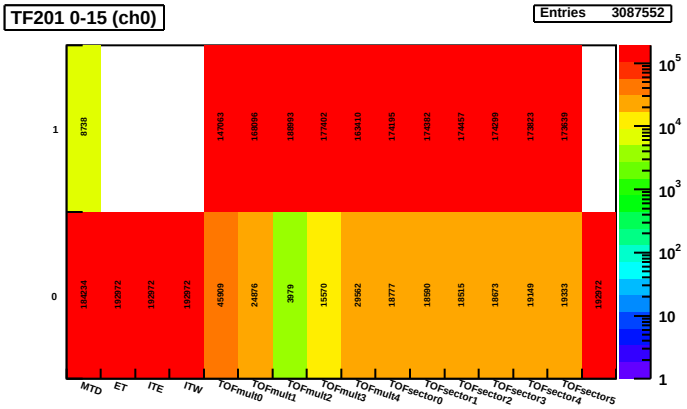

Endcap EMC L0 Input - High Tower
Entries 1.736748e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

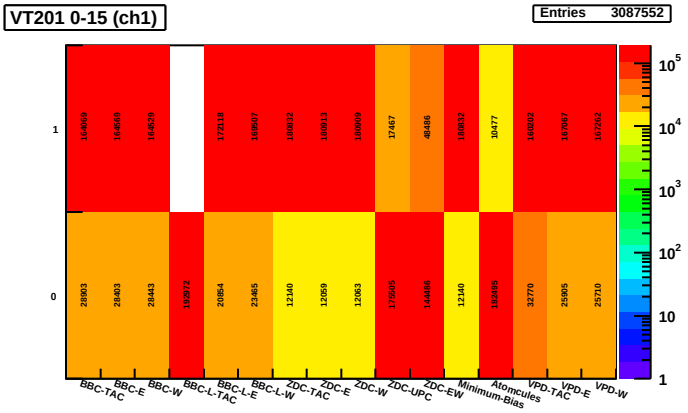

Endcap EMC L0 Input - Patch Sum
Entries 1.736748e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

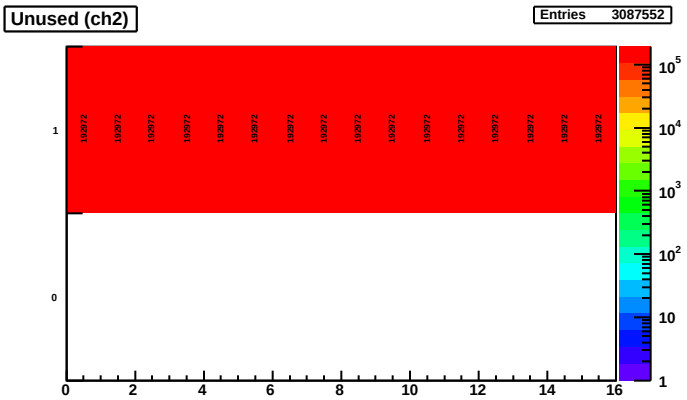

Barrel Jet Patches
Entries 3473496
JP ADC
JP ID

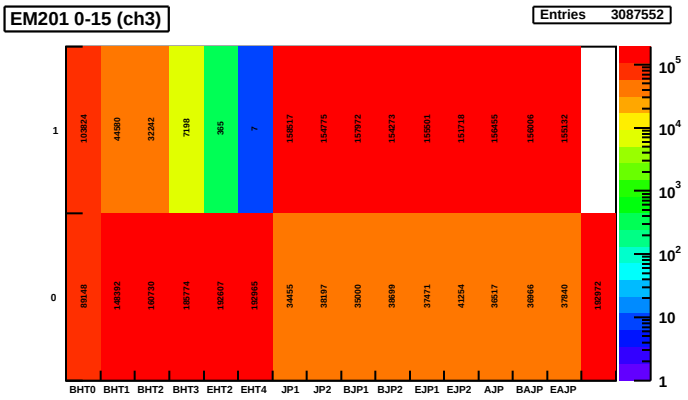

Endcap Jet Patches
Entries 1157832
JP ADC
JP ID

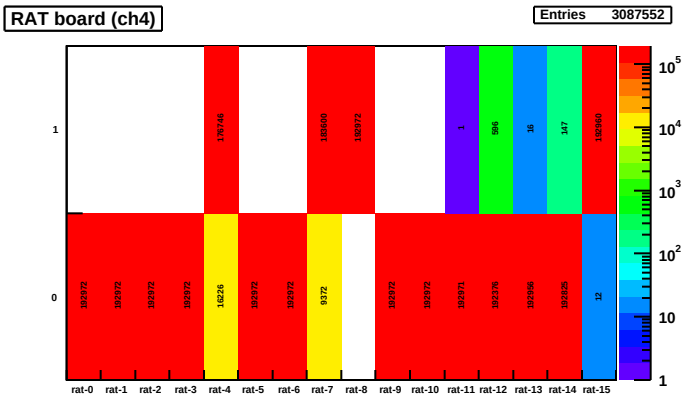

Hybrid Jet Patches
Entries 385944
JP ADC
JP ID

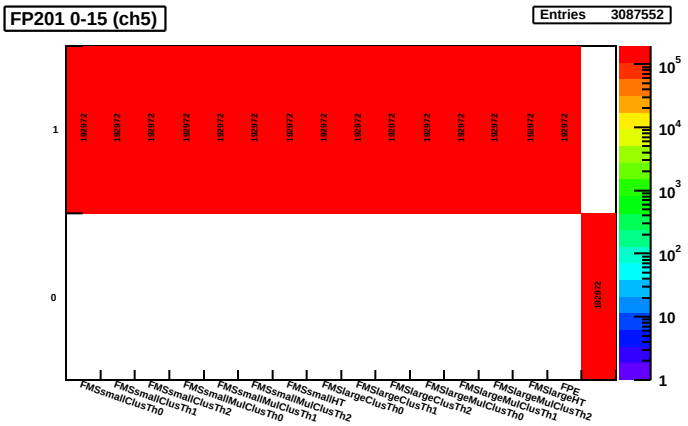

TF201 0-15 (ch0)
Entries 3087552
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

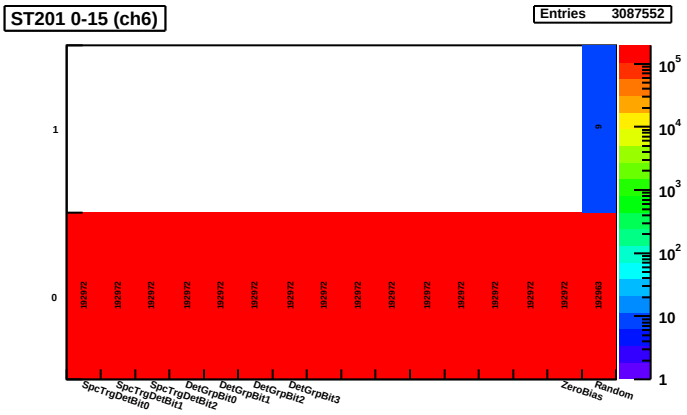

VT201 0-15 (ch1)
Entries 3087552
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

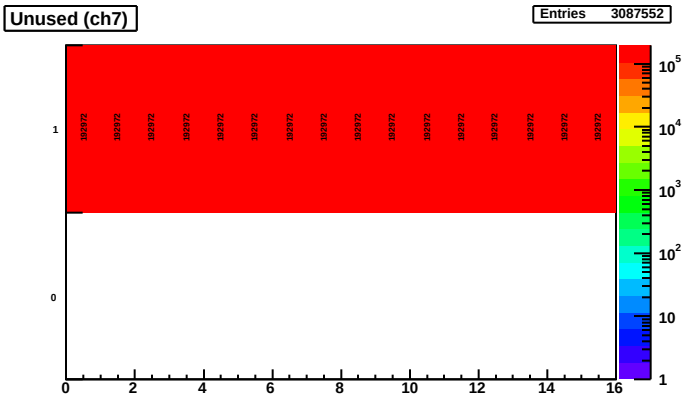

Unused (ch2)
Entries 3087552

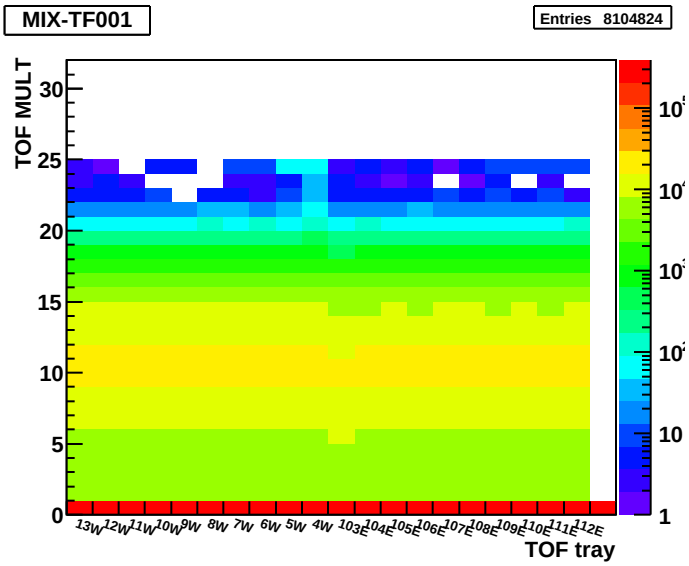

EM201 0-15 (ch3)
Entries 3087552
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP

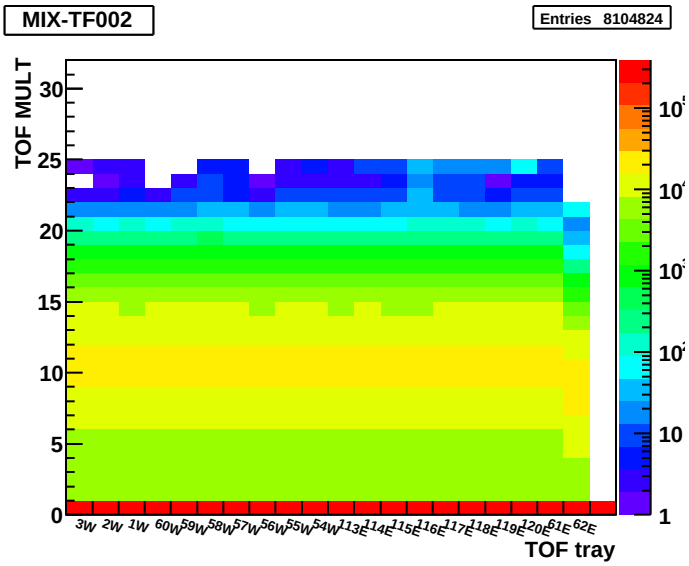

RAT board (ch4)
Entries 3087552
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

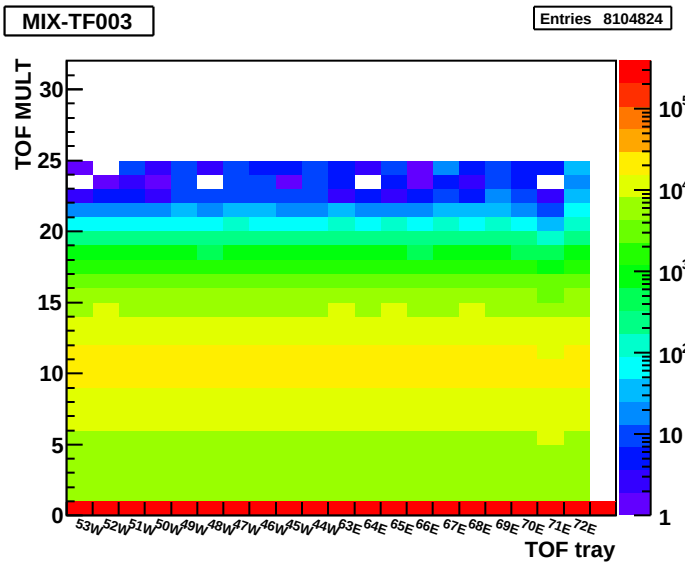

FP201 0-15 (ch5)
Entries 3087552
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

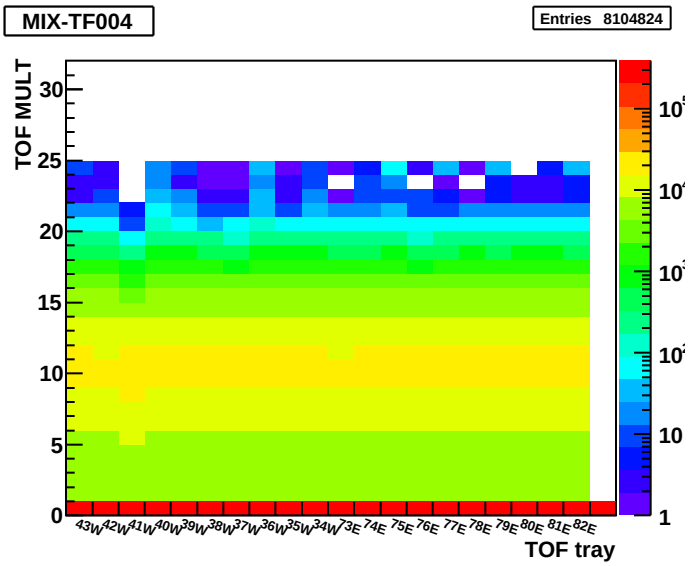

ST201 0-15 (ch6)
Entries 3087552
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

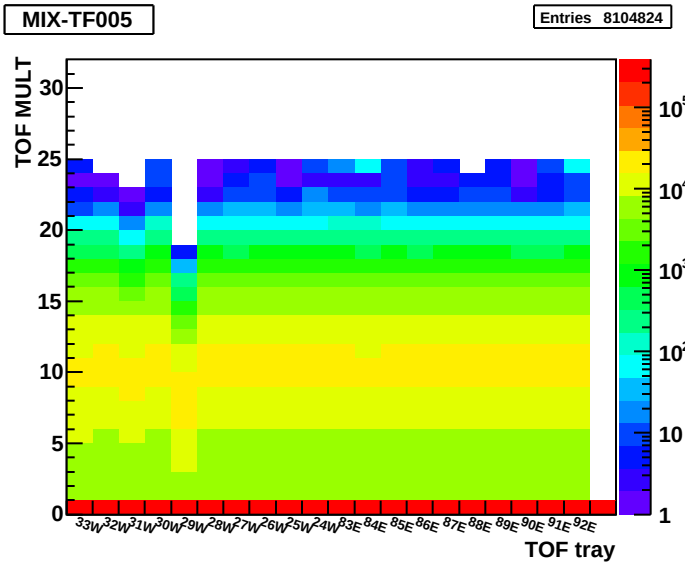

Unused (ch7)
Entries 3087552

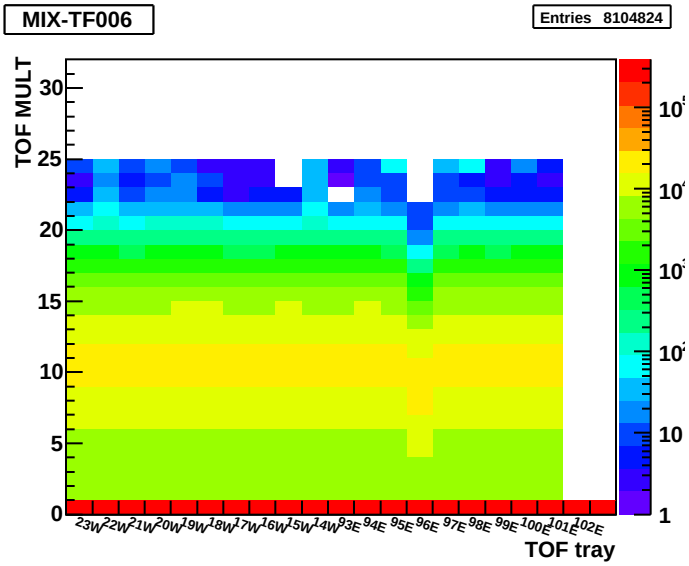

MIX-TF001
Entries 8104824
TOF MULT
TOF tray

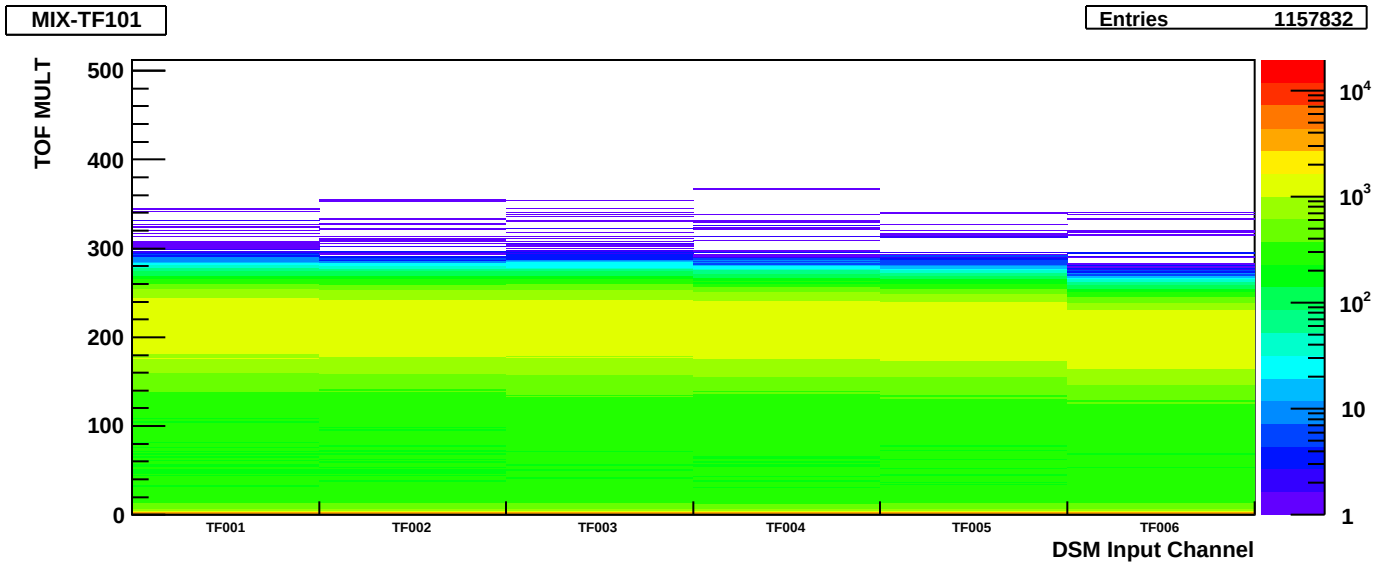

MIX-TF002
Entries 8104824
TOF MULT
TOF tray

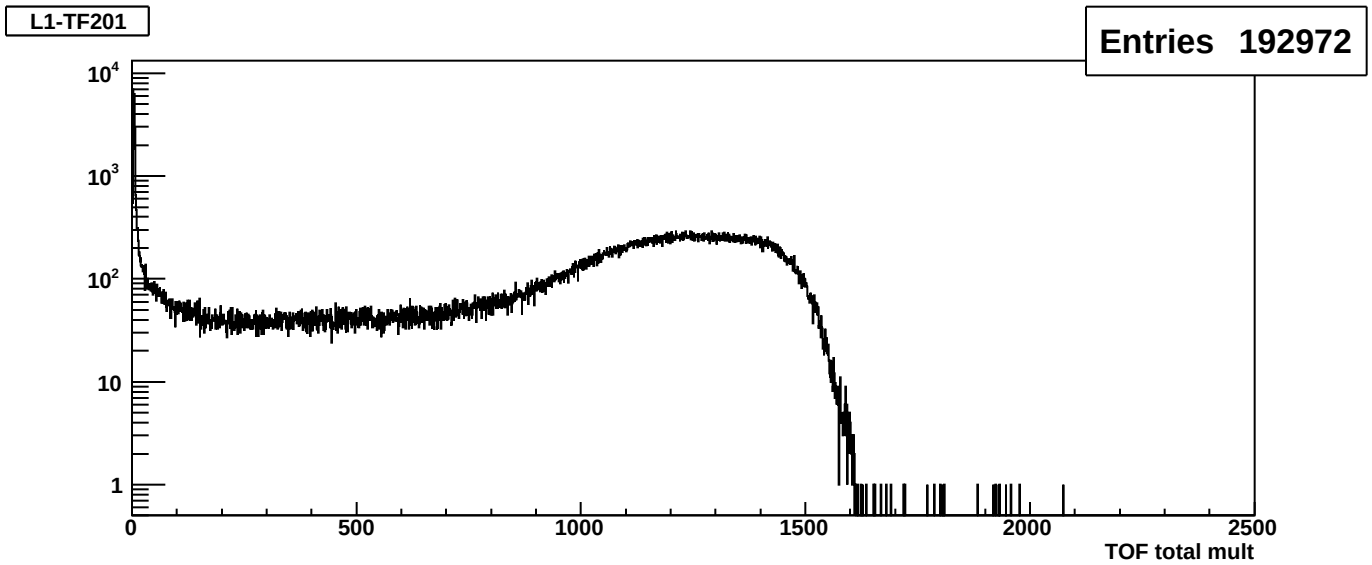

MIX-TF003
Entries 8104824
TOF MULT
TOF tray

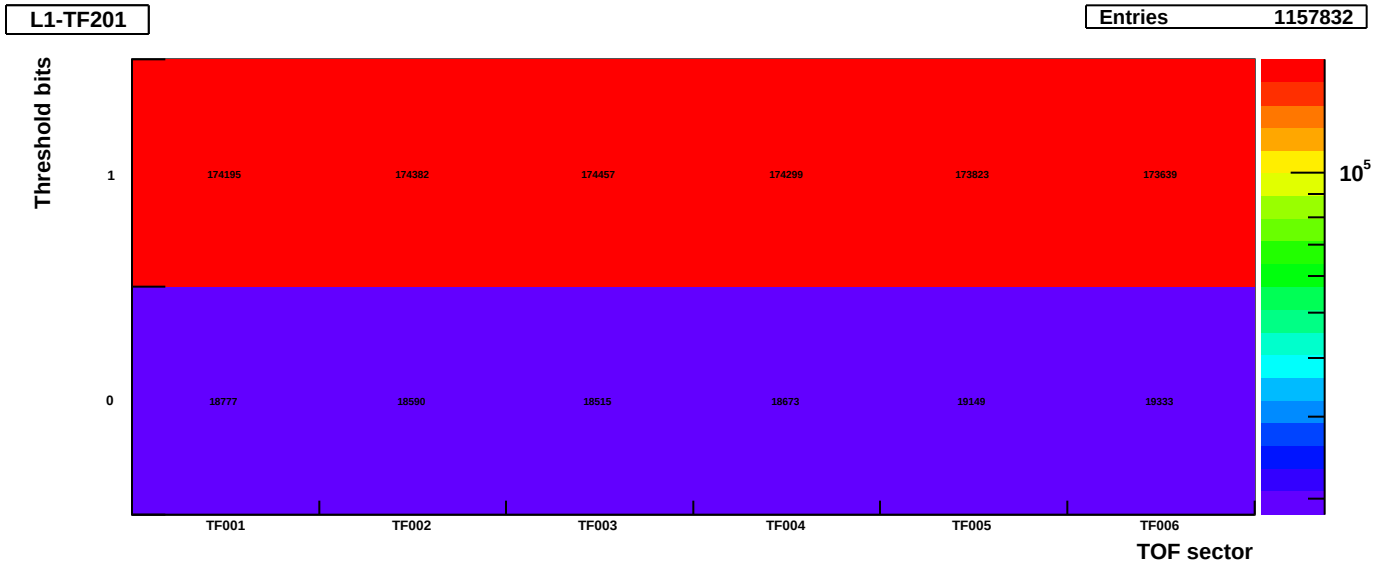

MIX-TF004
Entries 8104824
TOF MULT
TOF tray

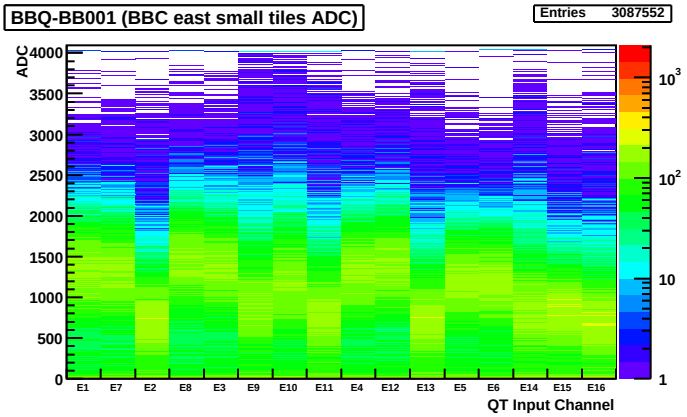

MIX-TF005
Entries 8104824
TOF MULT
TOF tray

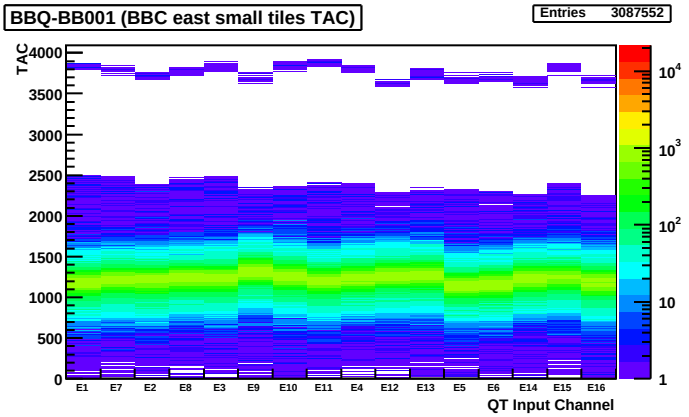

MIX-TF006
Entries 8104824
TOF MULT
TOF tray

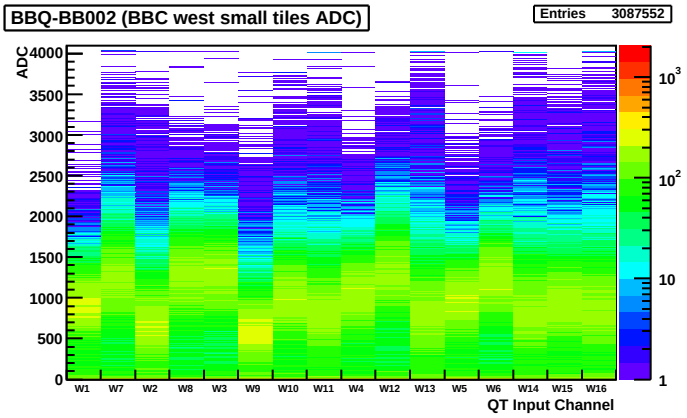

MIX-TF101
Entries 1157832
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

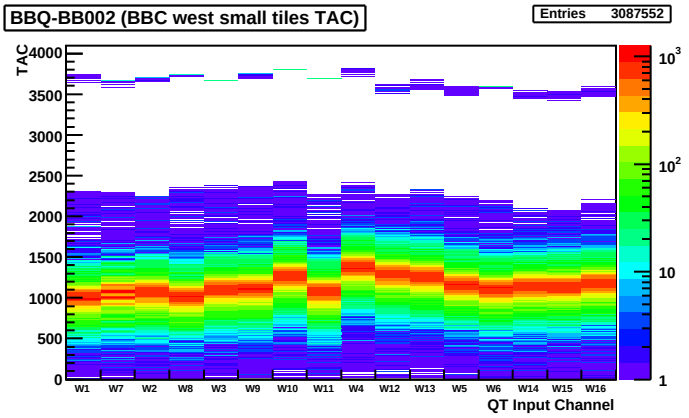

L1-TF201
Entries 192972
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

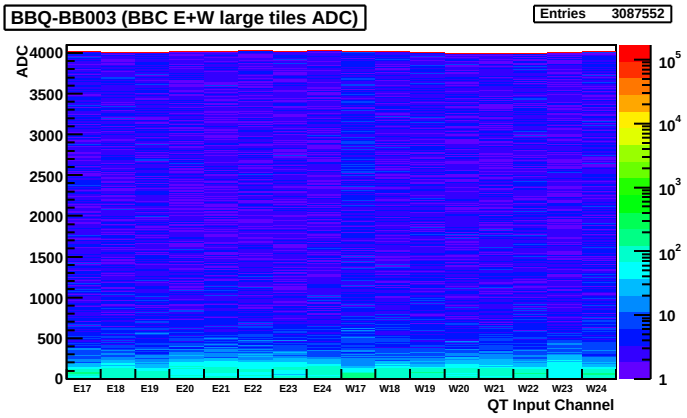

L1-TF201
Entries 1157832
Threshold bits
1
0
174195
174382
174457
174299
173823
173639
18777
18590
18515
18673
19149
19333
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

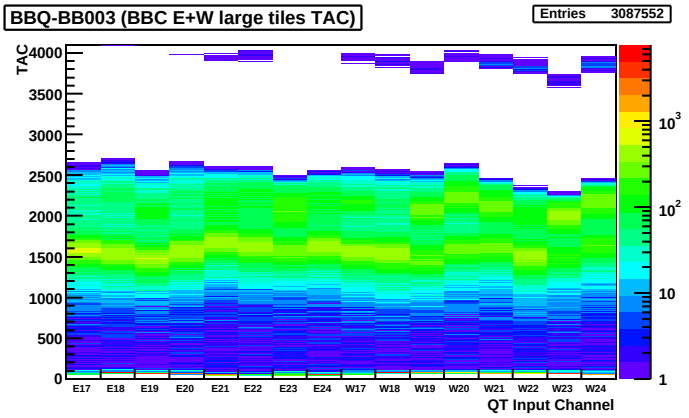

BBQ-BB001 (BBC east small tiles ADC)
Entries 3087552
ADC
QT Input Channel

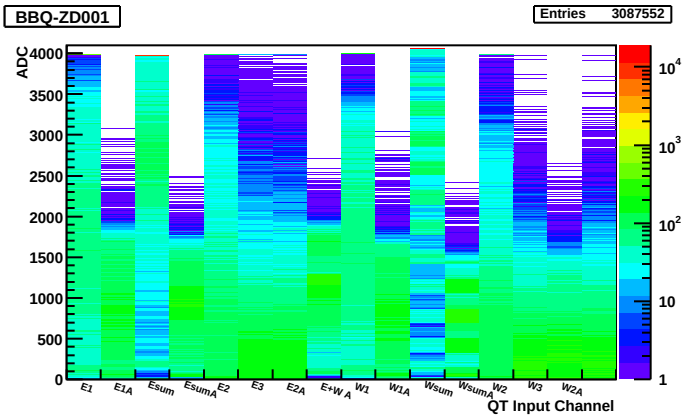

BBQ-BB001 (BBC east small tiles TAC)
Entries 3087552
TAC
QT Input Channel

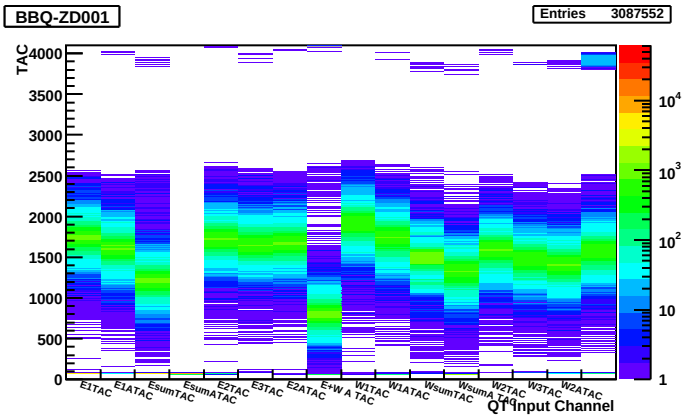

BBQ-BB002 (BBC west small tiles ADC)
Entries 3087552
ADC
QT Input Channel

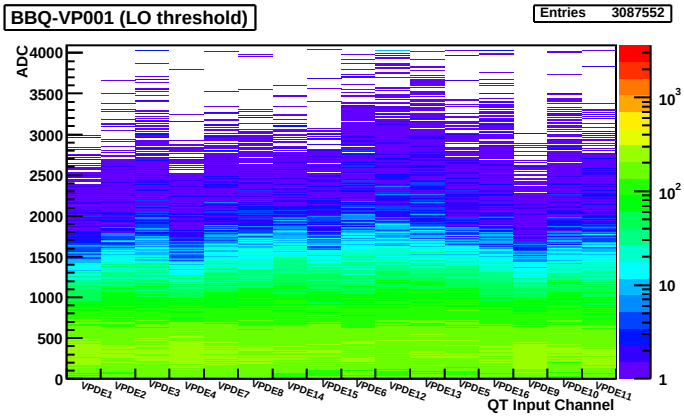

BBQ-BB002 (BBC west small tiles TAC)
Entries 3087552
TAC
QT Input Channel

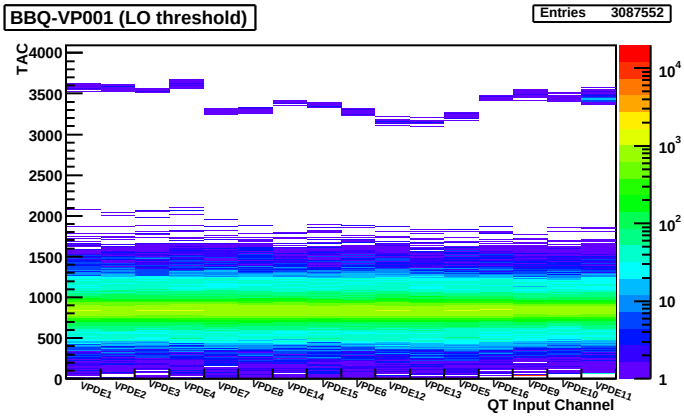

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3087552
ADC
QT Input Channel

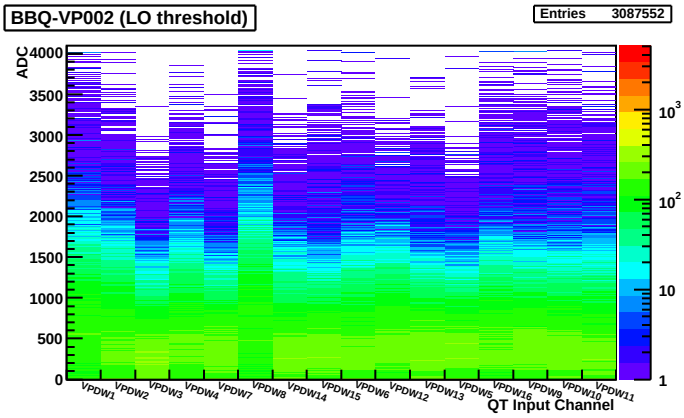

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3087552
TAC
QT Input Channel

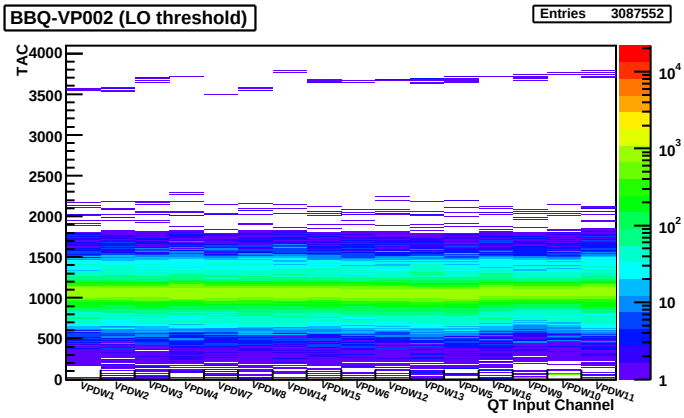

BBQ-ZD001
Entries 3087552
ADC
QT Input Channel

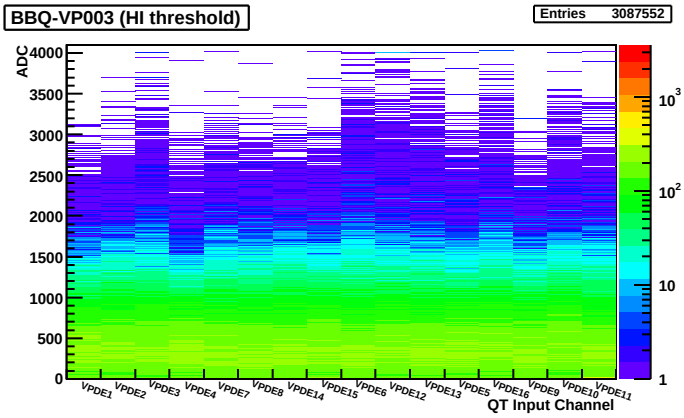

BBQ-ZD001
Entries 3087552
TAC
QT Input Channel

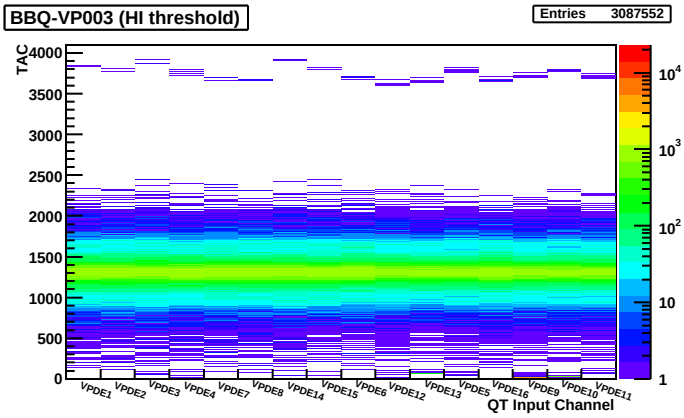

BBQ-VP001 (LO threshold)
Entries 3087552
ADC
QT Input Channel

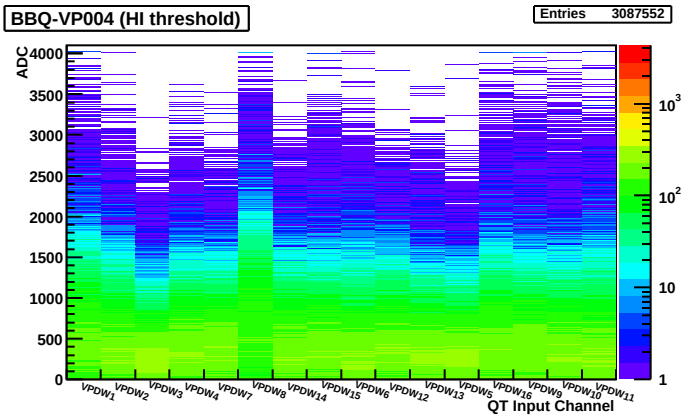

BBQ-VP001 (LO threshold)
Entries 3087552
TAC
QT Input Channel

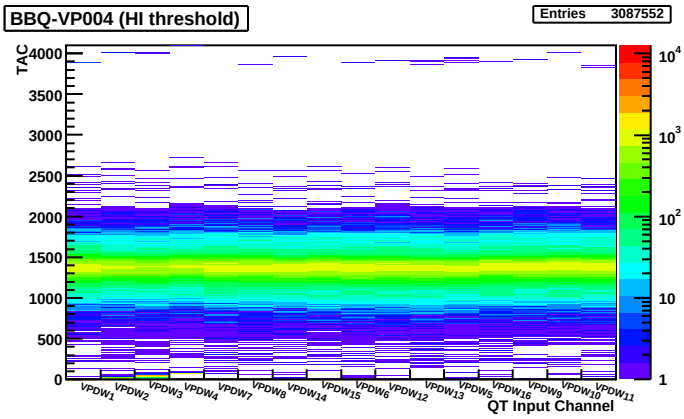

BBQ-VP002 (LO threshold)
Entries 3087552
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 3087552
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 3087552
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 3087552
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 3087552
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 3087552
TAC
QT Input Channel