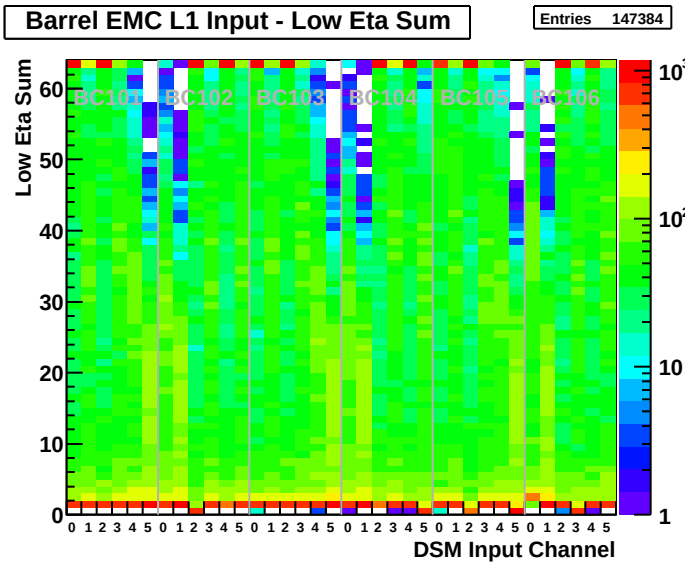
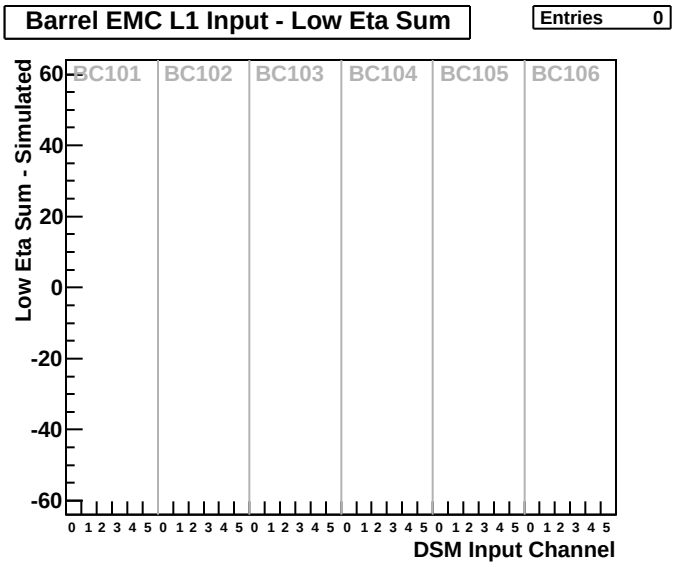
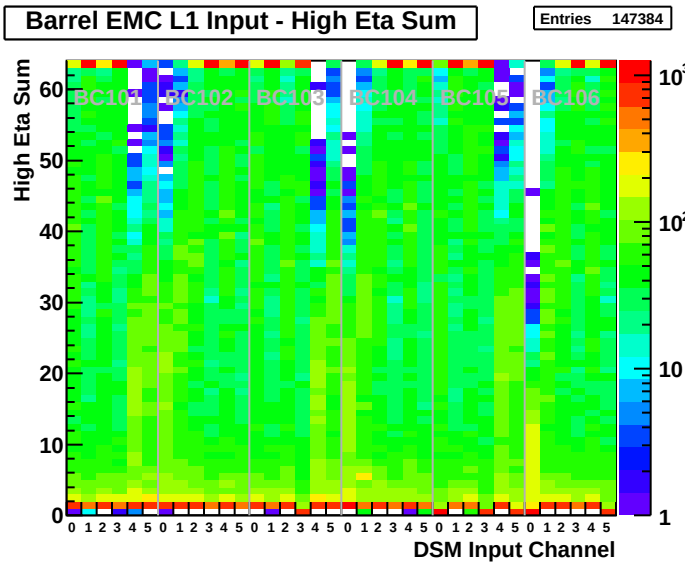
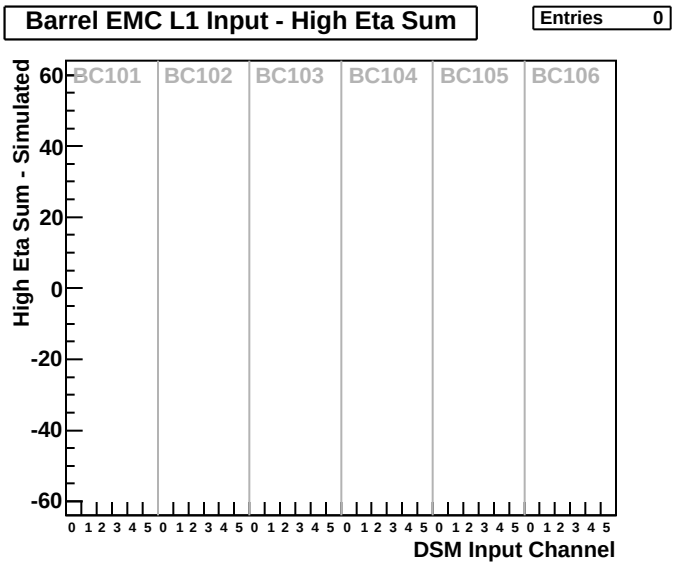
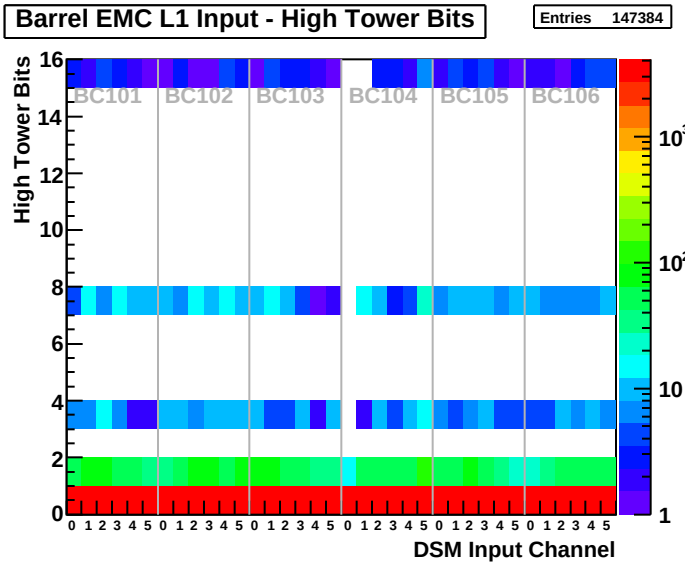
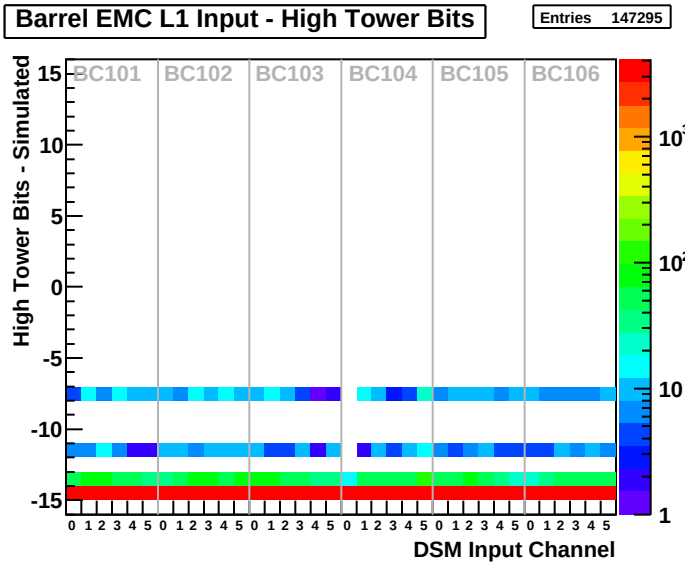

Barrel EMC L1 Input - Low Eta Sum
Entries 147384
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

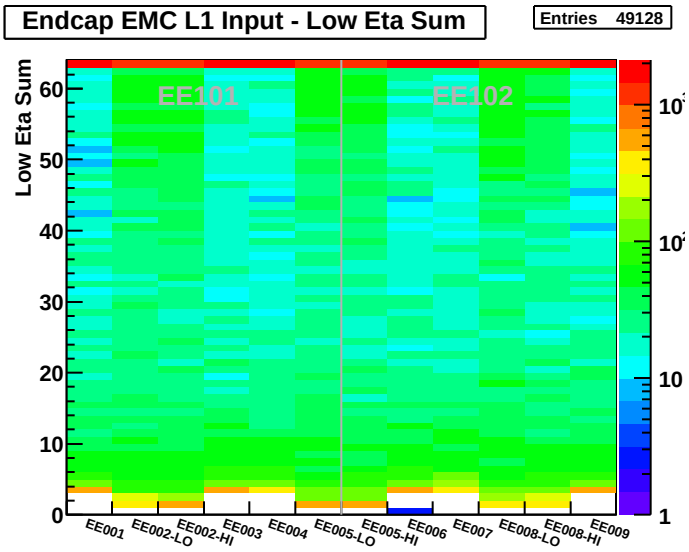

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

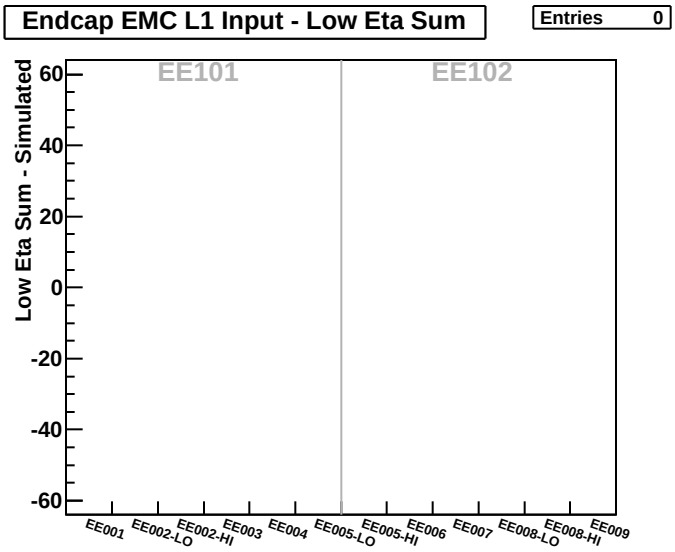

Barrel EMC L1 Input - High Eta Sum
Entries 147384
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

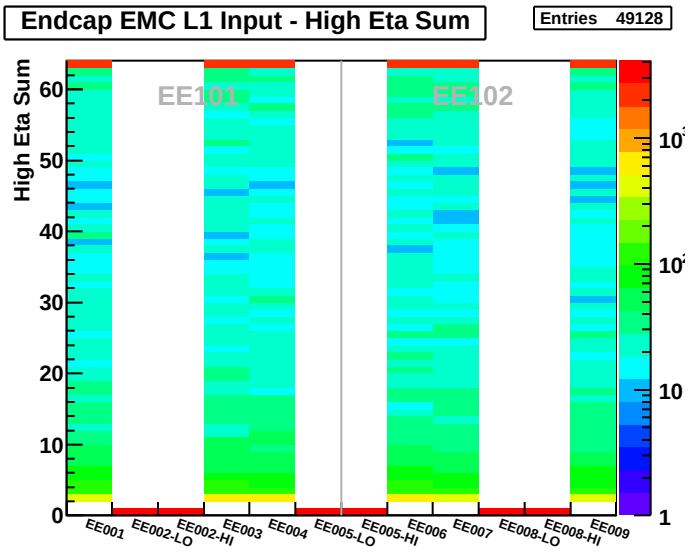

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

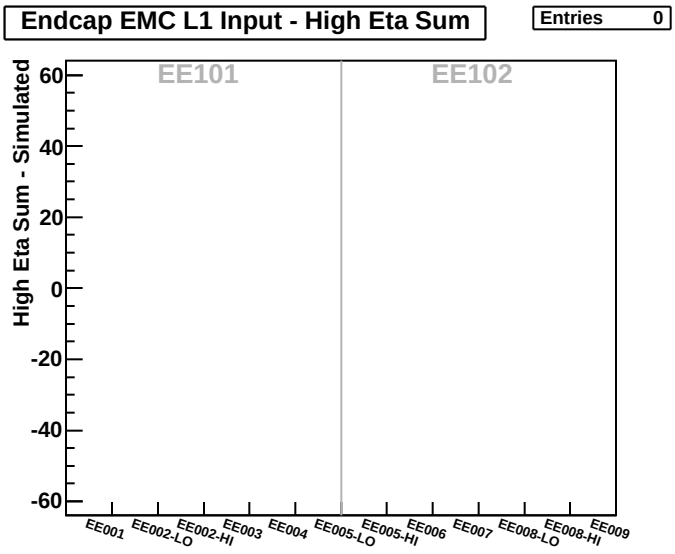

Barrel EMC L1 Input - High Tower Bits
Entries 147384
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

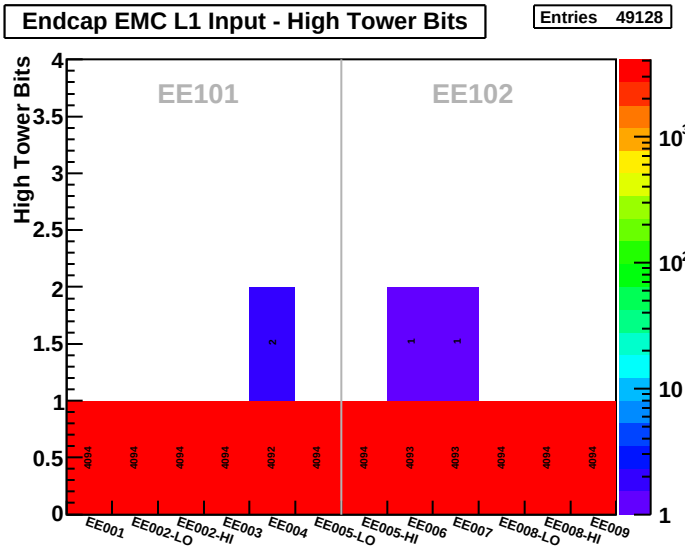

Barrel EMC L1 Input - High Tower Bits
Entries 147295
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

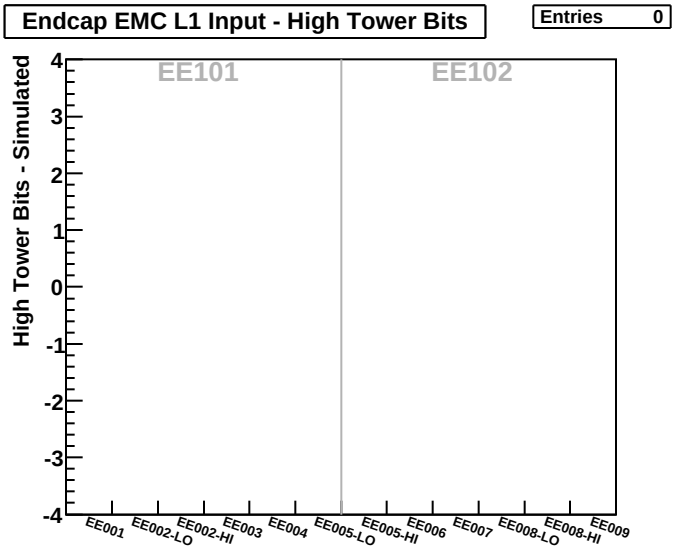

Endcap EMC L1 Input - Low Eta Sum
Entries 49128
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

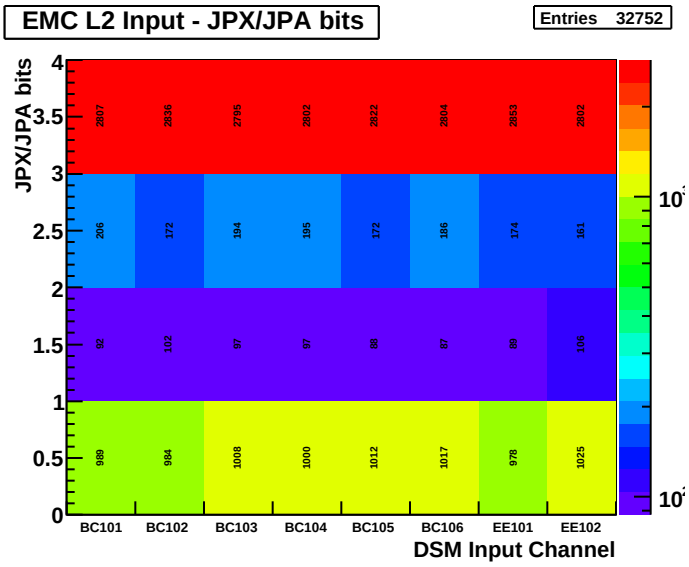

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

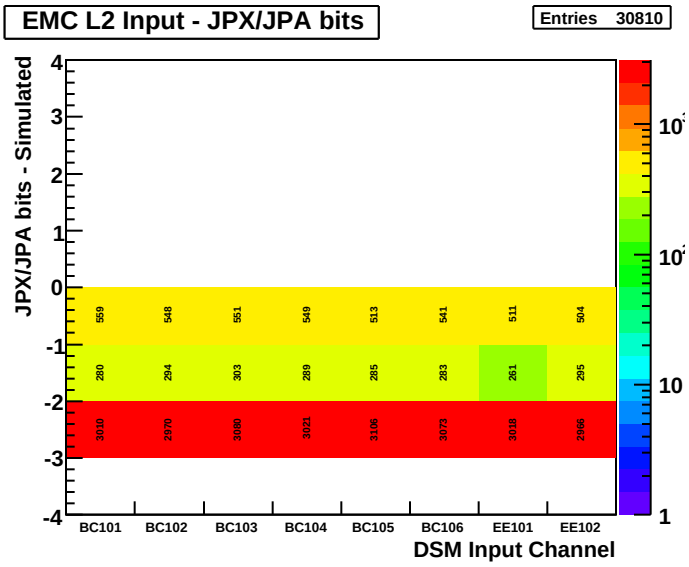

Endcap EMC L1 Input - High Eta Sum
Entries 49128
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

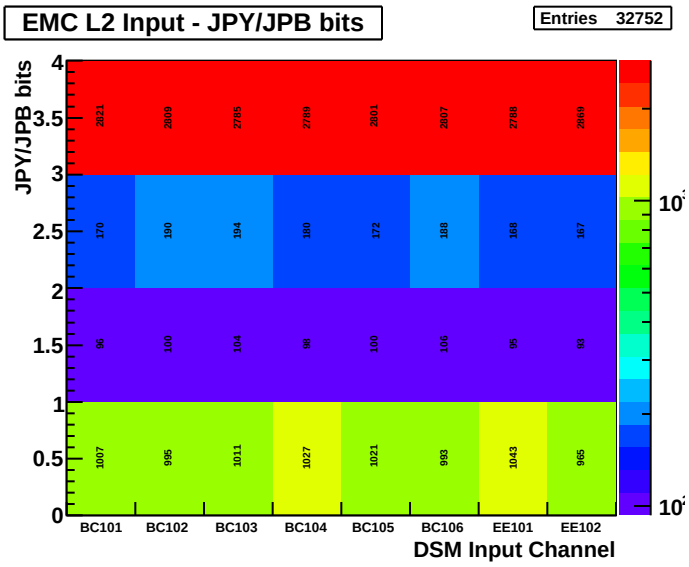

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

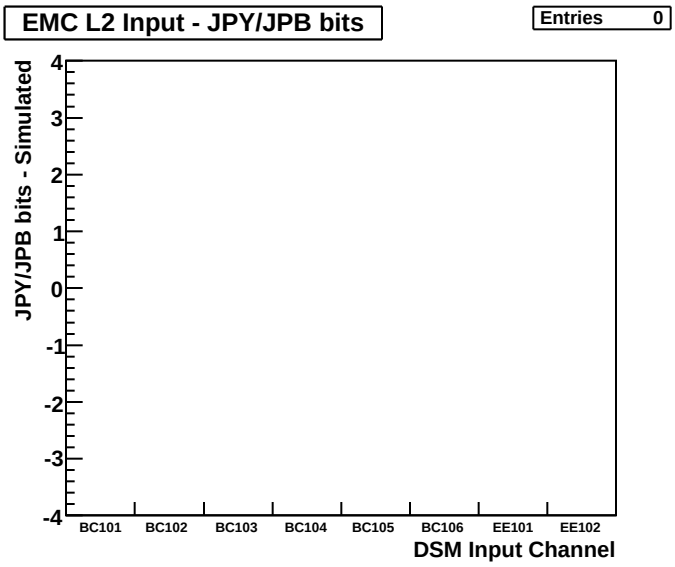

Endcap EMC L1 Input - High Tower Bits
Entries 49128
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

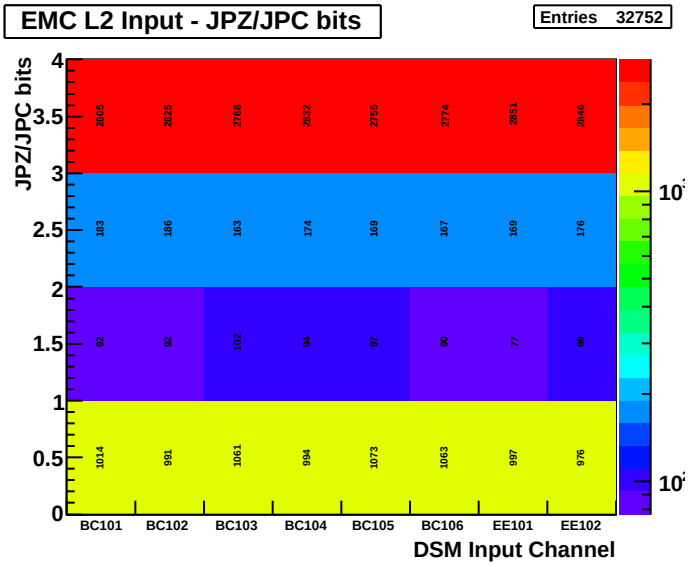

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

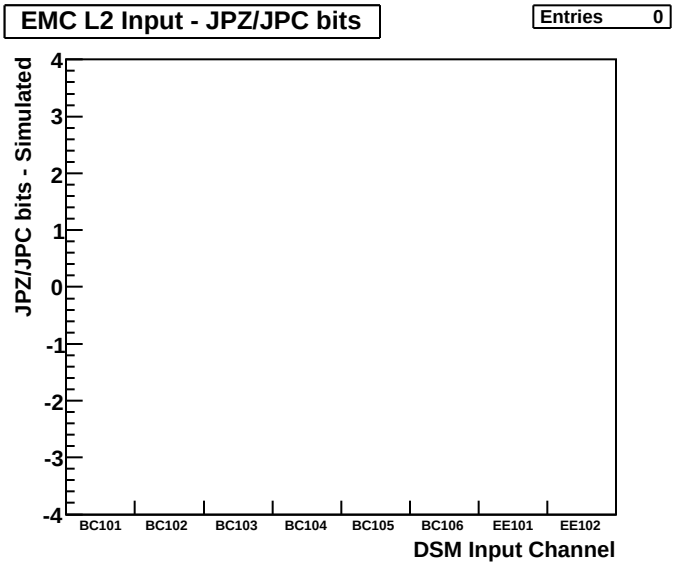

EMC L2 Input - JPX/JPA bits
Entries 32752
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2807 2836 2795 2802 2822 2804 2853 2802
206 172 194 195 172 186 174 161
92 102 97 97 88 87 89 106
989 984 1008 1000 1012 1017 978 1025

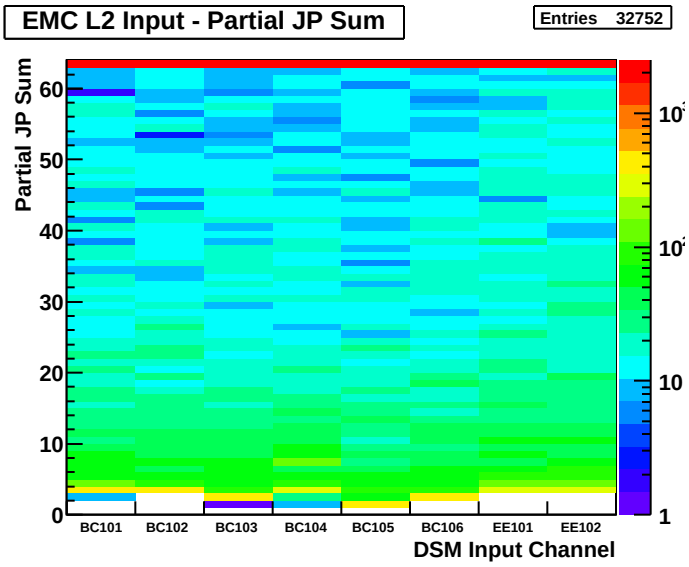

EMC L2 Input - JPX/JPA bits
Entries 30810
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
559 548 551 549 513 541 511 504
280 294 303 289 285 283 261 295
3010 2970 3060 3021 3106 3073 3018 2966

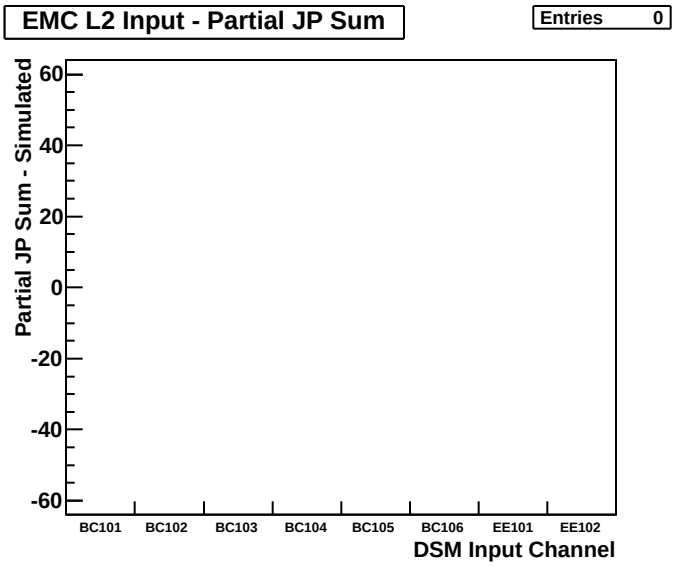

EMC L2 Input - JPY/JPB bits
Entries 32752
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2821 2809 2785 2789 2801 2807 2788 2869
170 190 194 180 172 188 168 167
96 100 104 98 100 106 95 93
1007 995 1011 1027 1021 993 1043 965

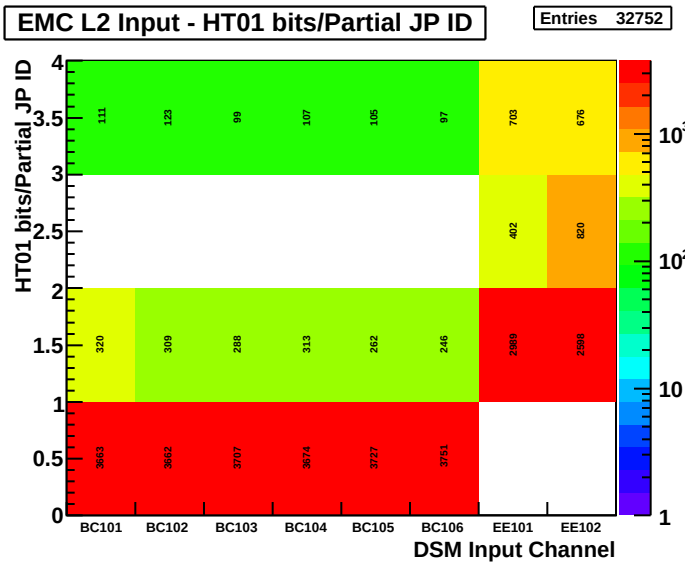

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

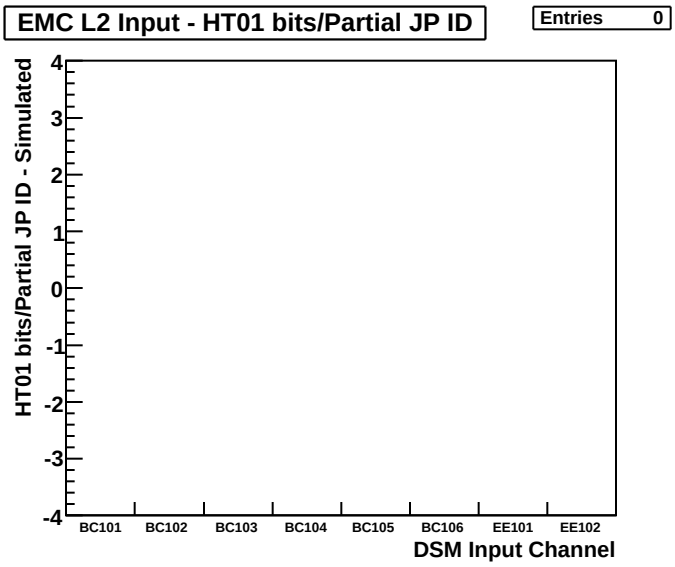

EMC L2 Input - JPZ/JPC bits
Entries 32752
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2805 2825 2768 2832 2755 2774 2851 2846
183 186 163 174 169 167 169 176
92 92 102 94 97 90 77 96
1014 991 1061 994 1073 1063 997 976

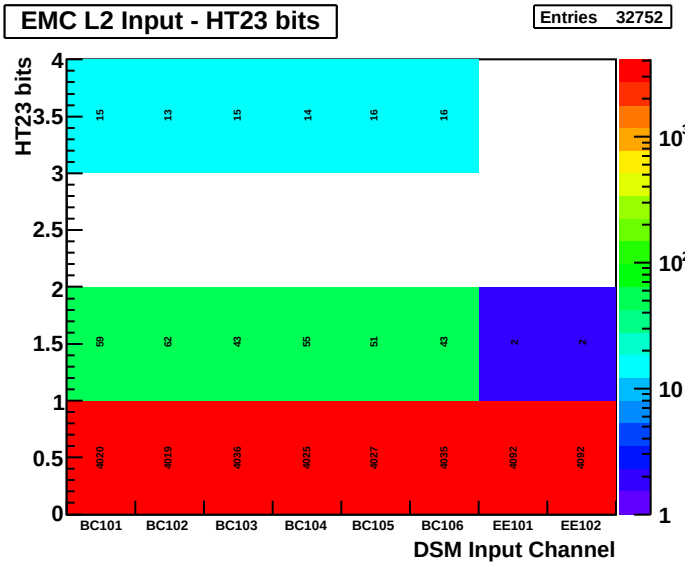

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

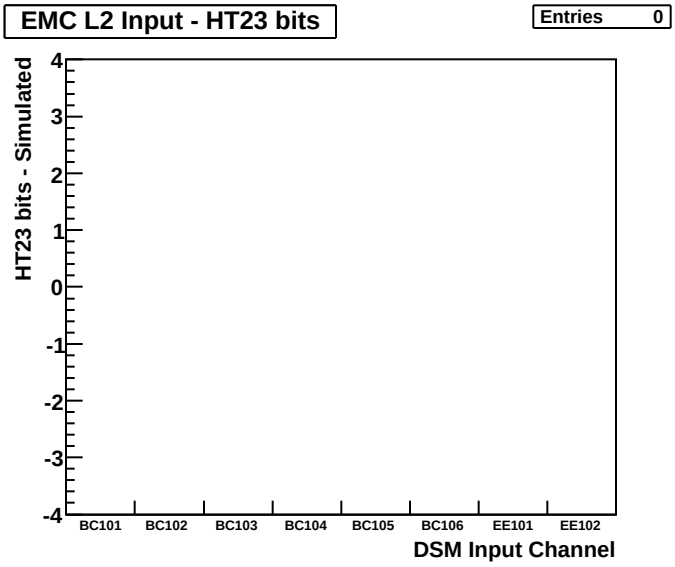

EMC L2 Input - Partial JP Sum
Entries 32752
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

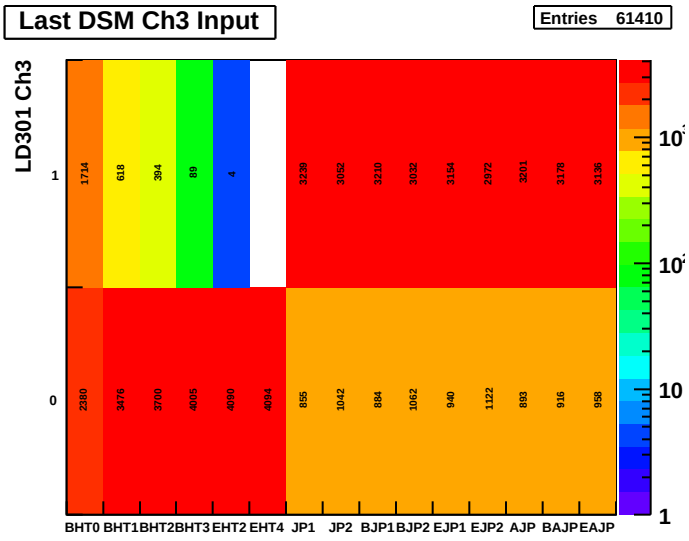

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

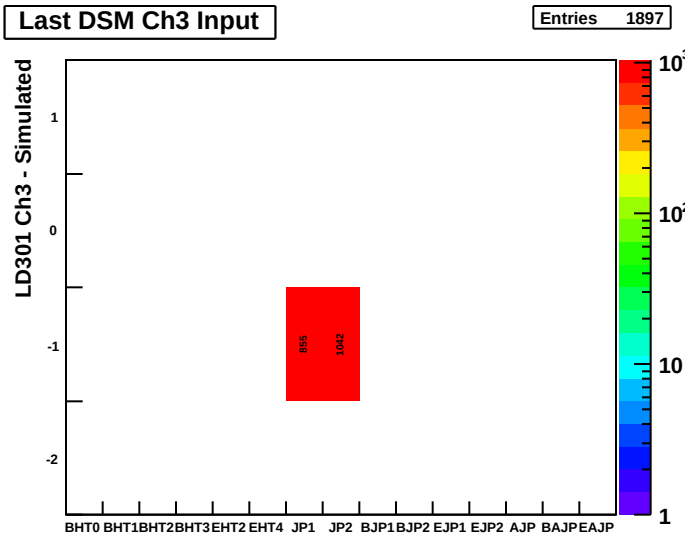

EMC L2 Input - HT01 bits/Partial JP ID
Entries 32752
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

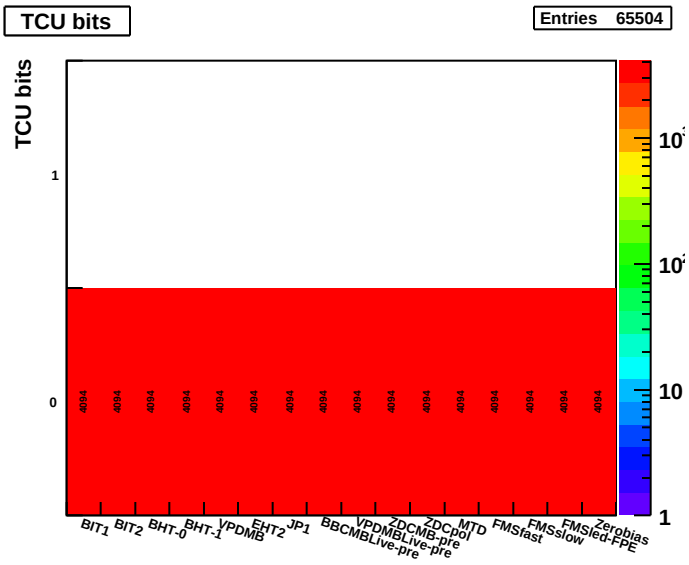

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

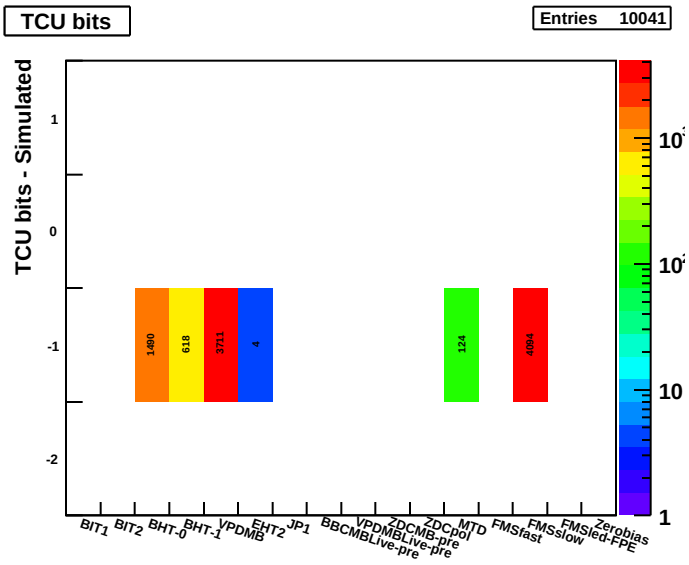

EMC L2 Input - HT23 bits
Entries 32752
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

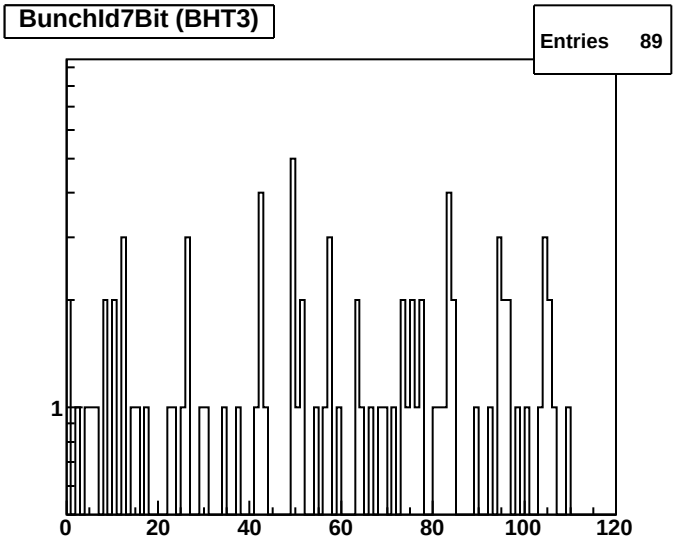

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

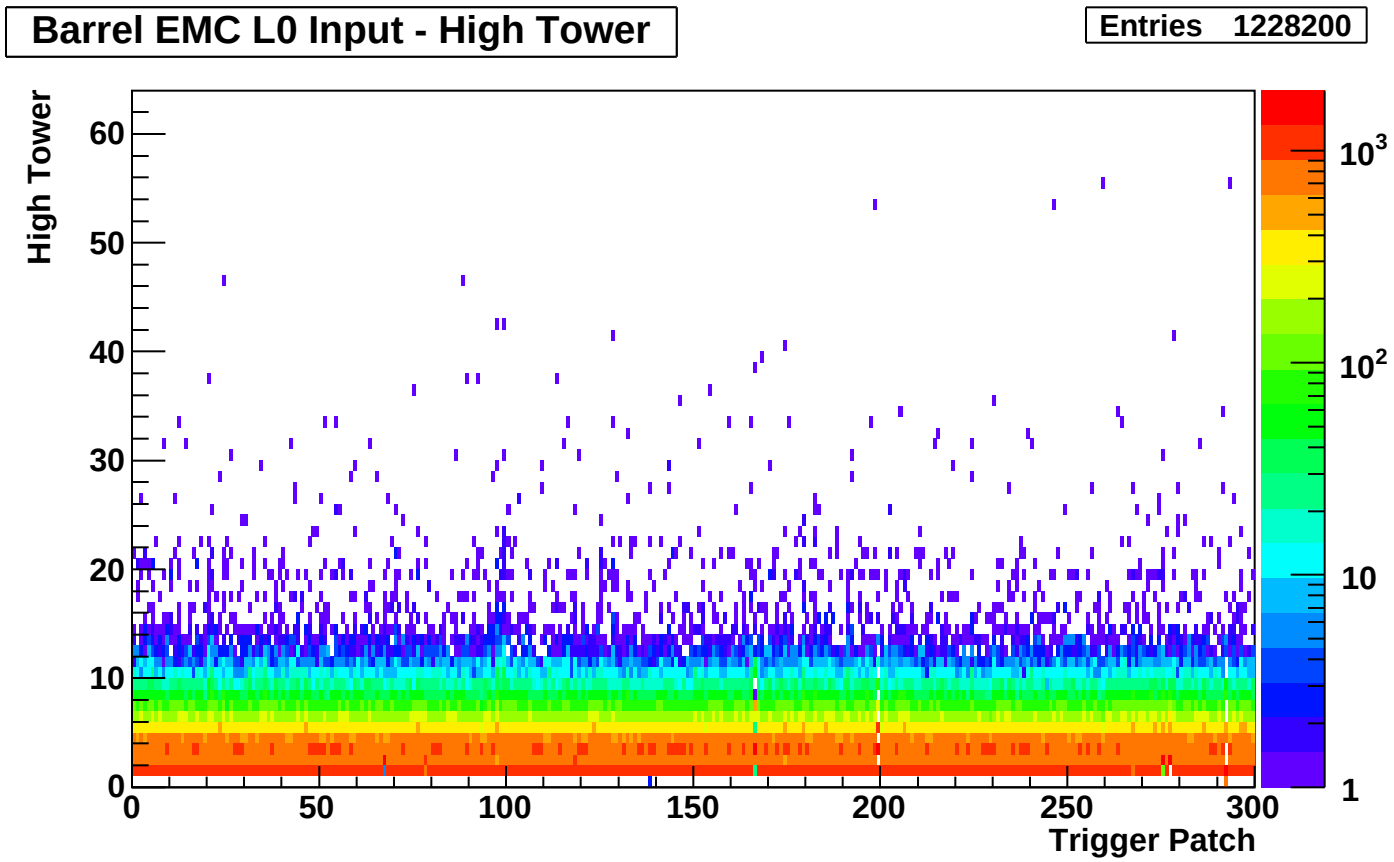

Last DSM Ch3 Input
Entries 61410
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

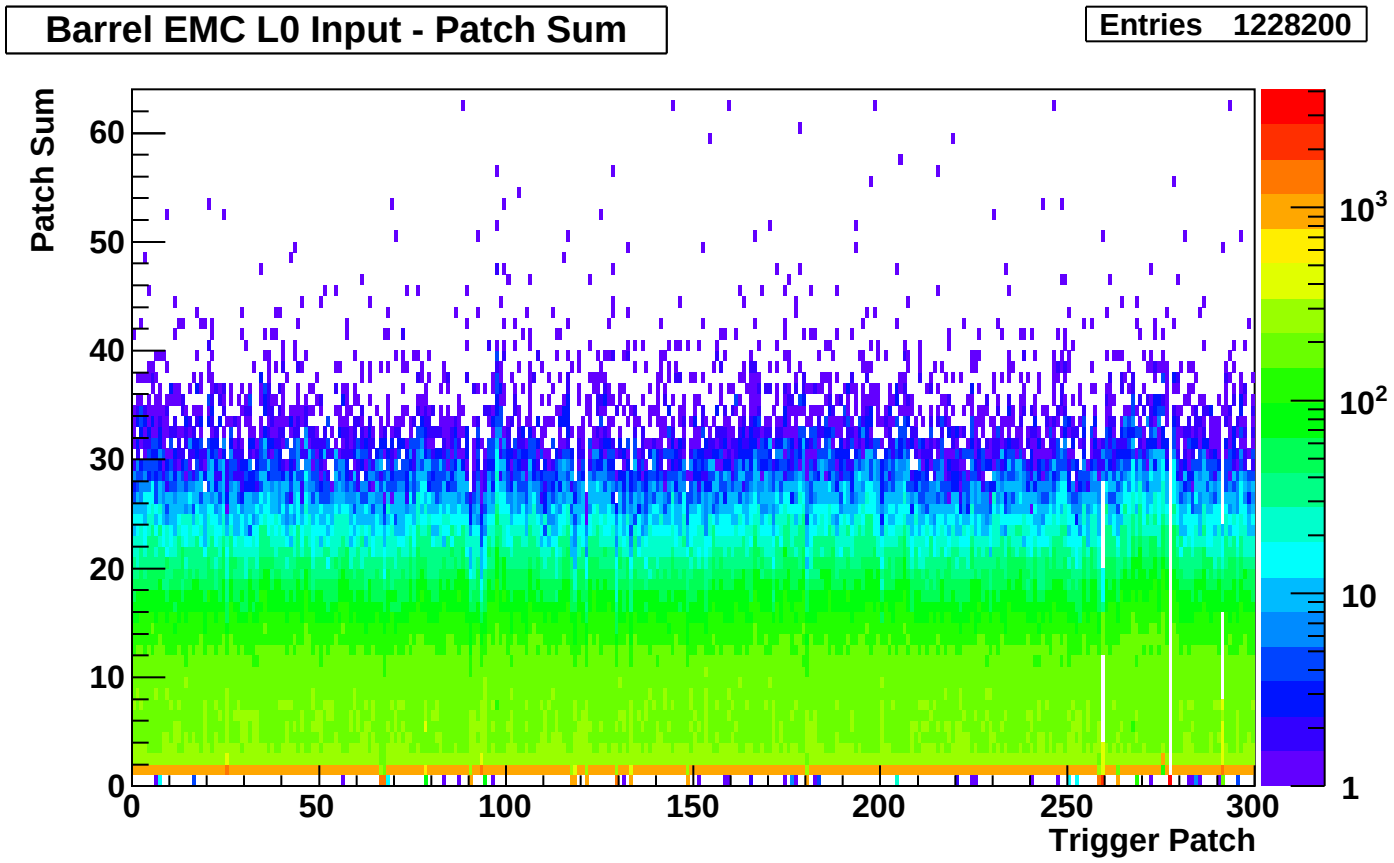

Last DSM Ch3 Input
Entries 1897
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

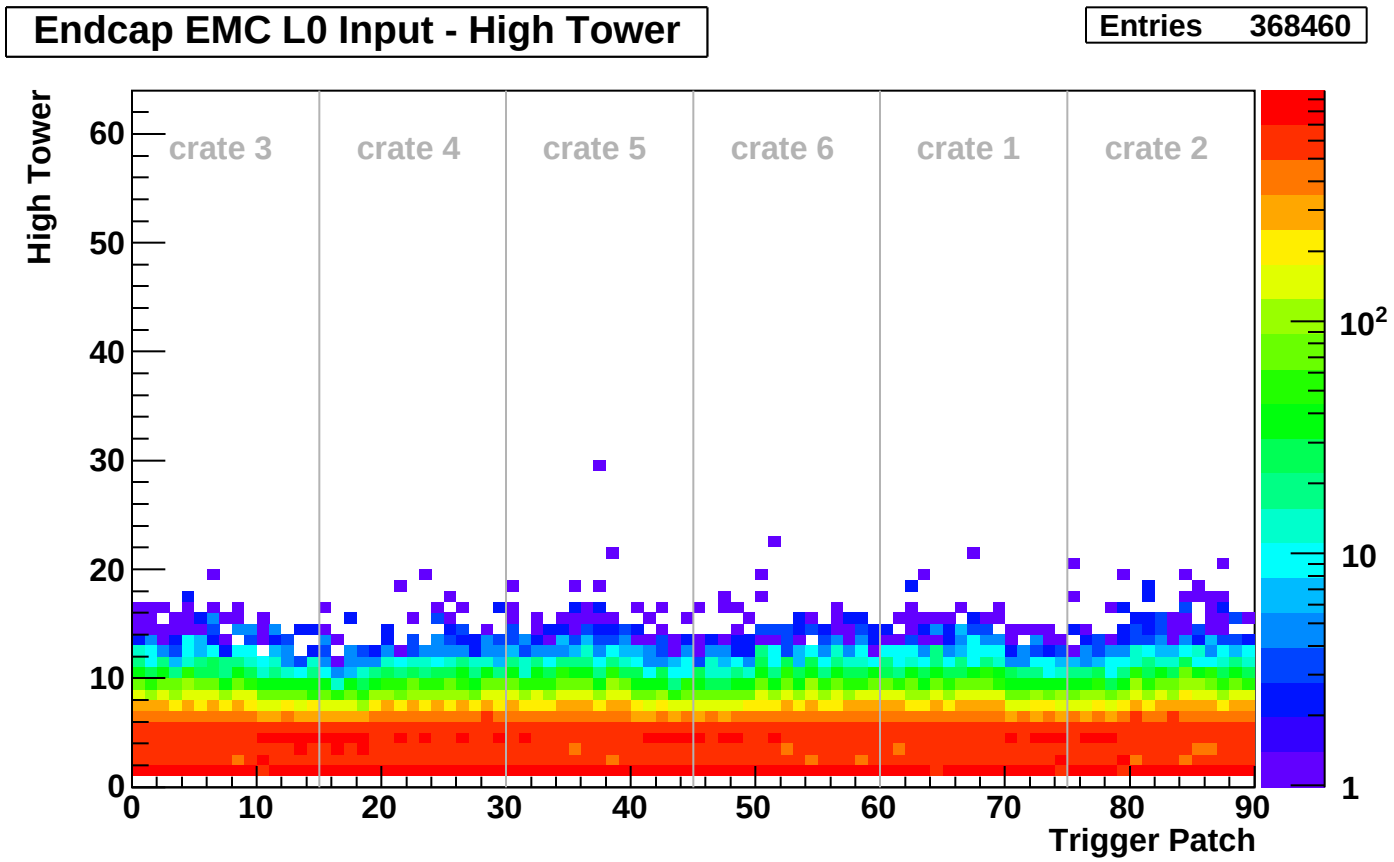

TCU bits
Entries 65504
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

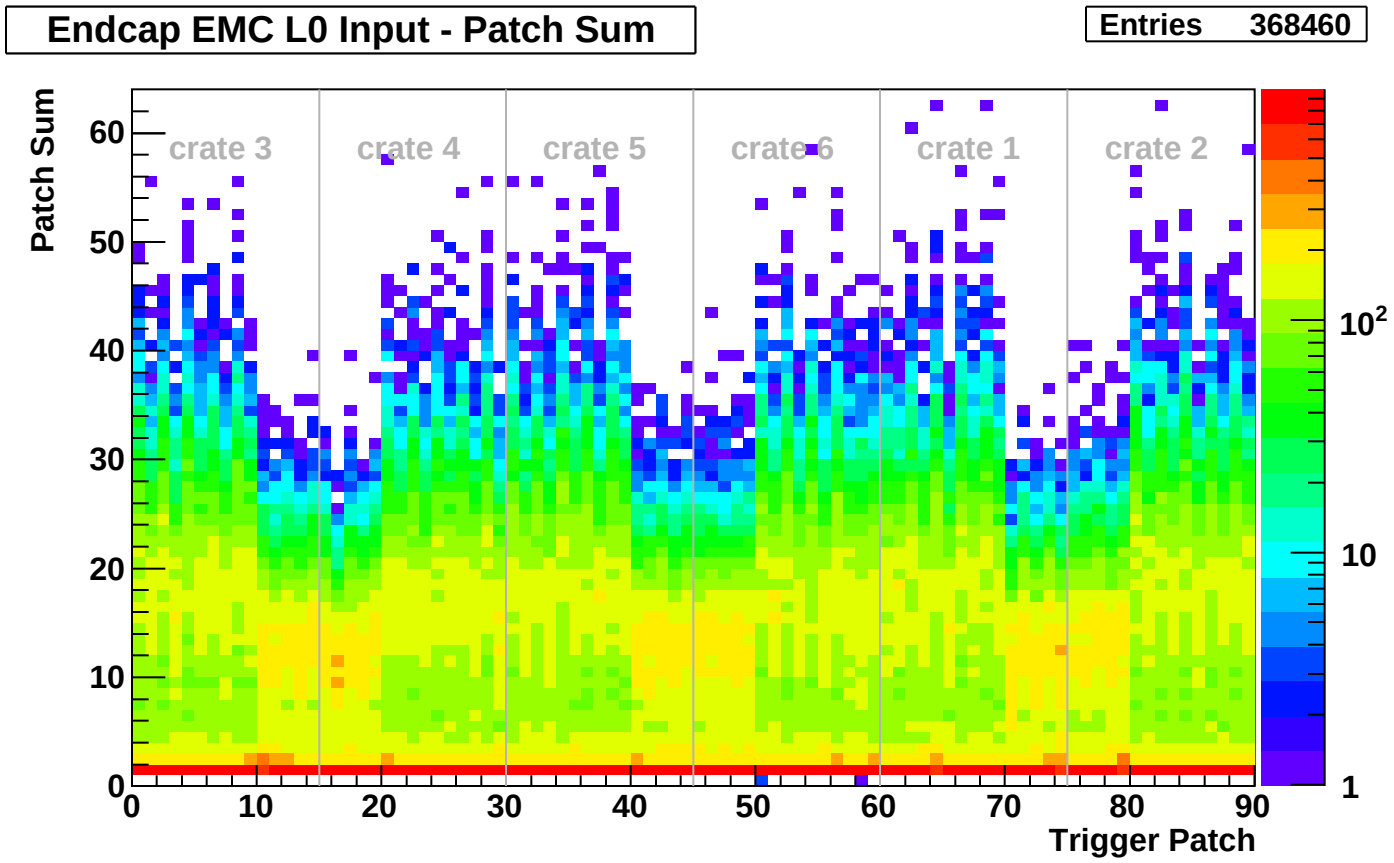

TCU bits
Entries 10041
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

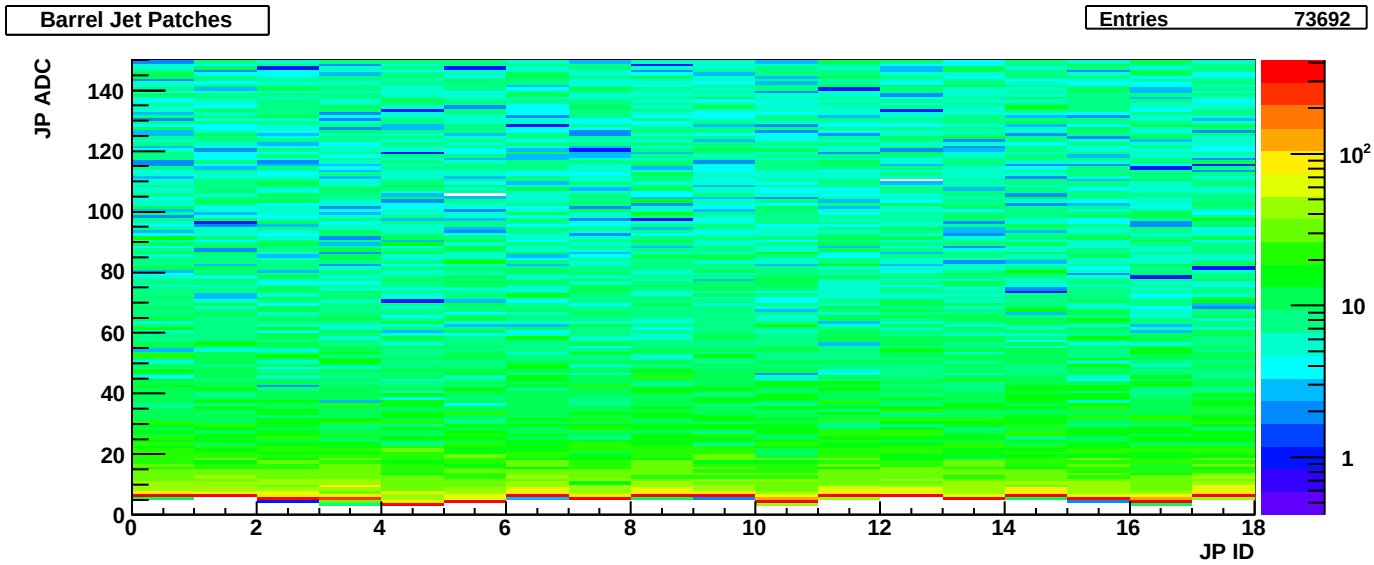

BunchId7Bit (BHT3)
Entries 89

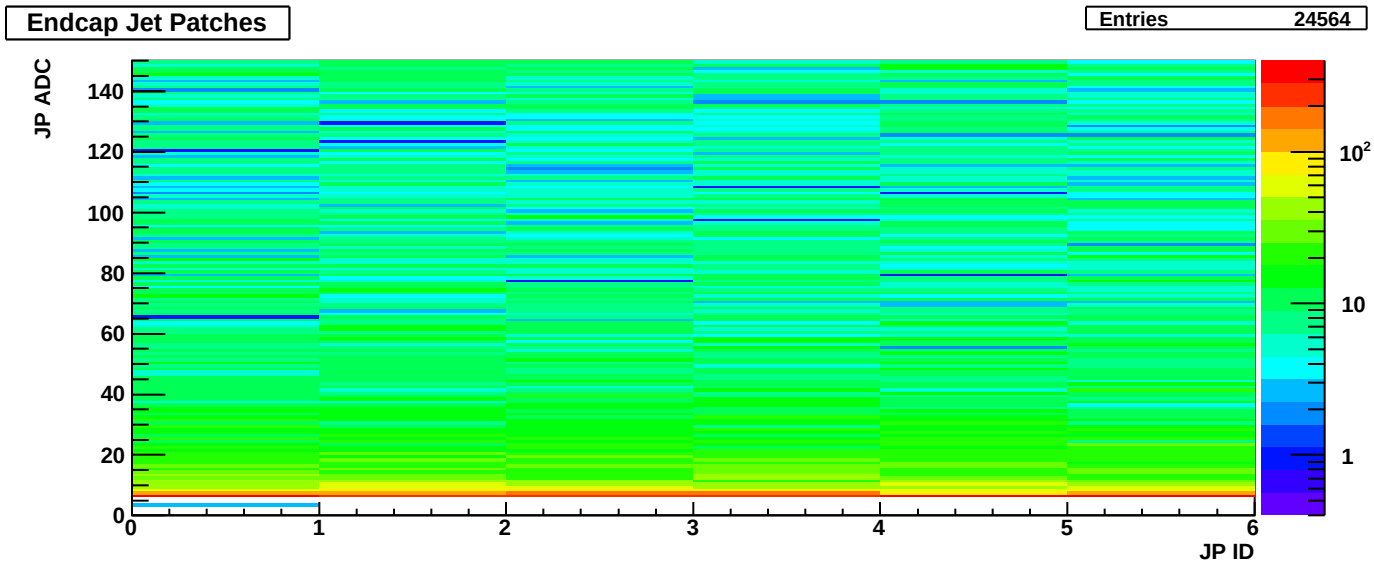

Barrel EMC L0 Input - High Tower
Entries 1228200
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

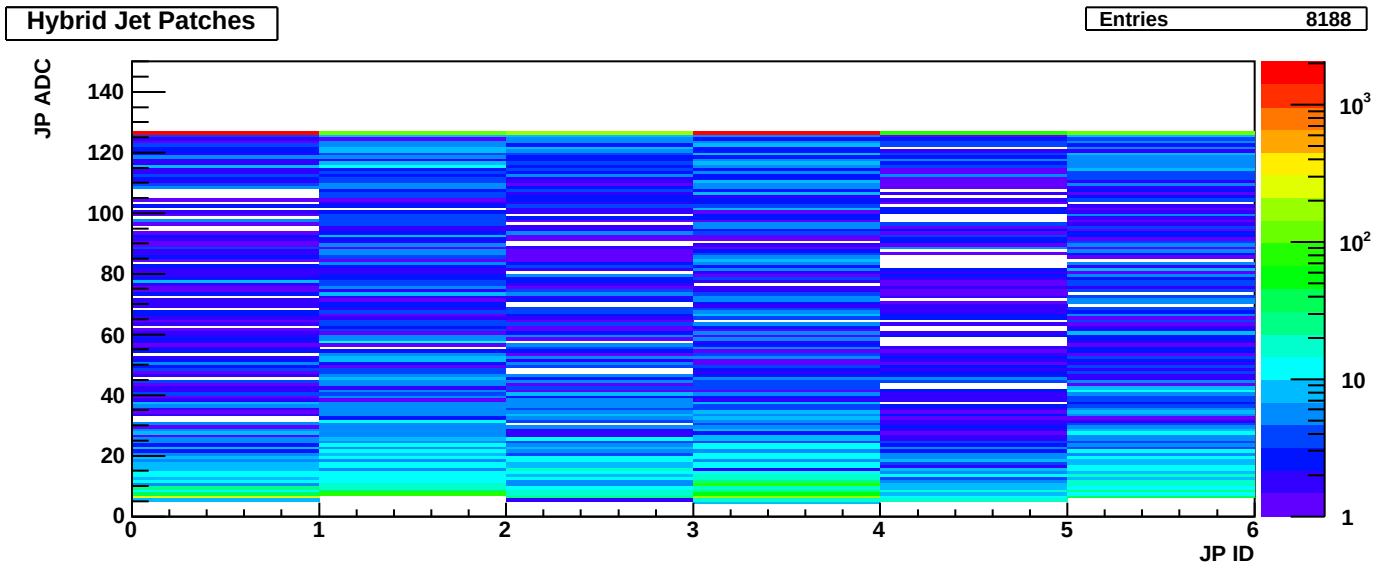

Barrel EMC L0 Input - Patch Sum
Entries 1228200
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

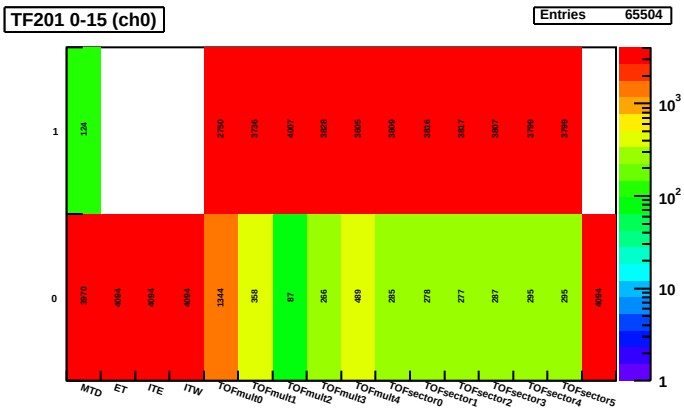

Endcap EMC L0 Input - High Tower
Entries 368460
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2

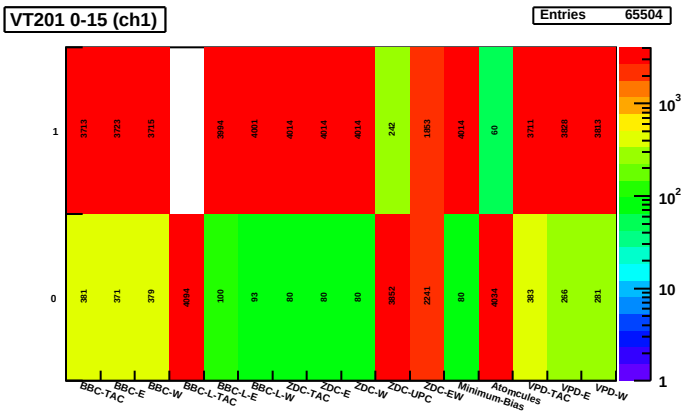

Endcap EMC L0 Input - Patch Sum
Entries 368460
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2

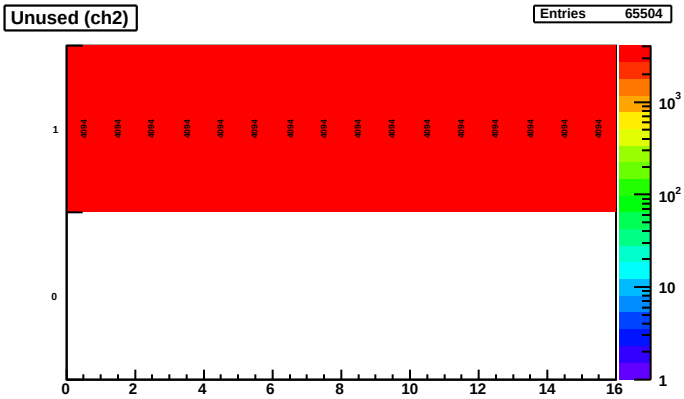

Barrel Jet Patches
Entries 73692
JP ADC
JP ID

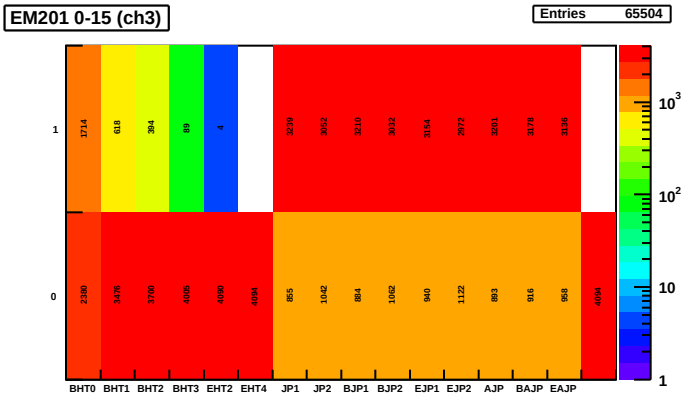

Endcap Jet Patches
Entries 24564
JP ADC
JP ID

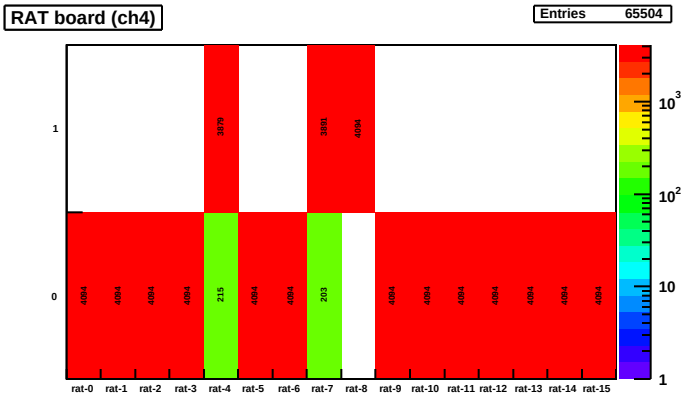

Hybrid Jet Patches
Entries 8188
JP ADC
JP ID

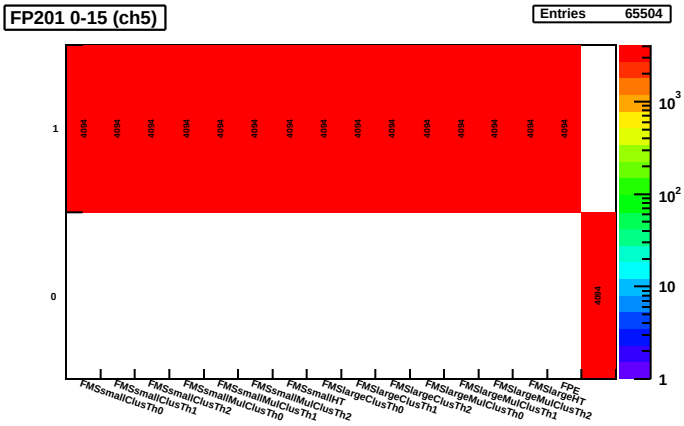

TF201 0-15 (ch0)
Entries 65504
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

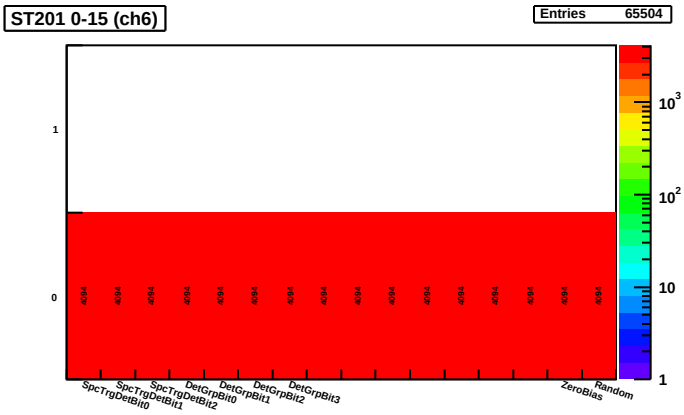

VT201 0-15 (ch1)
Entries 65504
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

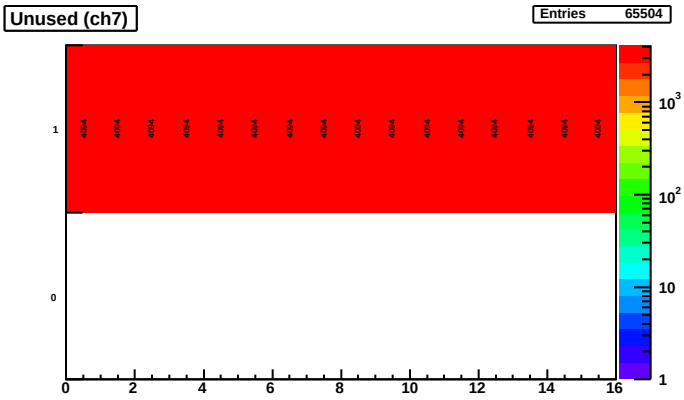

Unused (ch2)
Entries 65504

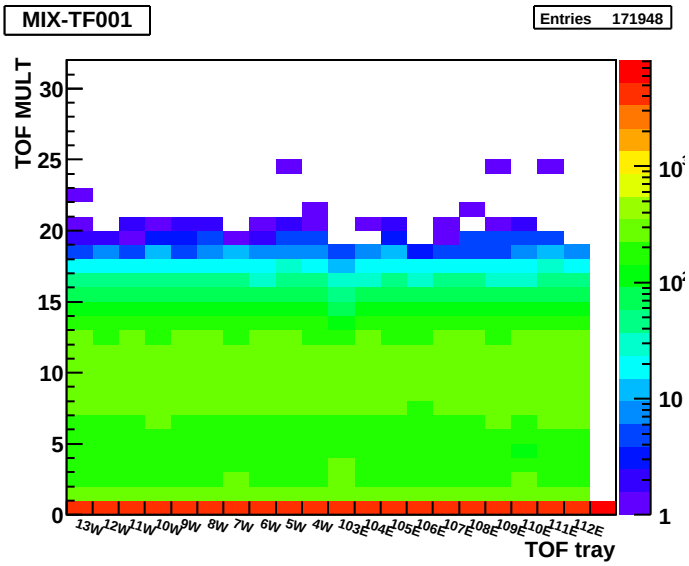

EM201 0-15 (ch3)
Entries 65504
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

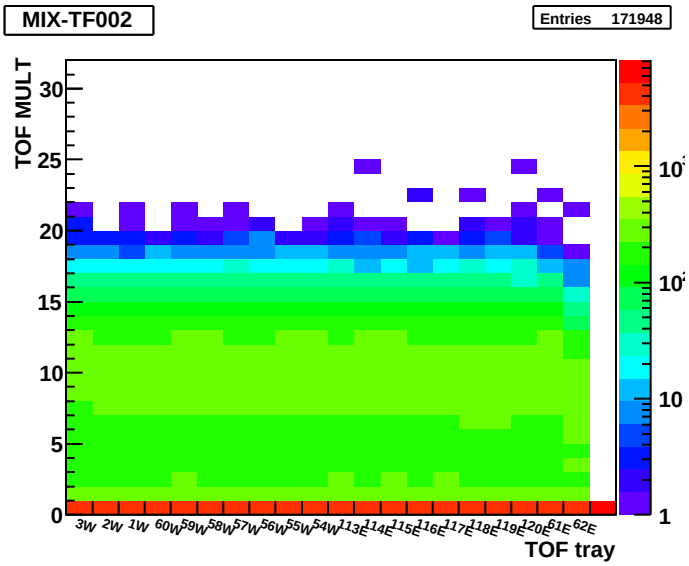

RAT board (ch4)
Entries 65504
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

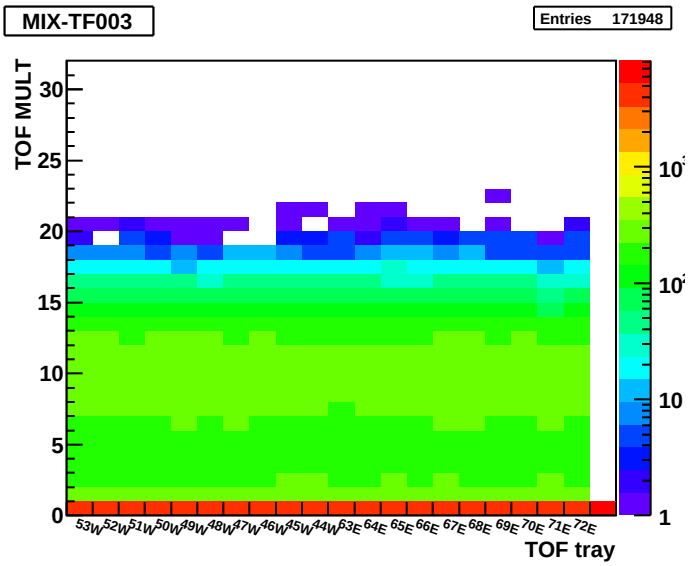

FP201 0-15 (ch5)
Entries 65504
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

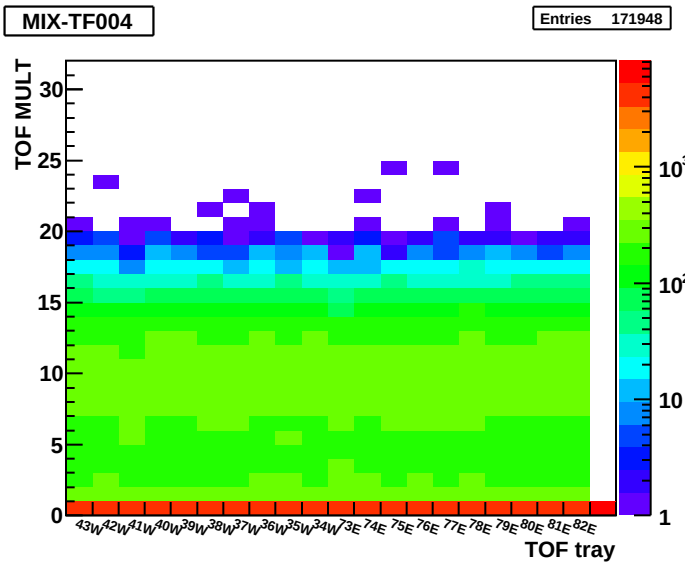

ST201 0-15 (ch6)
Entries 65504
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

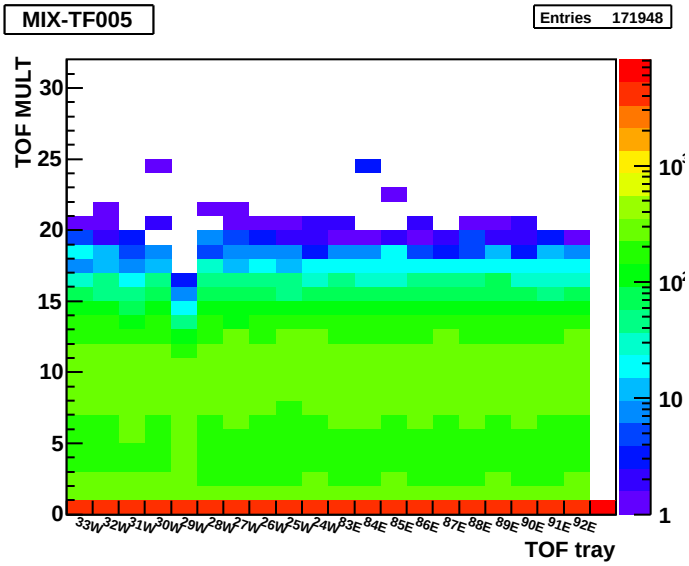

Unused (ch7)
Entries 65504

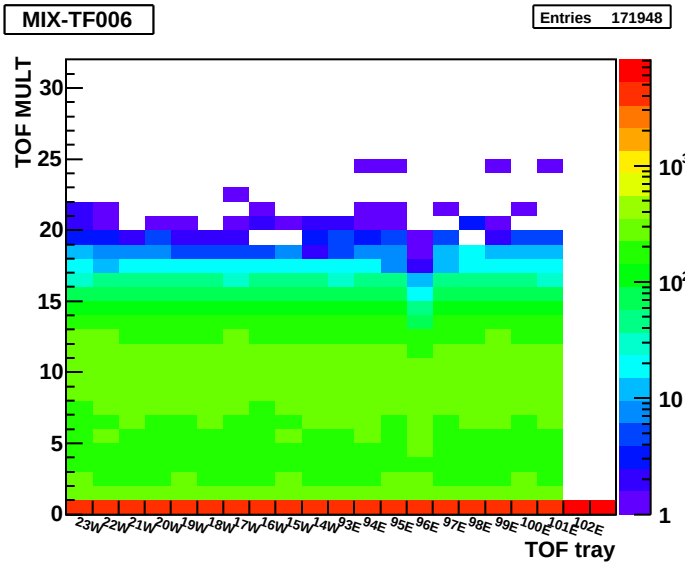

MIX-TF001
Entries 171948
TOF MULT
TOF tray

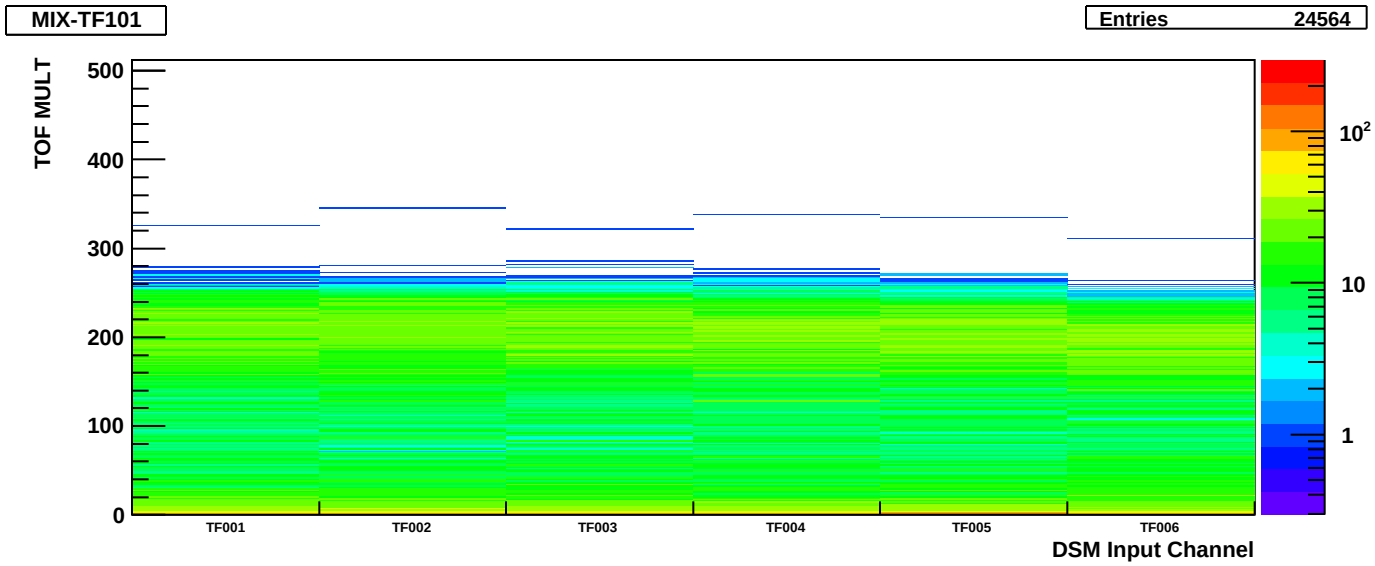

MIX-TF002
Entries 171948
TOF MULT
TOF tray

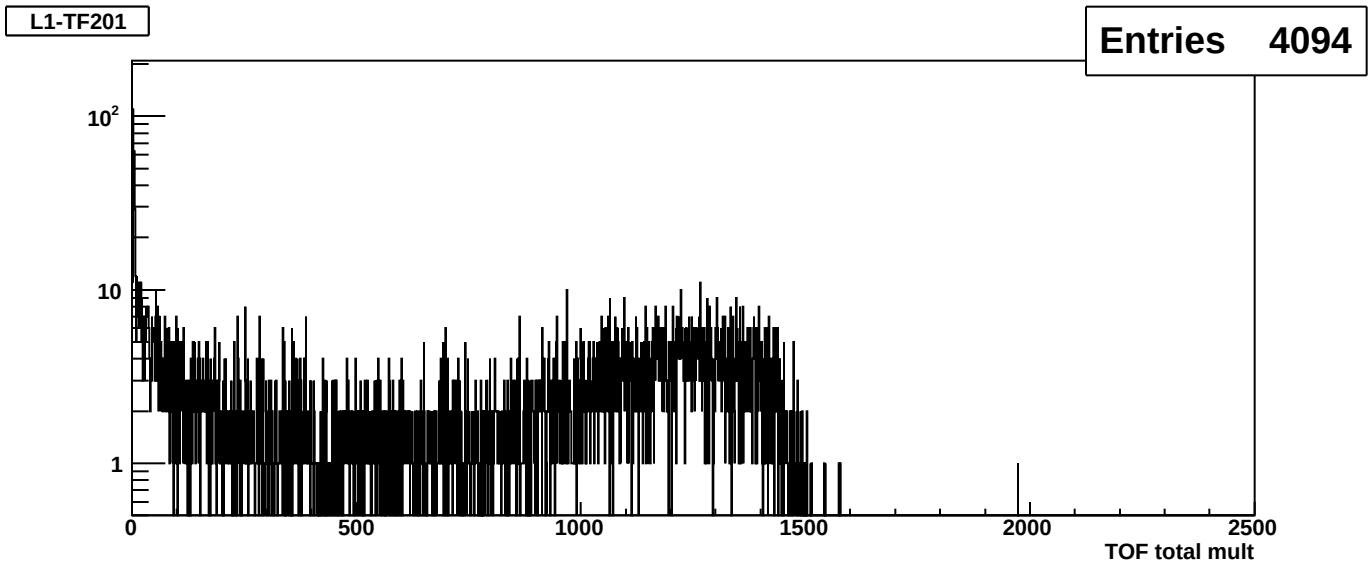

MIX-TF003
Entries 171948
TOF MULT
TOF tray

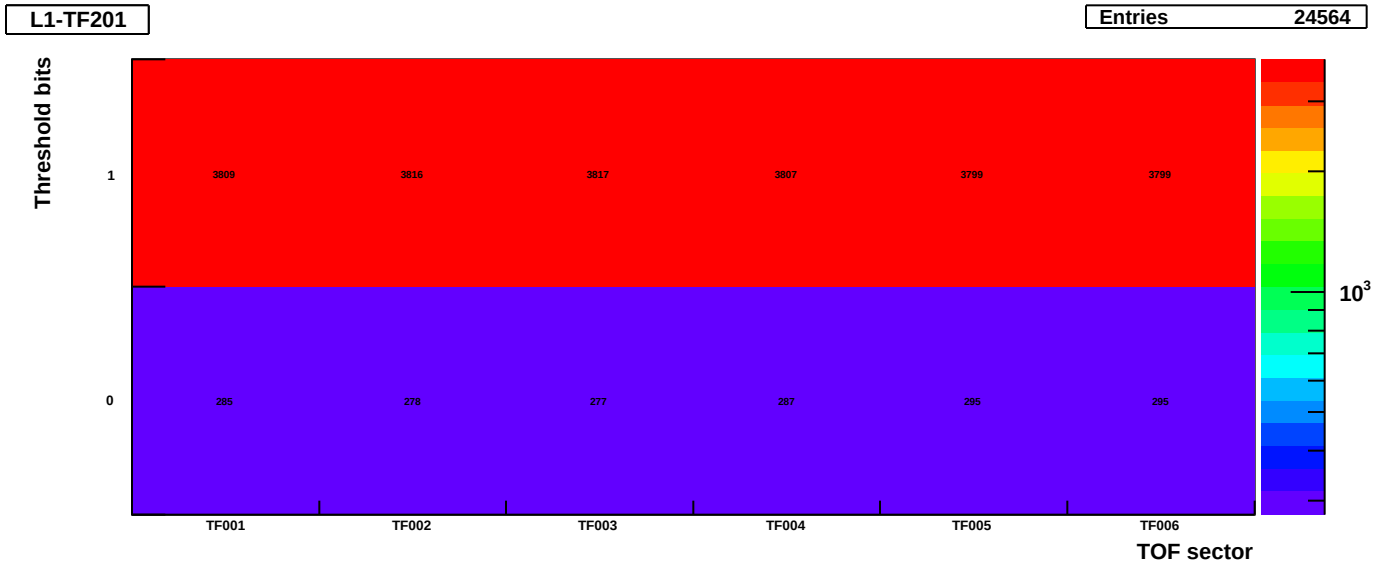

MIX-TF004
Entries 171948
TOF MULT
TOF tray

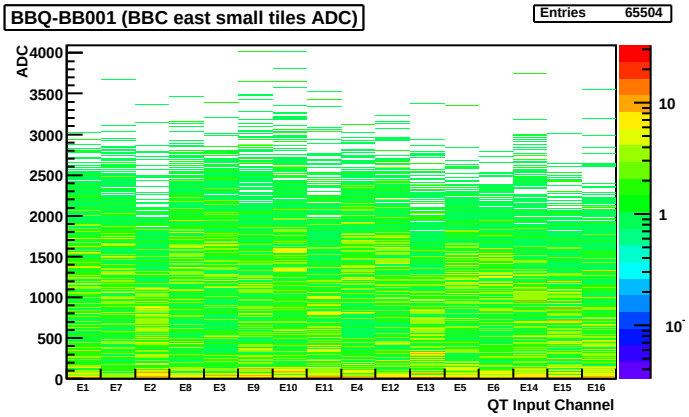

MIX-TF005
Entries 171948
TOF MULT
TOF tray

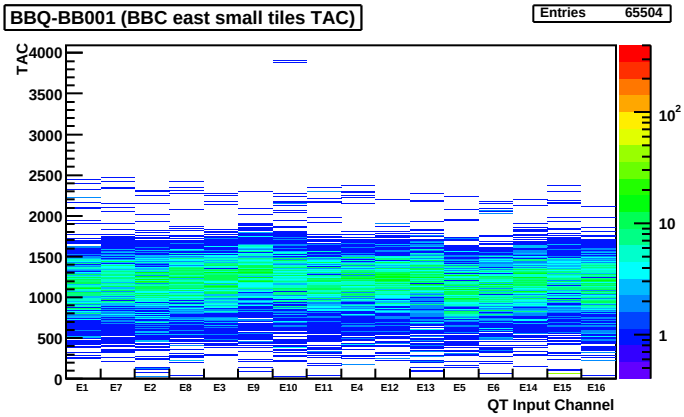

MIX-TF006
Entries 171948
TOF MULT
TOF tray

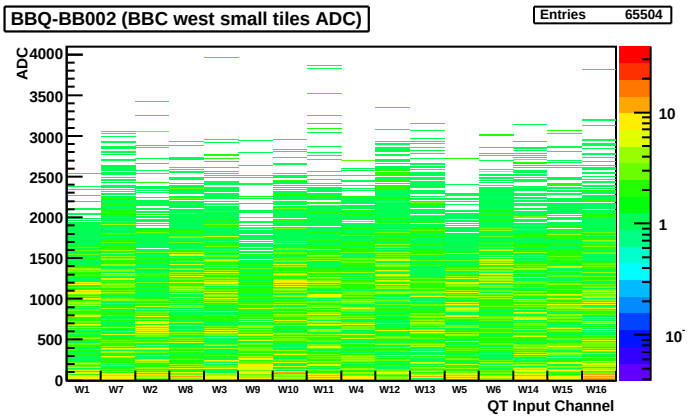

MIX-TF101
Entries 24564
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^2
10
1

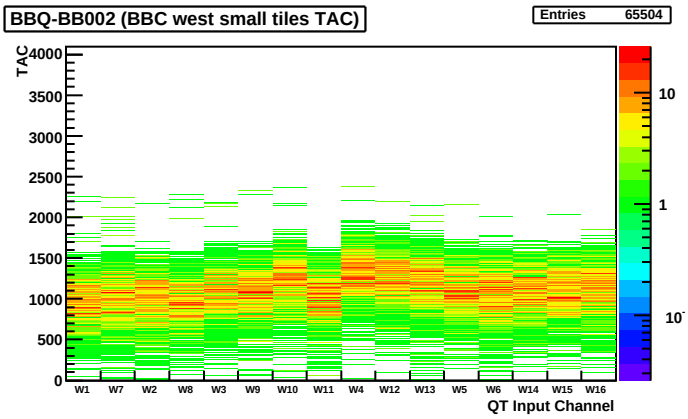

L1-TF201
Entries 4094
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

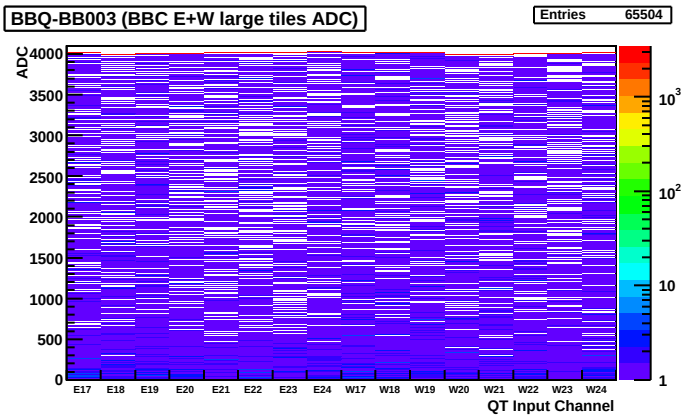

L1-TF201
Entries 24564
Threshold bits
1
0
3809
3816
3817
3807
3799
3799
285
278
277
287
295
295
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^3

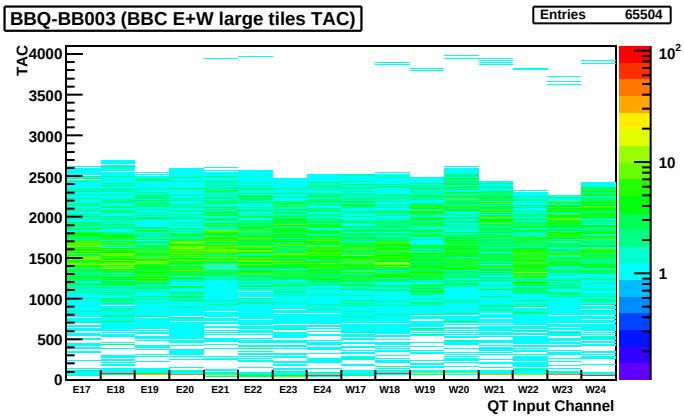

BBQ-BB001 (BBC east small tiles ADC)
Entries 65504
ADC
QT Input Channel

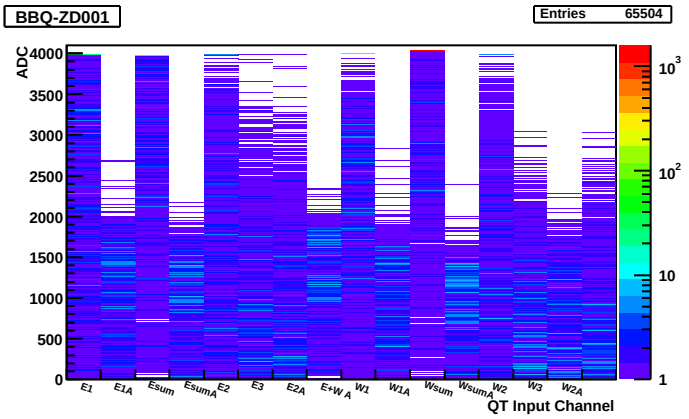

BBQ-BB001 (BBC east small tiles TAC)
Entries 65504
TAC
QT Input Channel

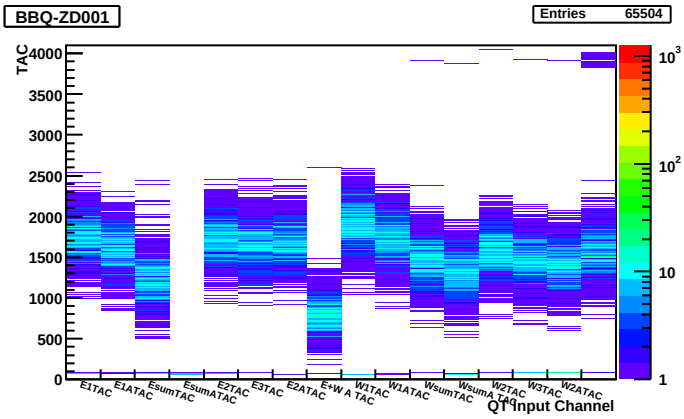

BBQ-BB002 (BBC west small tiles ADC)
Entries 65504
ADC
QT Input Channel

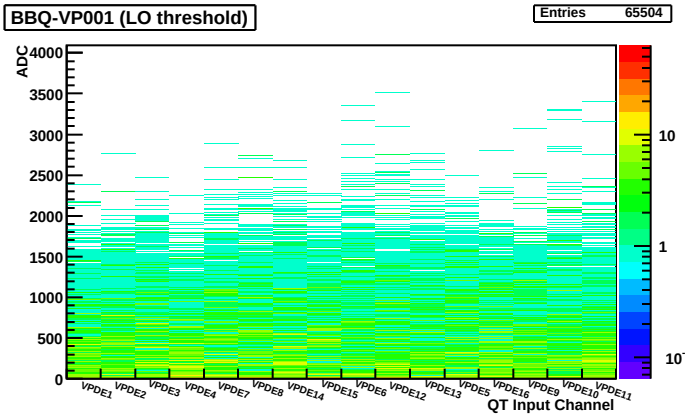

BBQ-BB002 (BBC west small tiles TAC)
Entries 65504
TAC
QT Input Channel

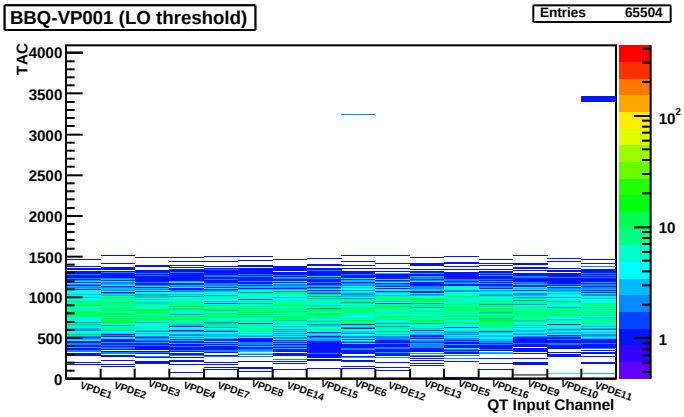

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 65504
ADC
QT Input Channel

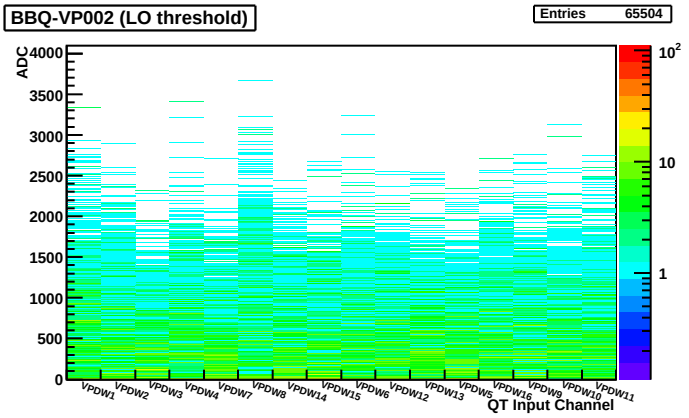

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 65504
TAC
QT Input Channel

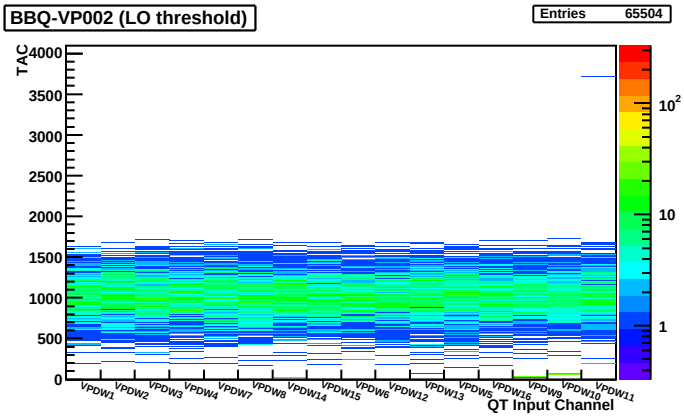

BBQ-ZD001
Entries 65504
ADC
QT Input Channel

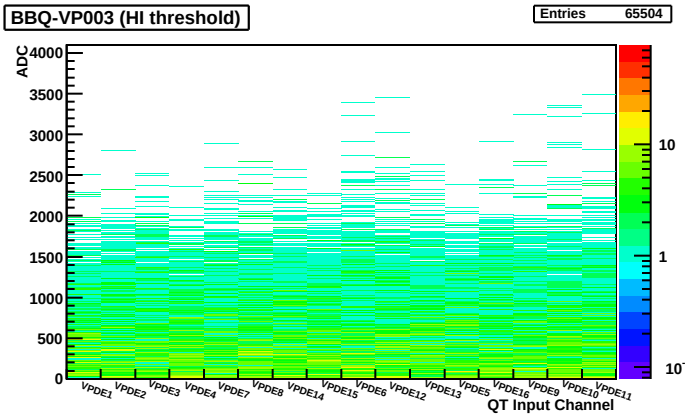

BBQ-ZD001
Entries 65504
TAC
QT Input Channel

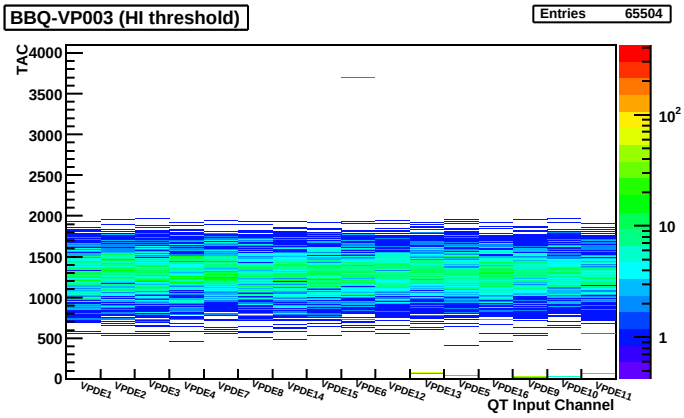

BBQ-VP001 (LO threshold)
Entries 65504
ADC
QT Input Channel

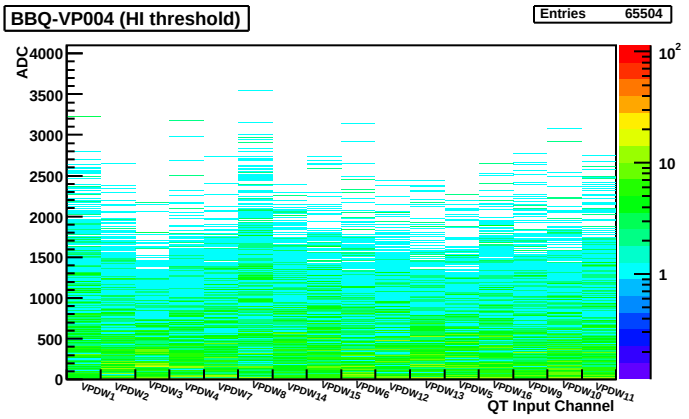

BBQ-VP001 (LO threshold)
Entries 65504
TAC
QT Input Channel

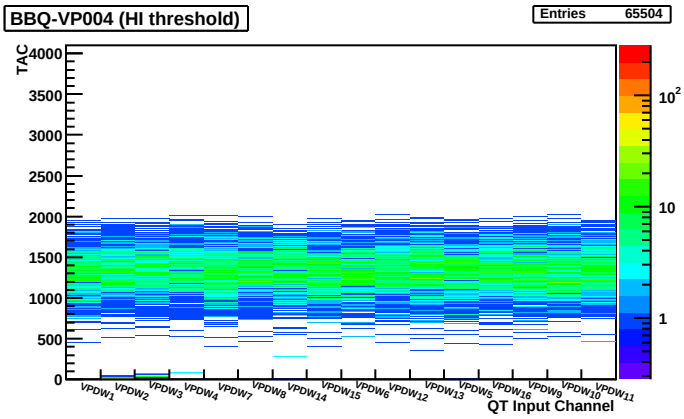

BBQ-VP002 (LO threshold)
Entries 65504
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 65504
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 65504
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 65504
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 65504
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 65504
TAC
QT Input Channel