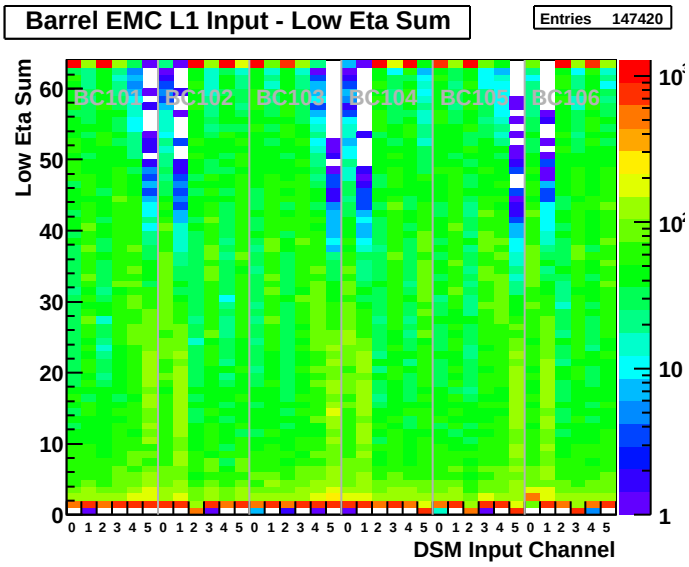
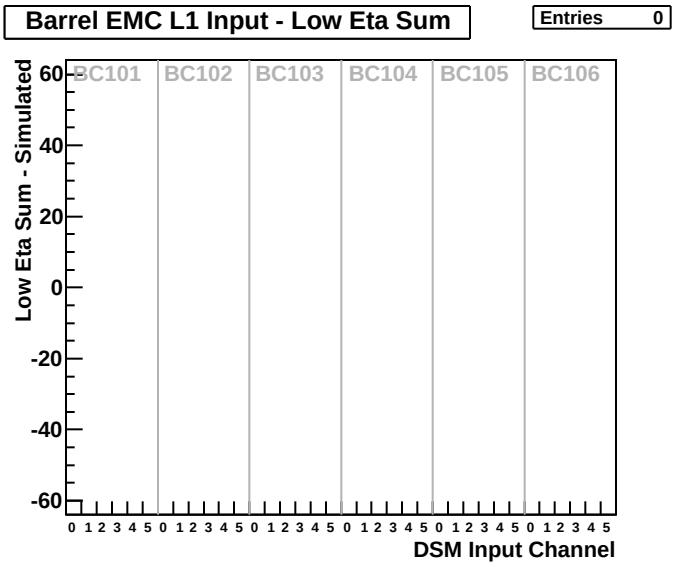
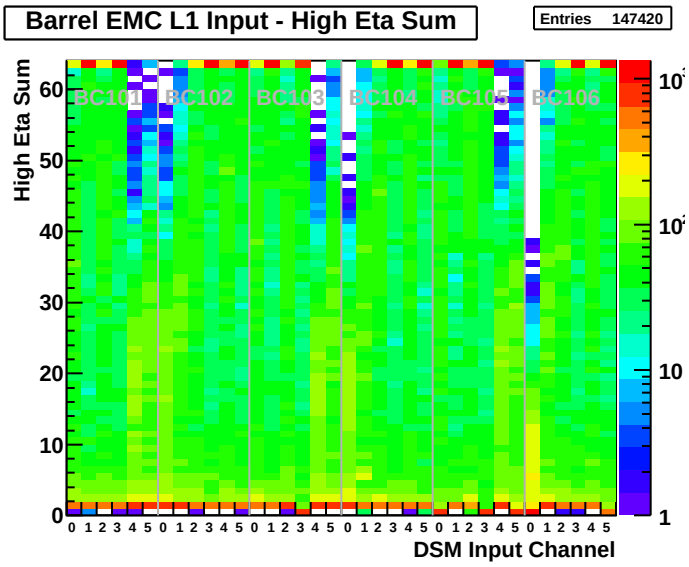
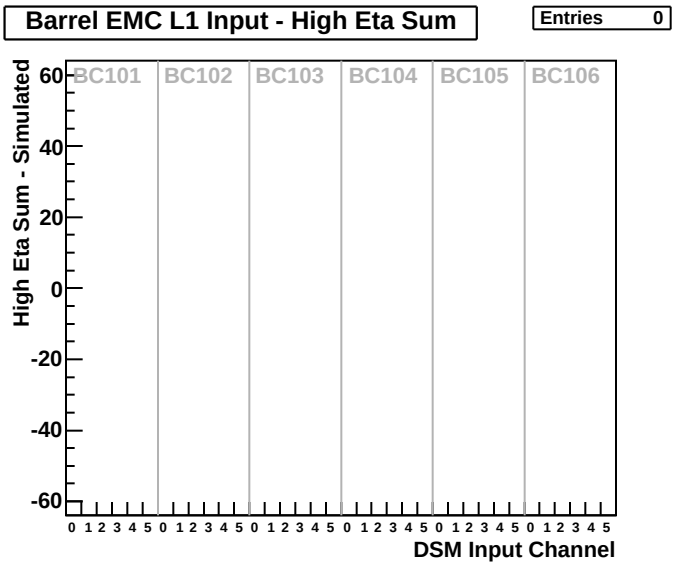
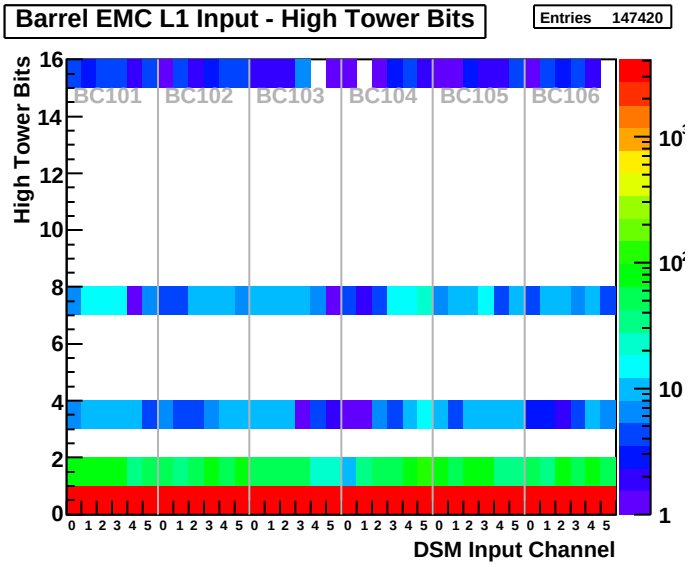
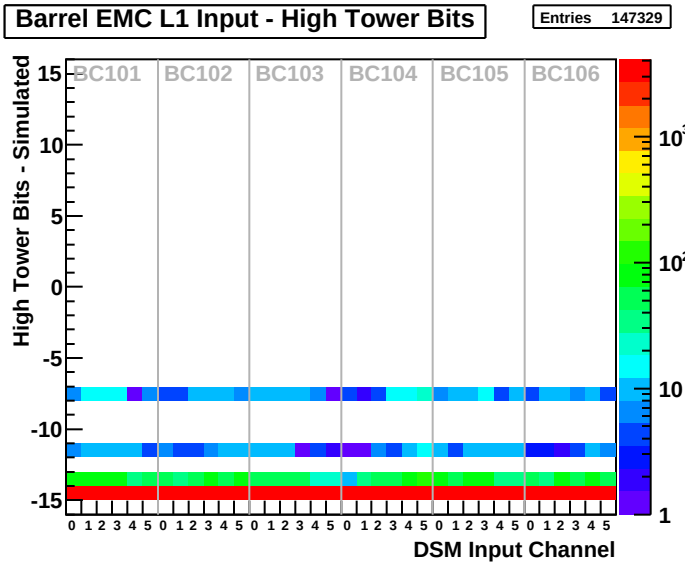

Barrel EMC L1 Input - Low Eta Sum
Entries 147420
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

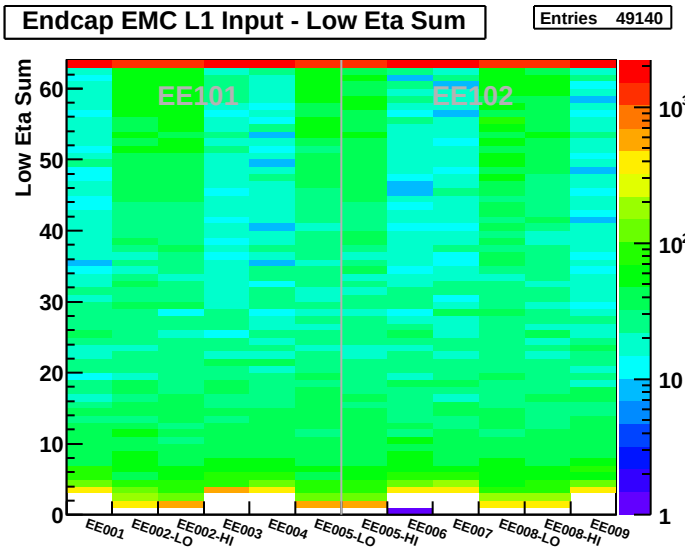

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

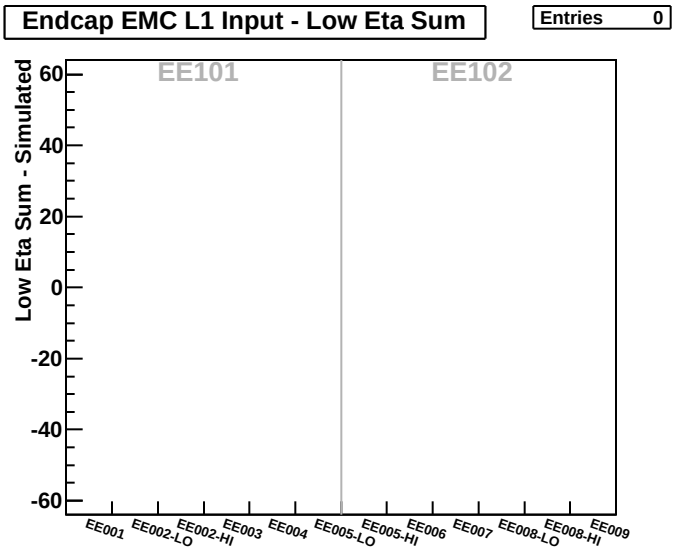

Barrel EMC L1 Input - High Eta Sum
Entries 147420
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

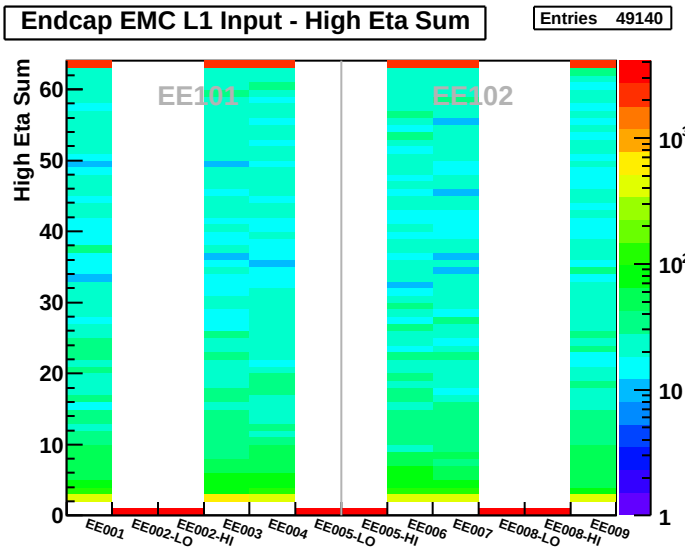

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

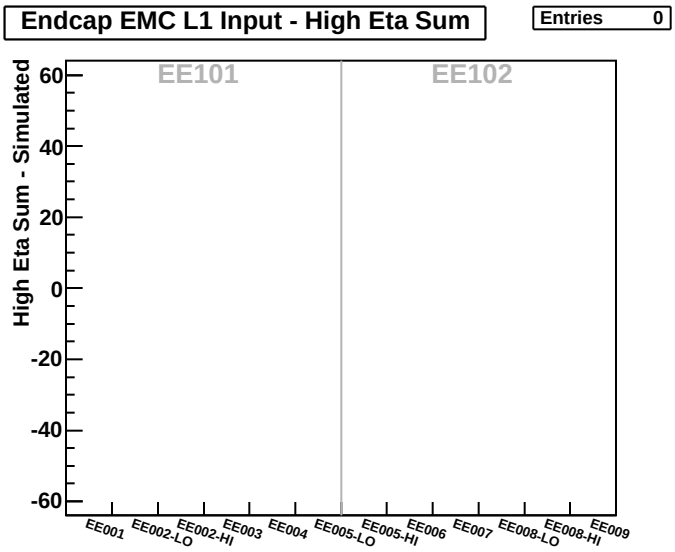

Barrel EMC L1 Input - High Tower Bits
Entries 147420
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

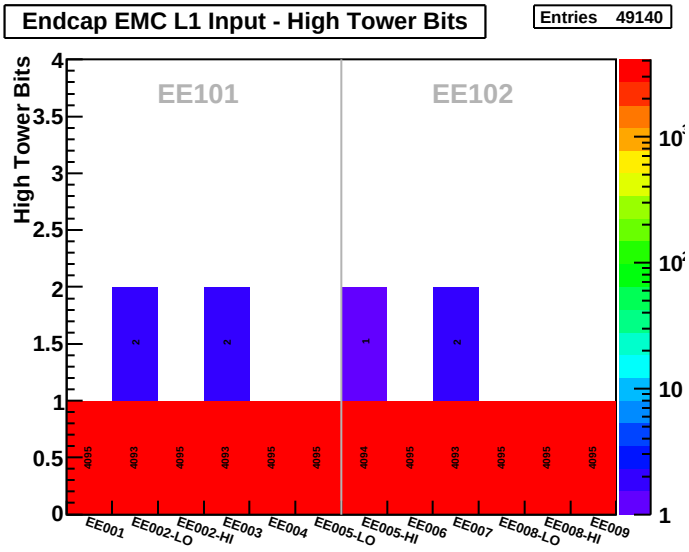

Barrel EMC L1 Input - High Tower Bits
Entries 147329
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

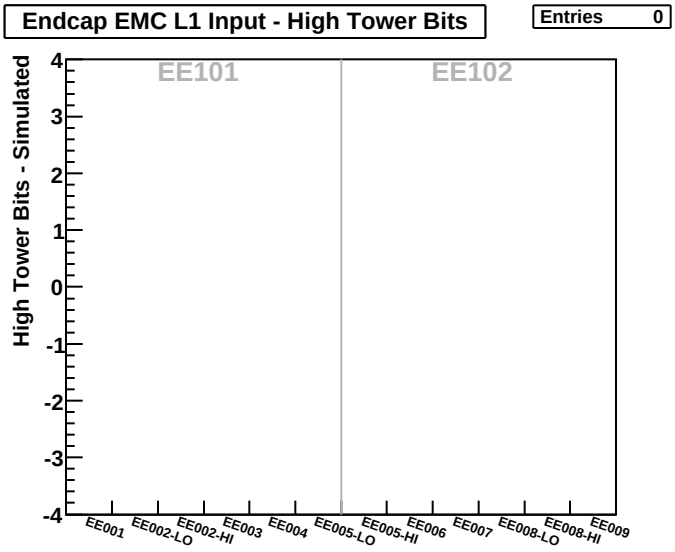

Endcap EMC L1 Input - Low Eta Sum
Entries 49140
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

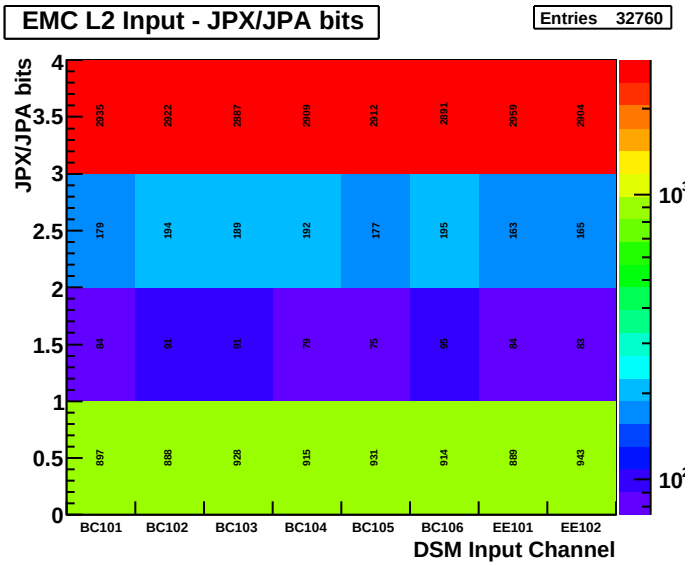

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

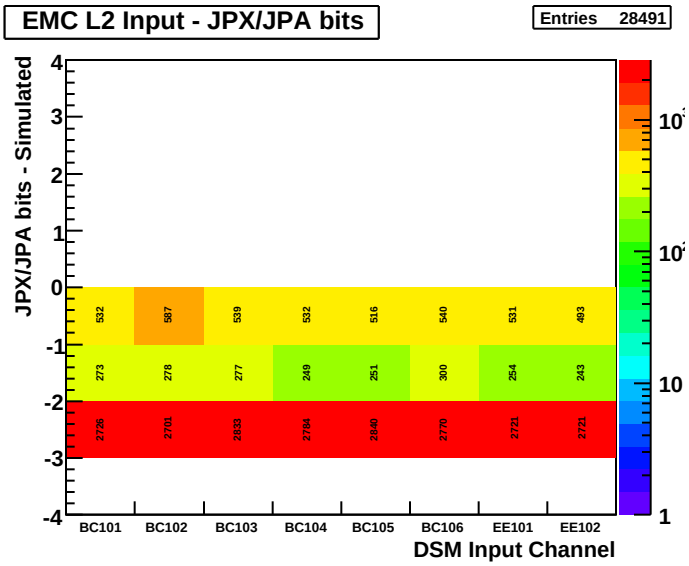

Endcap EMC L1 Input - High Eta Sum
Entries 49140
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

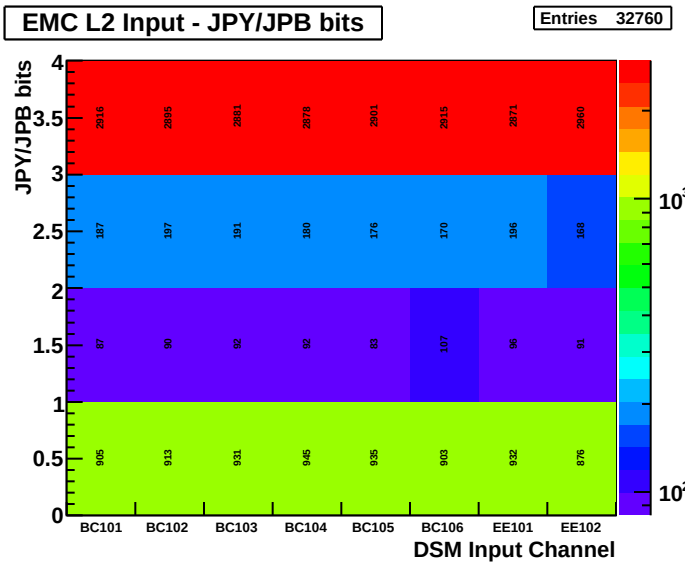

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

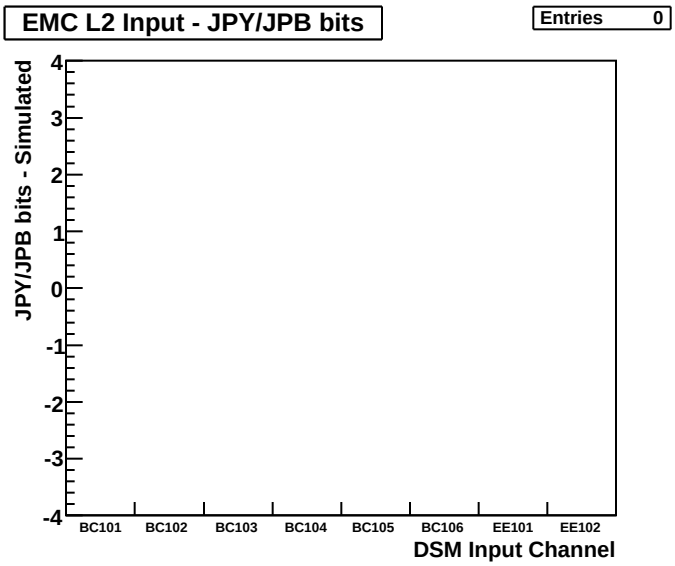

Endcap EMC L1 Input - High Tower Bits
Entries 49140
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

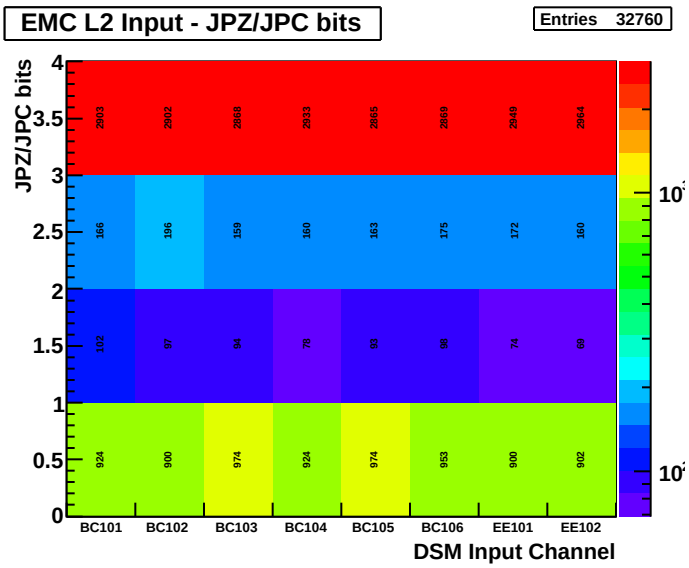

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

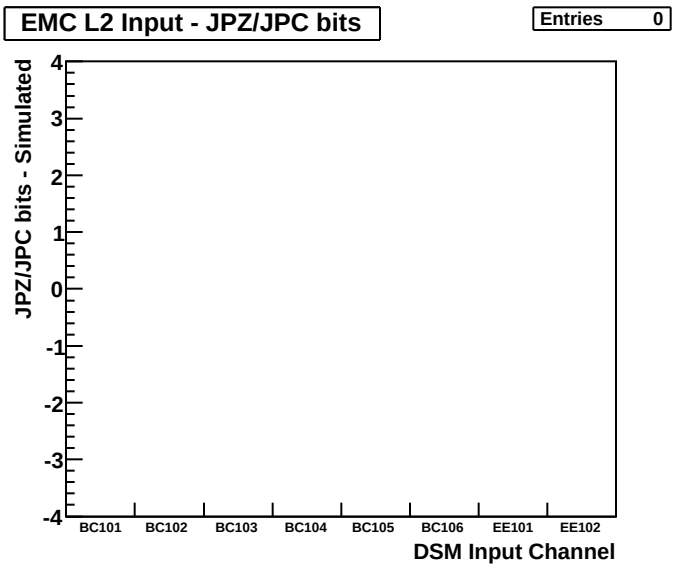

EMC L2 Input - JPX/JPA bits
Entries 32760
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2935 2922 2887 2909 2912 2891 2959 2904
179 194 189 192 177 195 163 165
84 91 91 79 75 95 84 83
897 888 928 915 931 914 889 943

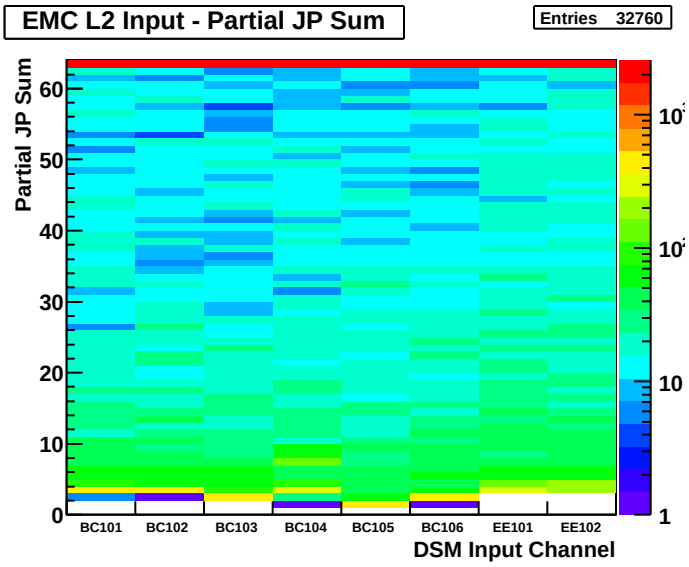

EMC L2 Input - JPX/JPA bits
Entries 28491
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
532 587 539 532 516 540 531 483
273 278 277 249 251 300 254 243
2726 2701 2333 2784 2840 2770 2721 2721

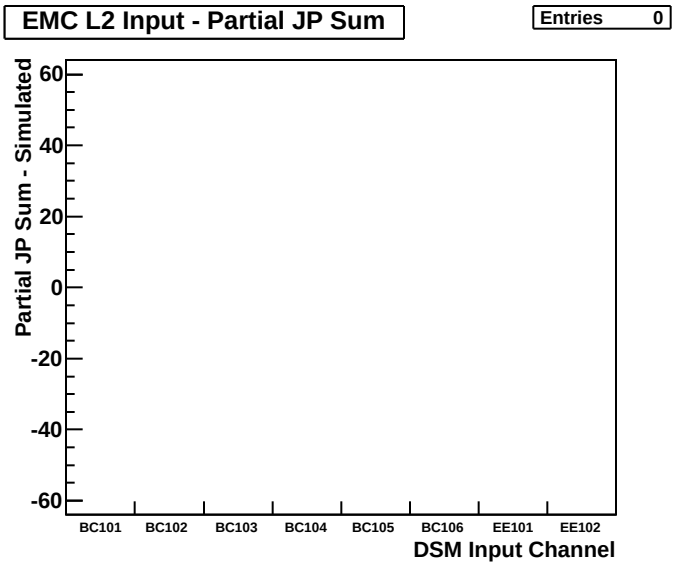

EMC L2 Input - JPY/JPB bits
Entries 32760
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2916 2895 2881 2878 2901 2915 2871 2960
187 197 191 180 176 170 196 168
87 90 92 92 83 107 96 91
905 913 931 945 935 903 932 876

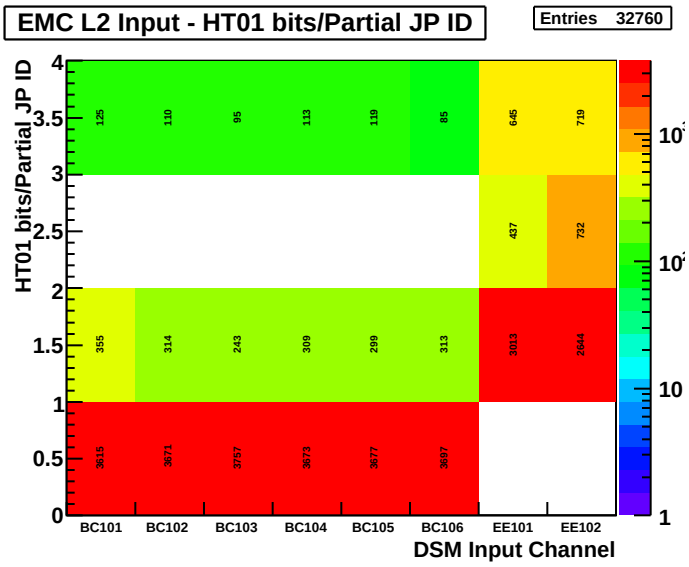

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

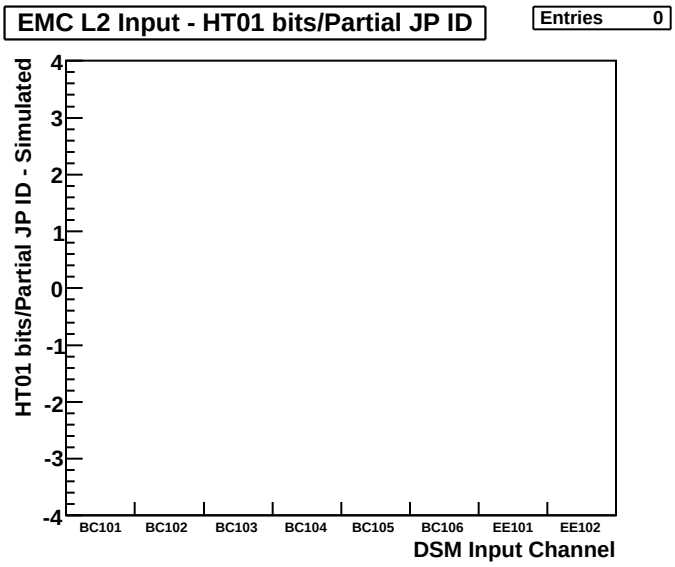

EMC L2 Input - JPZ/JPC bits
Entries 32760
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2903 2902 2868 2933 2865 2869 2949 2964
156 196 159 160 163 175 172 160
102 97 94 78 93 98 74 69
924 900 974 924 974 953 900 902

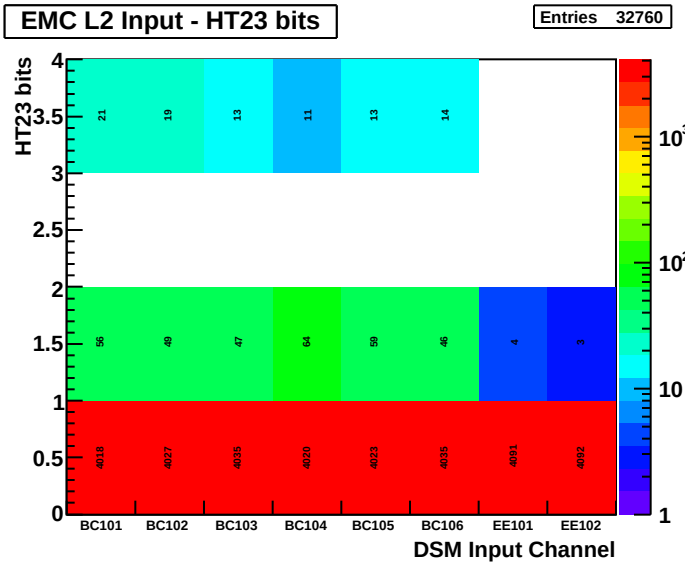

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

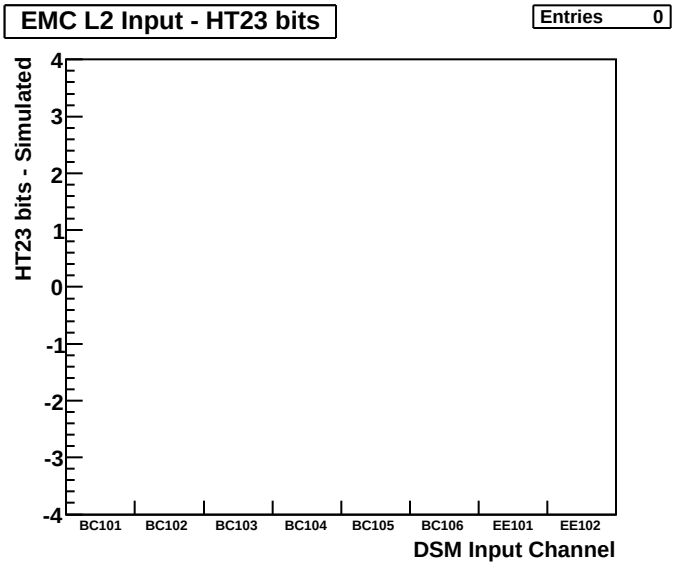

EMC L2 Input - Partial JP Sum
Entries 32760
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

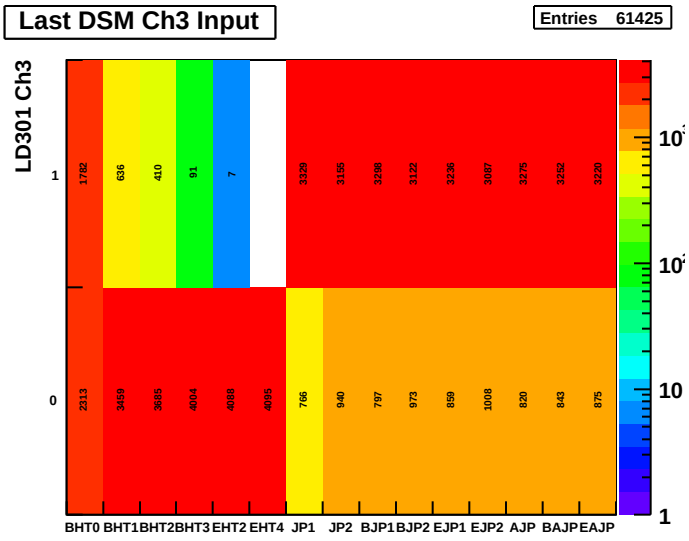

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

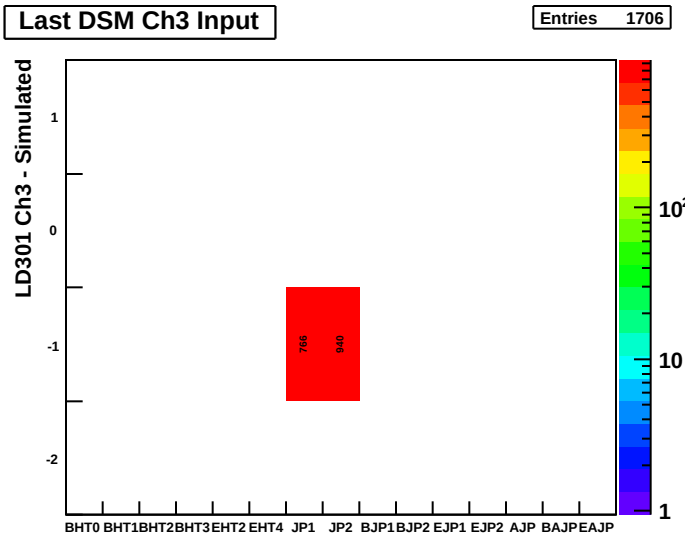

EMC L2 Input - HT01 bits/Partial JP ID
Entries 32760
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

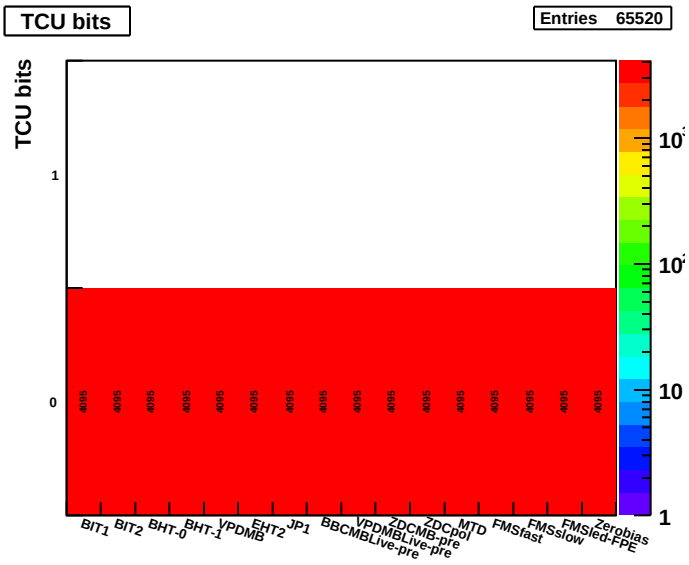

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

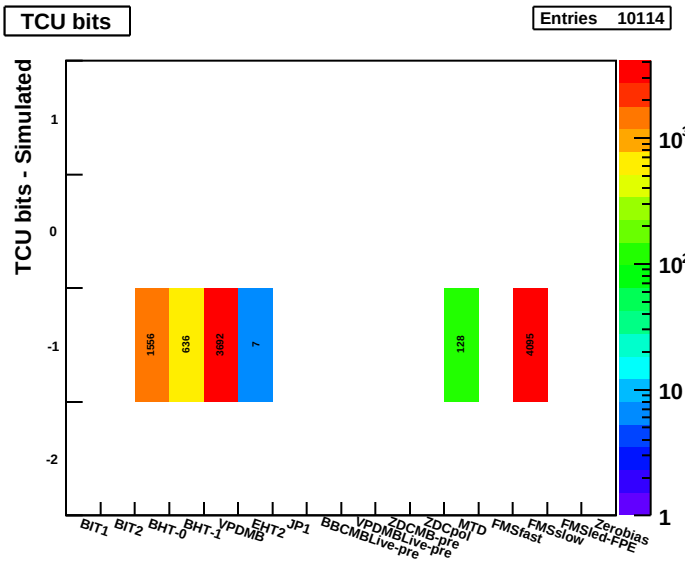

EMC L2 Input - HT23 bits
Entries 32760
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

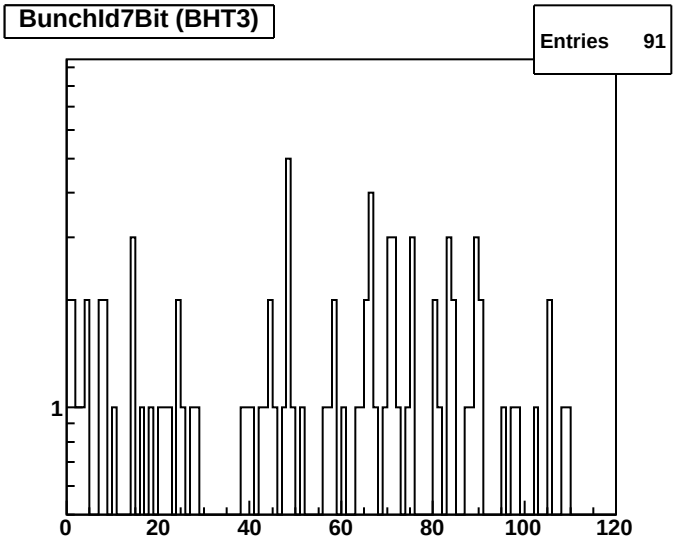

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

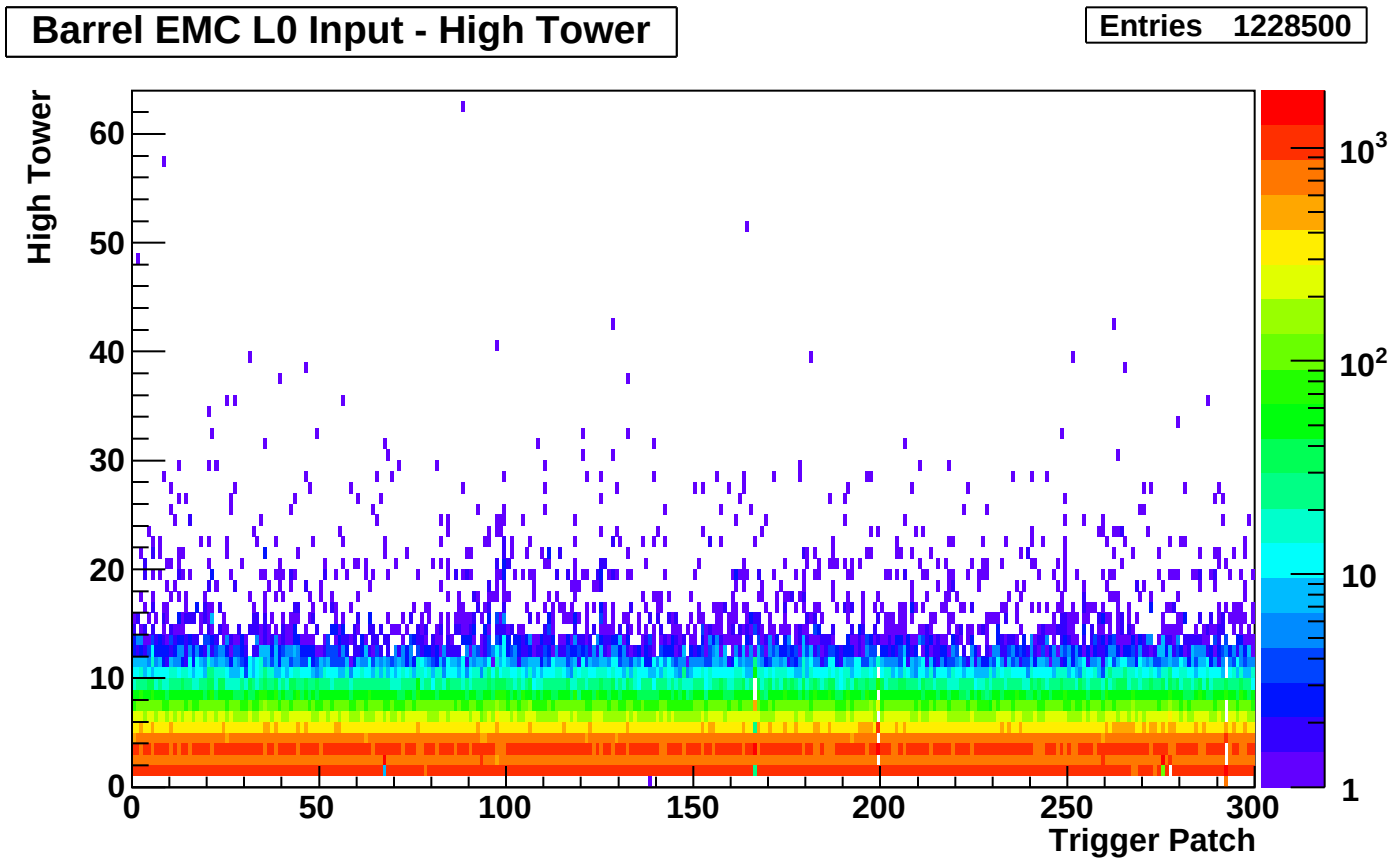

Last DSM Ch3 Input
Entries 61425
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

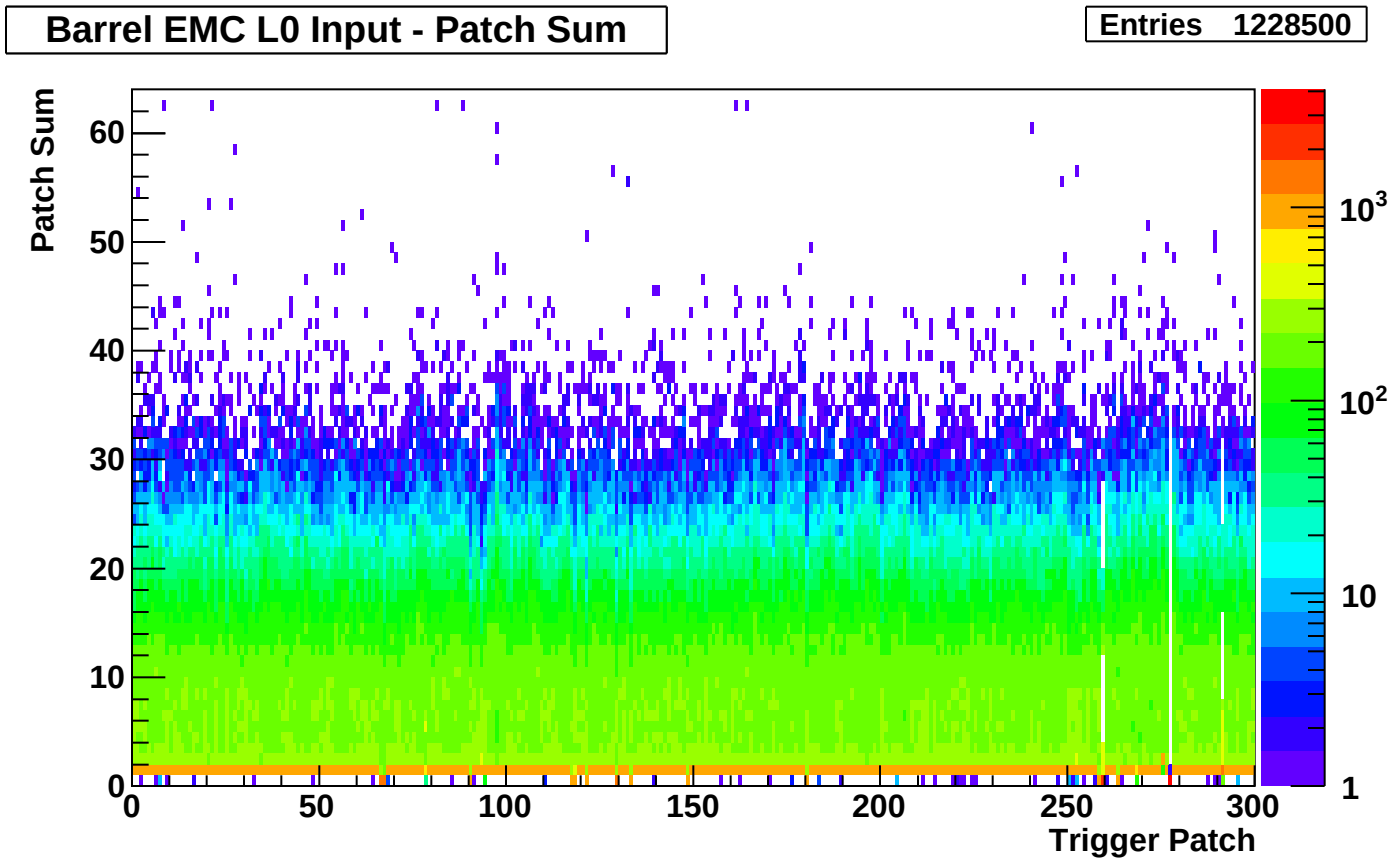

Last DSM Ch3 Input
Entries 1706
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

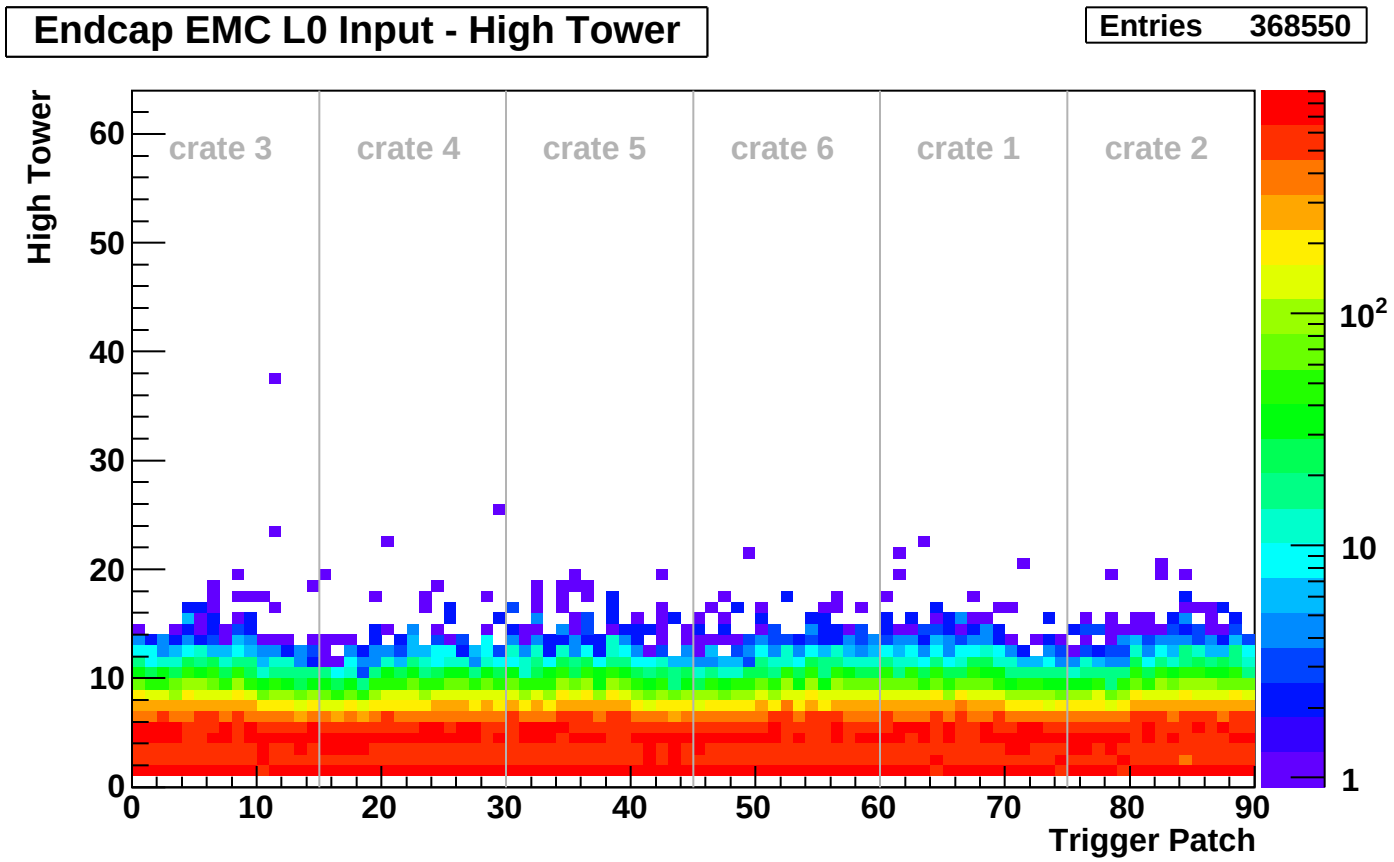

TCU bits
Entries 65520
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

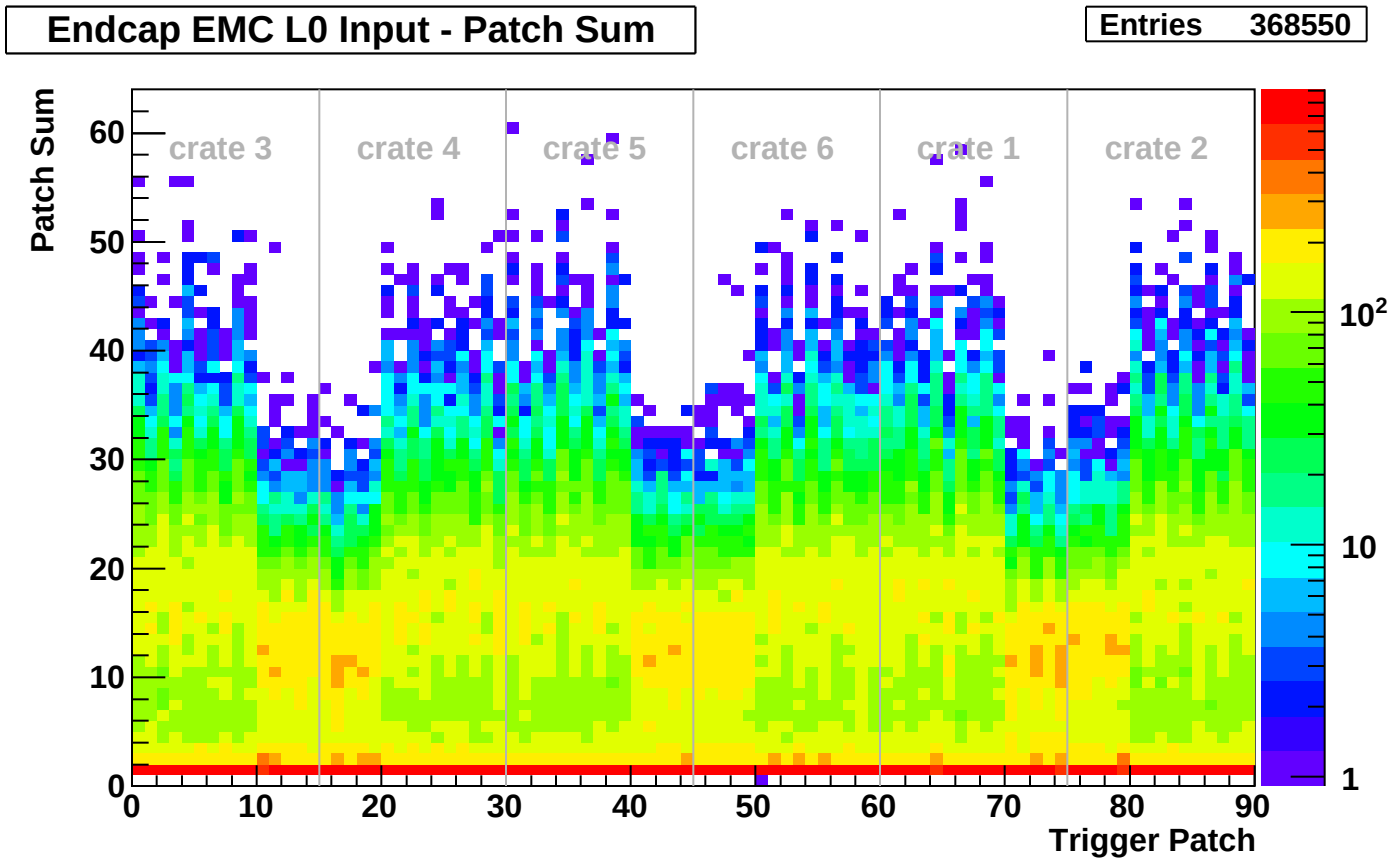

TCU bits
Entries 10114
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

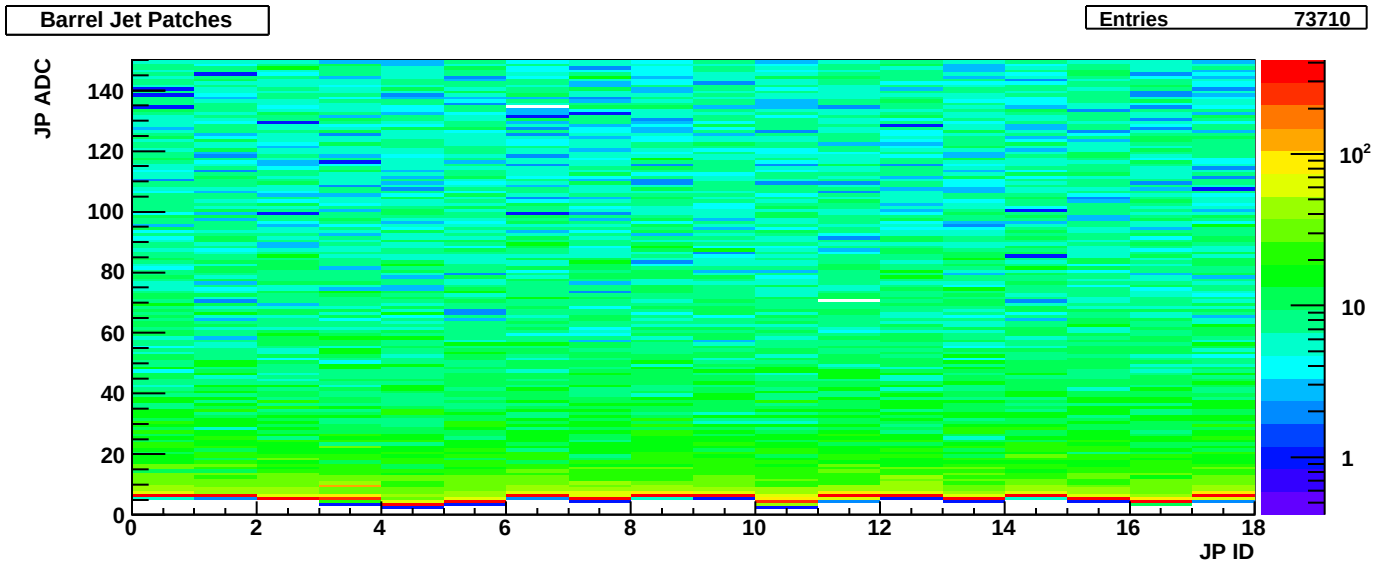

BunchId7Bit (BHT3)
Entries 91

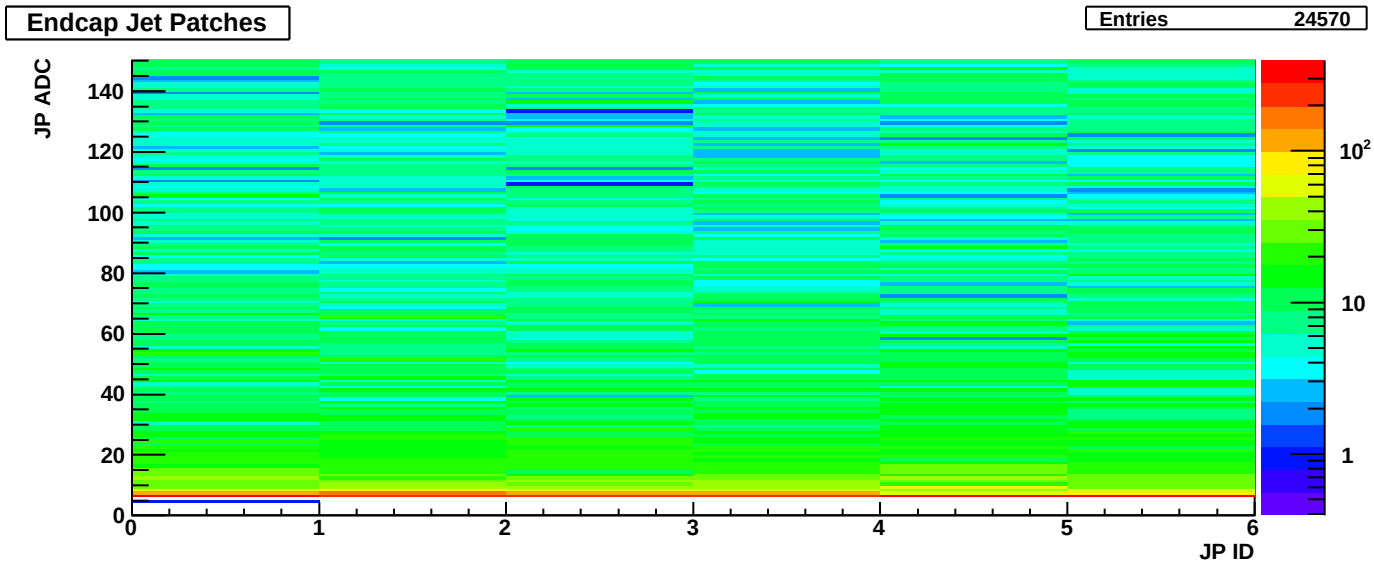

Barrel EMC L0 Input - High Tower
Entries 1228500
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

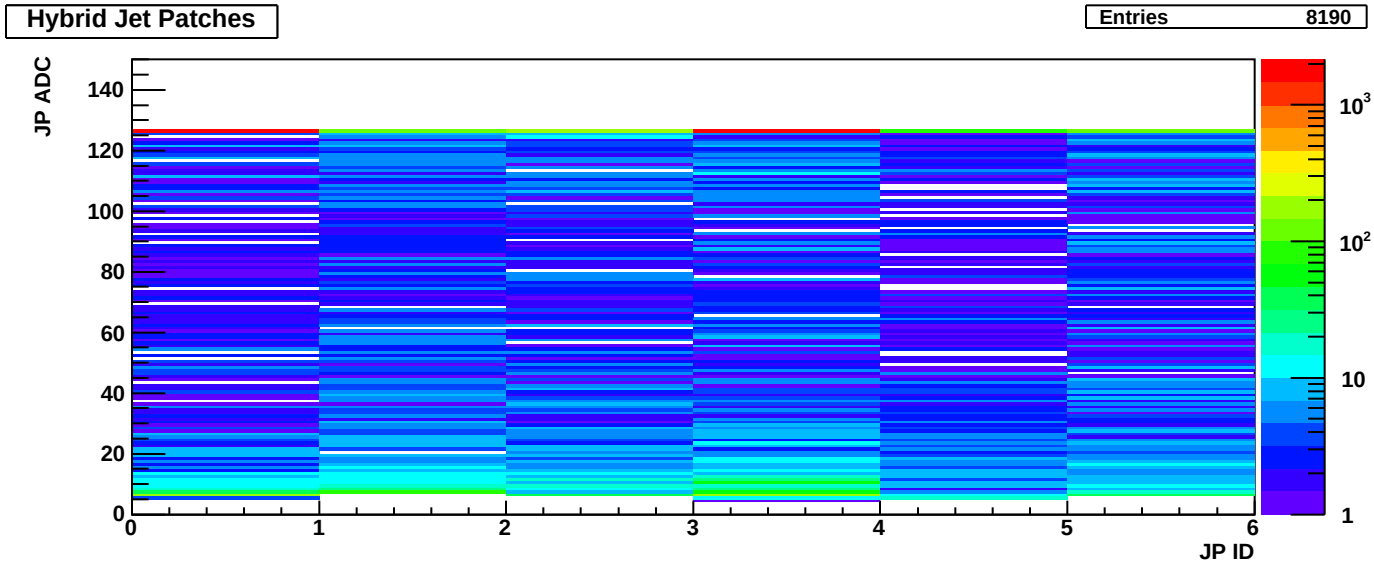

Barrel EMC L0 Input - Patch Sum
Entries 1228500
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

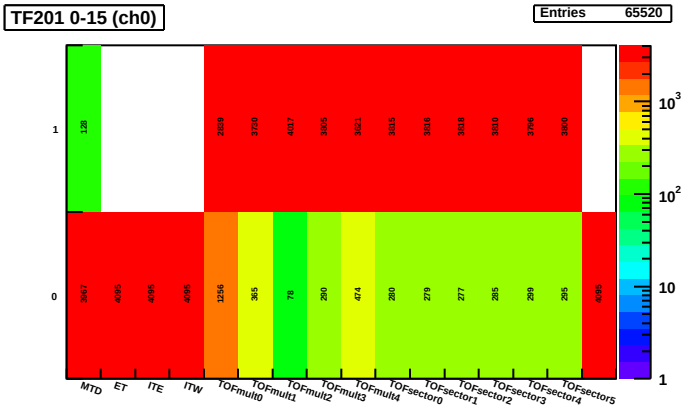

Endcap EMC L0 Input - High Tower
Entries 368550
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2

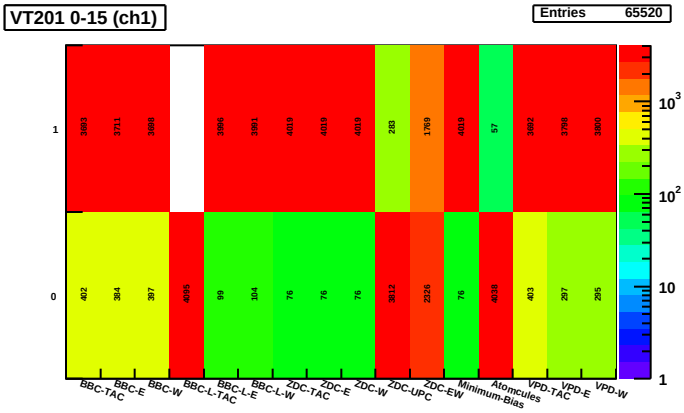

Endcap EMC L0 Input - Patch Sum
Entries 368550
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2

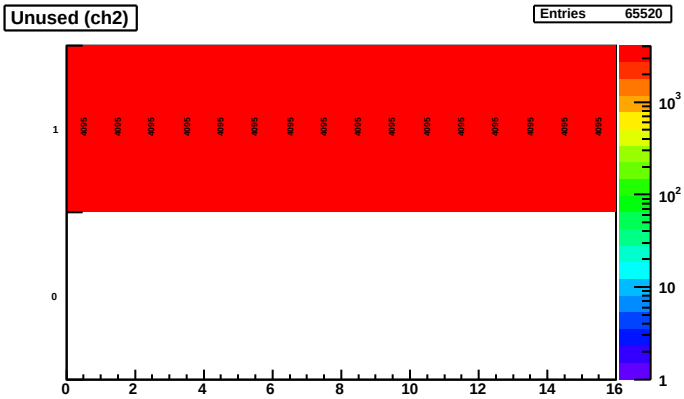

Barrel Jet Patches
Entries 73710
JP ADC
JP ID

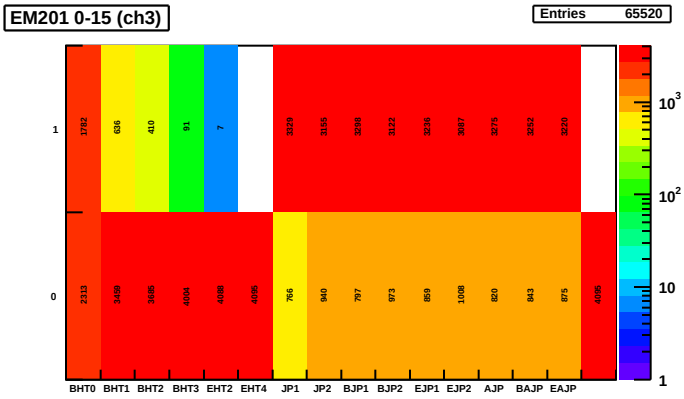

Endcap Jet Patches
Entries 24570
JP ADC
JP ID

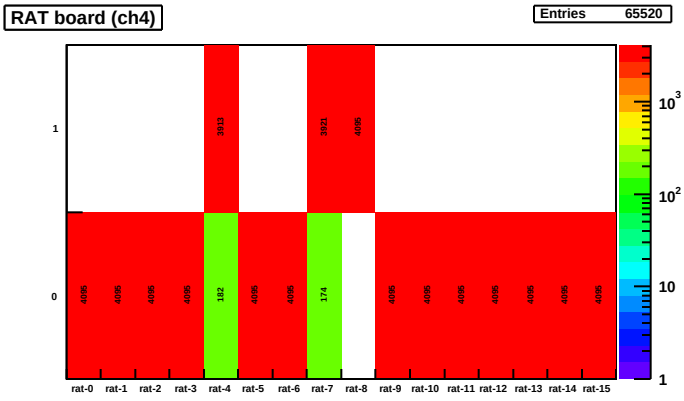

Hybrid Jet Patches
Entries 8190
JP ADC
JP ID

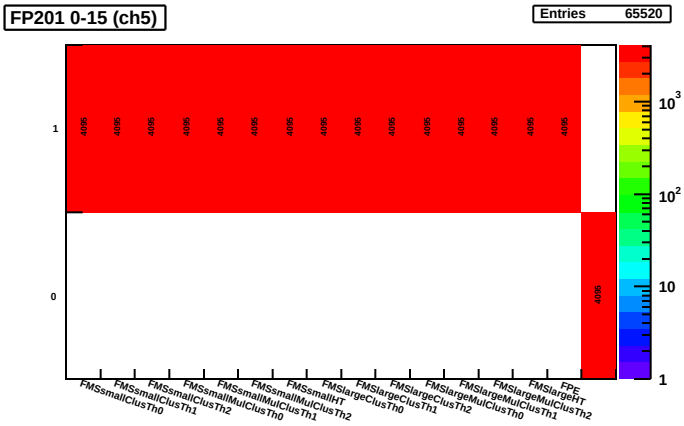

TF201 0-15 (ch0)
Entries 65520
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

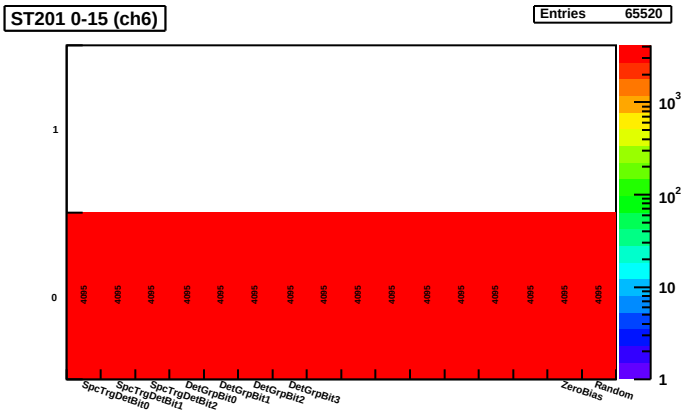

VT201 0-15 (ch1)
Entries 65520
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

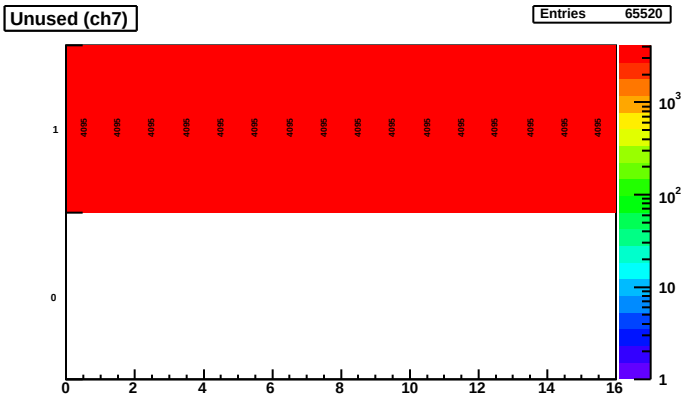

Unused (ch2)
Entries 65520

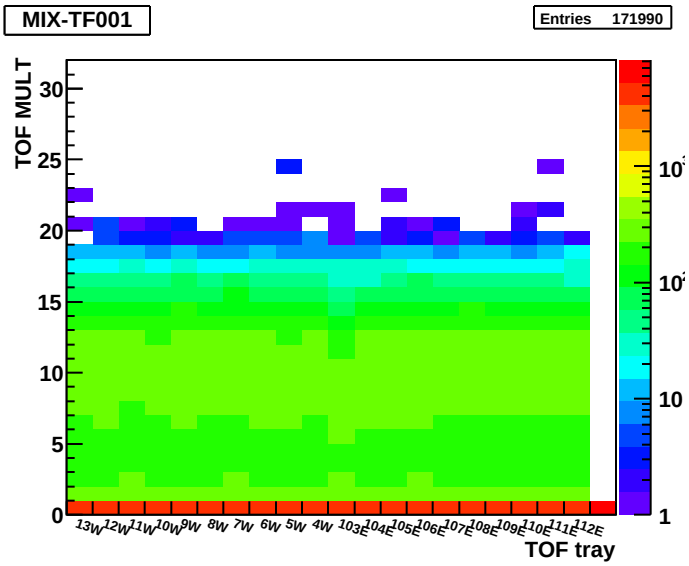

EM201 0-15 (ch3)
Entries 65520
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

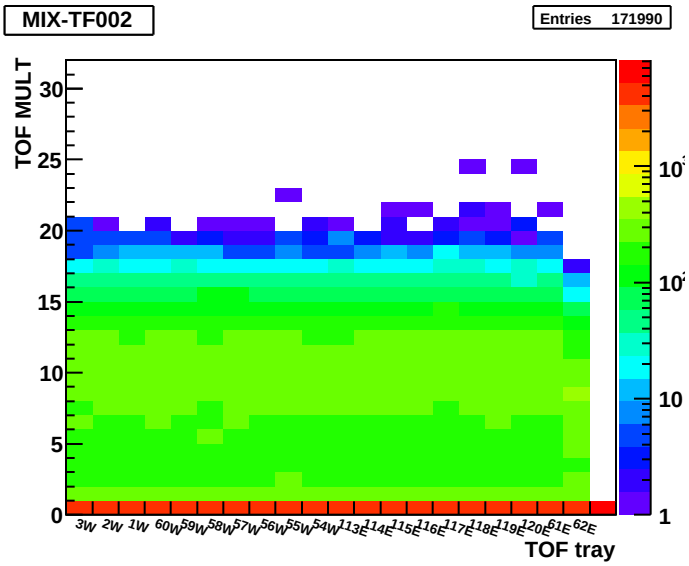

RAT board (ch4)
Entries 65520
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

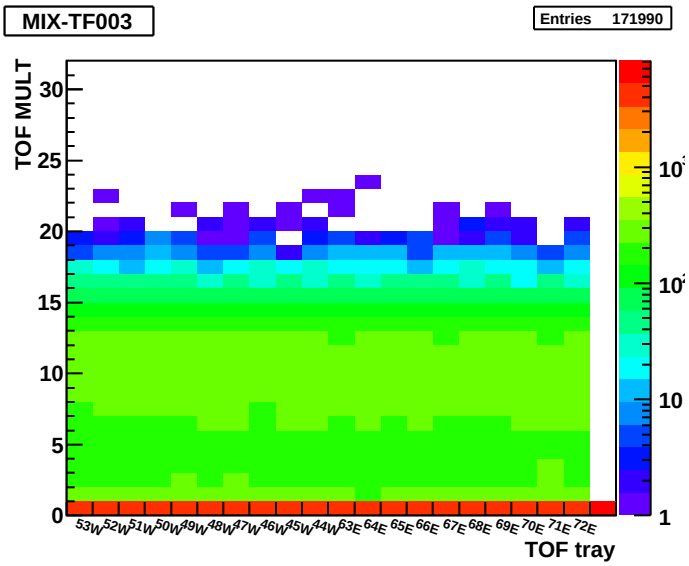

FP201 0-15 (ch5)
Entries 65520
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

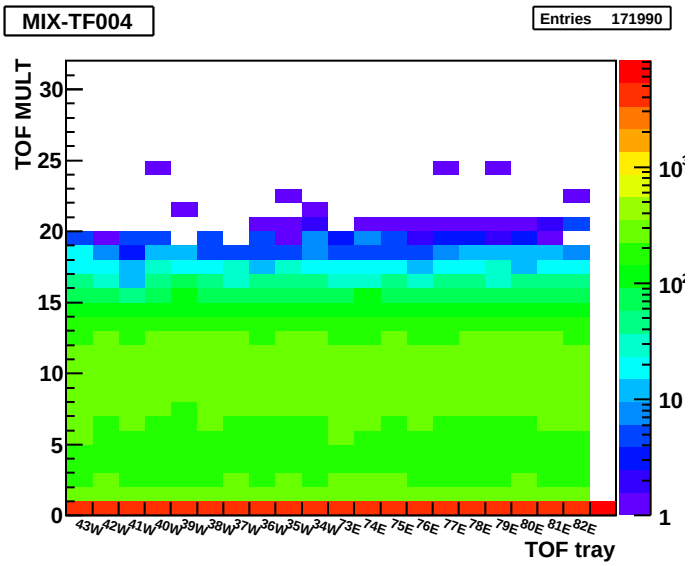

ST201 0-15 (ch6)
Entries 65520
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

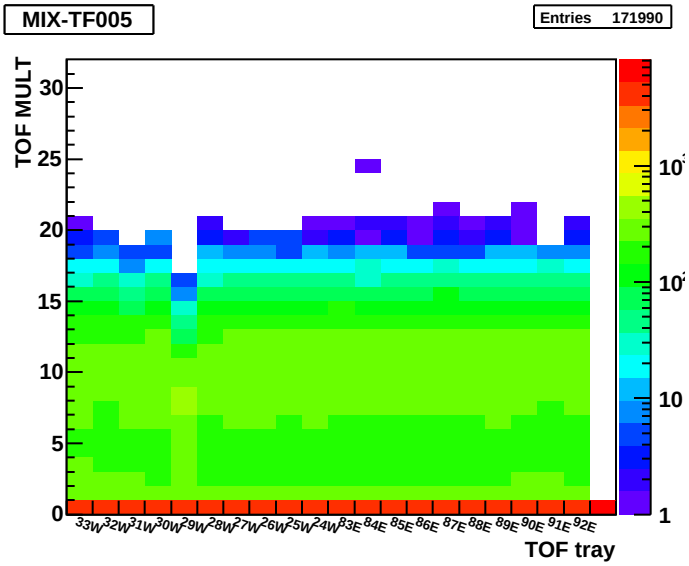

Unused (ch7)
Entries 65520

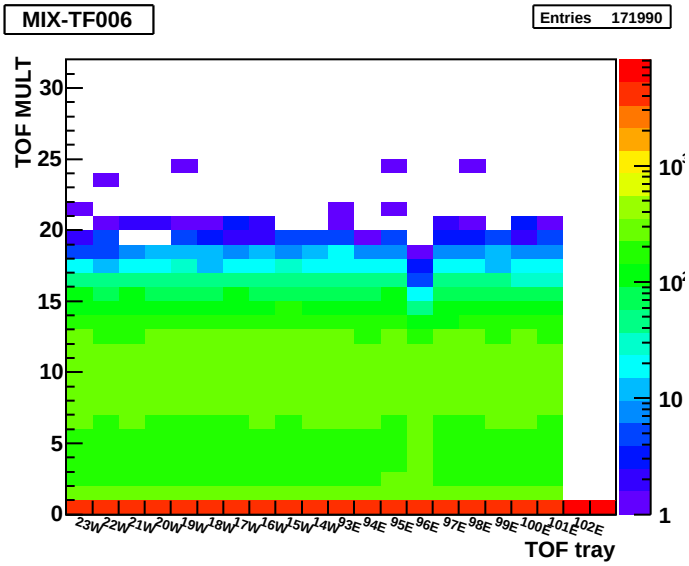

MIX-TF001
Entries 171990
TOF MULT
TOF tray

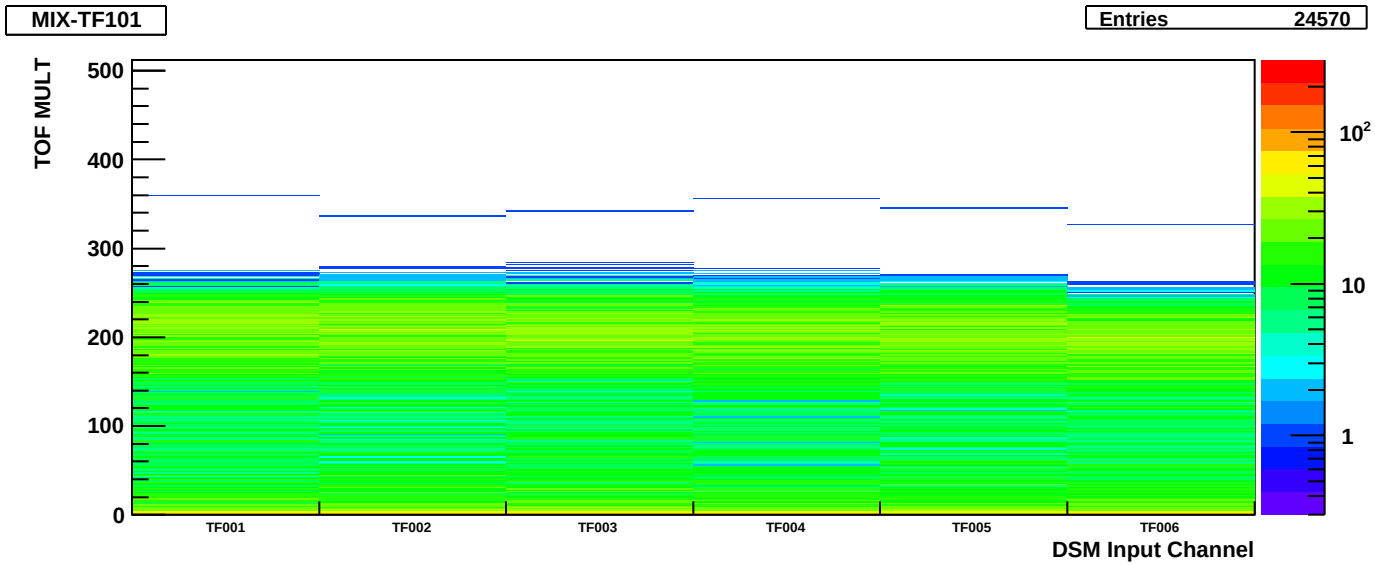

MIX-TF002
Entries 171990
TOF MULT
TOF tray

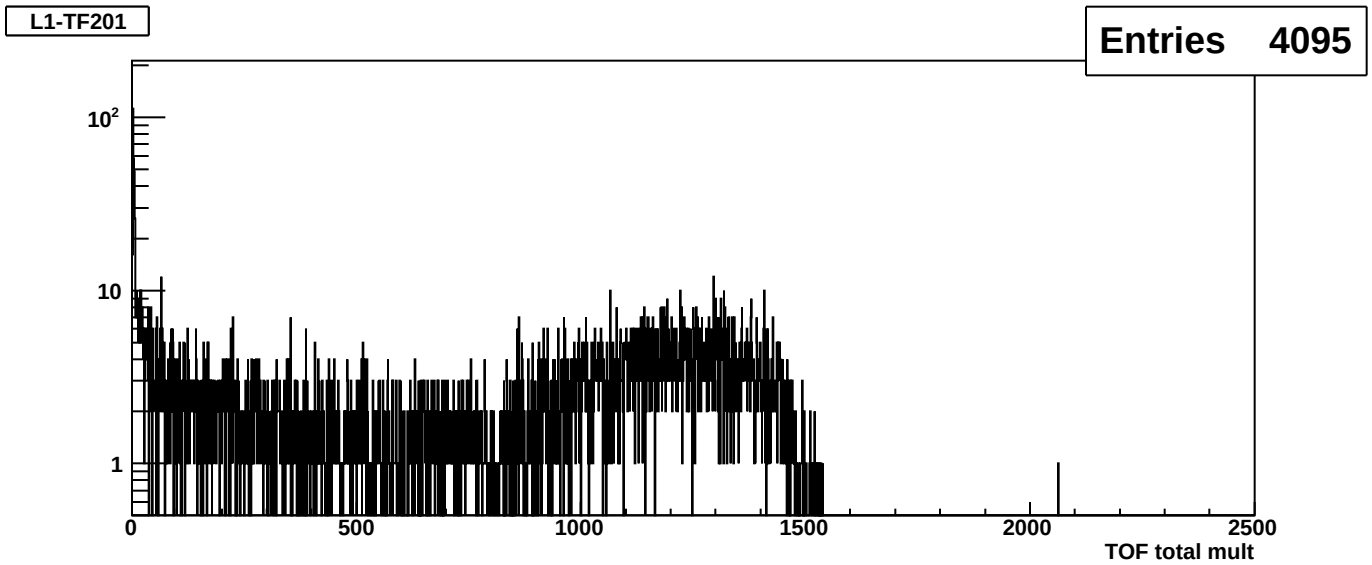

MIX-TF003
Entries 171990
TOF MULT
TOF tray

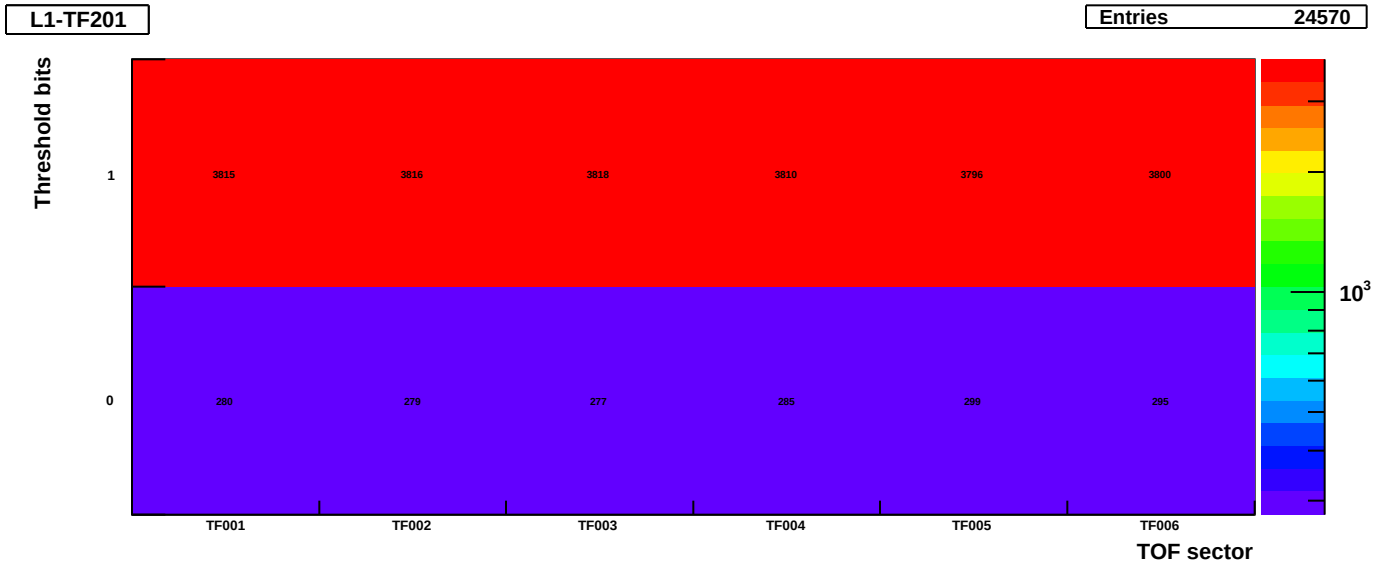

MIX-TF004
Entries 171990
TOF MULT
TOF tray

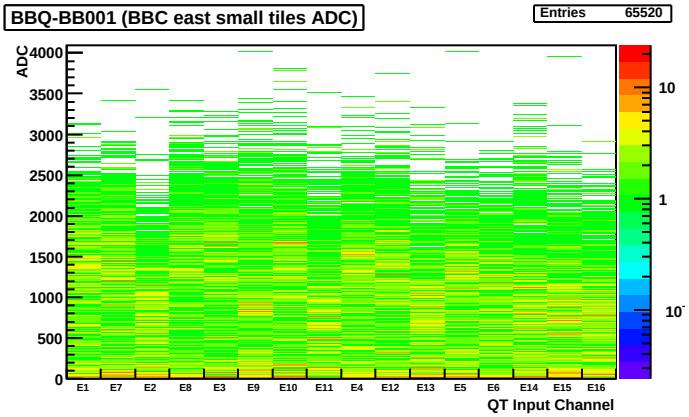

MIX-TF005
Entries 171990
TOF MULT
TOF tray

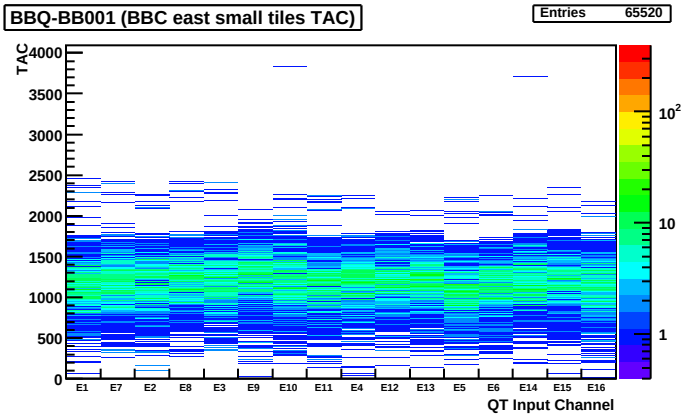

MIX-TF006
Entries 171990
TOF MULT
TOF tray

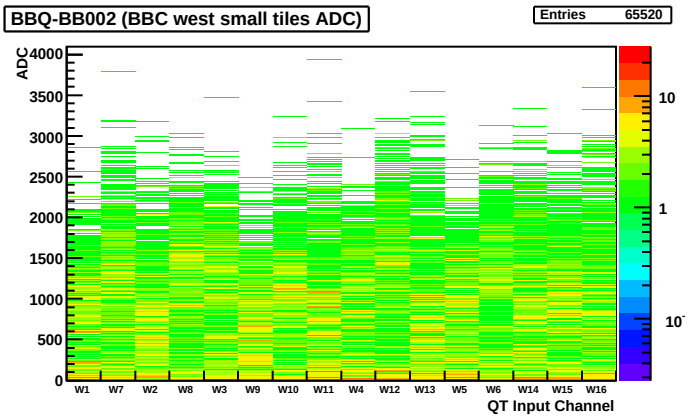

MIX-TF101
Entries 24570
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^2
10
1

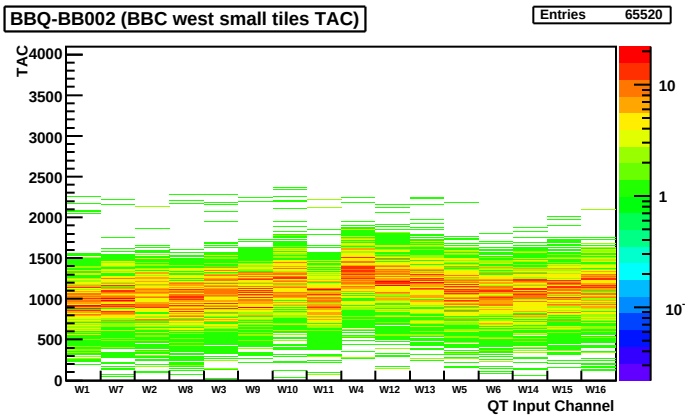

L1-TF201
Entries 4095
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

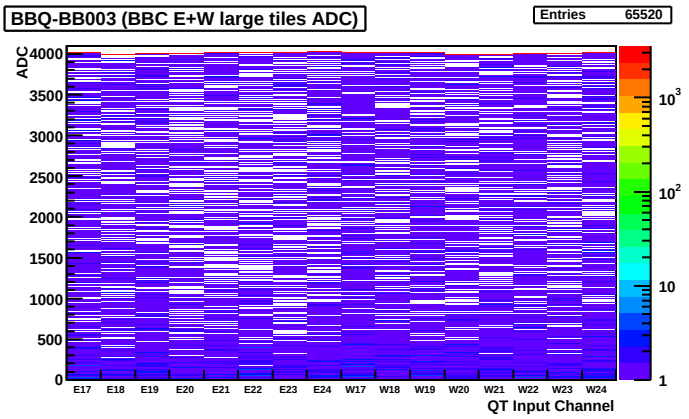

L1-TF201
Entries 24570
Threshold bits
1
0
3815
3816
3818
3810
3796
3800
280
279
277
285
299
295
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^3

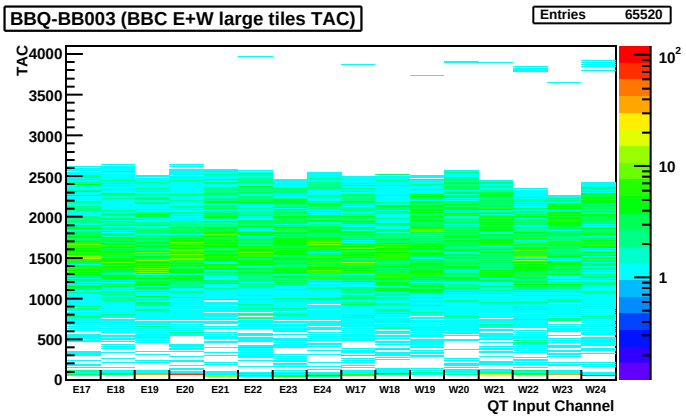

BBQ-BB001 (BBC east small tiles ADC)
Entries 65520
ADC
QT Input Channel

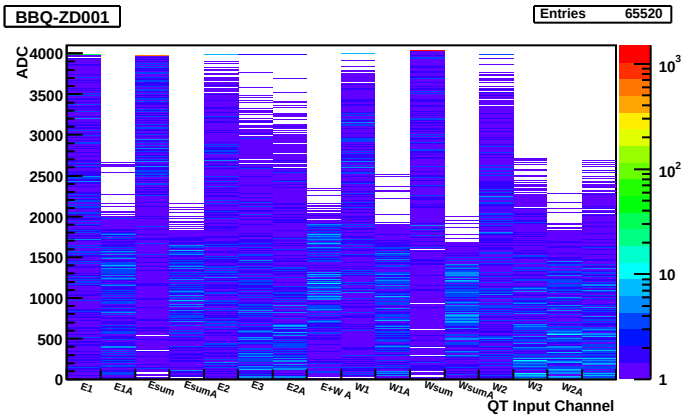

BBQ-BB001 (BBC east small tiles TAC)
Entries 65520
TAC
QT Input Channel

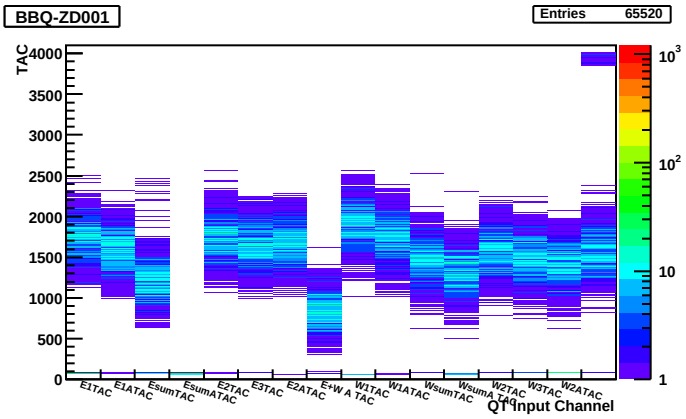

BBQ-BB002 (BBC west small tiles ADC)
Entries 65520
ADC
QT Input Channel

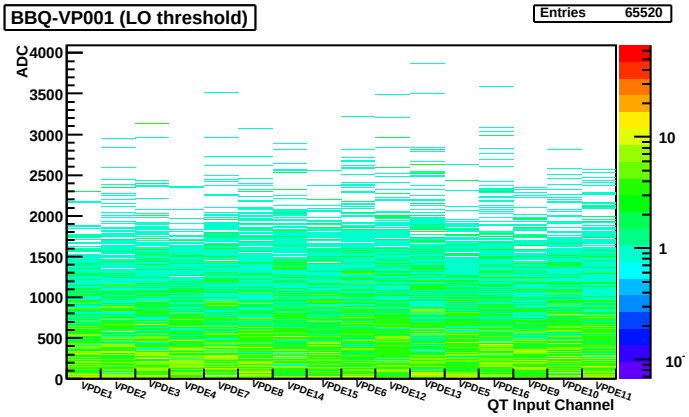

BBQ-BB002 (BBC west small tiles TAC)
Entries 65520
TAC
QT Input Channel

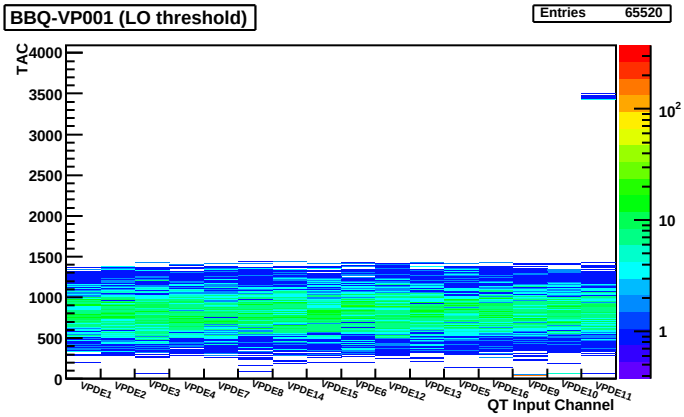

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 65520
ADC
QT Input Channel

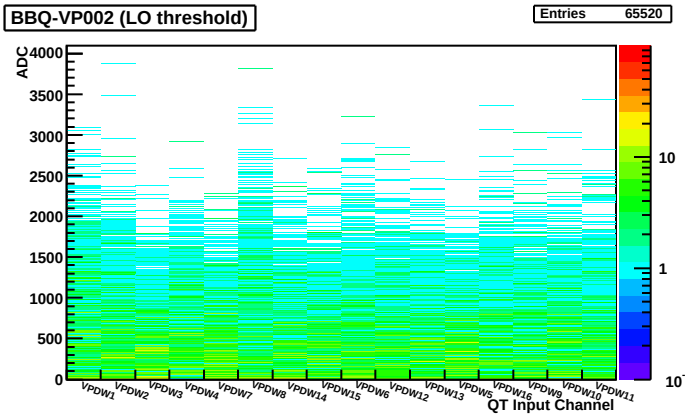

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 65520
TAC
QT Input Channel

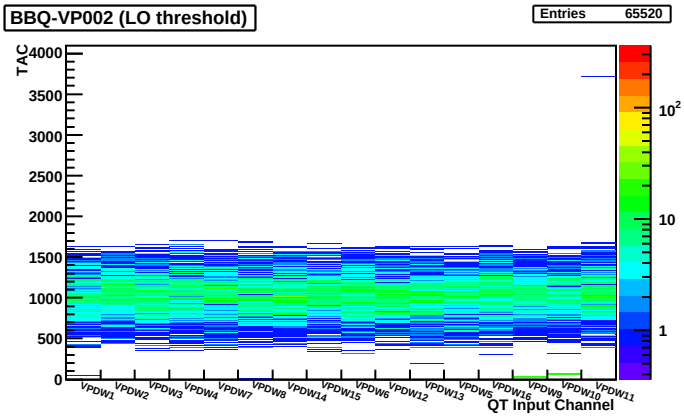

BBQ-ZD001
Entries 65520
ADC
QT Input Channel

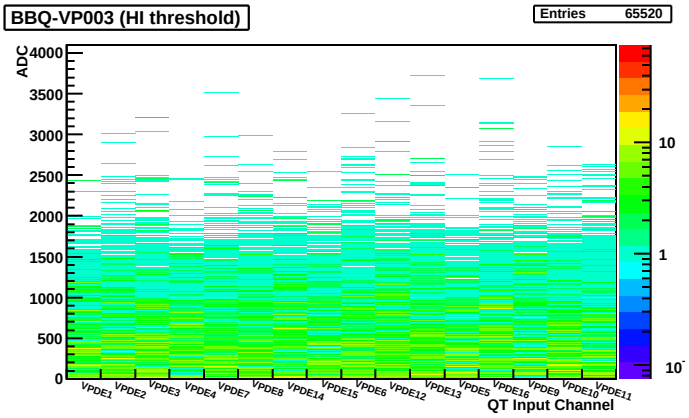

BBQ-ZD001
Entries 65520
TAC
QT Input Channel

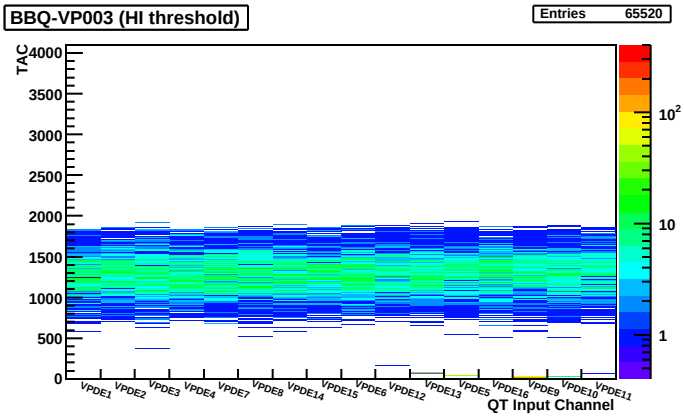

BBQ-VP001 (LO threshold)
Entries 65520
ADC
QT Input Channel

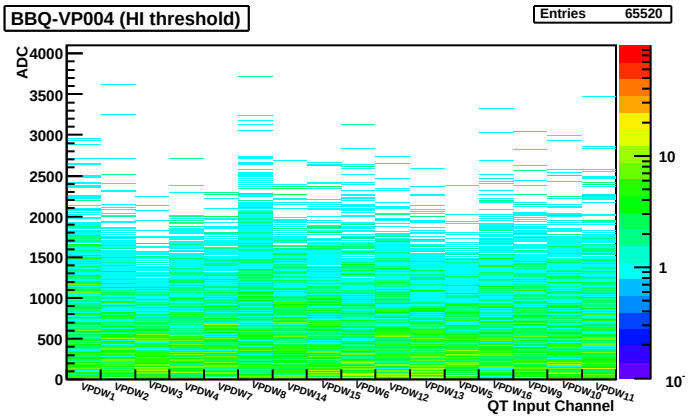

BBQ-VP001 (LO threshold)
Entries 65520
TAC
QT Input Channel

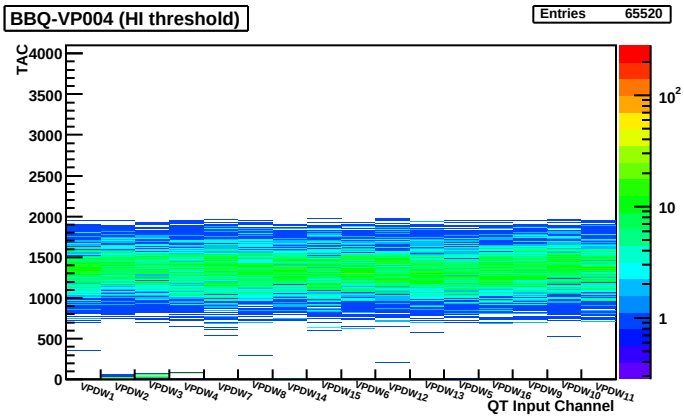

BBQ-VP002 (LO threshold)
Entries 65520
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 65520
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 65520
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 65520
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 65520
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 65520
TAC
QT Input Channel