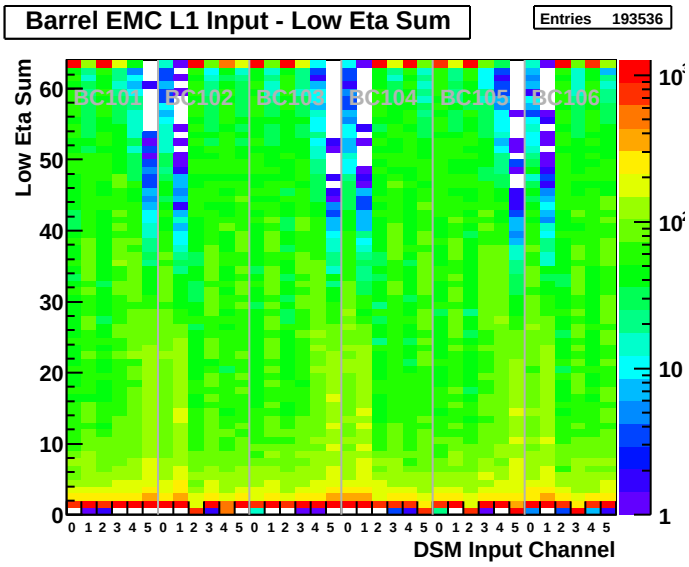
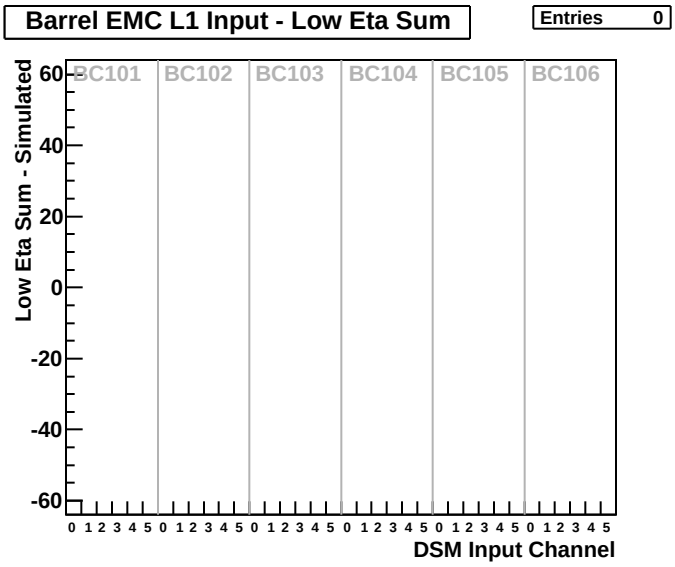
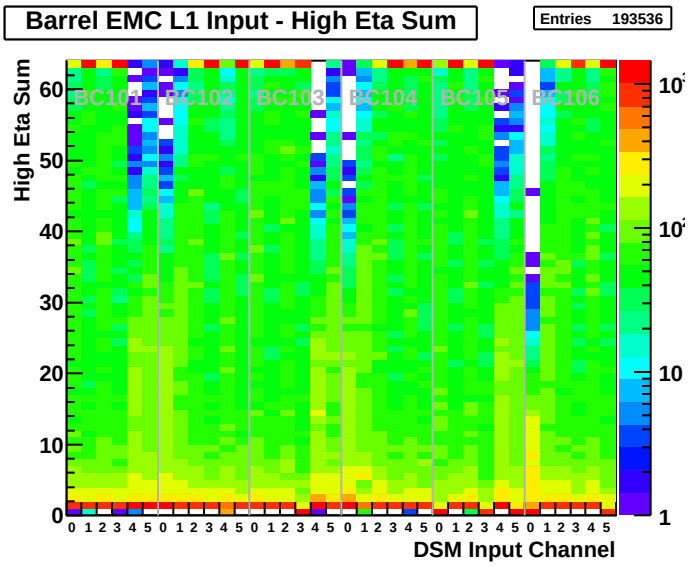
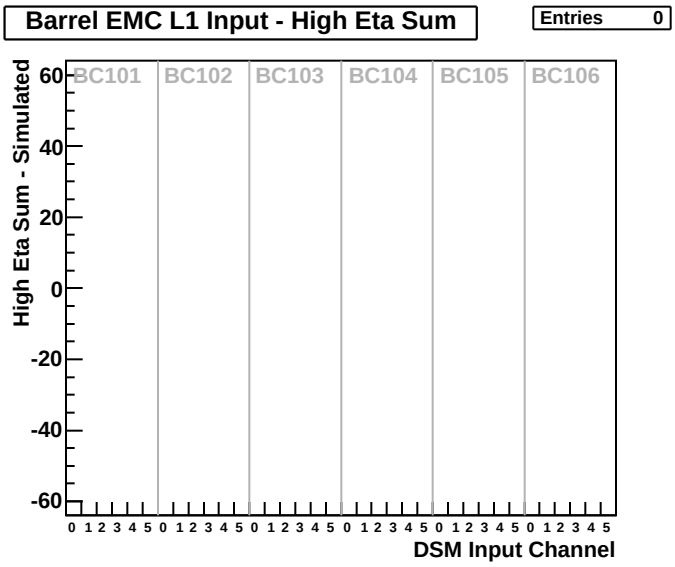
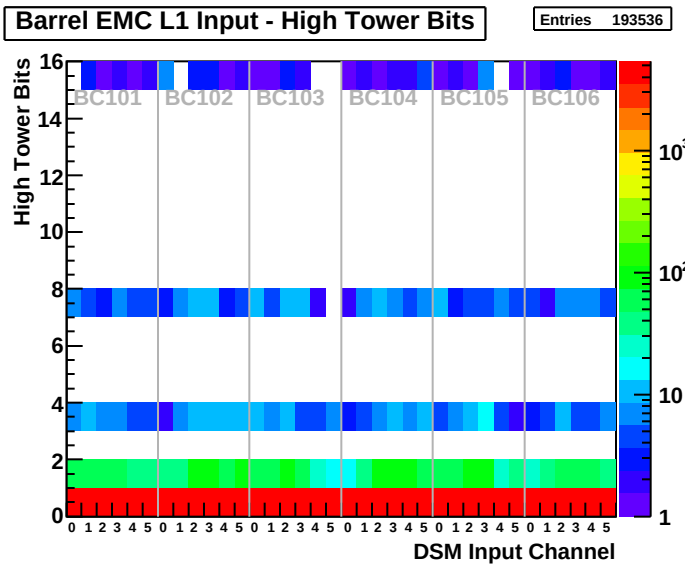
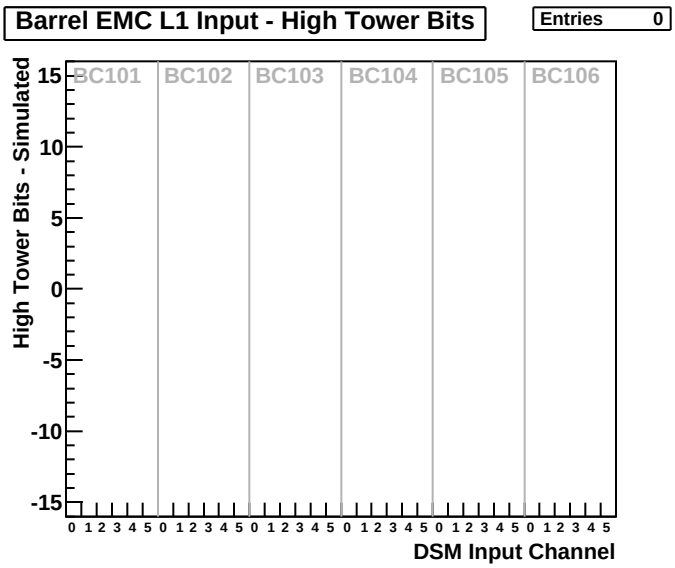

Barrel EMC L1 Input - Low Eta Sum
Entries 193536
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

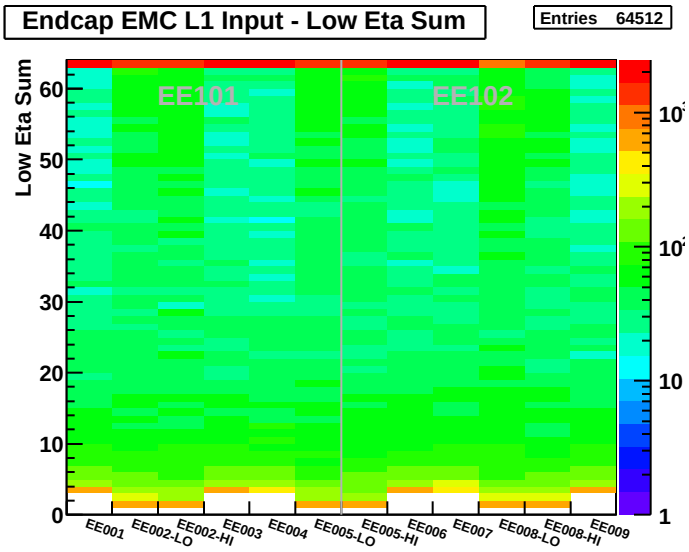

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

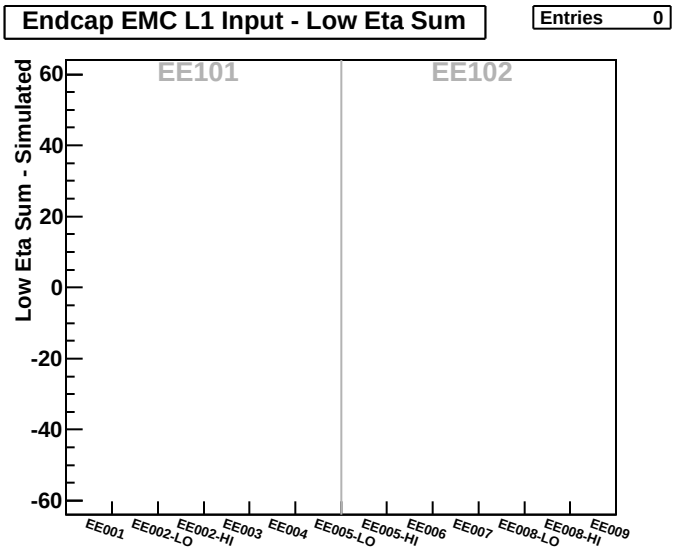

Barrel EMC L1 Input - High Eta Sum
Entries 193536
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

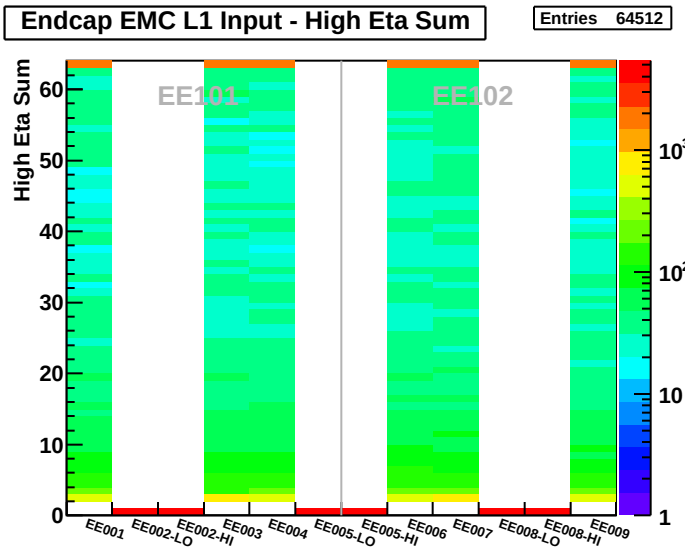

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

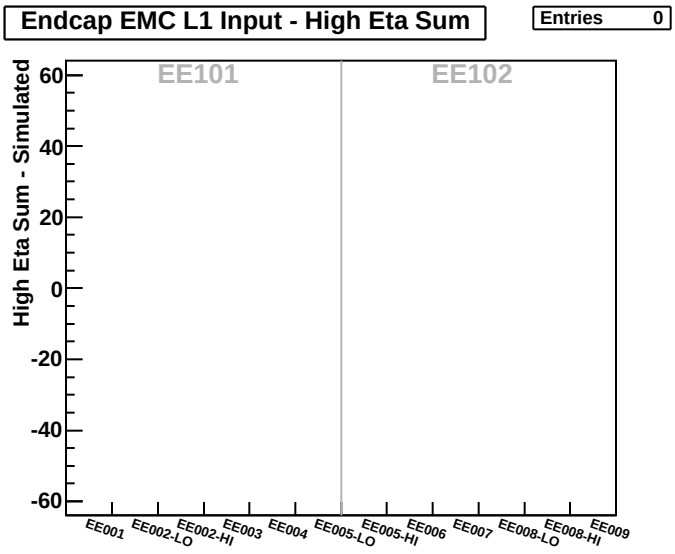

Barrel EMC L1 Input - High Tower Bits
Entries 193536
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

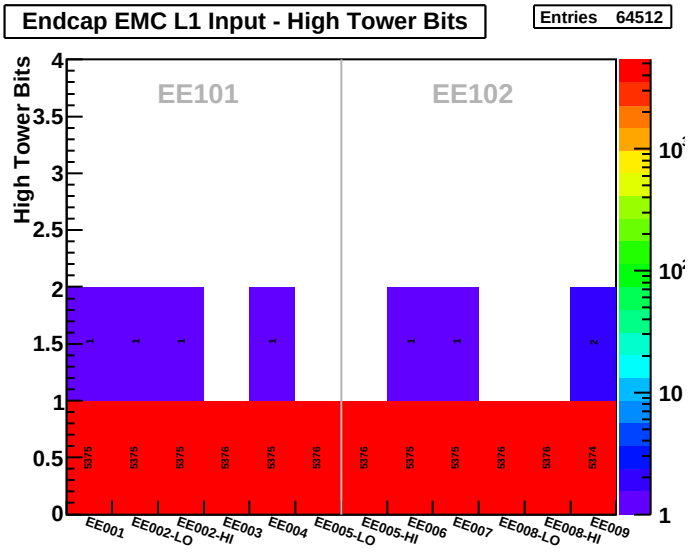

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

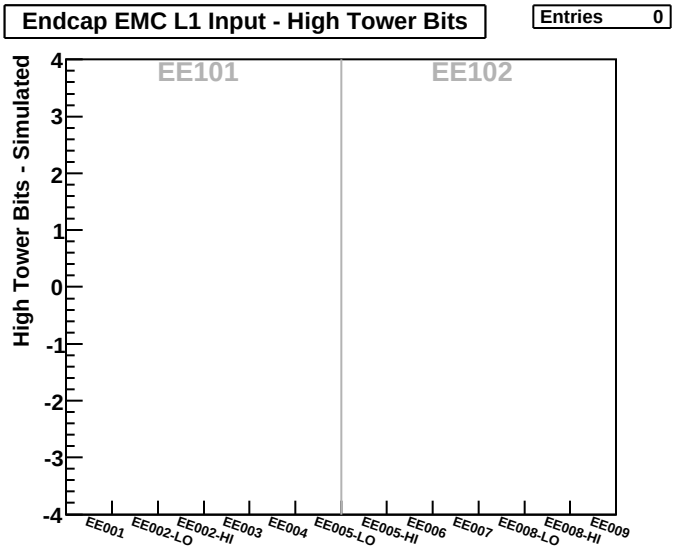

Endcap EMC L1 Input - Low Eta Sum
Entries 64512
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

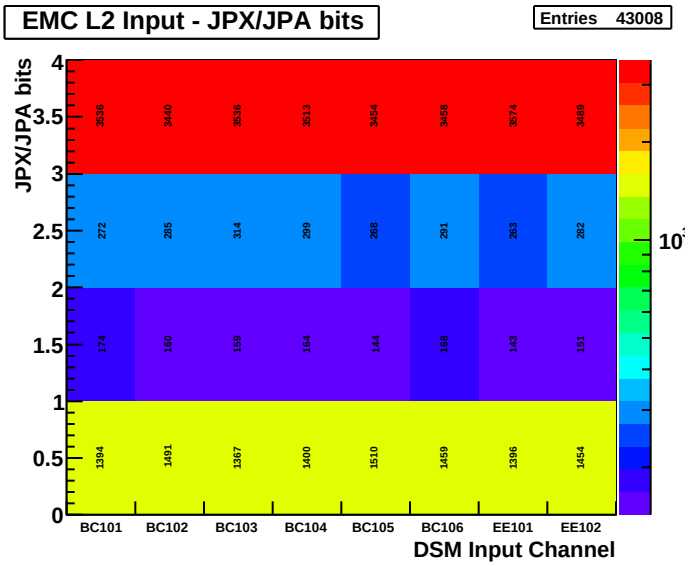

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

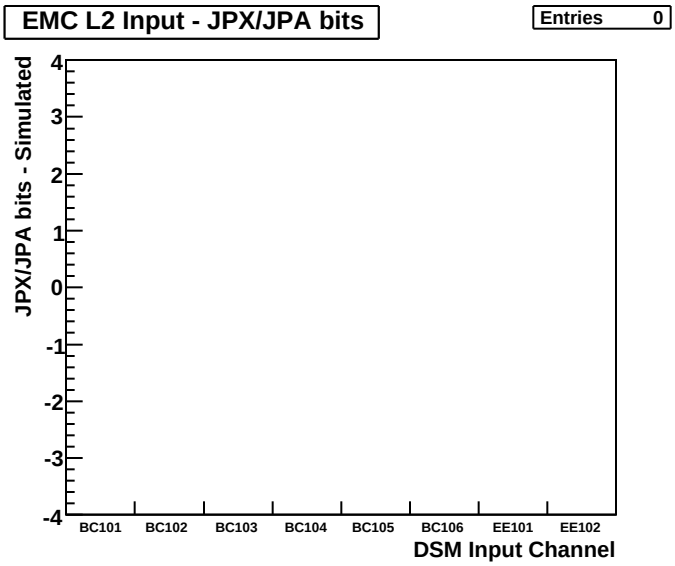

Endcap EMC L1 Input - High Eta Sum
Entries 64512
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

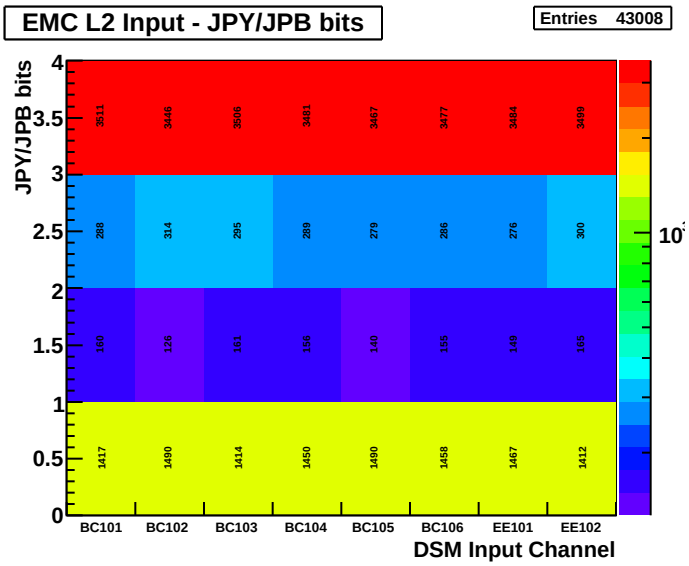

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

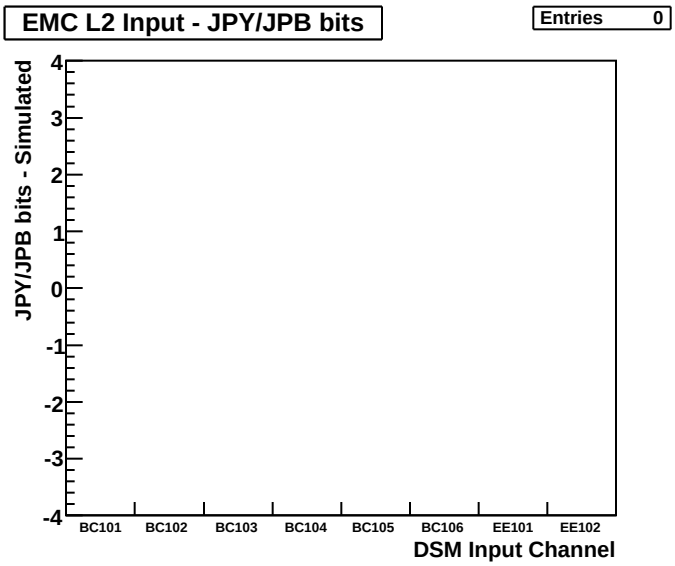

Endcap EMC L1 Input - High Tower Bits
Entries 64512
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

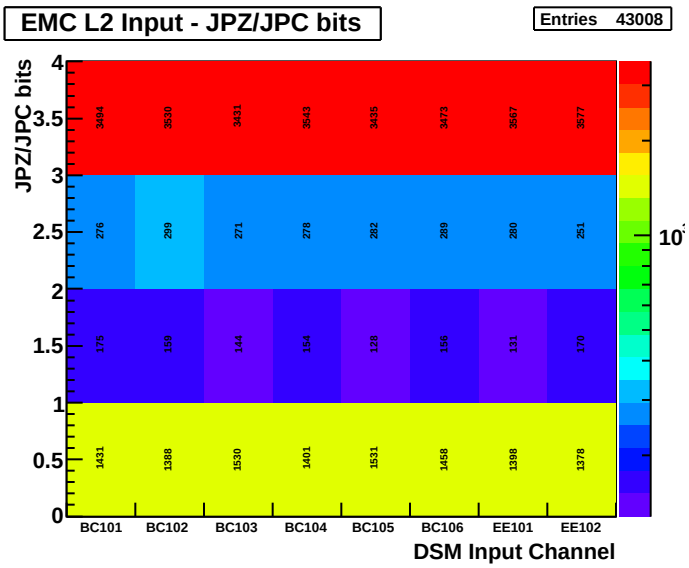

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

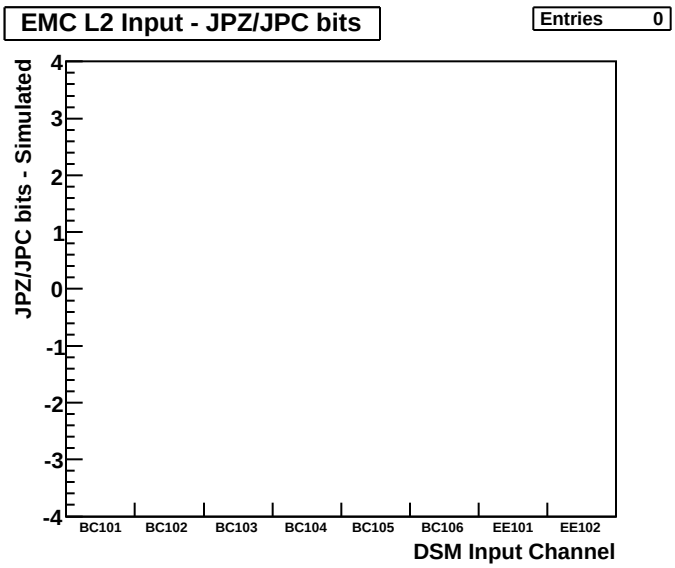

EMC L2 Input - JPX/JPA bits
Entries 43008
JPX/JPA bits
3556 3440 3536 3513 3454 3458 3574 3489
272 285 314 299 288 291 283 282
174 160 159 164 144 168 143 151
1394 1491 1367 1400 1510 1459 1396 1454
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

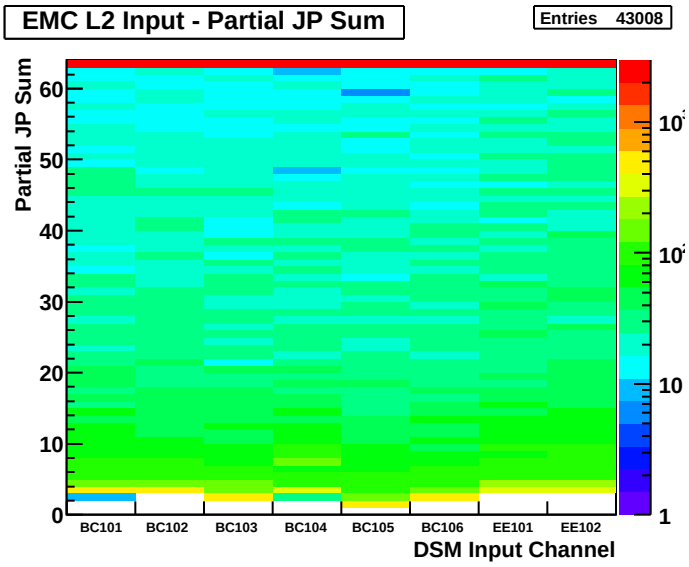

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

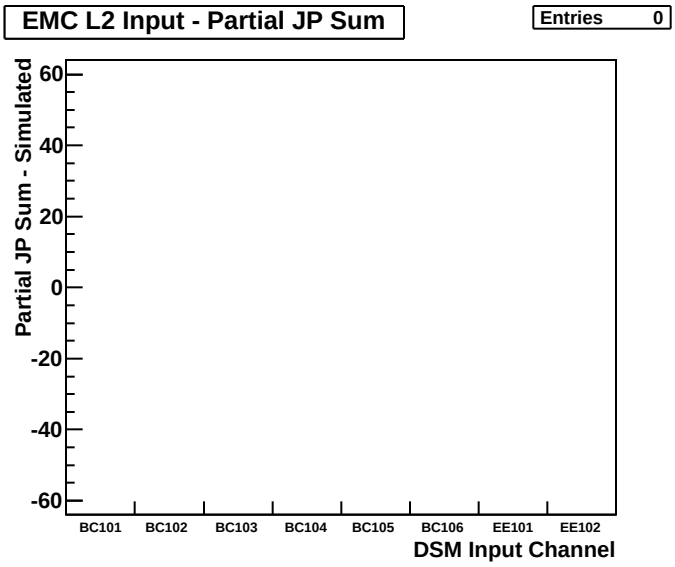

EMC L2 Input - JPY/JPB bits
Entries 43008
JPY/JPB bits
3511 3446 3506 3481 3467 3477 3484 3499
288 314 295 289 279 286 276 300
160 126 161 156 140 155 149 165
1417 1490 1414 1450 1490 1458 1467 1412
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

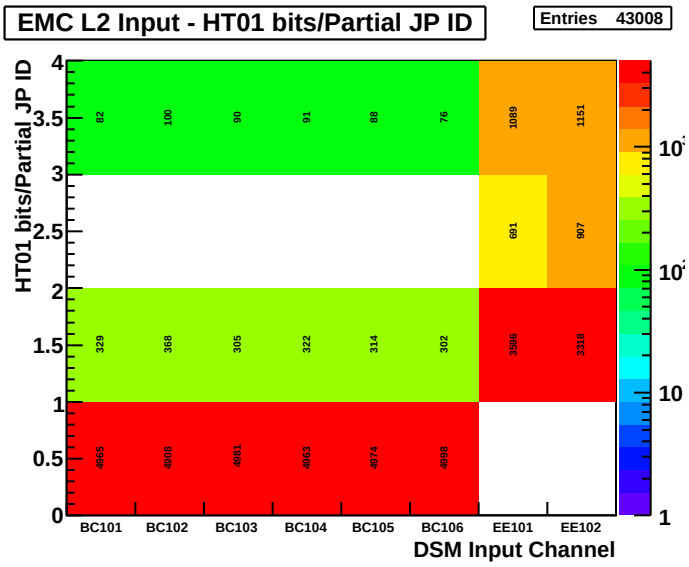

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

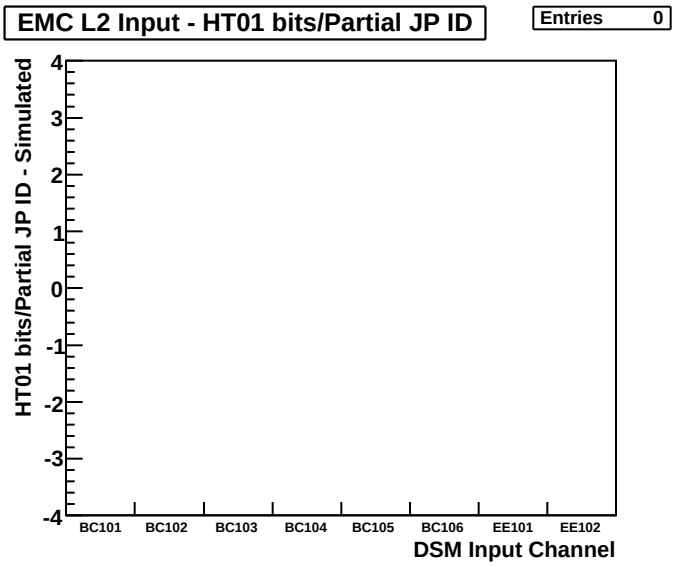

EMC L2 Input - JPZ/JPC bits
Entries 43008
JPZ/JPC bits
3494 3530 3431 3543 3435 3473 3567 3577
276 299 271 278 282 289 280 251
175 159 144 154 128 156 131 170
1431 1388 1530 1401 1531 1458 1398 1378
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

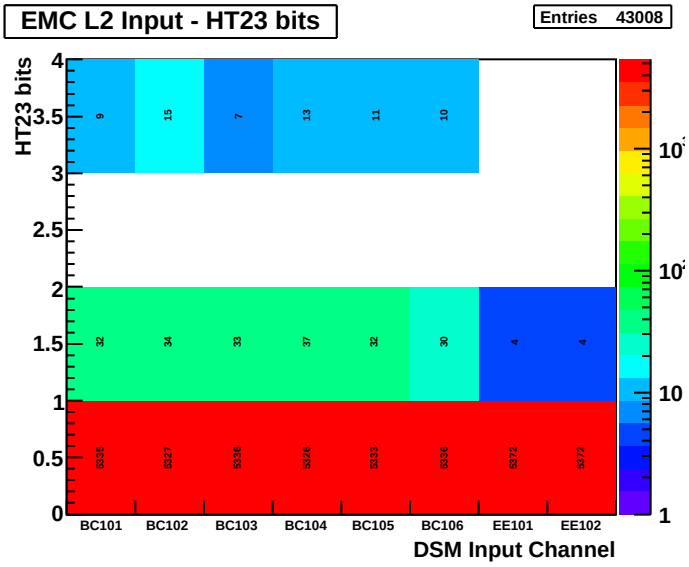

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
DSM Input Channel

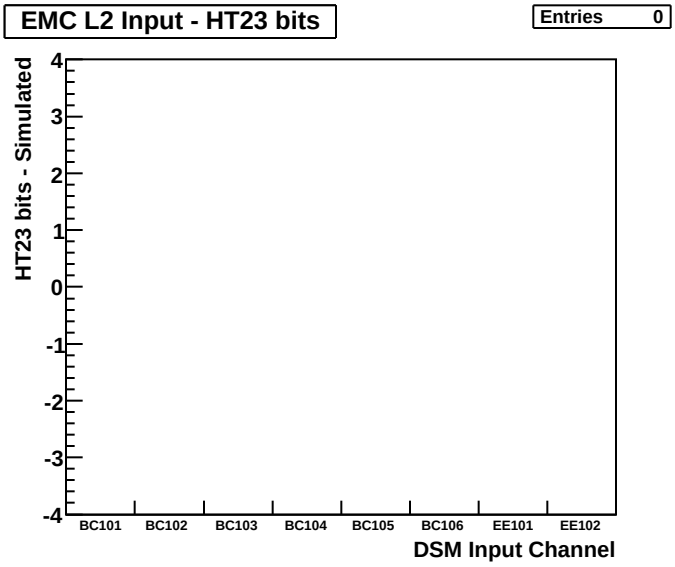

EMC L2 Input - Partial JP Sum
Entries 43008
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

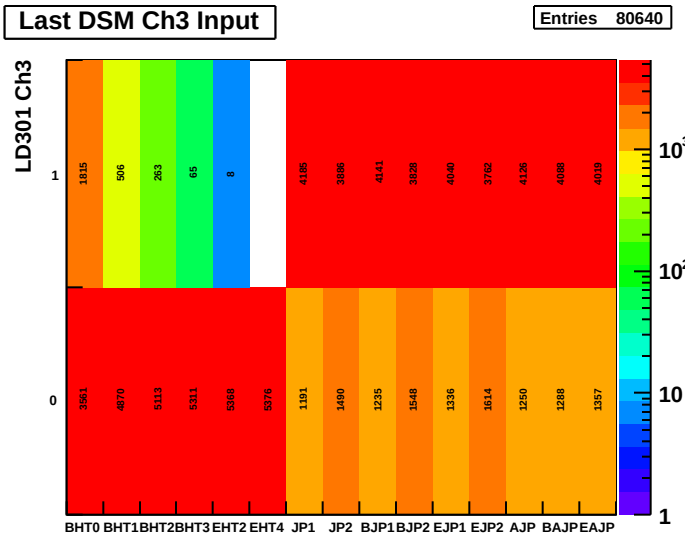

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

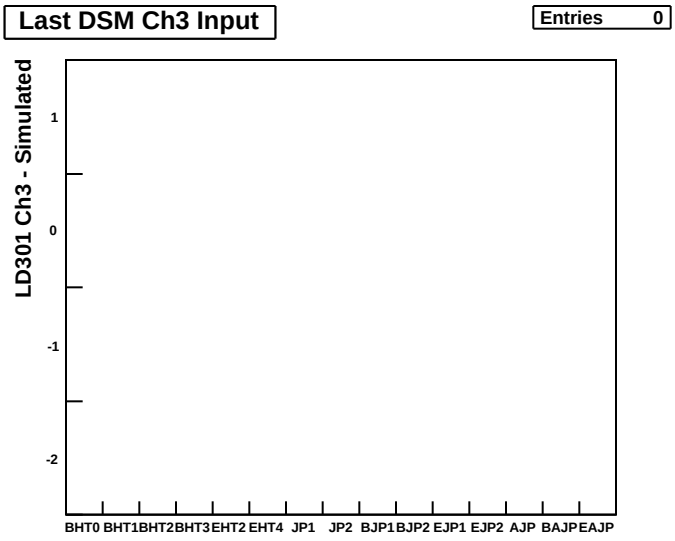

EMC L2 Input - HT01 bits/Partial JP ID
Entries 43008
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

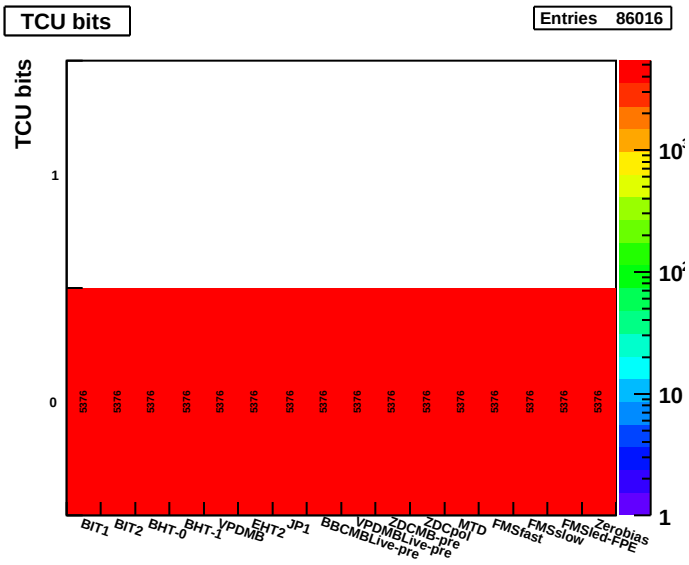

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

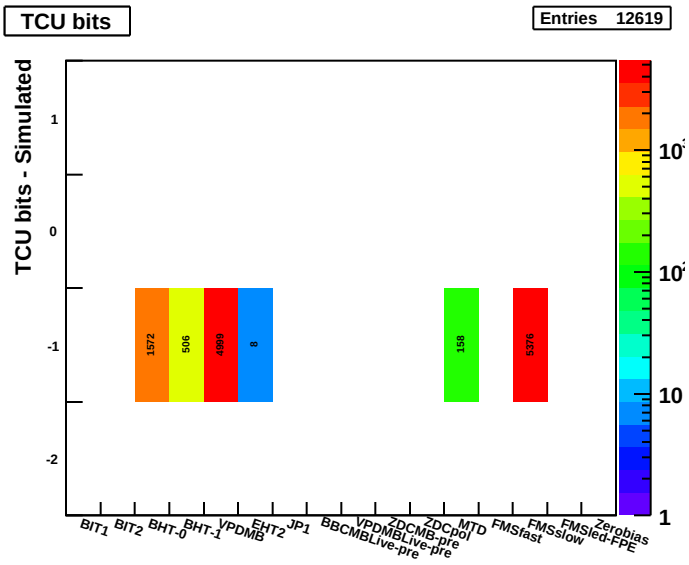

EMC L2 Input - HT23 bits
Entries 43008
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

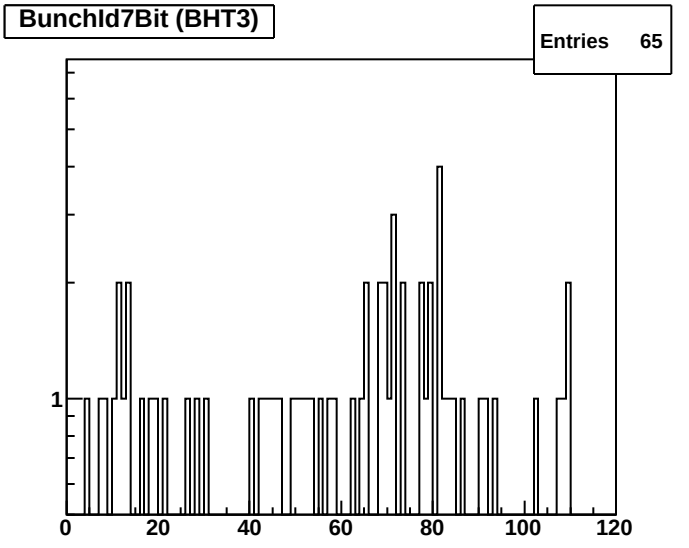

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

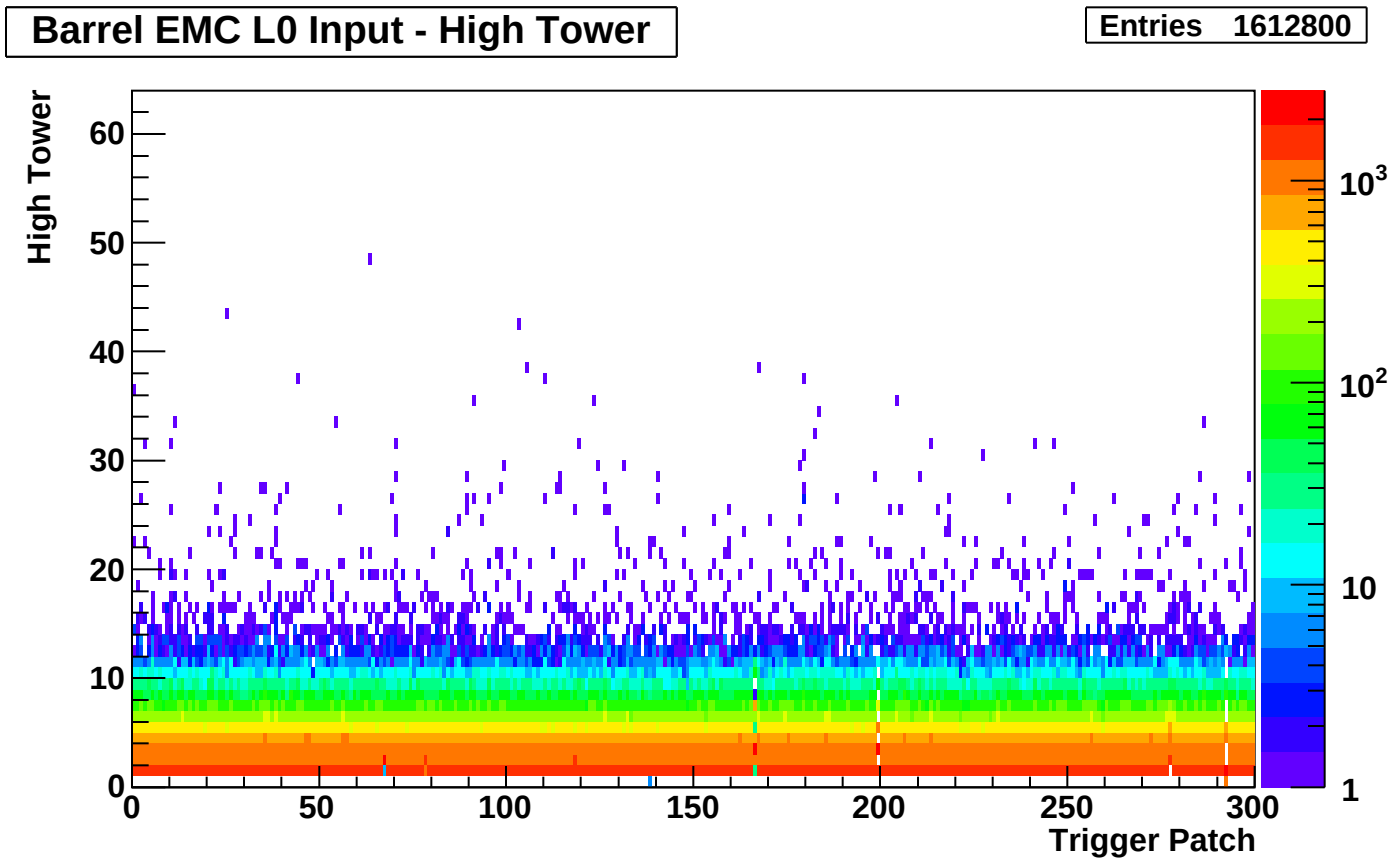

Last DSM Ch3 Input
Entries 80640
LD301 Ch3
1815 506 263 65 8 4185 3886 4141 3828 4040 3762 4126 4088 4019
3561 4870 5113 5311 5368 5376 1191 1490 1235 1548 1336 1614 1250 1288 1357
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

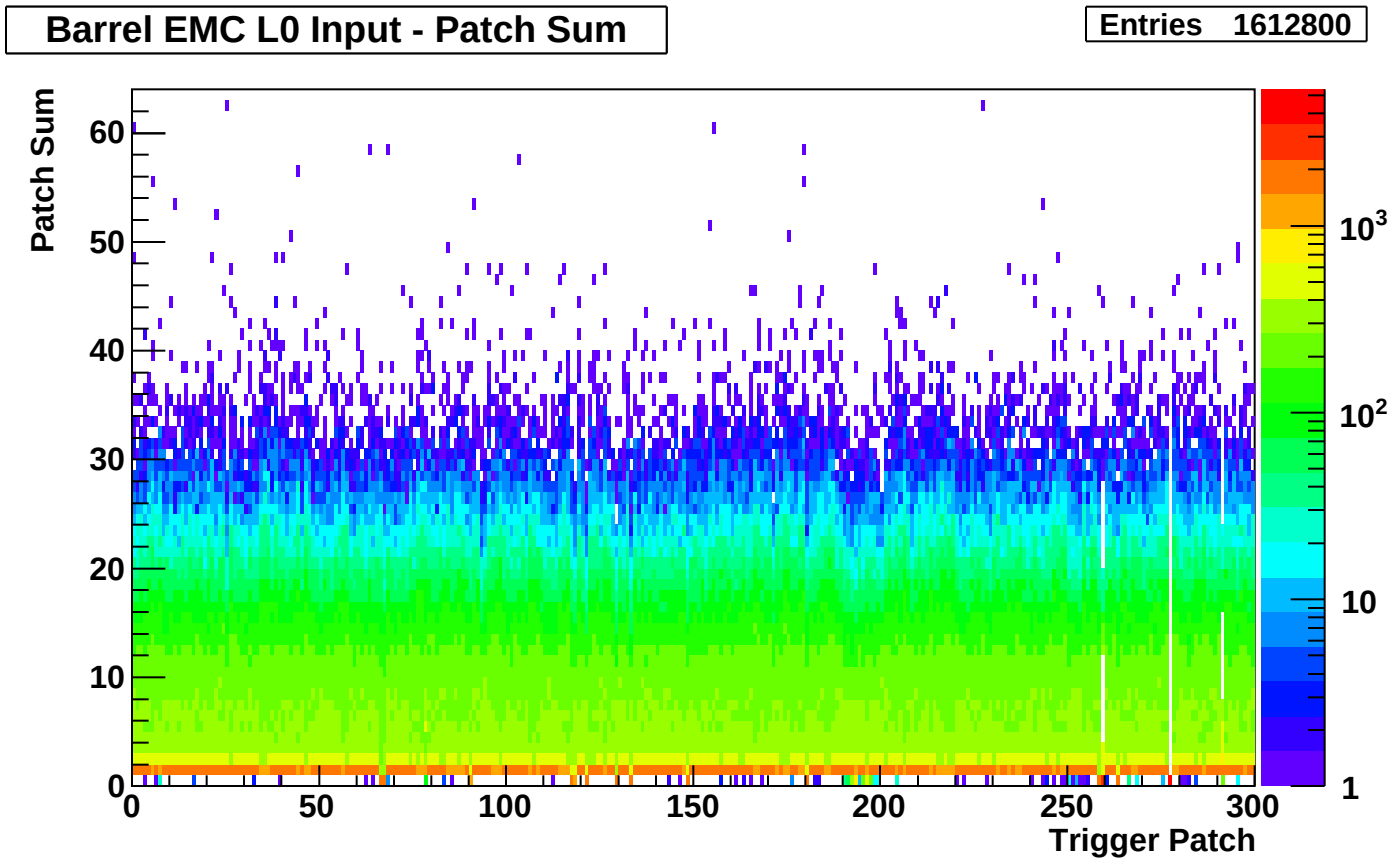

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

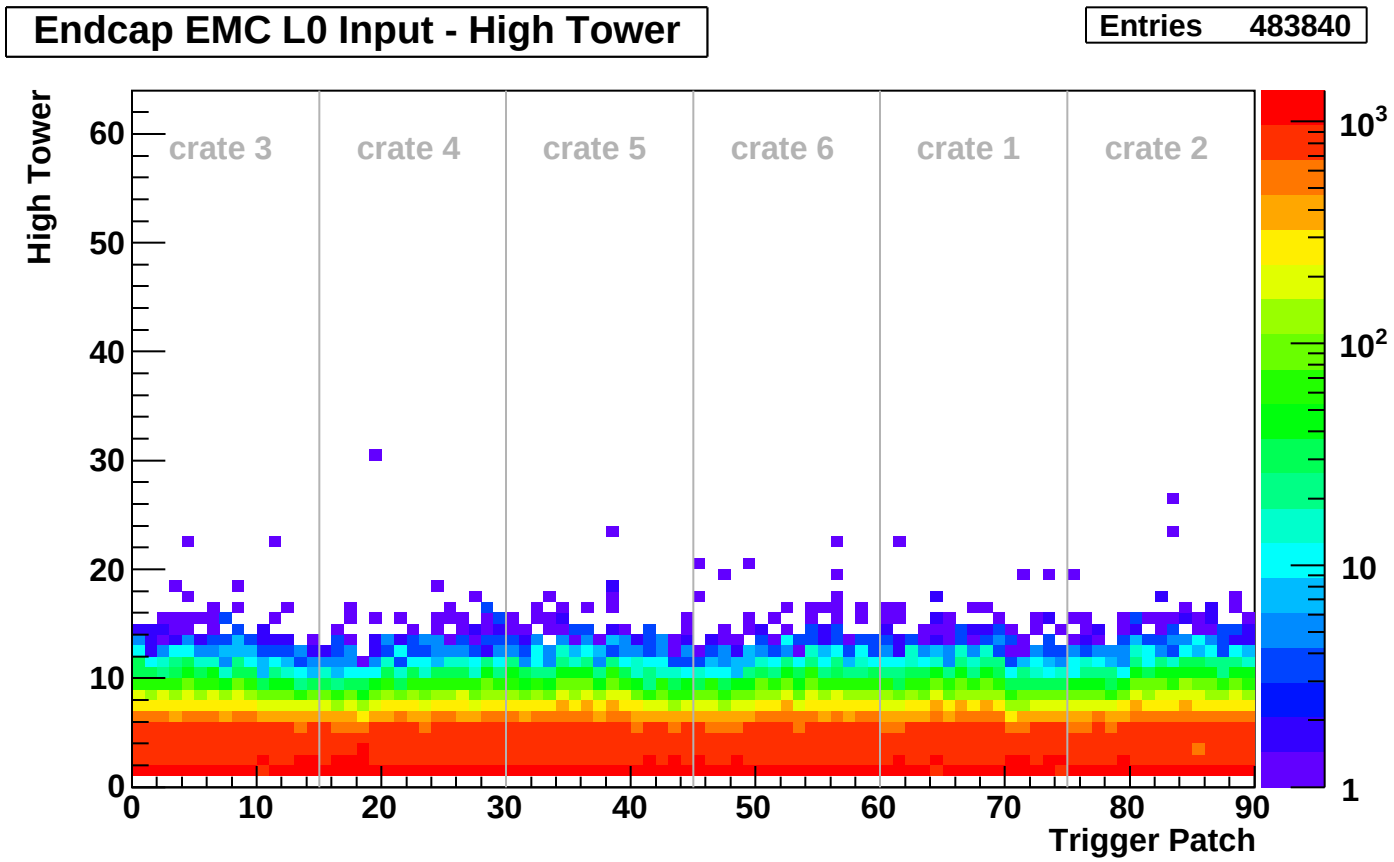

TCU bits
Entries 86016
TCU bits
5376 5376 5376 5376 5376 5376 5376 5376 5376 5376 5376 5376 5376 5376 5376 5376
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

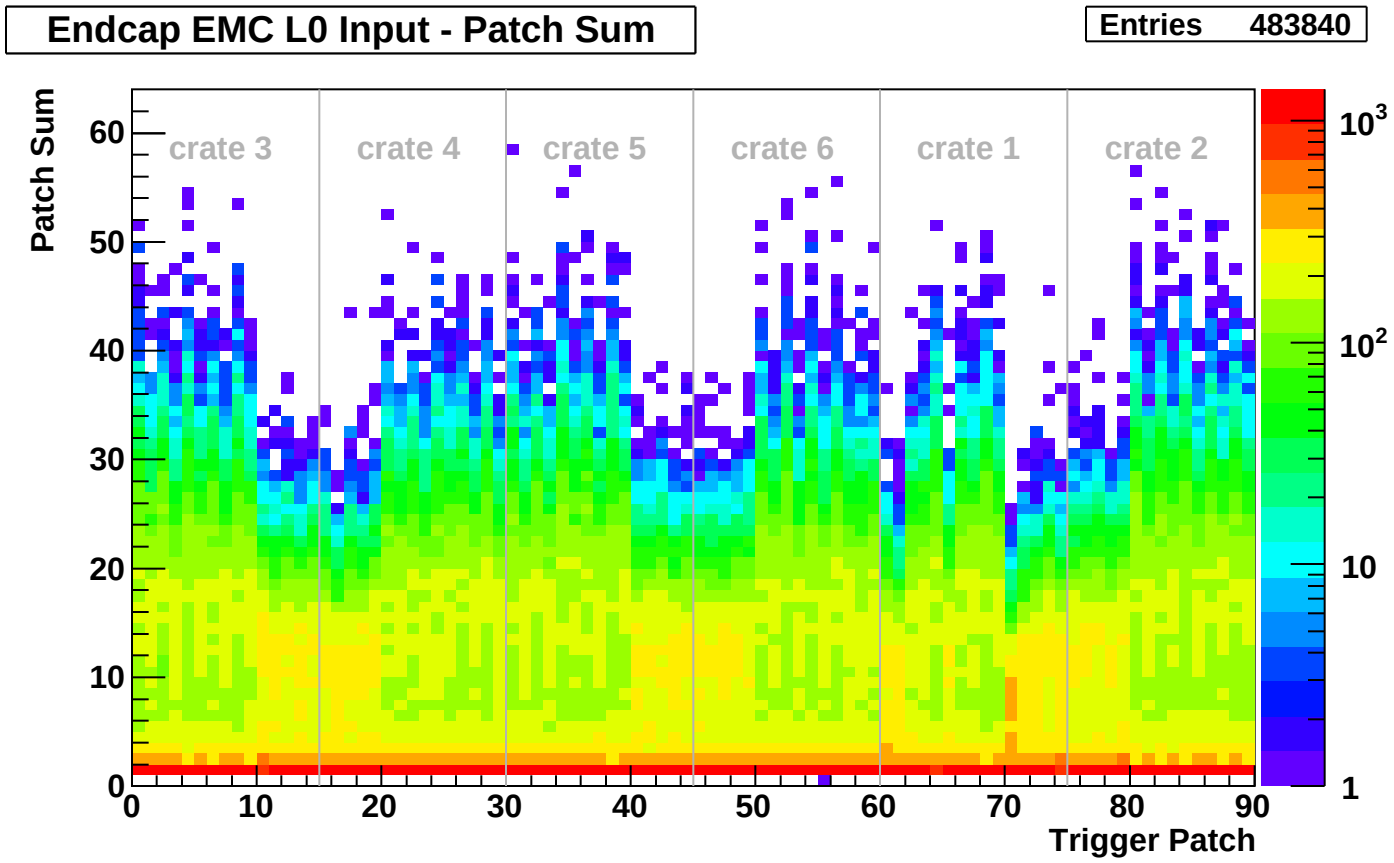

TCU bits
Entries 12619
TCU bits - Simulated
1572 506 4999 8 158 5376
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

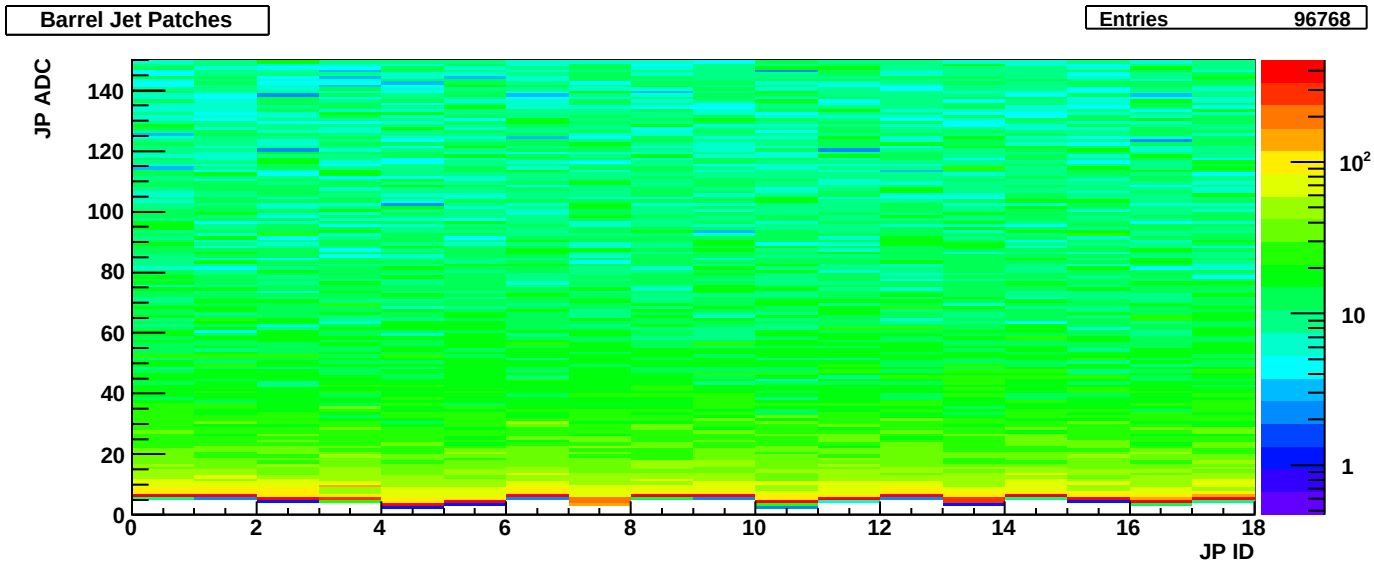

BunchId7Bit (BHT3)
Entries 65
0 20 40 60 80 100 120

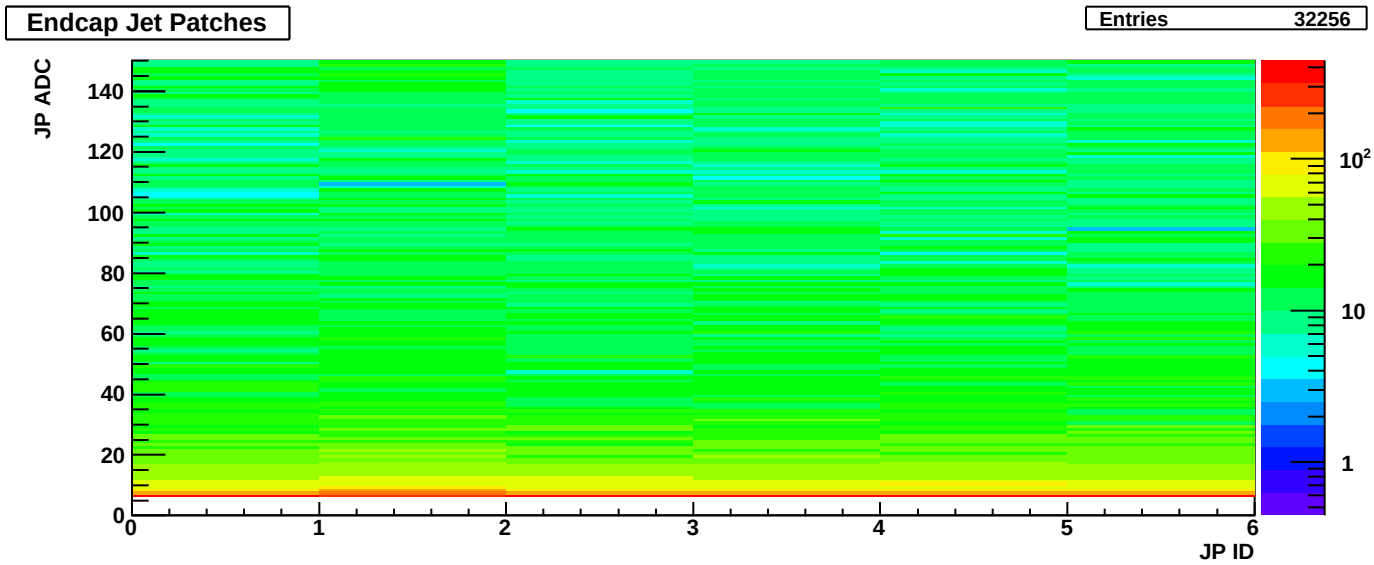

Barrel EMC L0 Input - High Tower
Entries 1612800
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

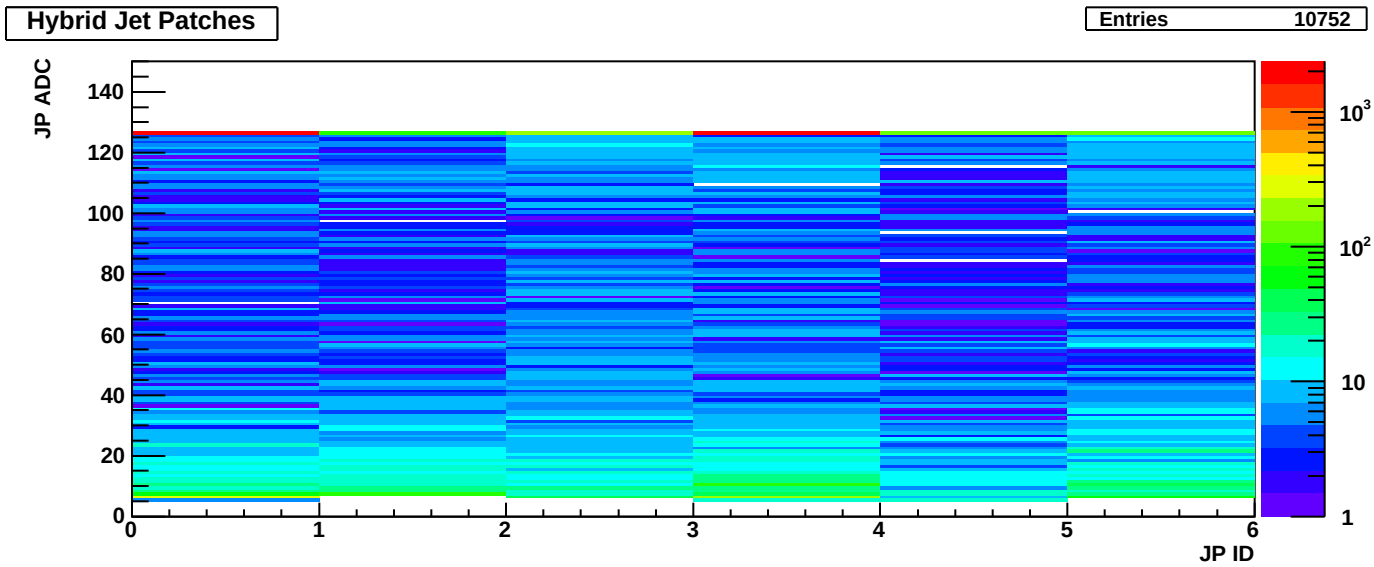

Barrel EMC L0 Input - Patch Sum
Entries 1612800
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

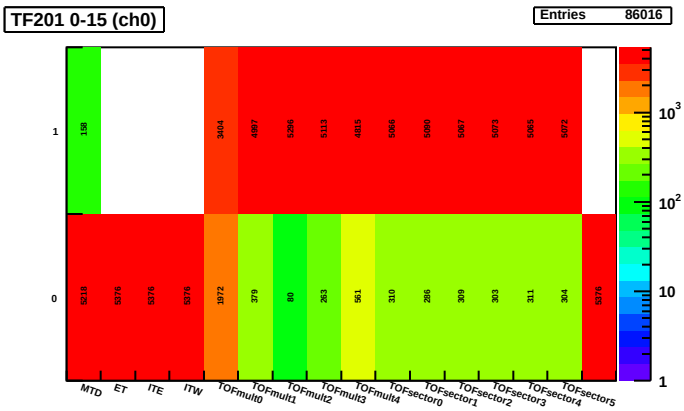

Endcap EMC L0 Input - High Tower
Entries 483840
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3

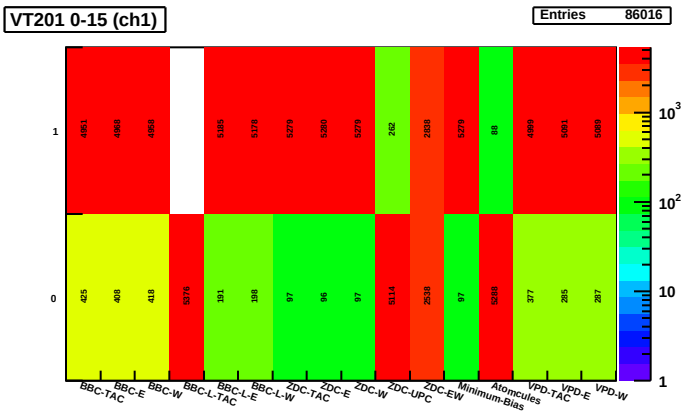

Endcap EMC L0 Input - Patch Sum
Entries 483840
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3

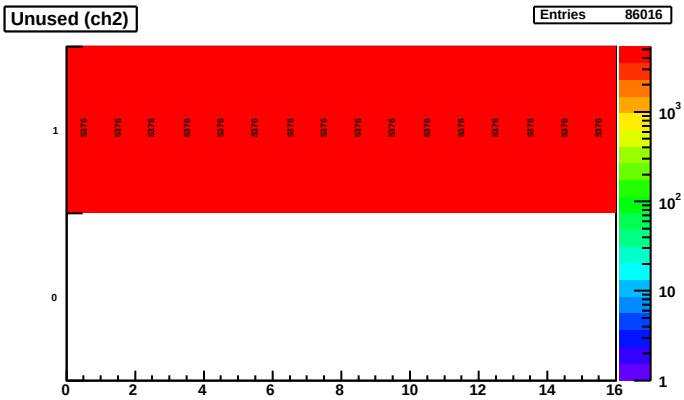

Barrel Jet Patches
Entries 96768
JP ADC
JP ID

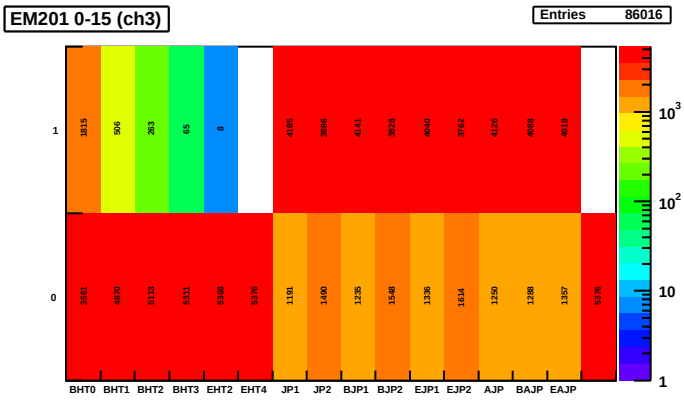

Endcap Jet Patches
Entries 32256
JP ADC
JP ID

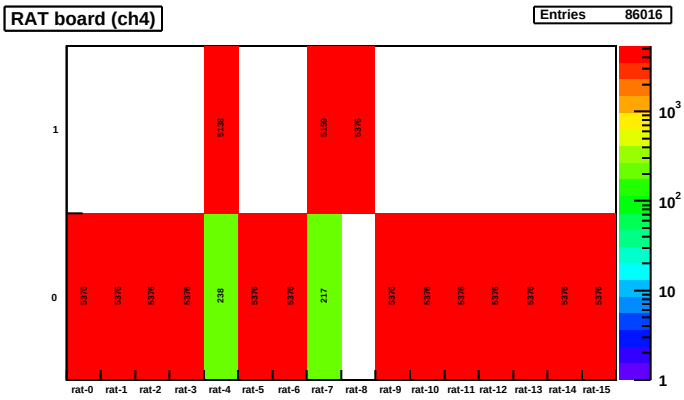

Hybrid Jet Patches
Entries 10752
JP ADC
JP ID

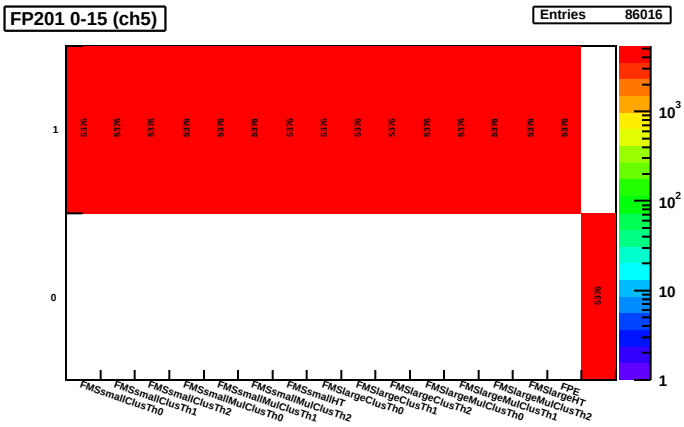

TF201 0-15 (ch0)
Entries 86016
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

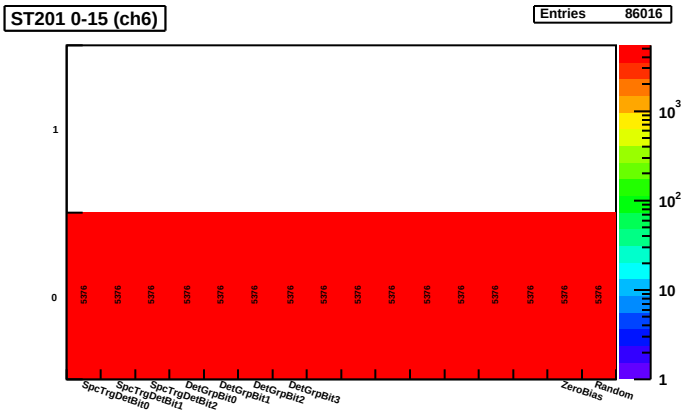

VT201 0-15 (ch1)
Entries 86016
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

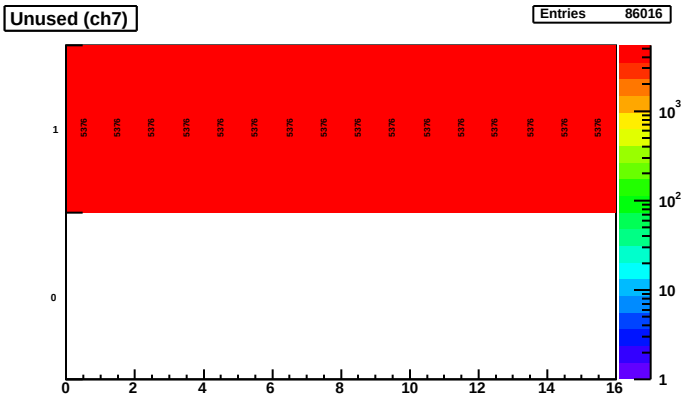

Unused (ch2)
Entries 86016

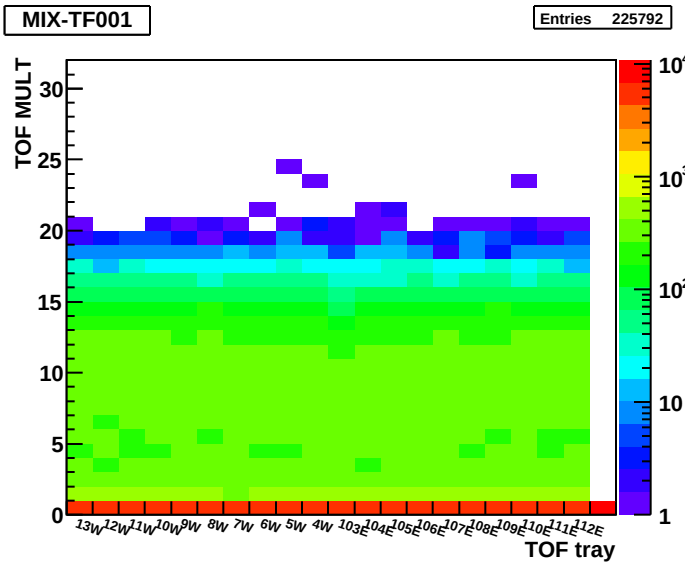

EM201 0-15 (ch3)
Entries 86016
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

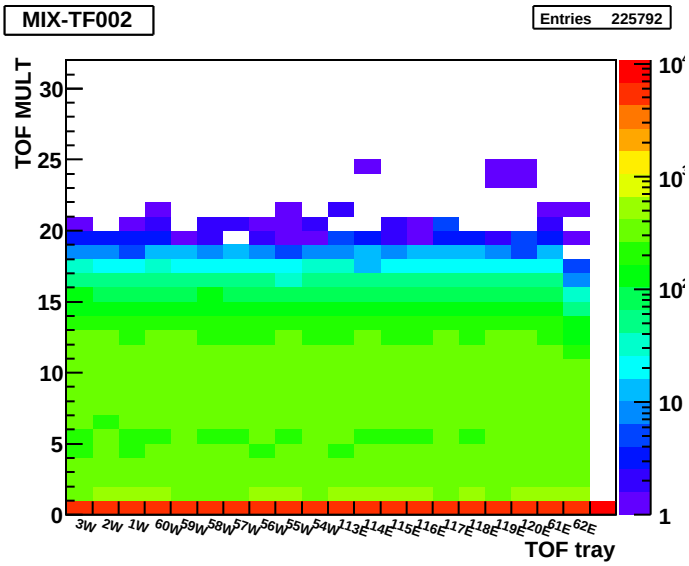

RAT board (ch4)
Entries 86016
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

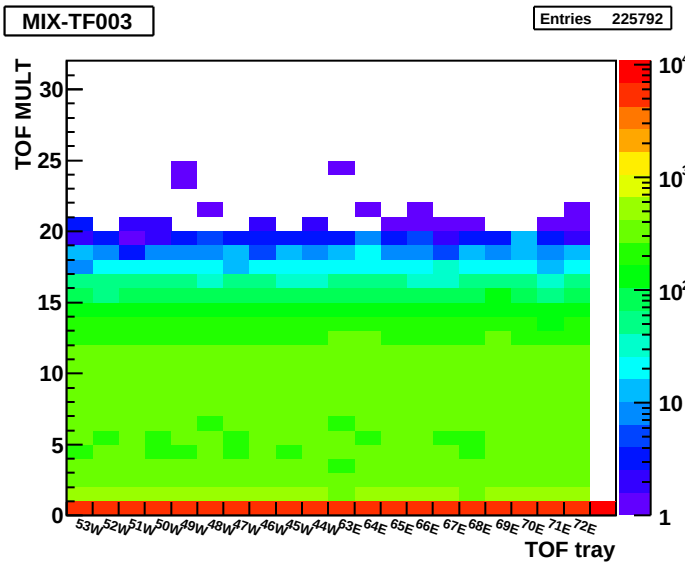

FP201 0-15 (ch5)
Entries 86016
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

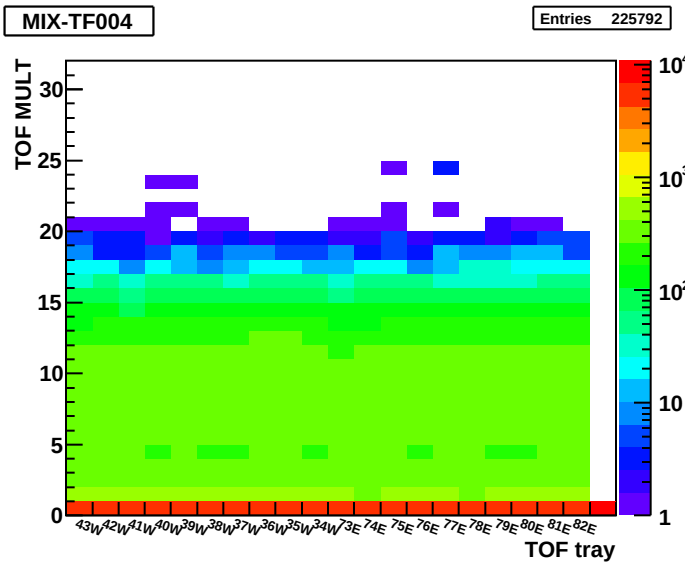

ST201 0-15 (ch6)
Entries 86016
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

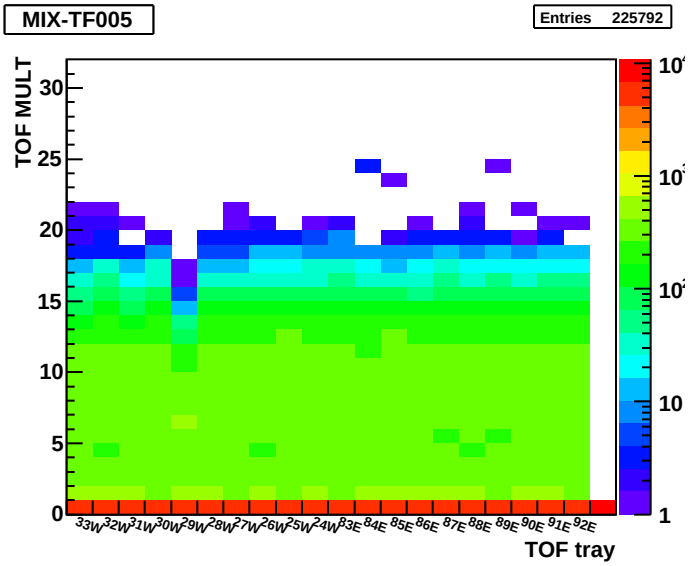

Unused (ch7)
Entries 86016

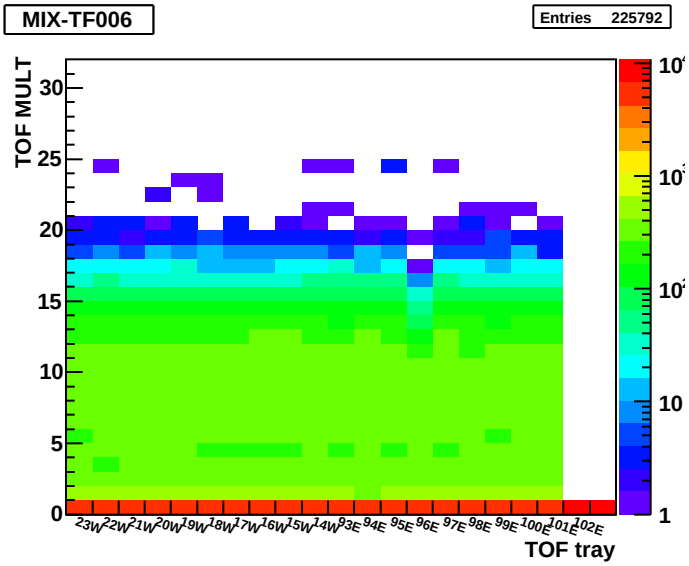

MIX-TF001
Entries 225792
TOF MULT
TOF tray

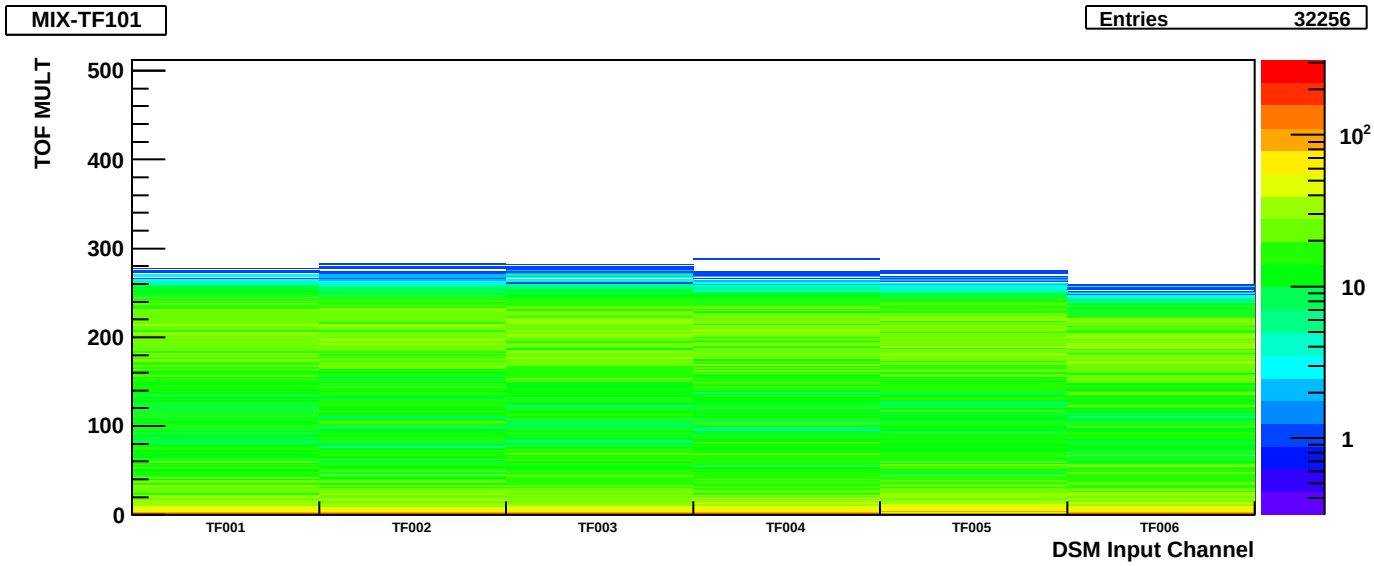

MIX-TF002
Entries 225792
TOF MULT
TOF tray

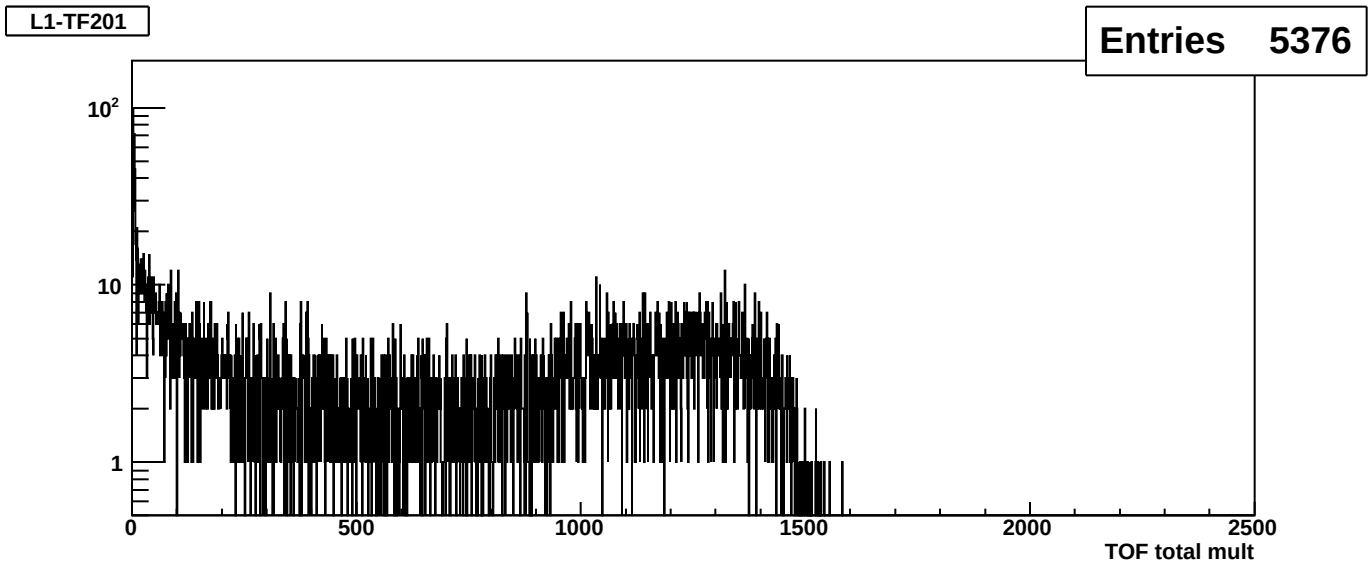

MIX-TF003
Entries 225792
TOF MULT
TOF tray

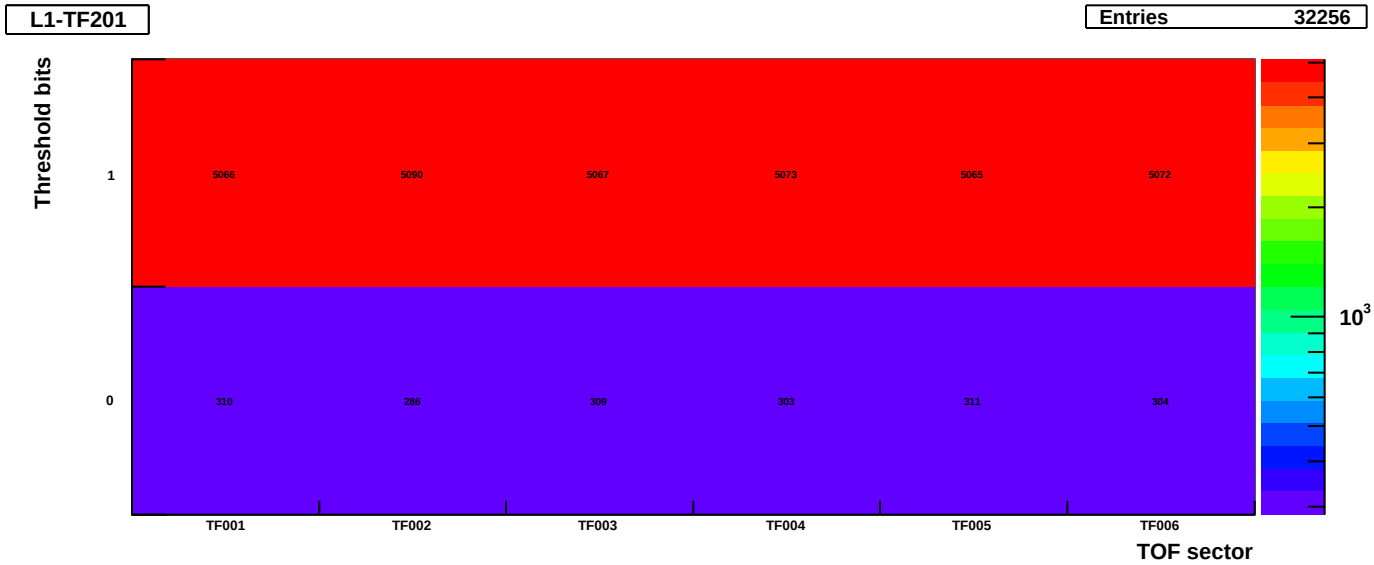

MIX-TF004
Entries 225792
TOF MULT
TOF tray

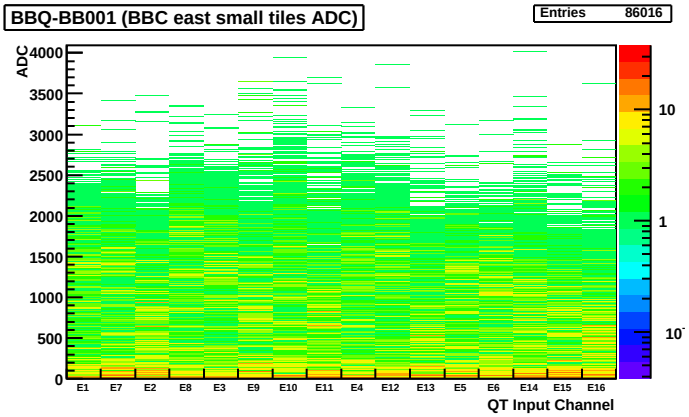

MIX-TF005
Entries 225792
TOF MULT
TOF tray

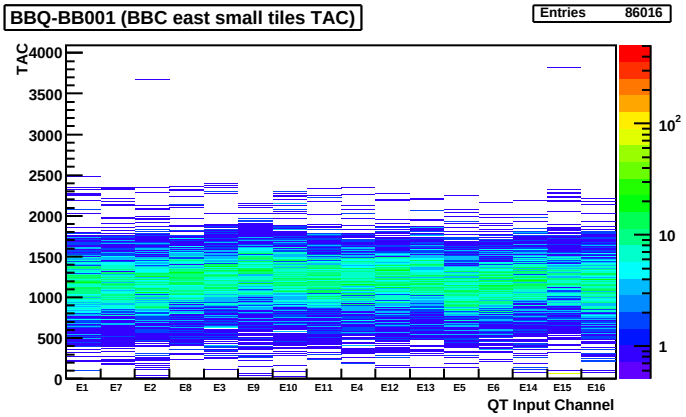

MIX-TF006
Entries 225792
TOF MULT
TOF tray

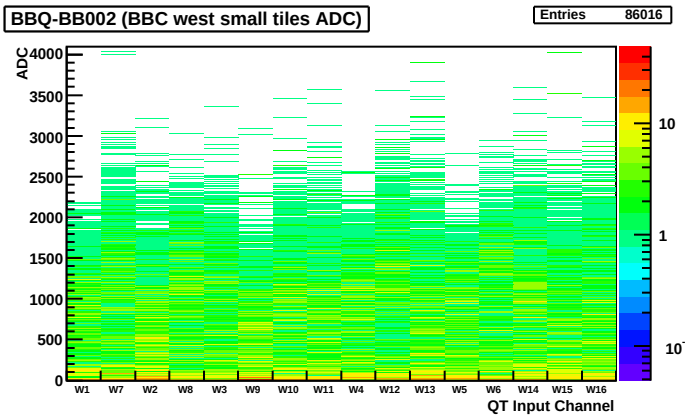

MIX-TF101
Entries 32256
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^2
10
1

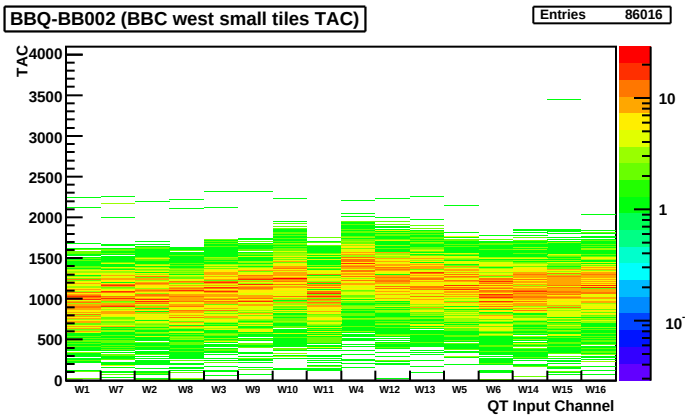

L1-TF201
Entries 5376
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

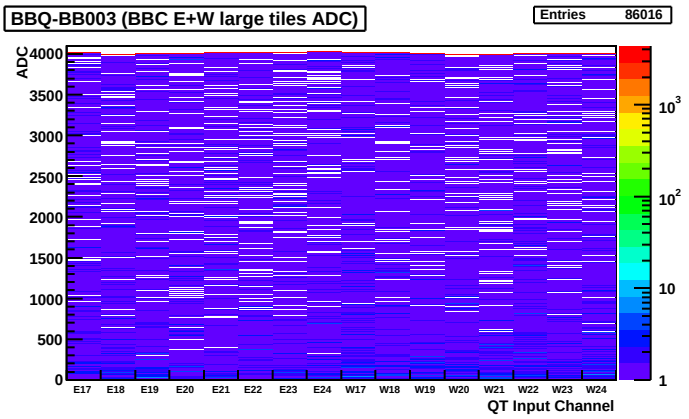

L1-TF201
Entries 32256
Threshold bits
1
0
5066
5090
5067
5073
5065
5072
310
286
309
303
311
304
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^3

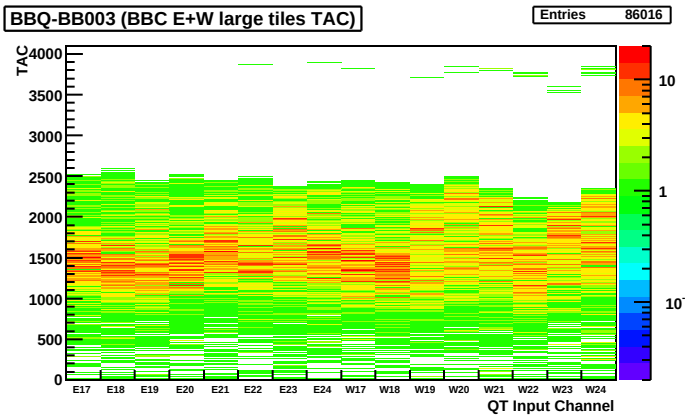

BBQ-BB001 (BBC east small tiles ADC)
Entries 86016
ADC
QT Input Channel

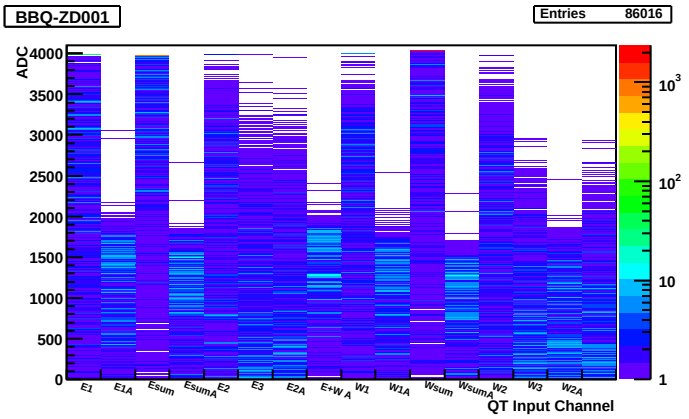

BBQ-BB001 (BBC east small tiles TAC)
Entries 86016
TAC
QT Input Channel

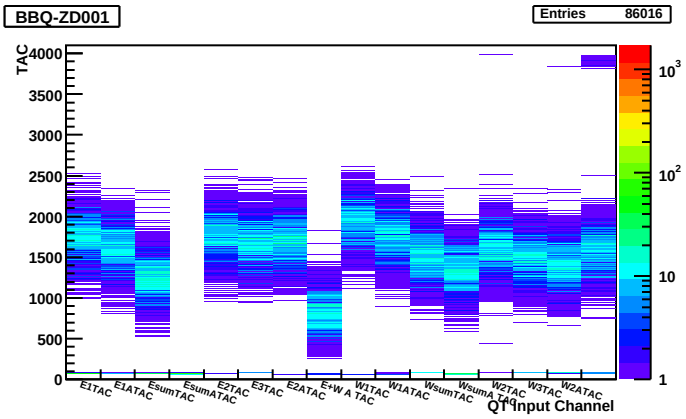

BBQ-BB002 (BBC west small tiles ADC)
Entries 86016
ADC
QT Input Channel

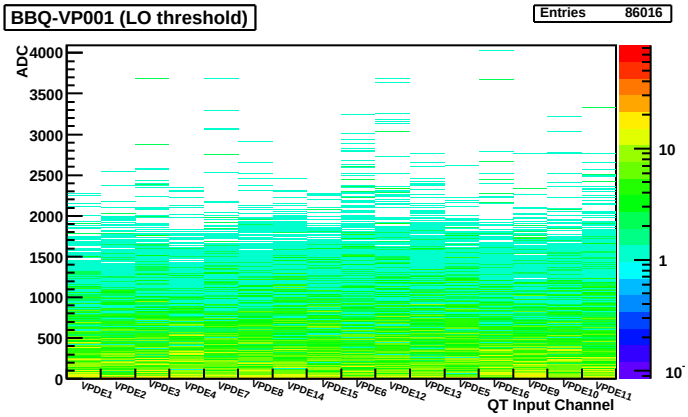

BBQ-BB002 (BBC west small tiles TAC)
Entries 86016
TAC
QT Input Channel

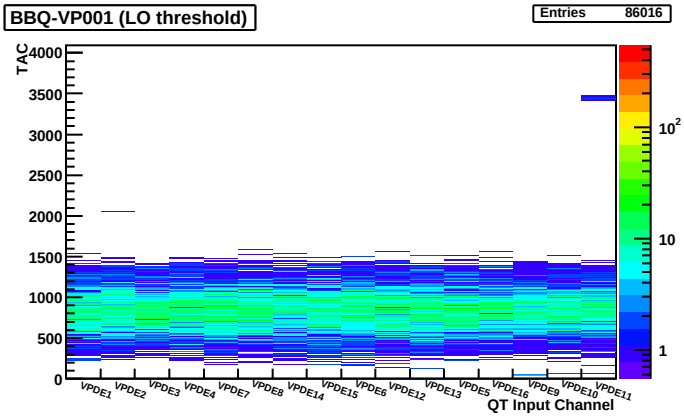

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 86016
ADC
QT Input Channel

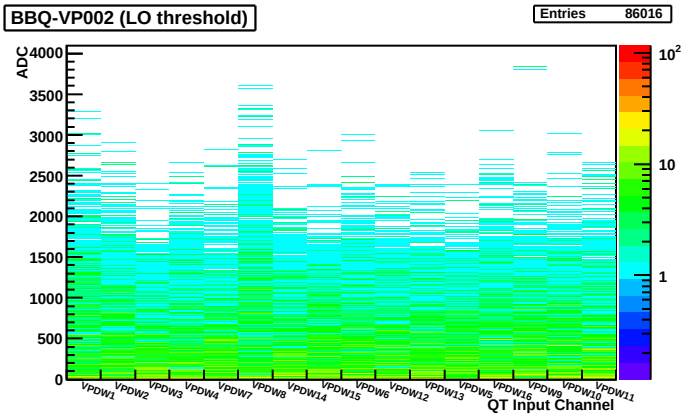

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 86016
TAC
QT Input Channel

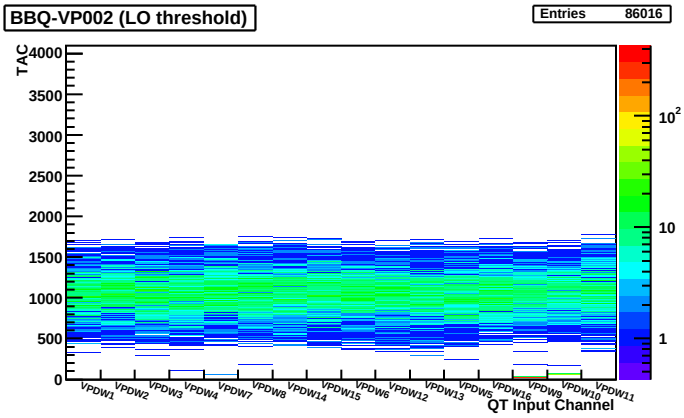

BBQ-ZD001
Entries 86016
ADC
QT Input Channel

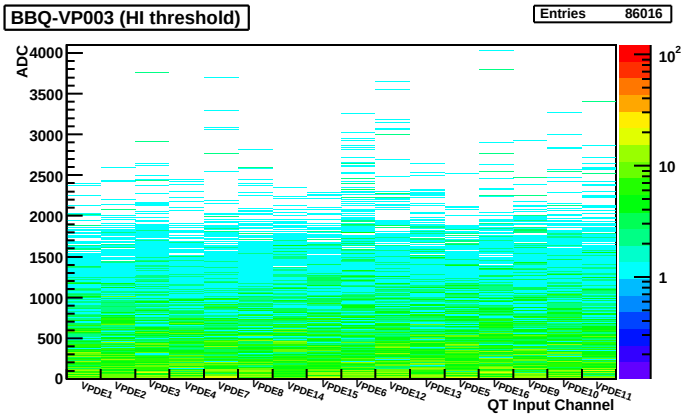

BBQ-ZD001
Entries 86016
TAC
QT Input Channel

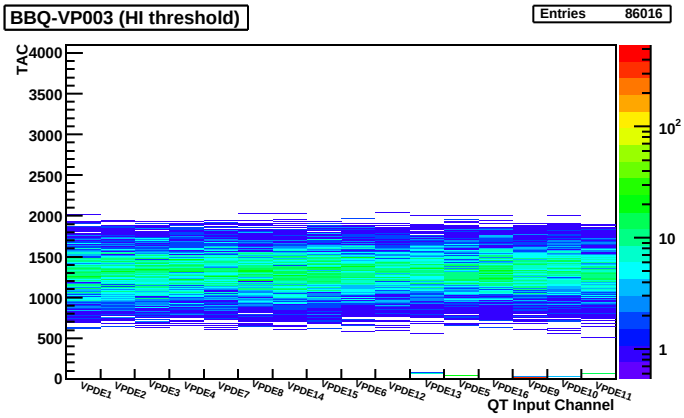

BBQ-VP001 (LO threshold)
Entries 86016
ADC
QT Input Channel

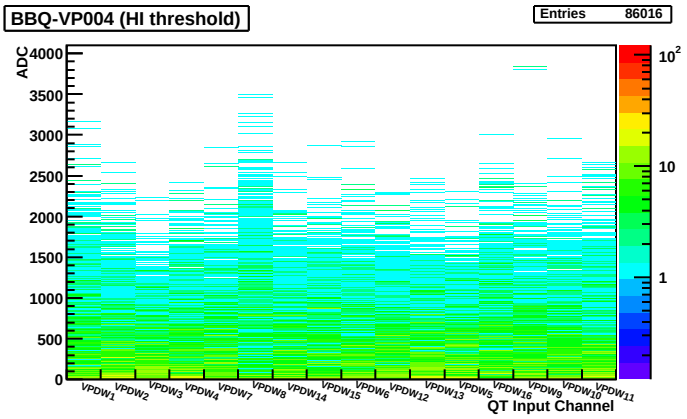

BBQ-VP001 (LO threshold)
Entries 86016
TAC
QT Input Channel

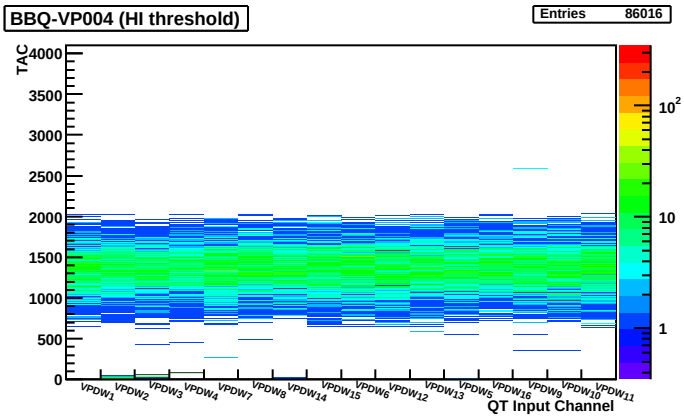

BBQ-VP002 (LO threshold)
Entries 86016
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 86016
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 86016
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 86016
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 86016
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 86016
TAC
QT Input Channel