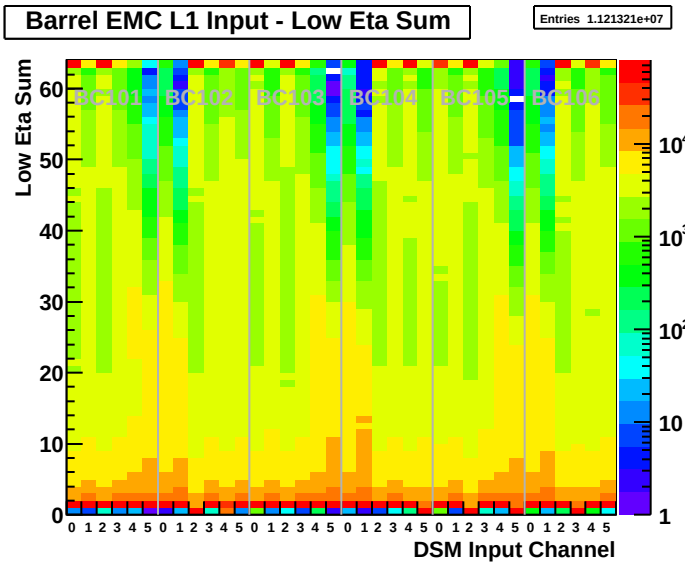
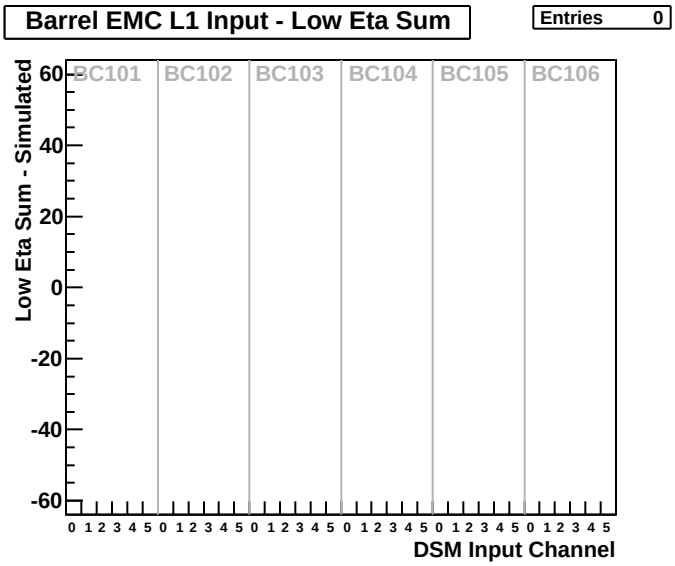
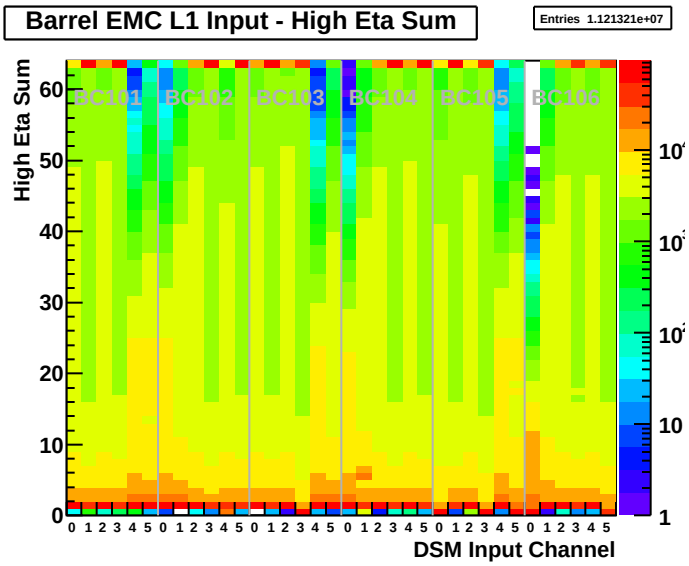
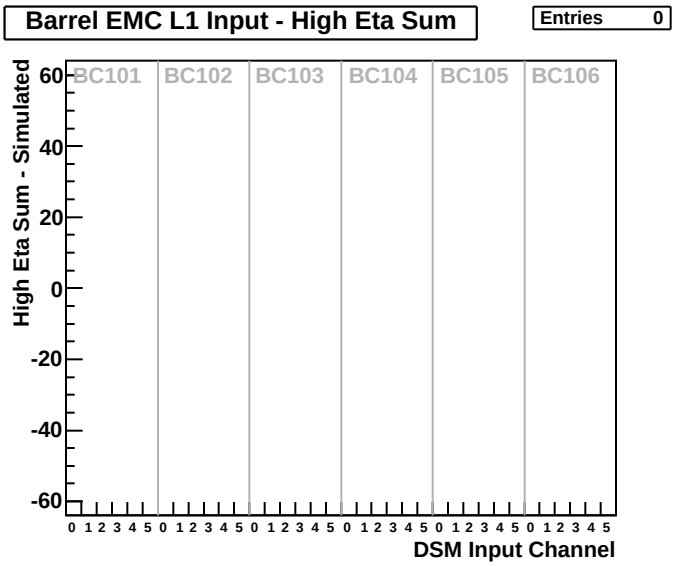
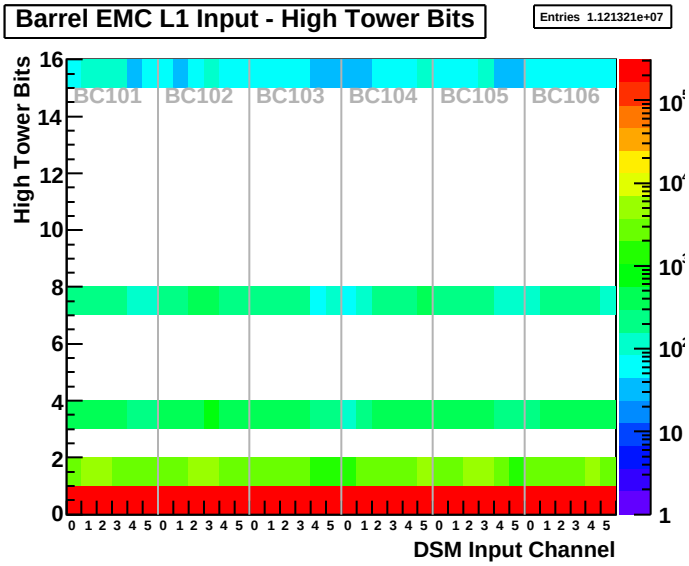
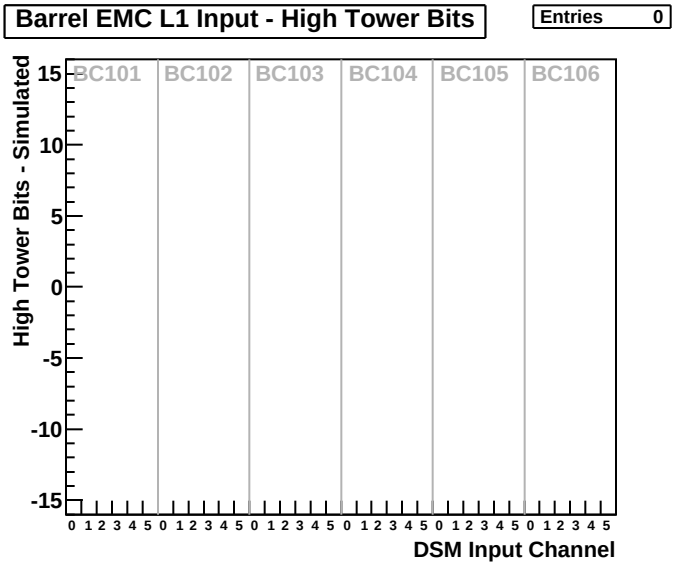

Barrel EMC L1 Input - Low Eta Sum
Entries 1.121321e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

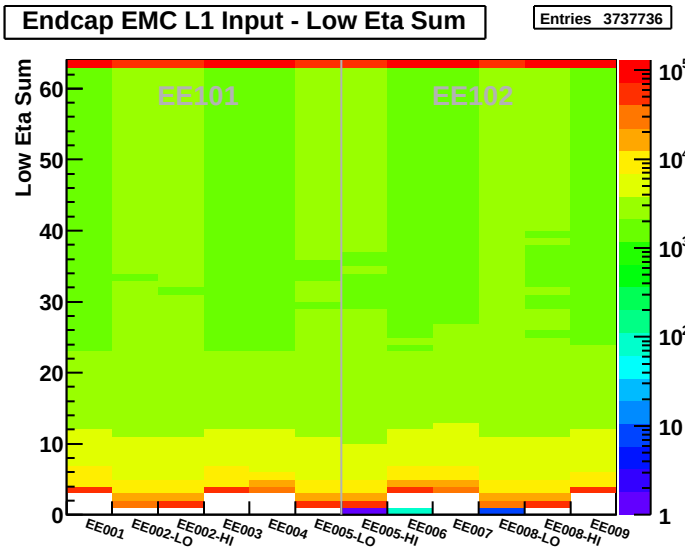

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

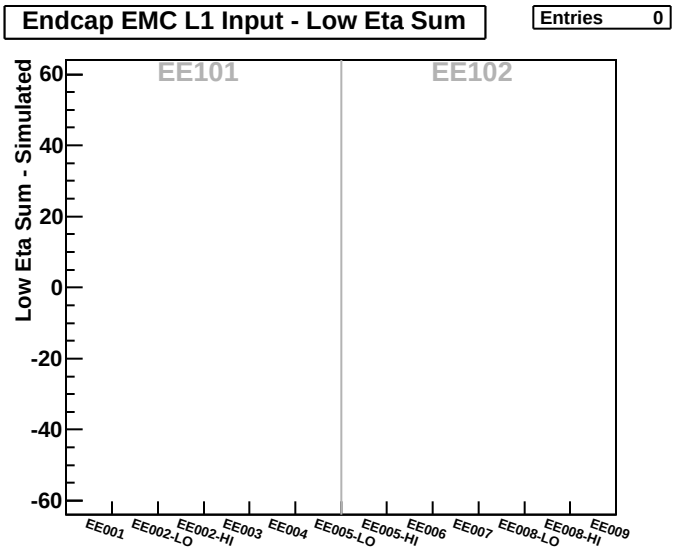

Barrel EMC L1 Input - High Eta Sum
Entries 1.121321e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

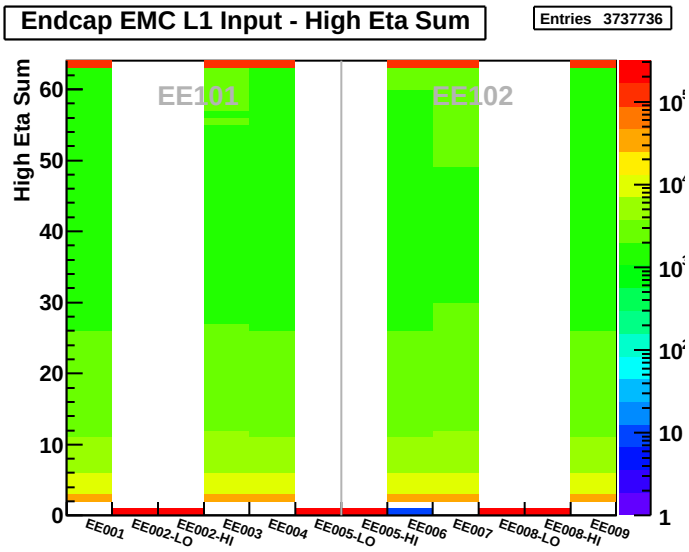

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

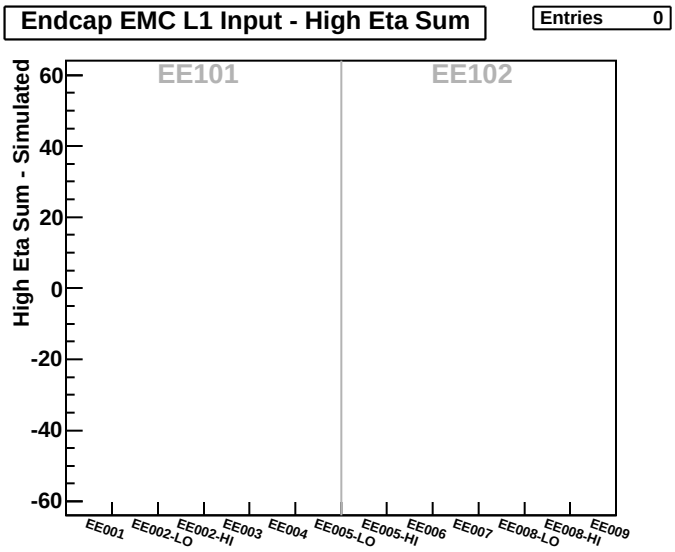

Barrel EMC L1 Input - High Tower Bits
Entries 1.121321e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

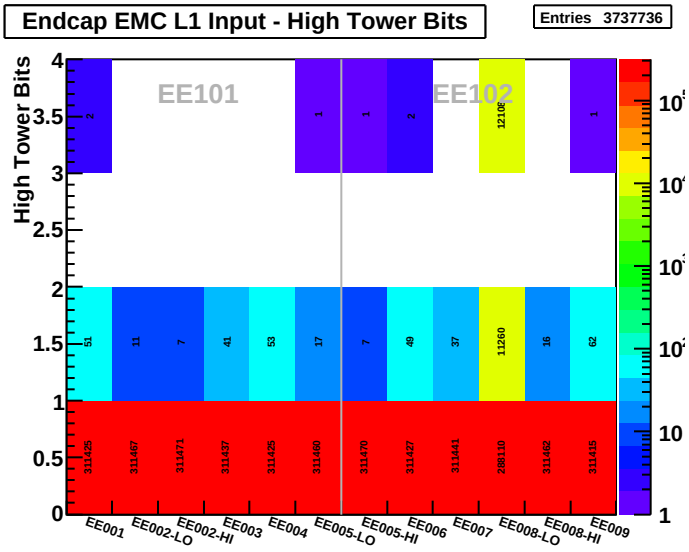

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

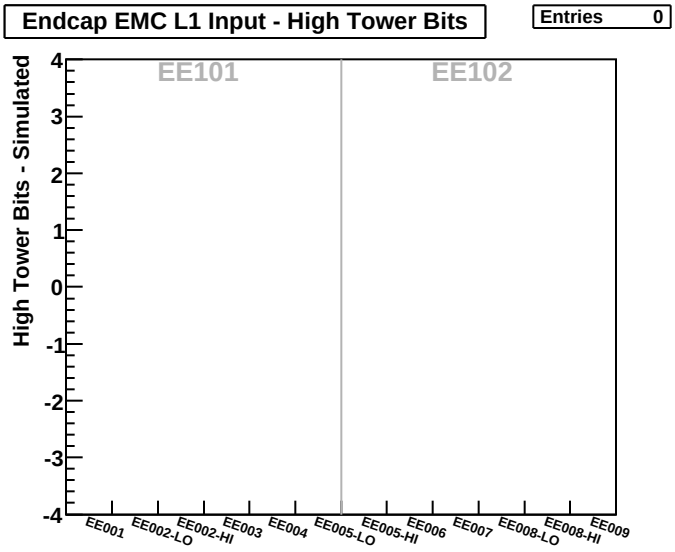

Endcap EMC L1 Input - Low Eta Sum
Entries 3737736
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

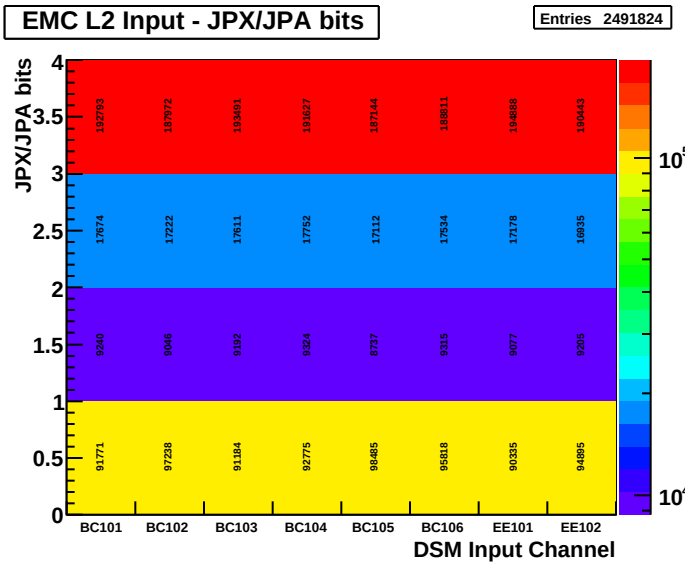

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

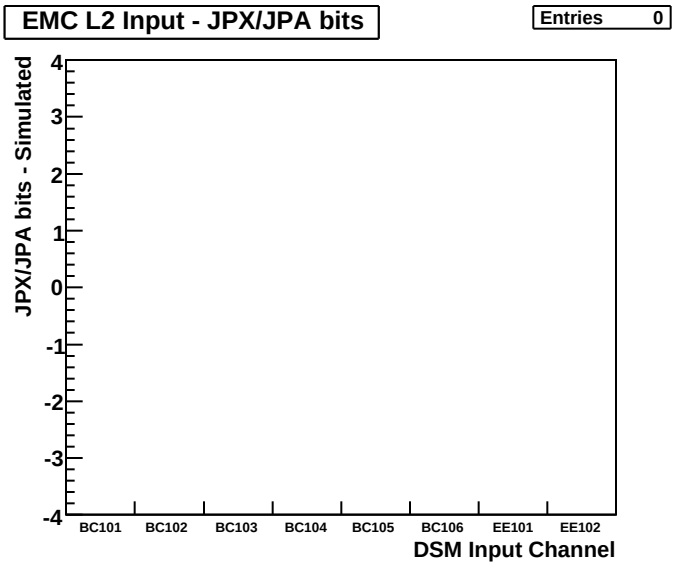

Endcap EMC L1 Input - High Eta Sum
Entries 3737736
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

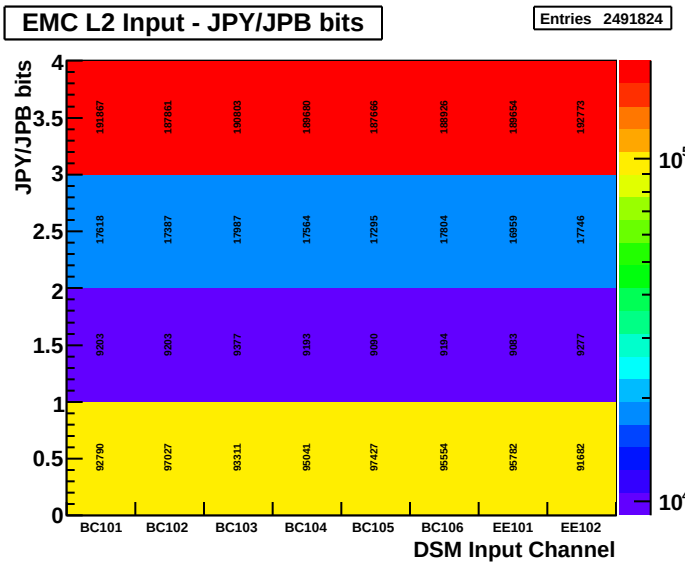

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

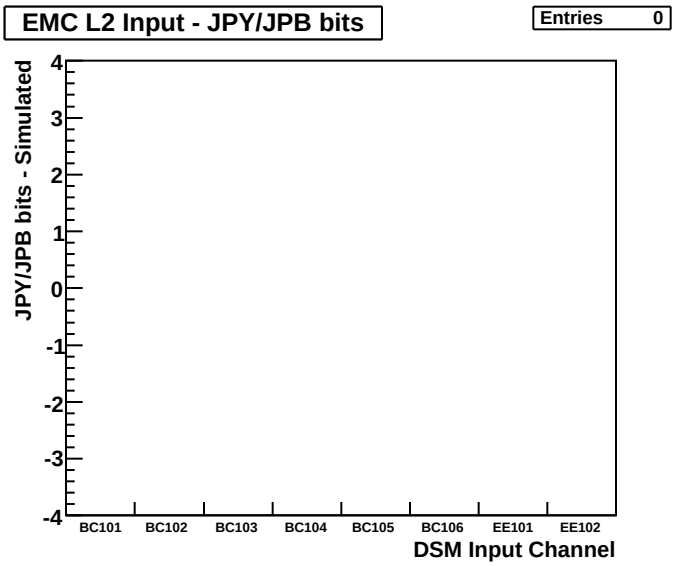

Endcap EMC L1 Input - High Tower Bits
Entries 3737736
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

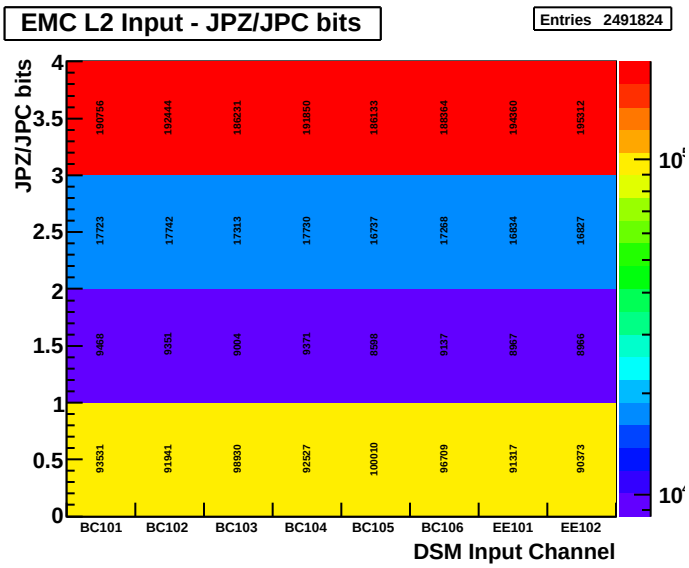

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

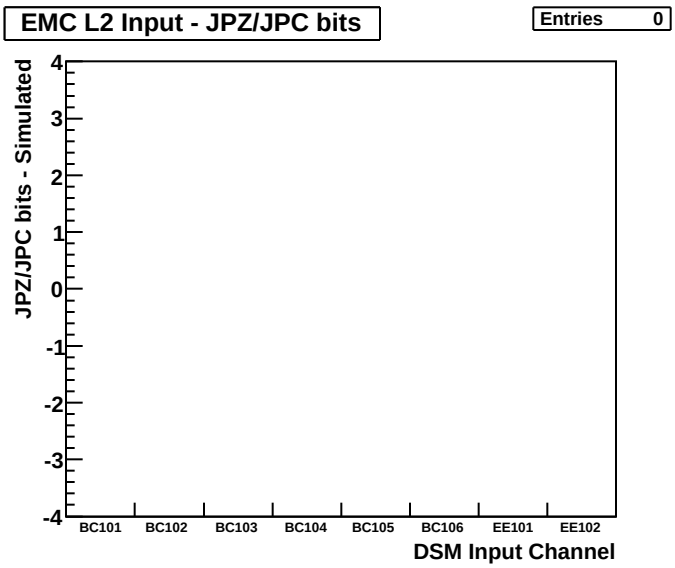

EMC L2 Input - JPX/JPA bits
Entries 2491824
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
192793 187972 193491 191627 187144 188911 194888 190443
17674 17222 17611 17752 17112 17534 17178 16935
9240 9046 9192 9324 8737 9315 9077 9205
91771 97238 91184 92775 98485 95818 90335 94895

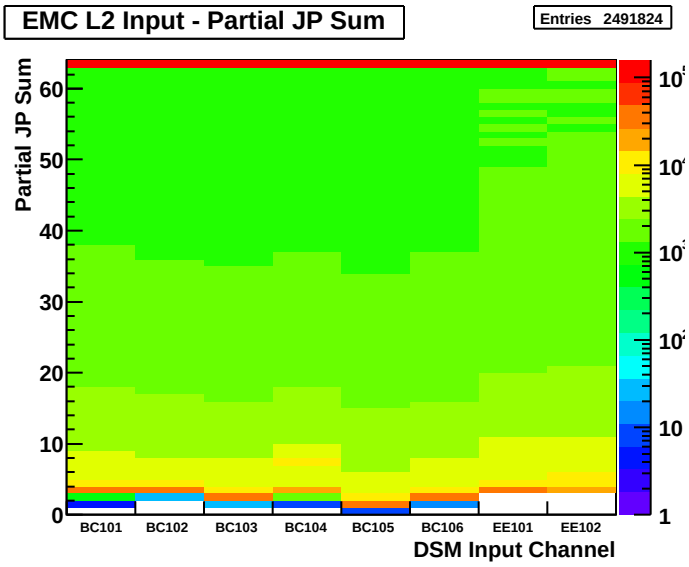

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

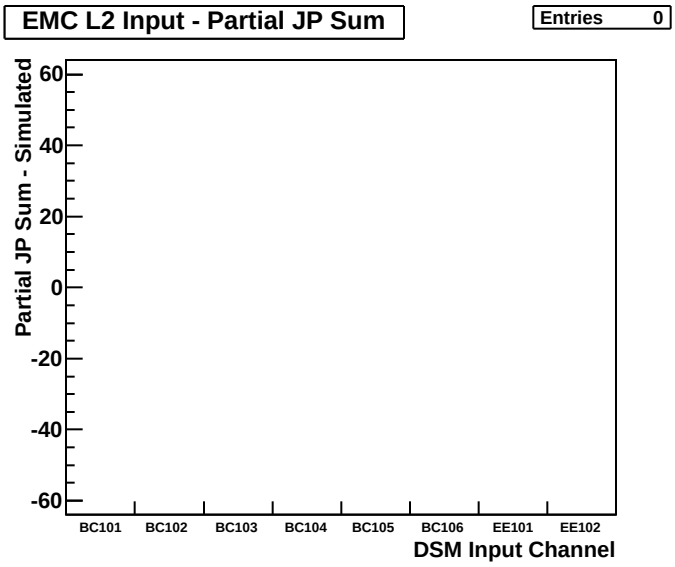

EMC L2 Input - JPY/JPB bits
Entries 2491824
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
191867 187861 190803 189680 187666 188926 188654 192773
17618 17387 17987 17564 17295 17804 16959 17746
9203 9203 9377 9193 9090 9194 9083 9277
92790 97027 93311 95041 97427 95554 95782 91682

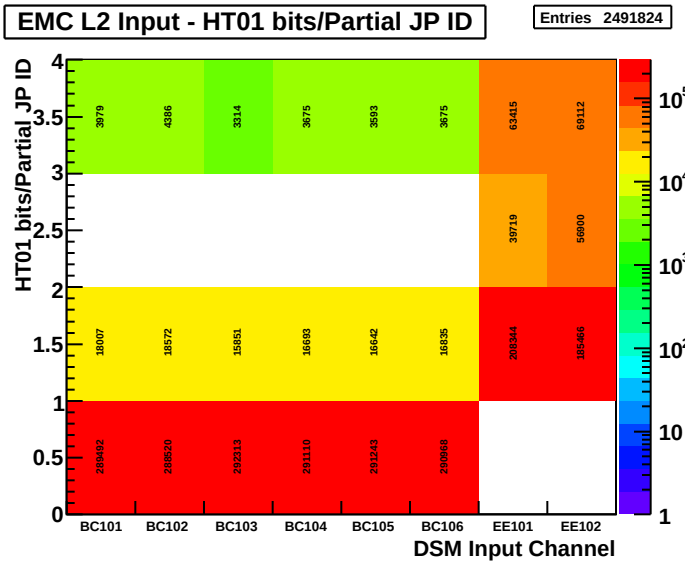

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

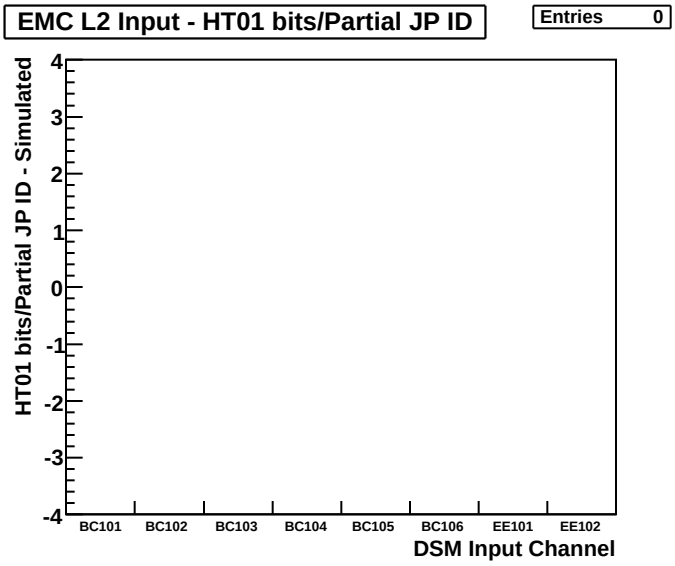

EMC L2 Input - JPZ/JPC bits
Entries 2491824
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
190756 192444 188231 191850 188133 188364 194360 193312
17723 17742 17313 17730 16737 17268 16834 16827
9468 9351 9004 9371 8598 9137 8967 8966
93531 91941 98830 92527 100010 96709 91317 90373

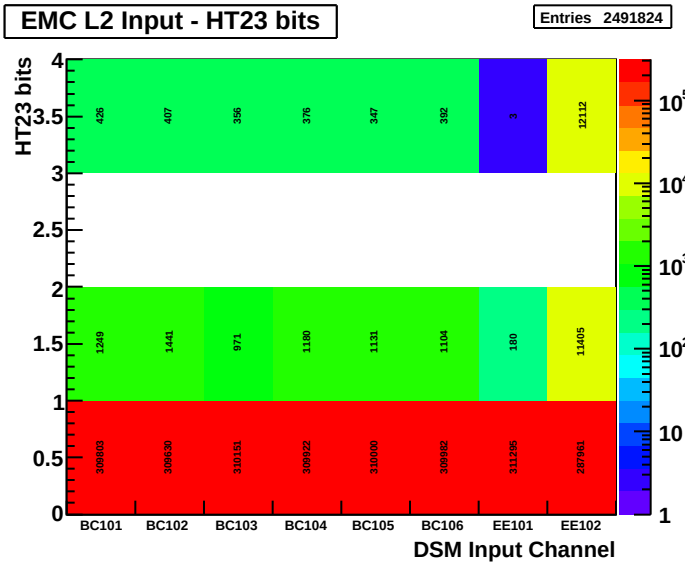

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

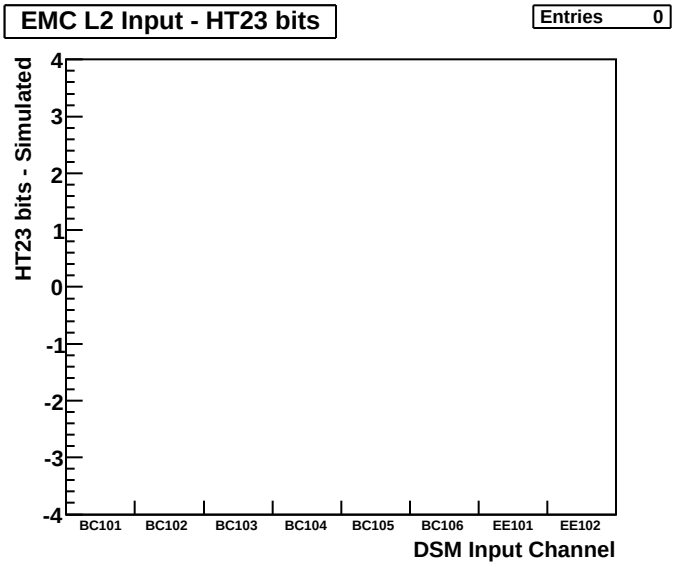

EMC L2 Input - Partial JP Sum
Entries 2491824
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

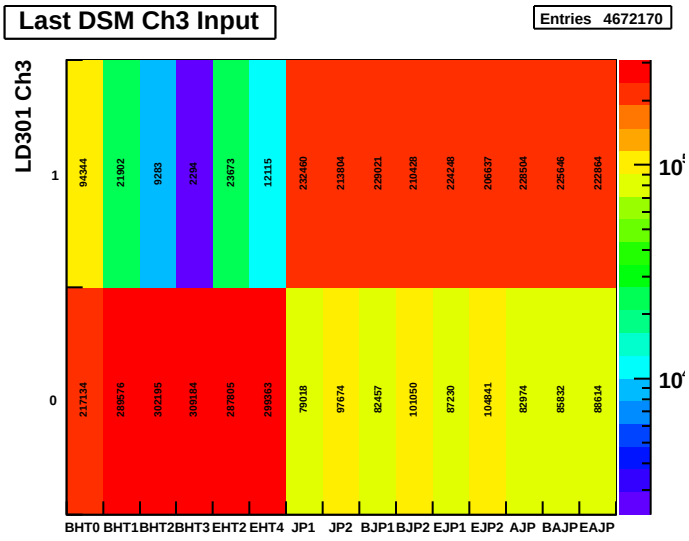

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

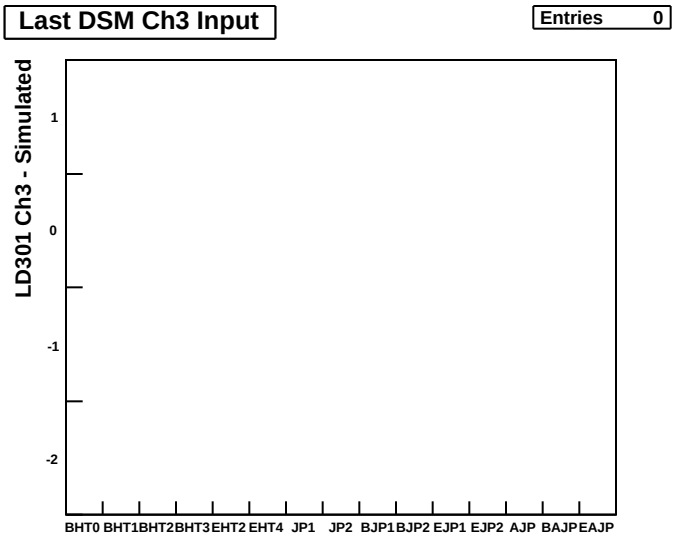

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2491824
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

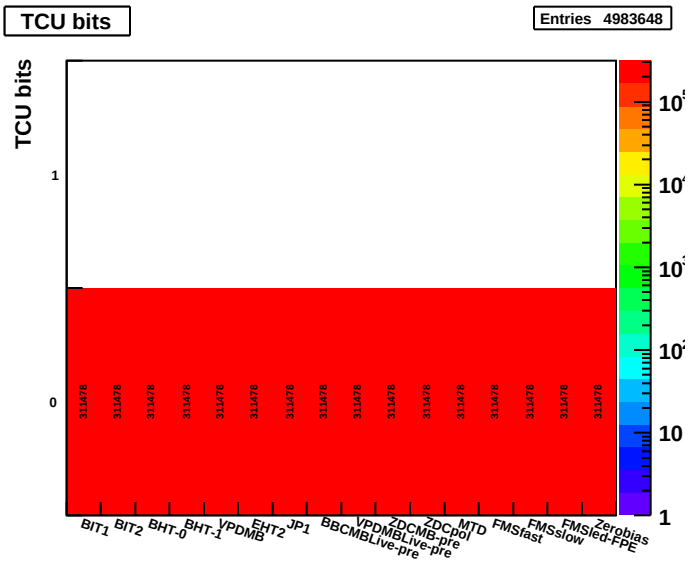

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

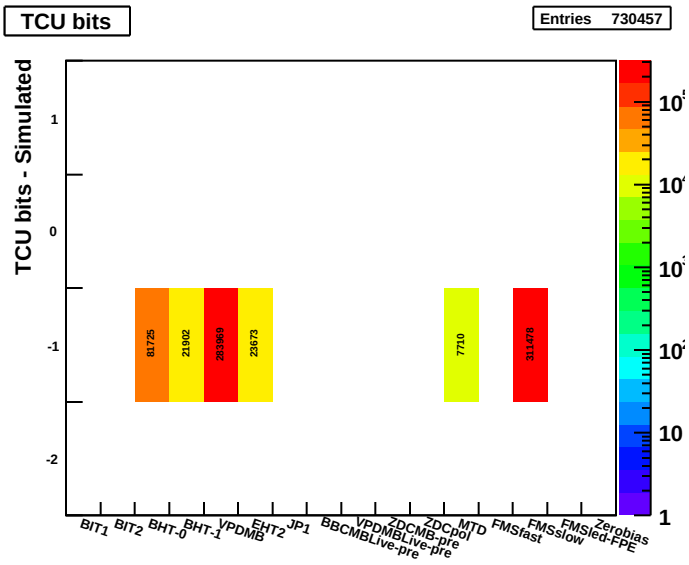

EMC L2 Input - HT23 bits
Entries 2491824
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

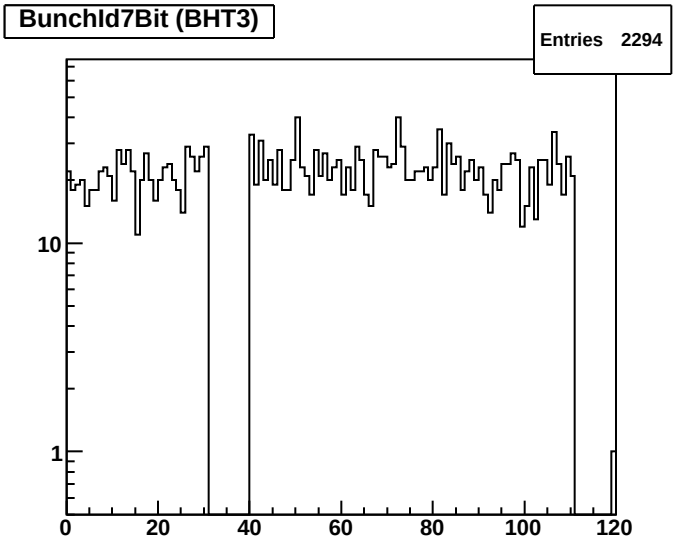

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

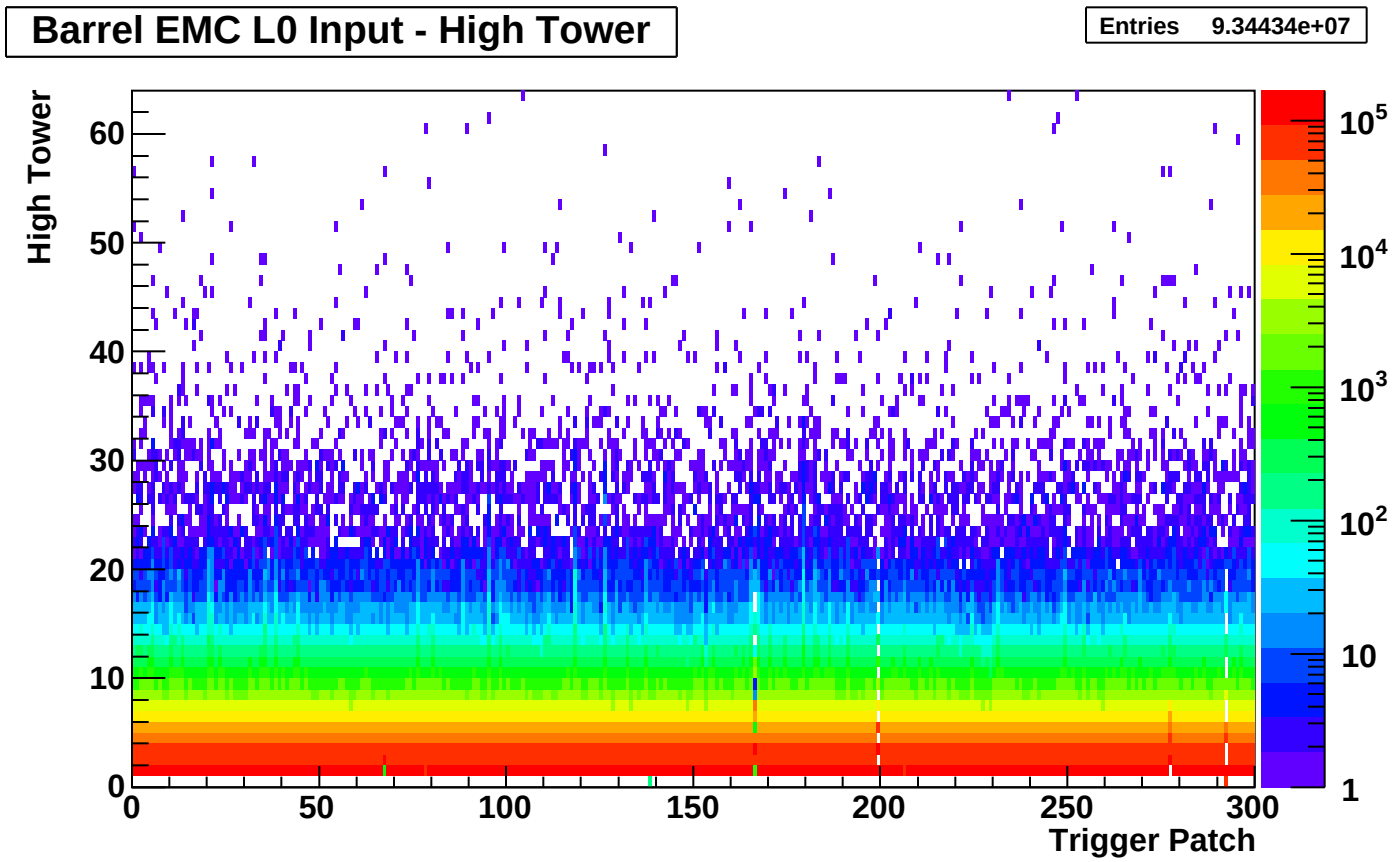

Last DSM Ch3 Input
Entries 4672170
LD301 Ch3
1
0
94344
21902
9283
2294
23673
12115
232460
213804
229021
210428
224248
206637
228504
225646
222864
217134
289576
302195
309184
287805
298963
79018
97674
82457
101050
87230
104841
82974
85832
88614
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

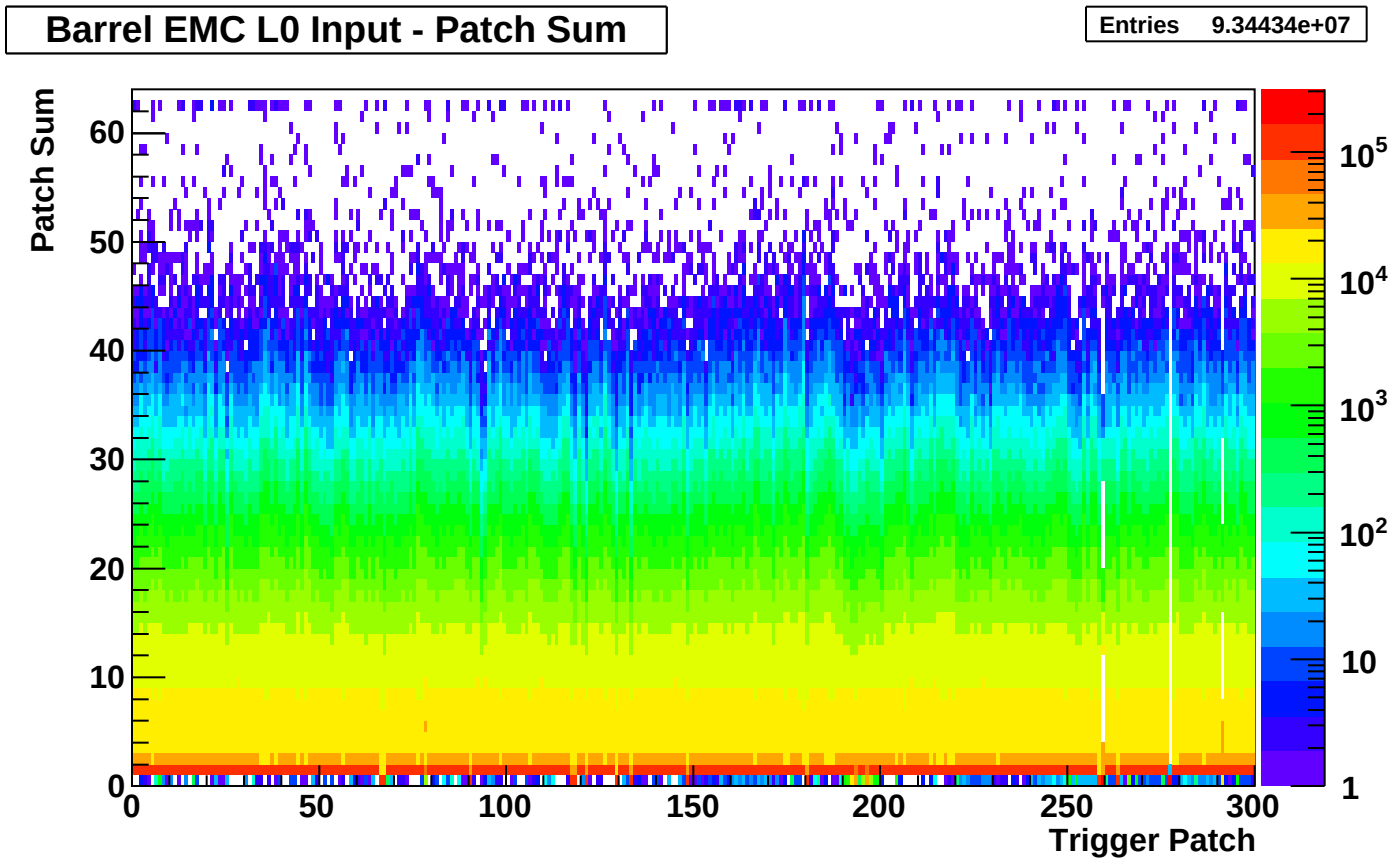

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
1
0
-1
-2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP

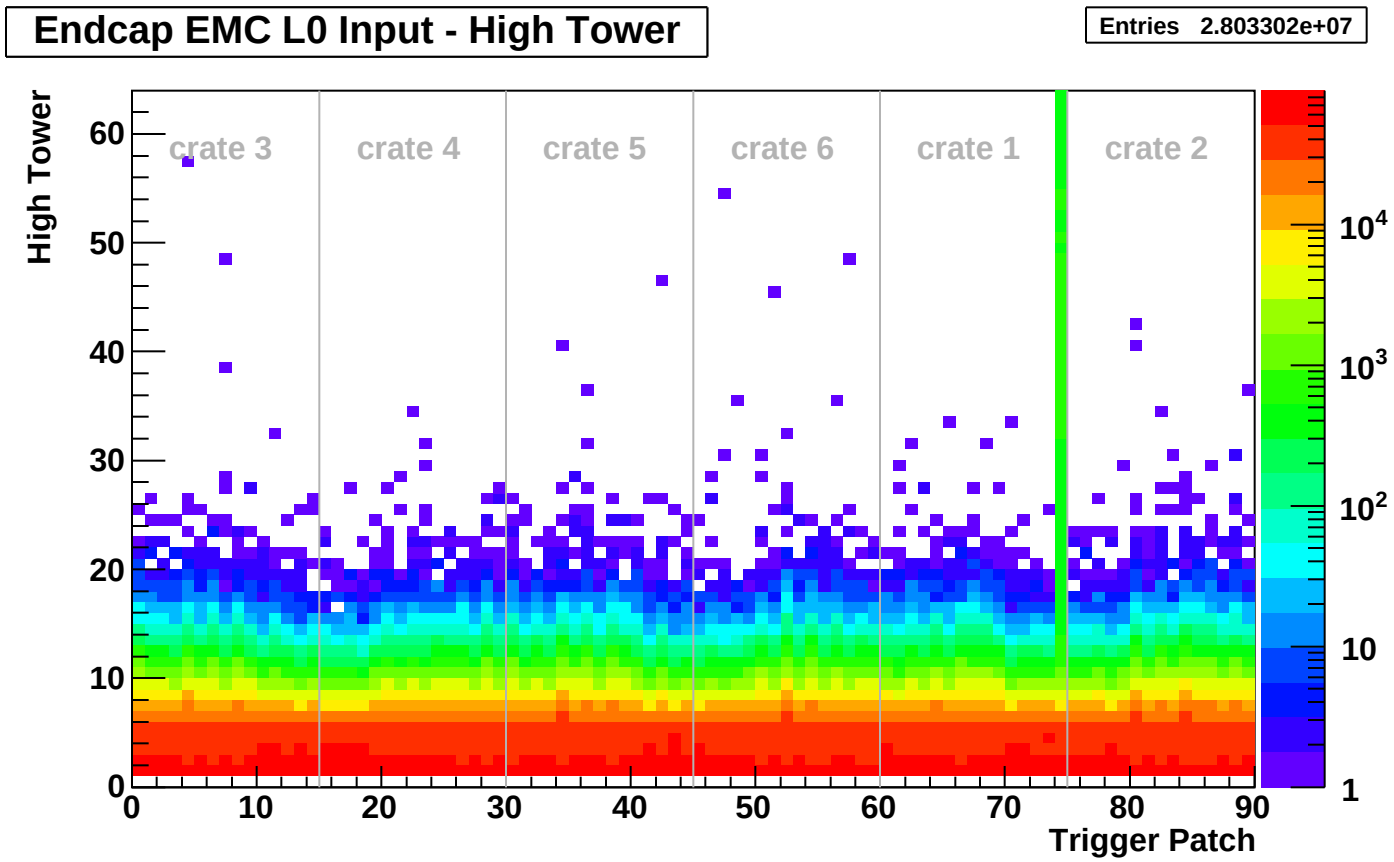

TCU bits
Entries 4983648
TCU bits
1
0
311478
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

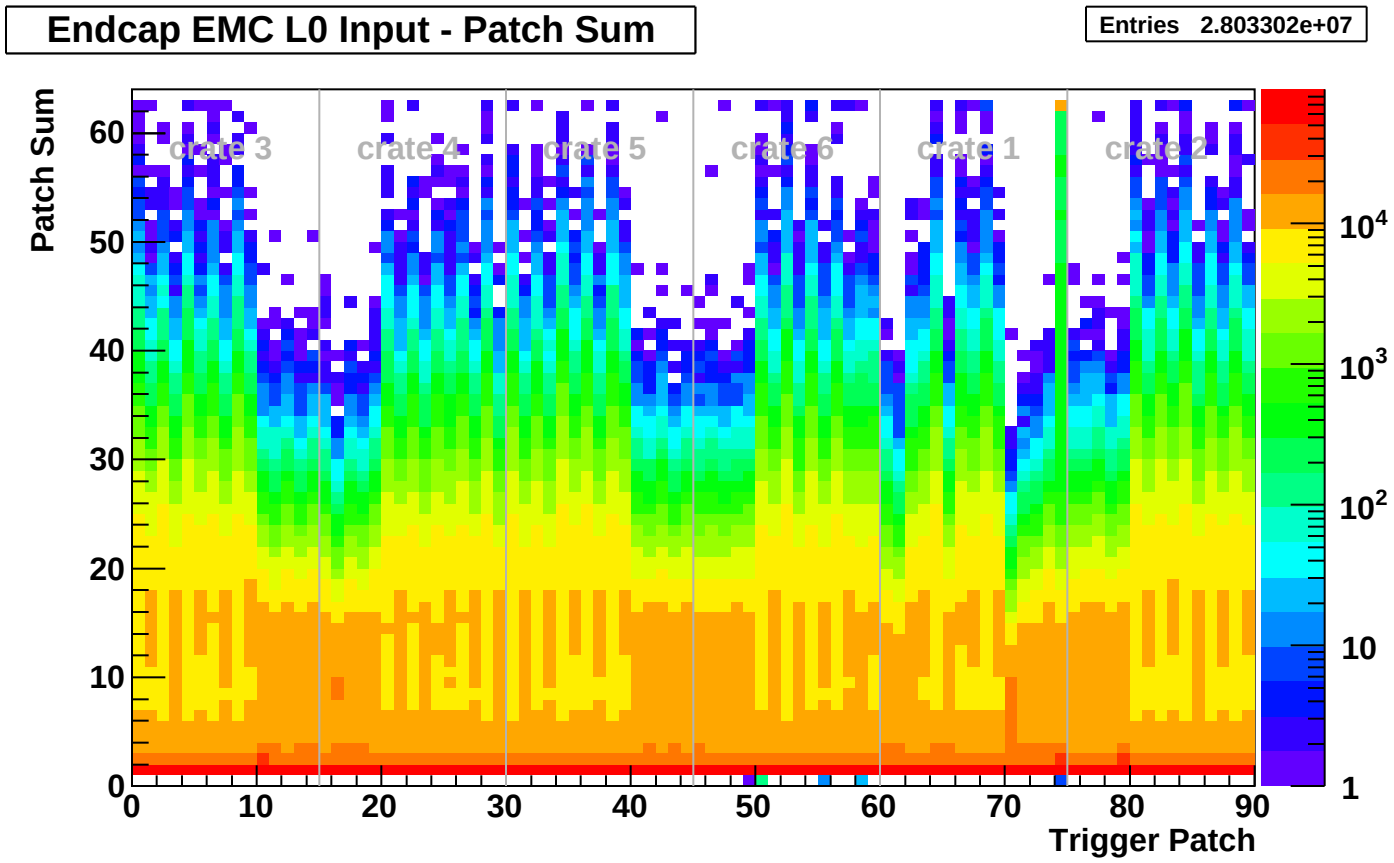

TCU bits
Entries 730457
TCU bits - Simulated
1
0
-1
-2
81725
21902
283969
23673
7710
311478
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

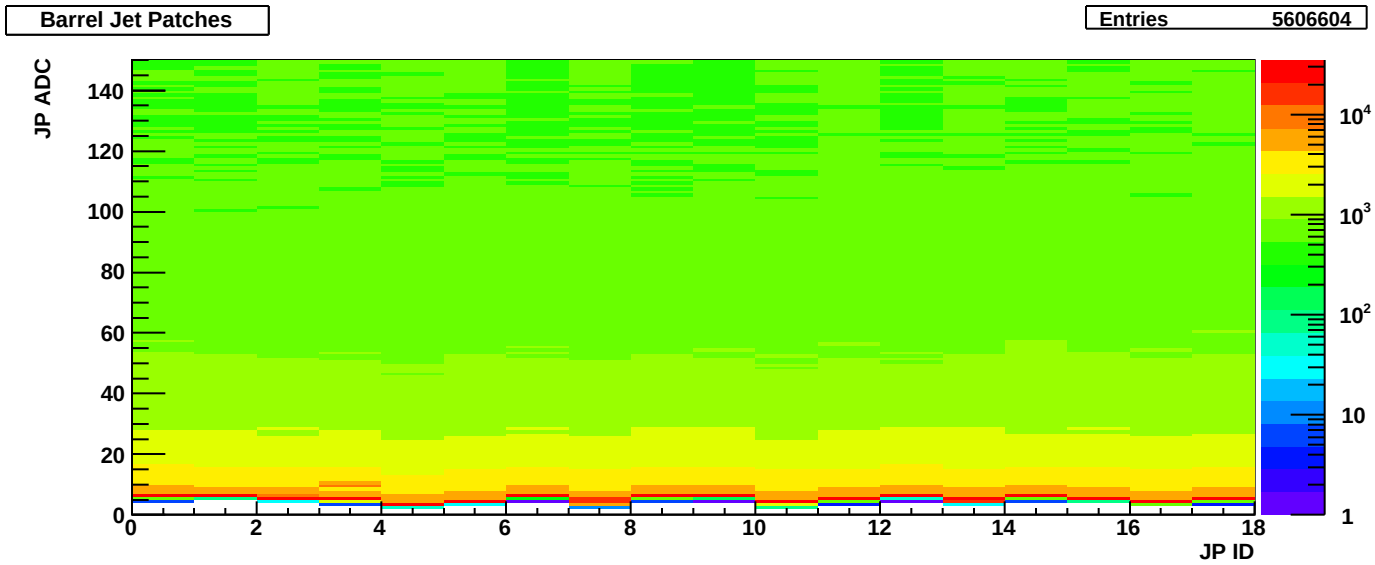

BunchId7Bit (BHT3)
Entries 2294
10
1
0
20
40
60
80
100
120

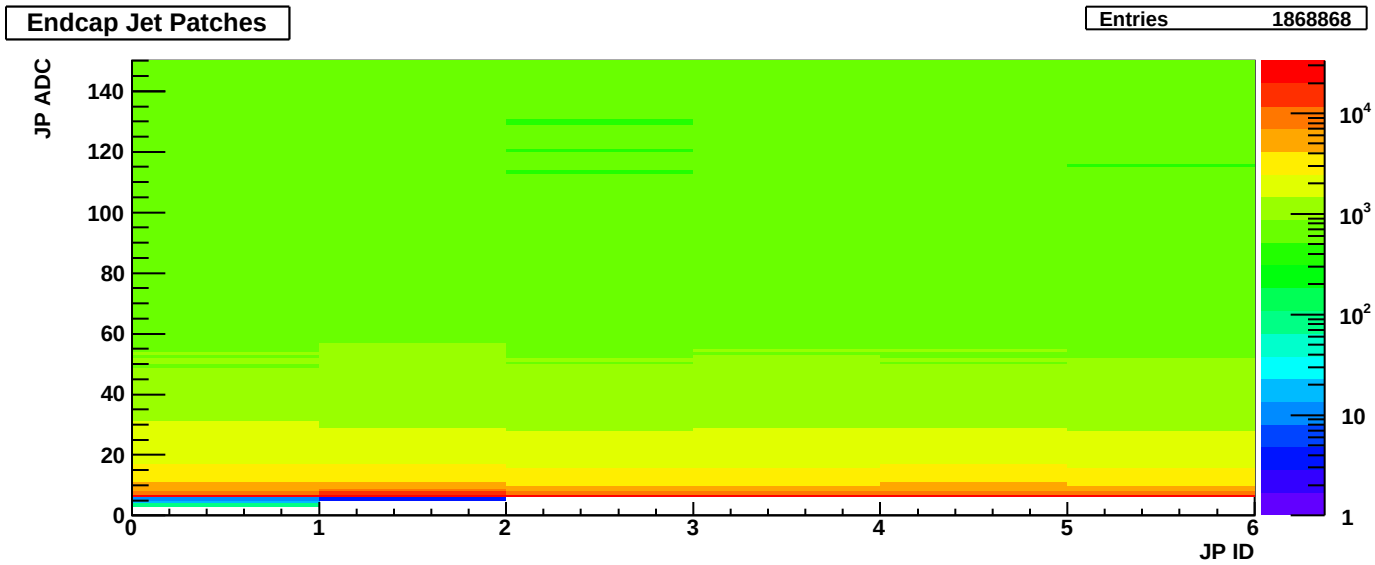

Barrel EMC L0 Input - High Tower
Entries 9.34434e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

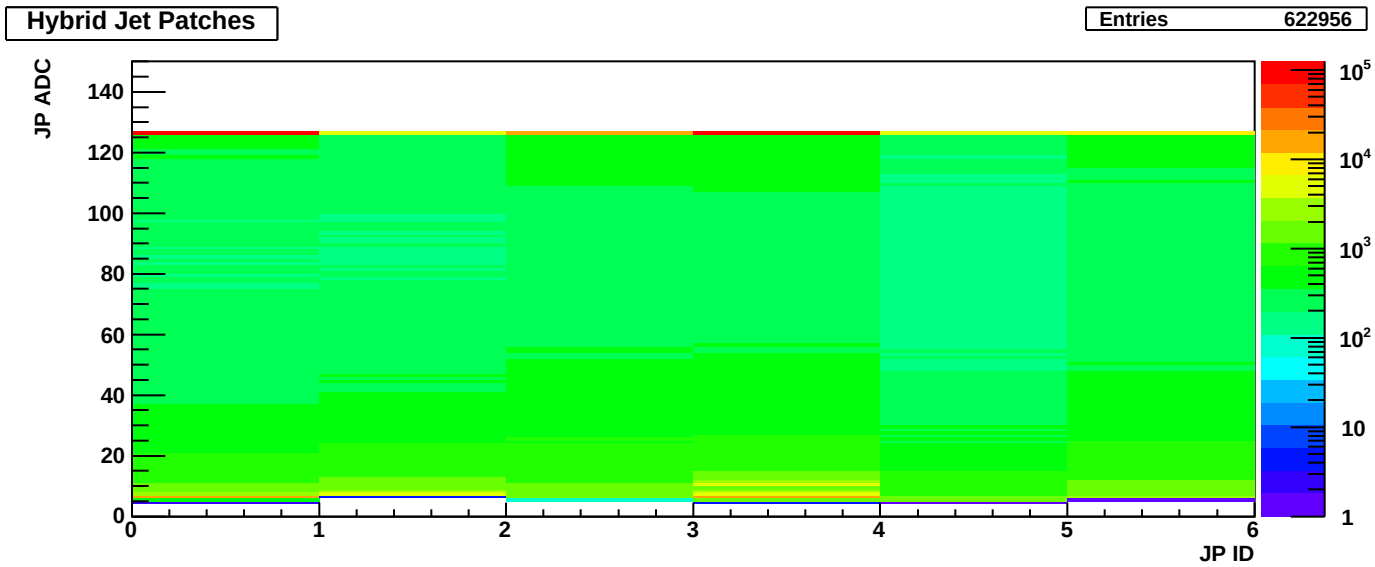

Barrel EMC L0 Input - Patch Sum
Entries 9.34434e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5


Endcap EMC L0 Input - High Tower
Entries 2.803302e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

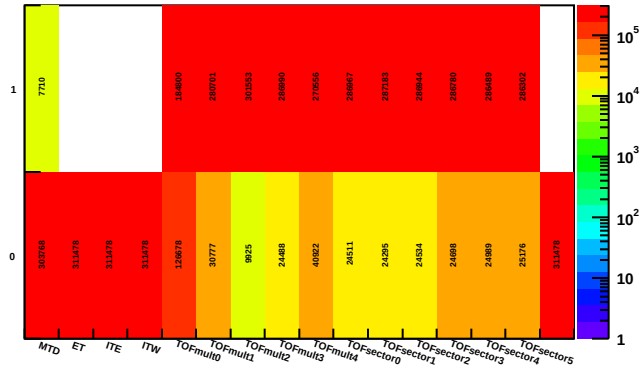

Endcap EMC L0 Input - Patch Sum
Entries 2.803302e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2


Barrel Jet Patches
Entries 5606604
JP ADC
JP ID

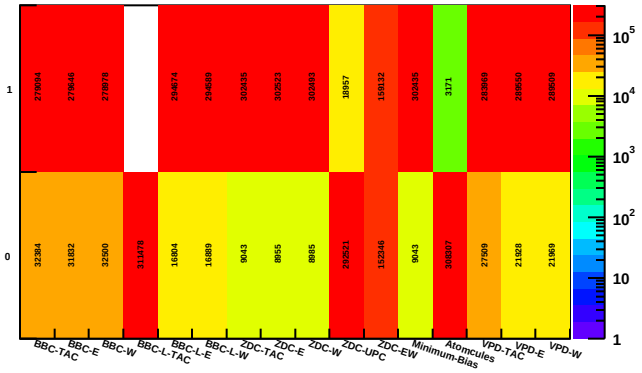

Endcap Jet Patches
Entries 1868868
JP ADC
JP ID


Hybrid Jet Patches
Entries 622956
JP ADC
JP ID

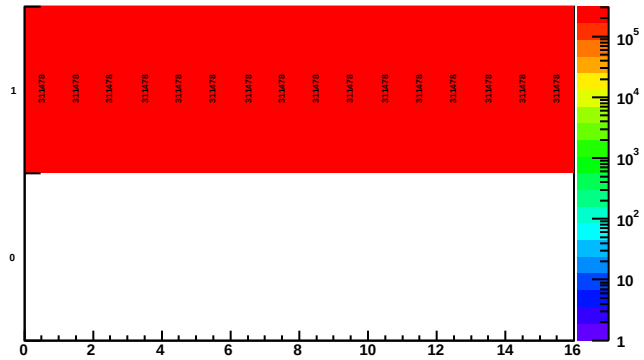
TF201 0-15 (ch0) Entries 4983648



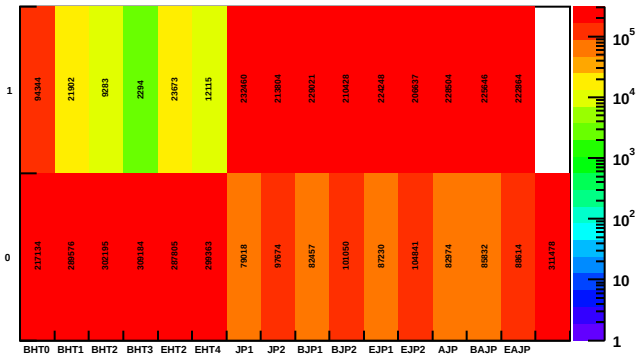
VT201 0-15 (ch1) Entries 4983648



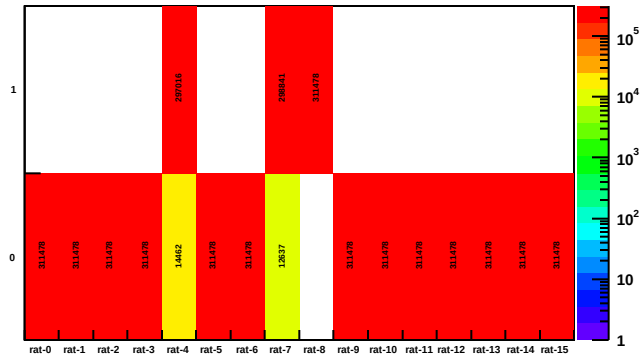
Unused (ch2) Entries 4983648



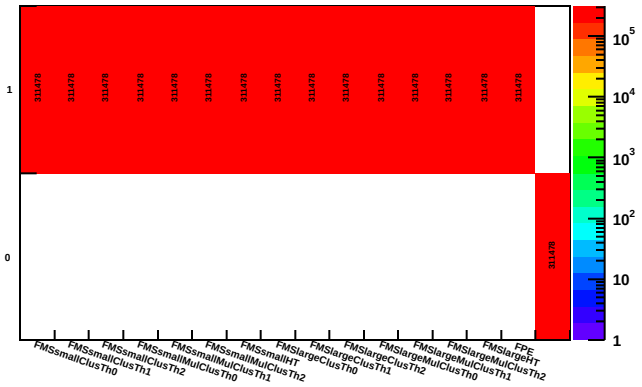
EM201 0-15 (ch3) Entries 4983648



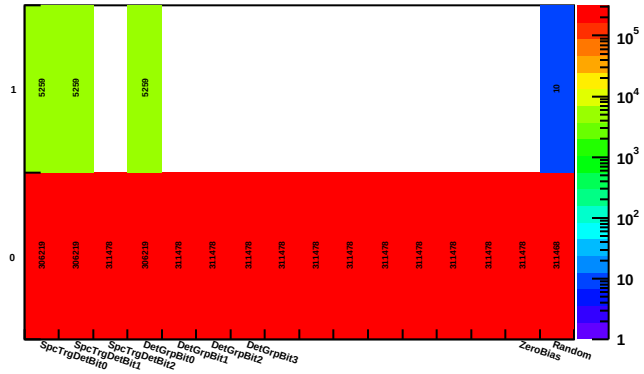
RAT board (ch4) Entries 4983648



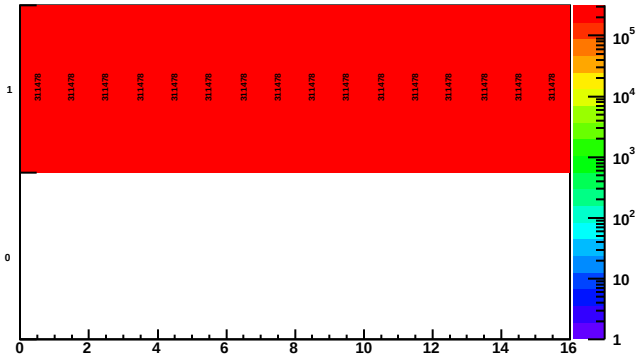
FP201 0-15 (ch5) Entries 4983648

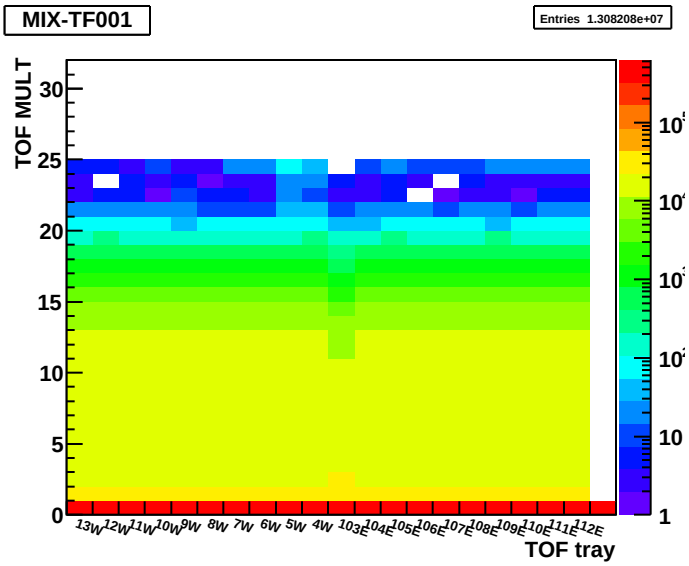


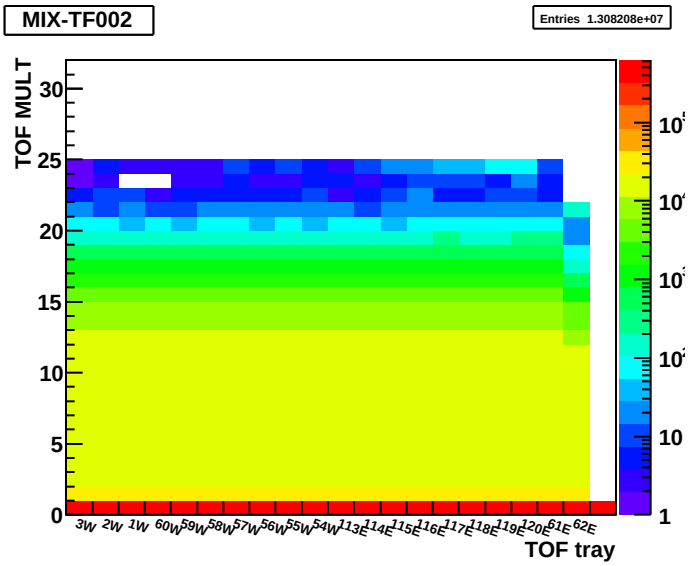
ST201 0-15 (ch6) Entries 4983648

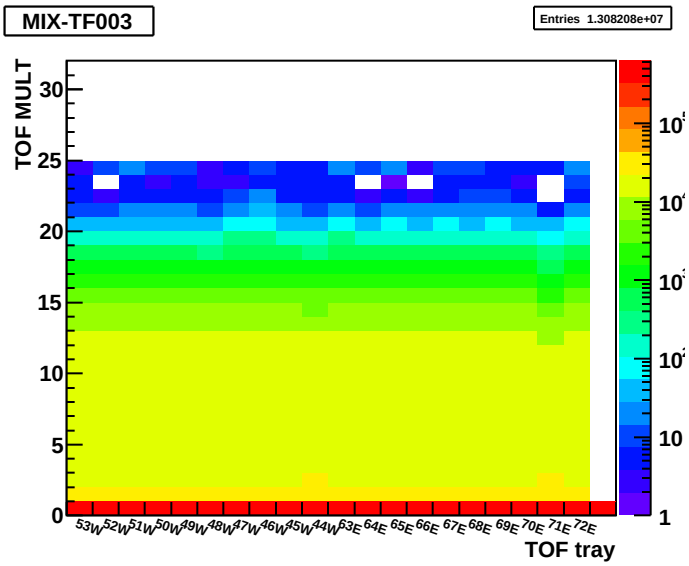


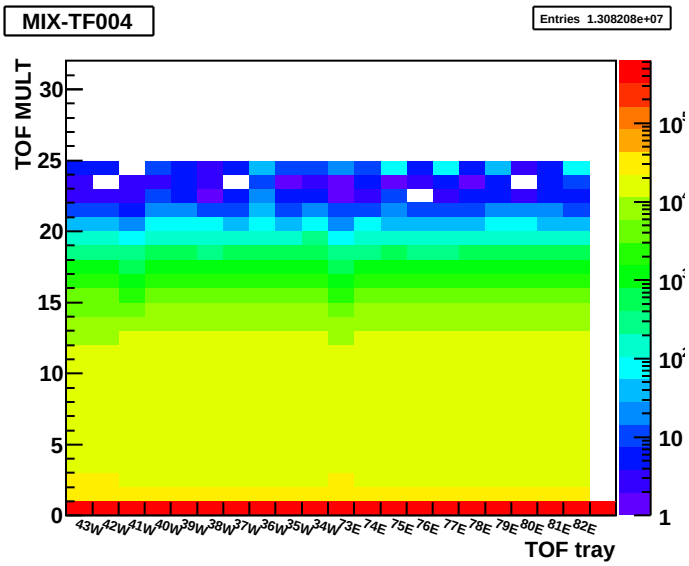
Unused (ch7) Entries 4983648

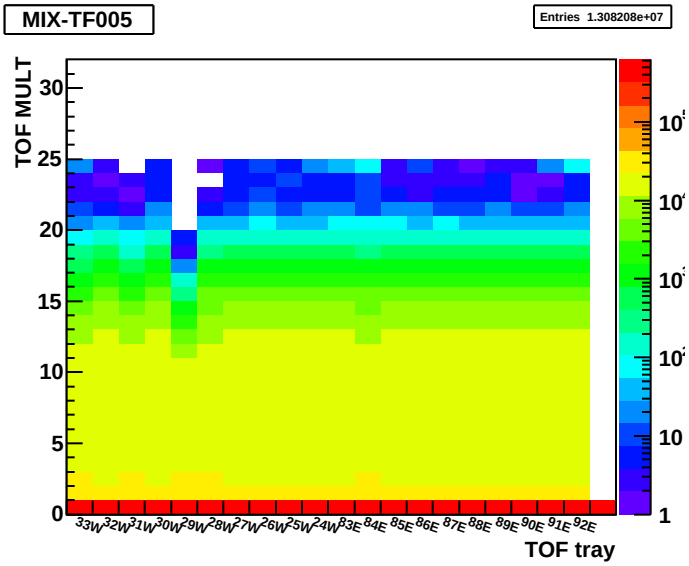


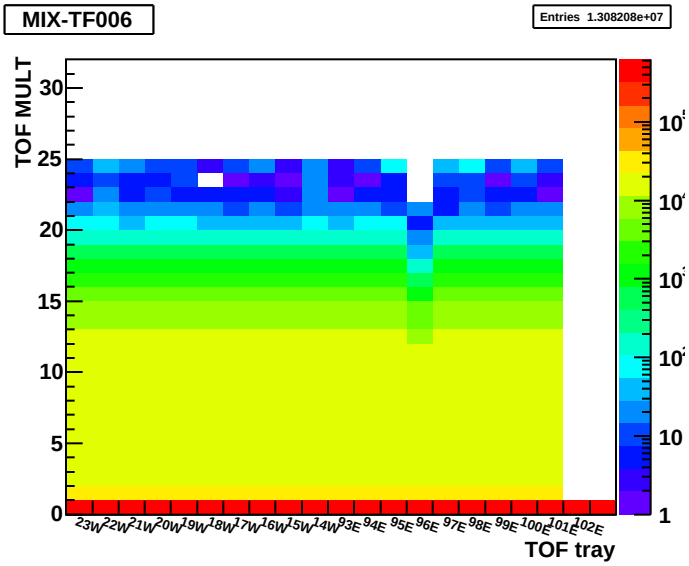

MIX-TF001
Entries 1.308208e+07
TOF MULT
TOF tray

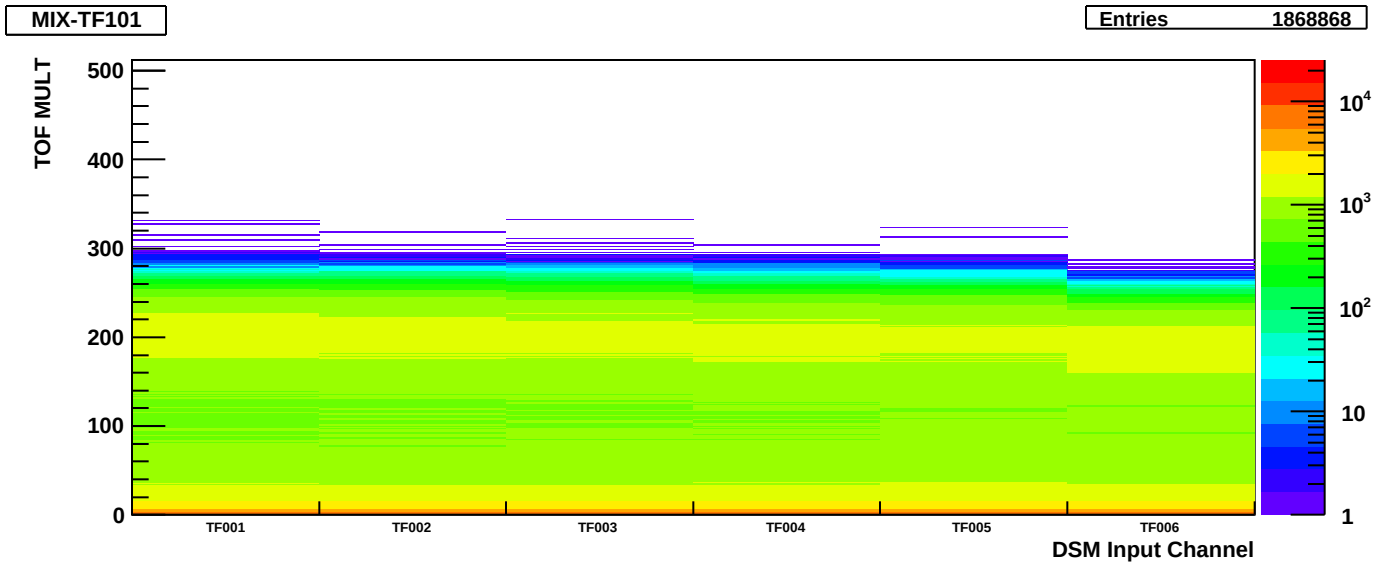

MIX-TF002
Entries 1.308208e+07
TOF MULT
TOF tray

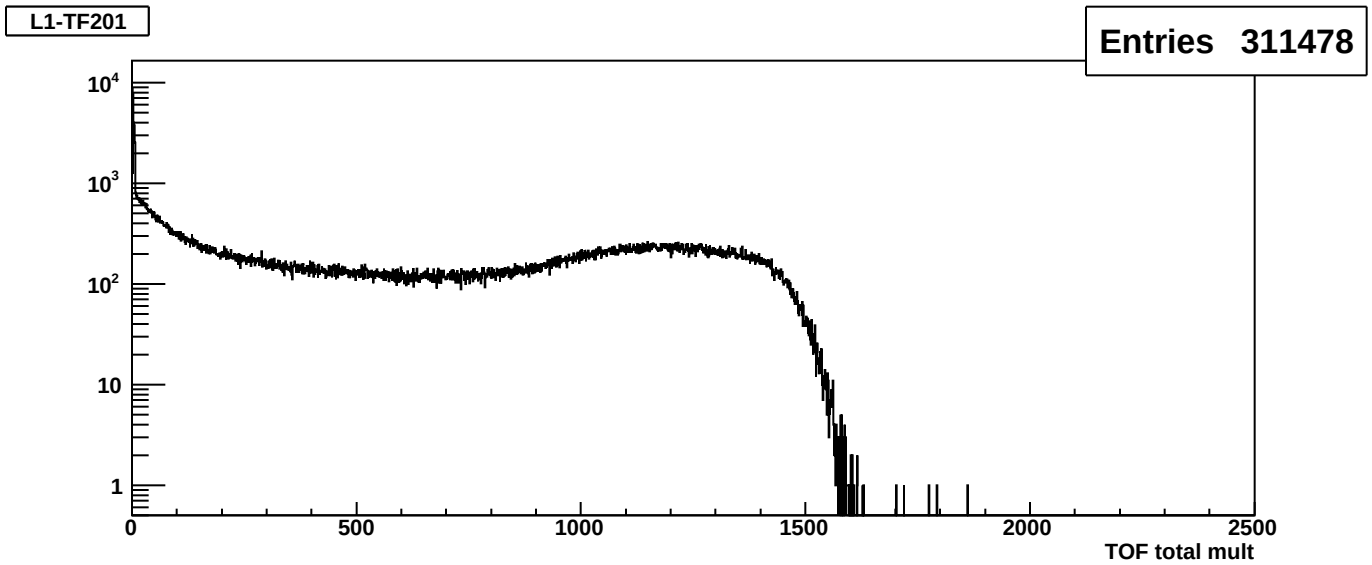

MIX-TF003
Entries 1.308208e+07
TOF MULT
TOF tray

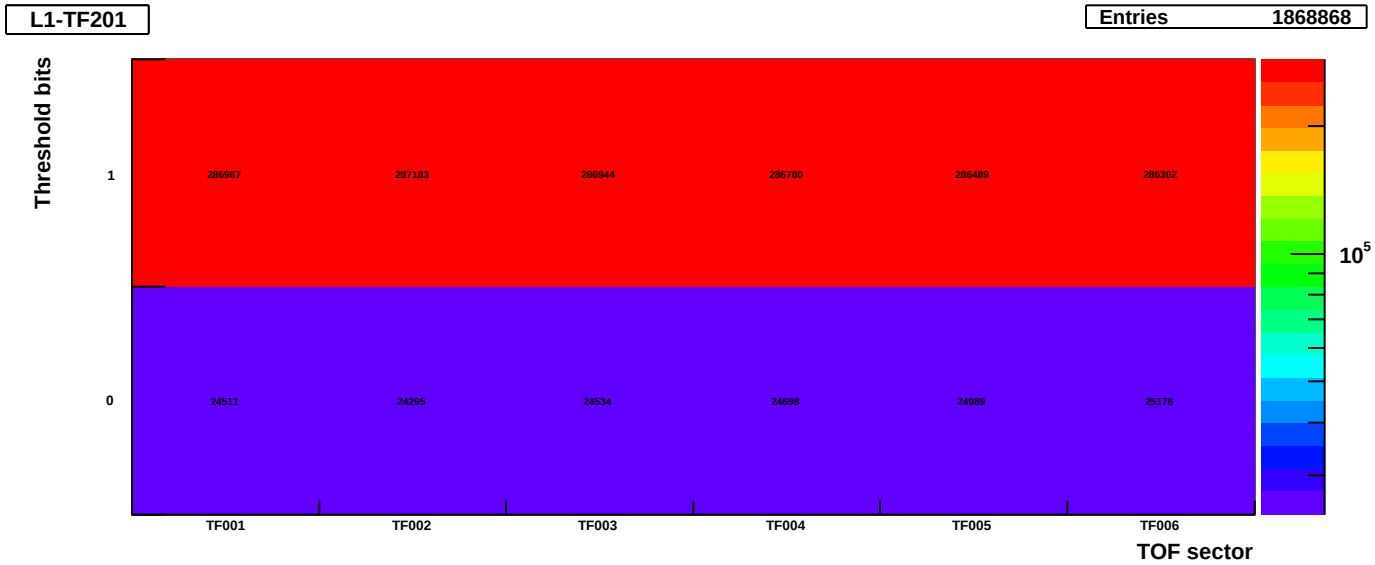

MIX-TF004
Entries 1.308208e+07
TOF MULT
TOF tray

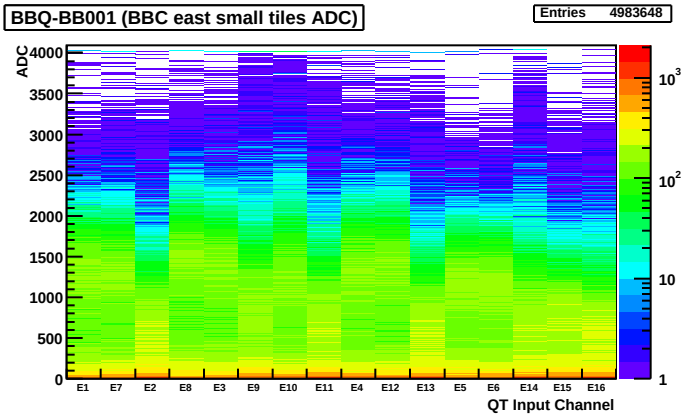

MIX-TF005
Entries 1.308208e+07
TOF MULT
TOF tray

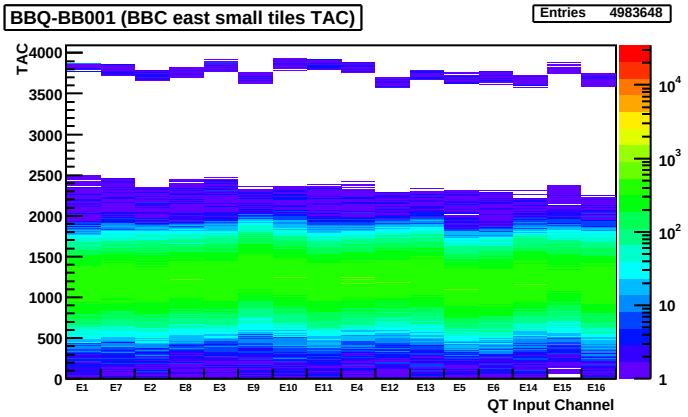

MIX-TF006
Entries 1.308208e+07
TOF MULT
TOF tray

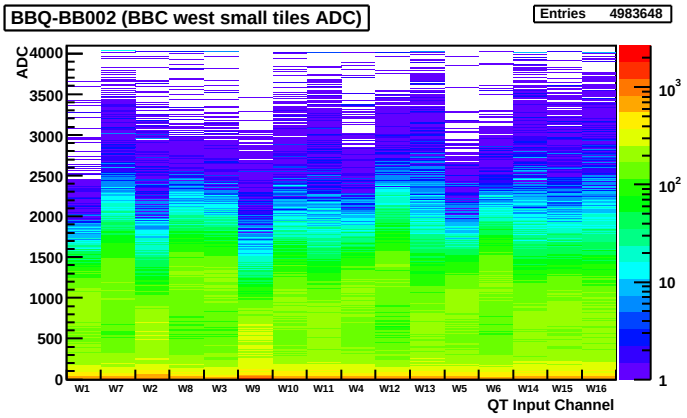

MIX-TF101
Entries 1868868
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

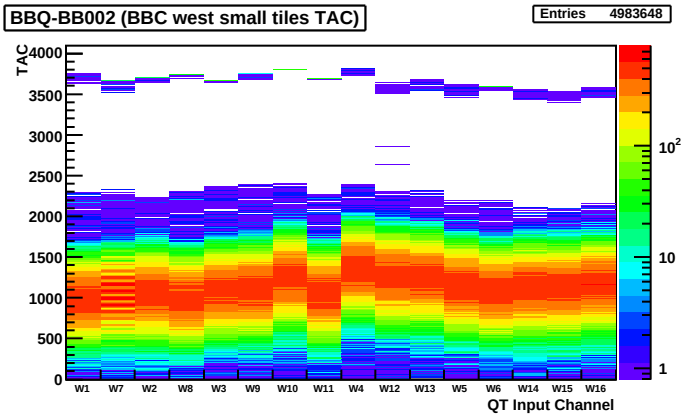

L1-TF201
Entries 311478
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

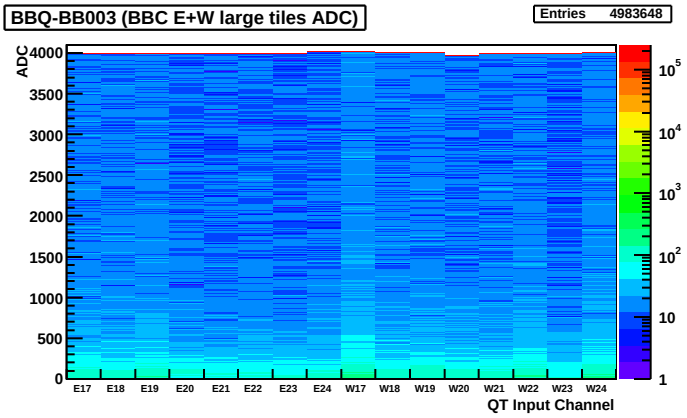

L1-TF201
Entries 1868868
Threshold bits
1
0
286967
287183
286944
286780
286489
286302
24511
24295
24534
24698
24989
25176
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

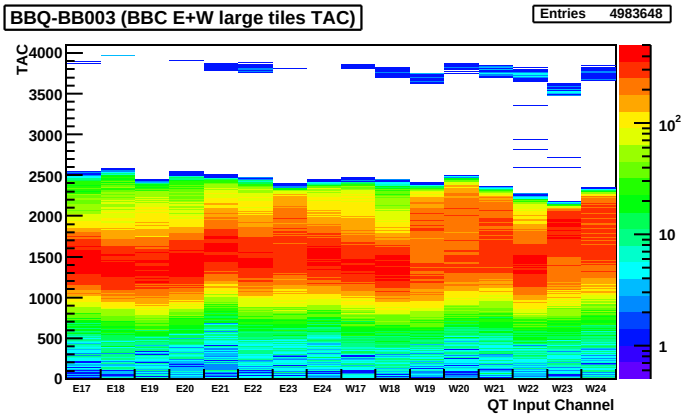

BBQ-BB001 (BBC east small tiles ADC)
Entries 4983648
ADC
QT Input Channel

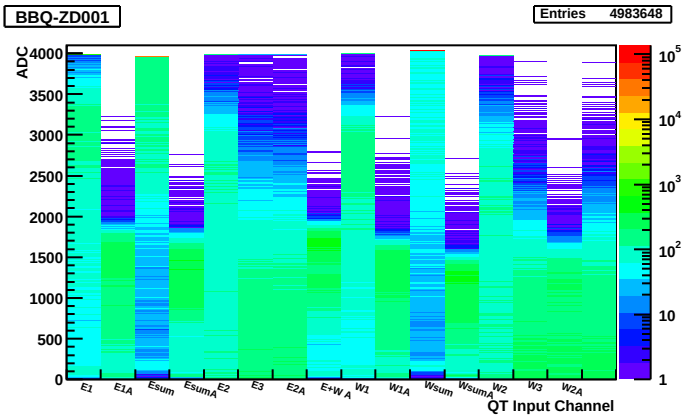

BBQ-BB001 (BBC east small tiles TAC)
Entries 4983648
TAC
QT Input Channel

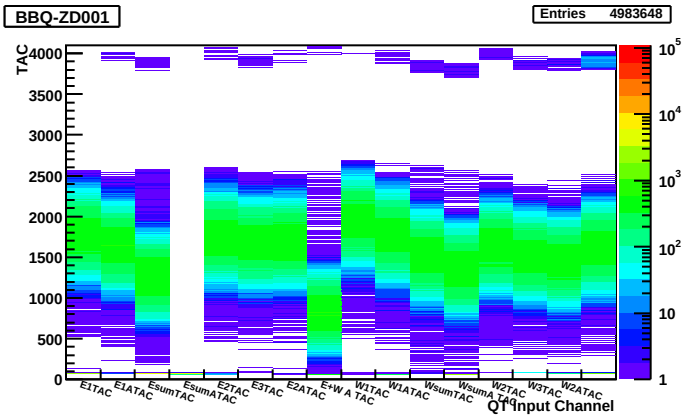

BBQ-BB002 (BBC west small tiles ADC)
Entries 4983648
ADC
QT Input Channel

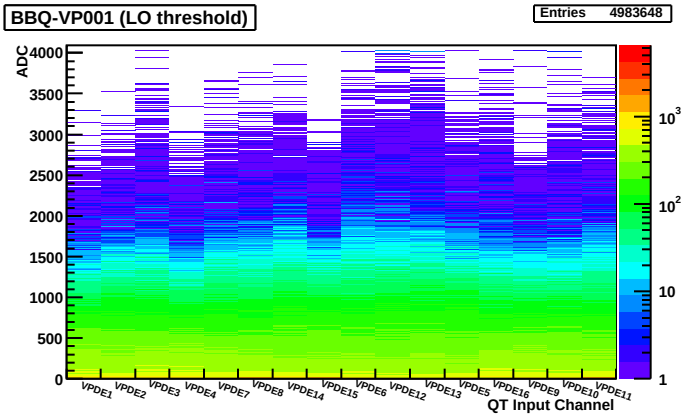

BBQ-BB002 (BBC west small tiles TAC)
Entries 4983648
TAC
QT Input Channel

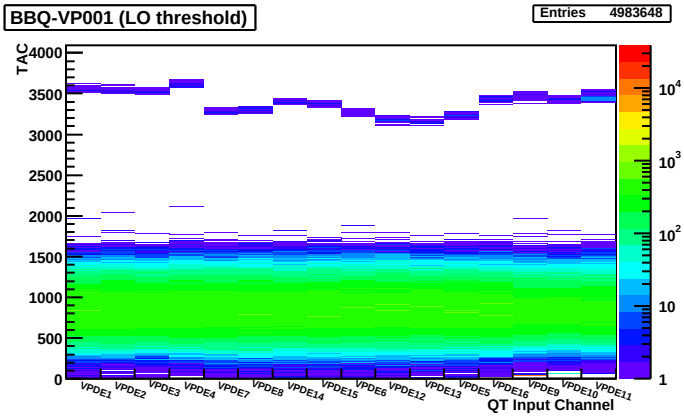

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4983648
ADC
QT Input Channel

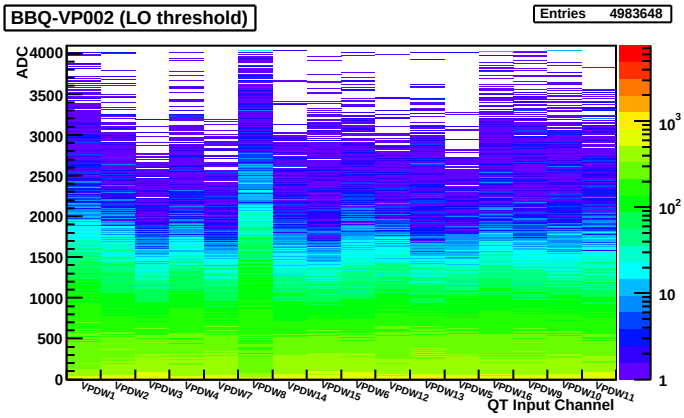

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4983648
TAC
QT Input Channel

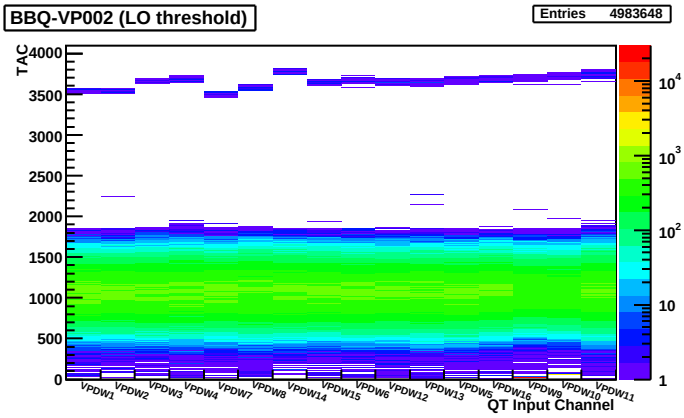

BBQ-ZD001
Entries 4983648
ADC
QT Input Channel

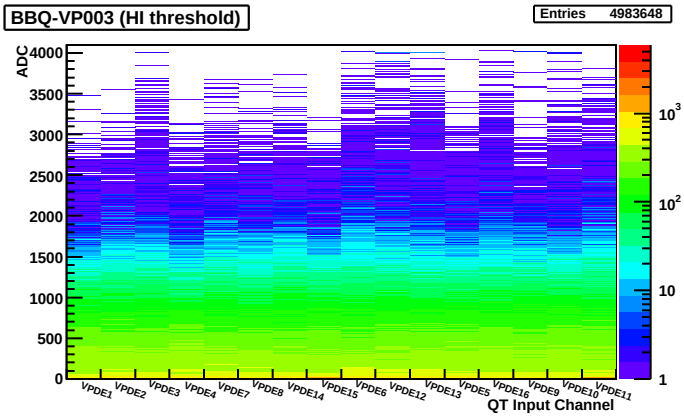

BBQ-ZD001
Entries 4983648
TAC
QT Input Channel

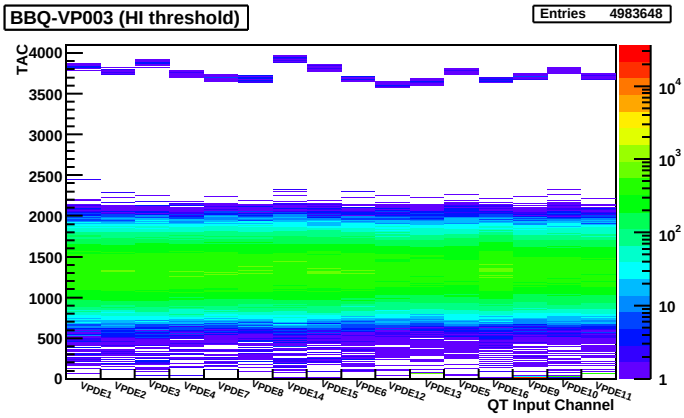

BBQ-VP001 (LO threshold)
Entries 4983648
ADC
QT Input Channel

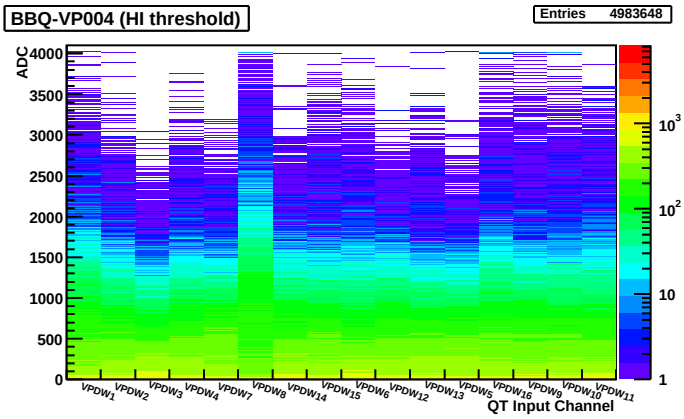

BBQ-VP001 (LO threshold)
Entries 4983648
TAC
QT Input Channel

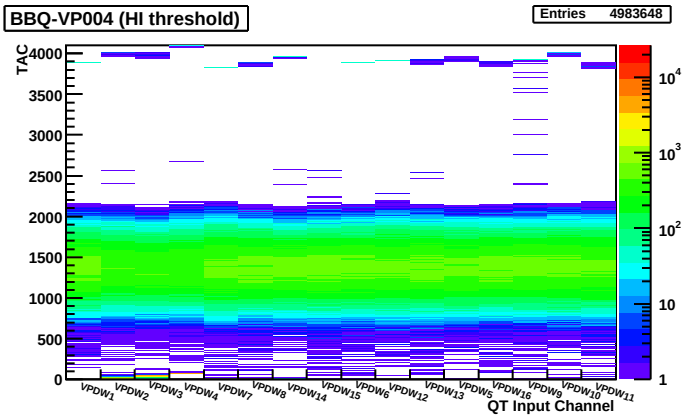

BBQ-VP002 (LO threshold)
Entries 4983648
ADC
QT Input Channel


BBQ-VP002 (LO threshold)
Entries 4983648
TAC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 4983648
ADC
QT Input Channel


BBQ-VP003 (HI threshold)
Entries 4983648
TAC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 4983648
ADC
QT Input Channel


BBQ-VP004 (HI threshold)
Entries 4983648
TAC
QT Input Channel