IST Raw ADC QA

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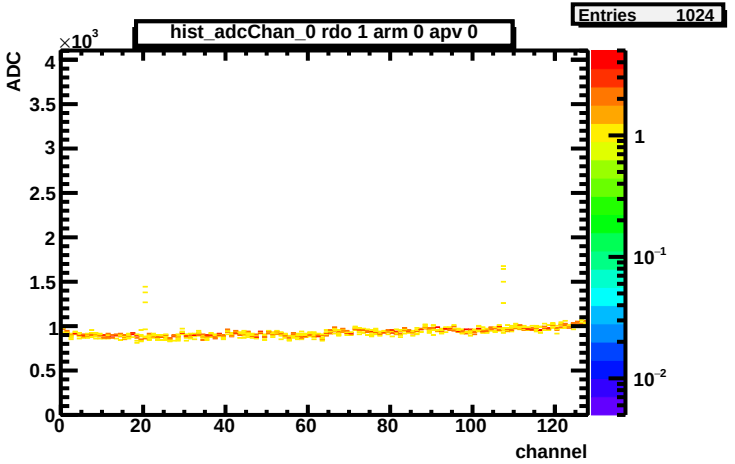
Mon Jun 20 13:24:43 2016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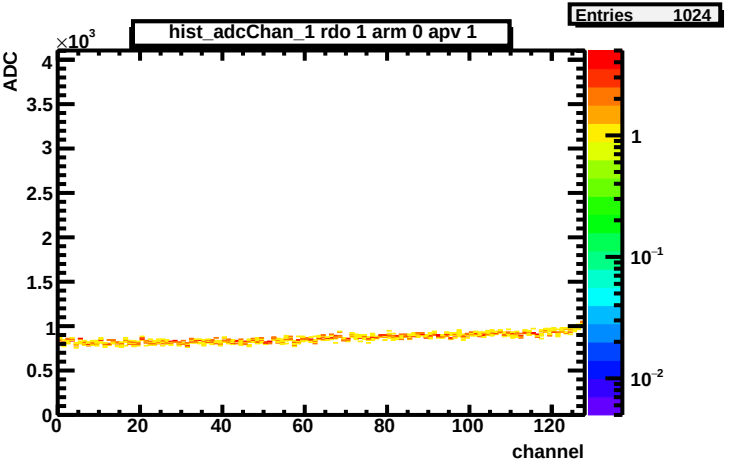
Entries 1024
ADC
channel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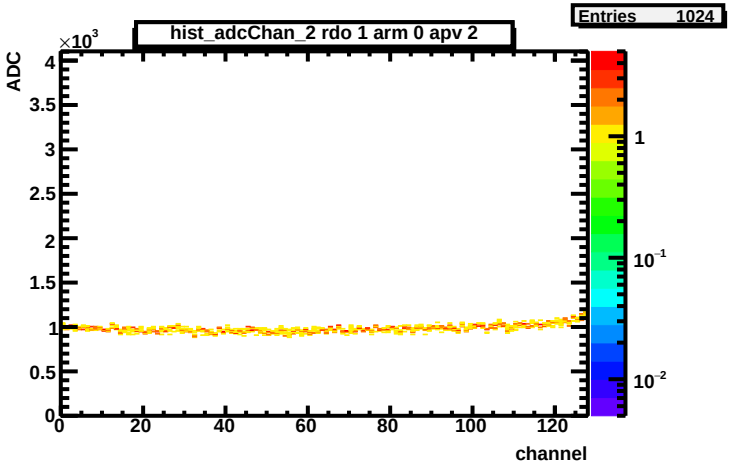
ADC
channel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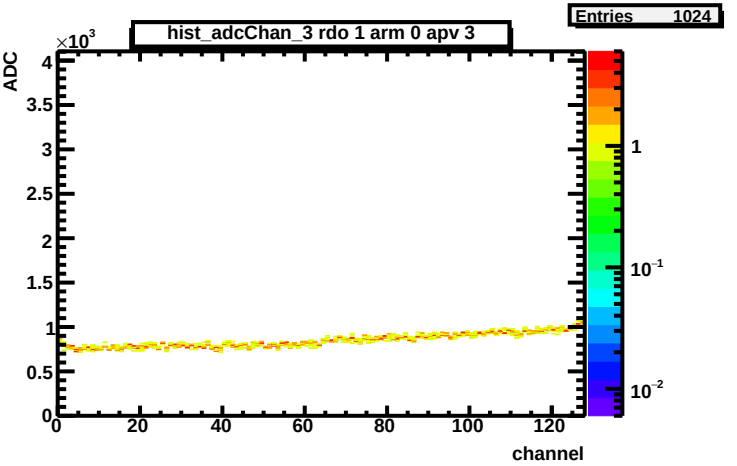
hist_adcChan_3 rdo 1 arm 0 apv 3
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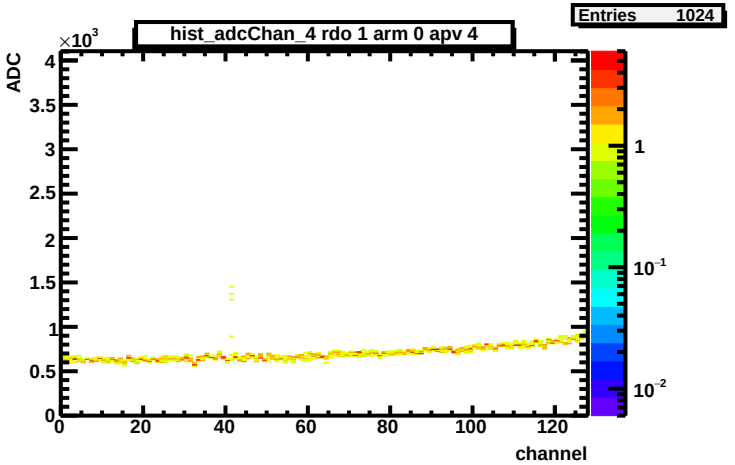
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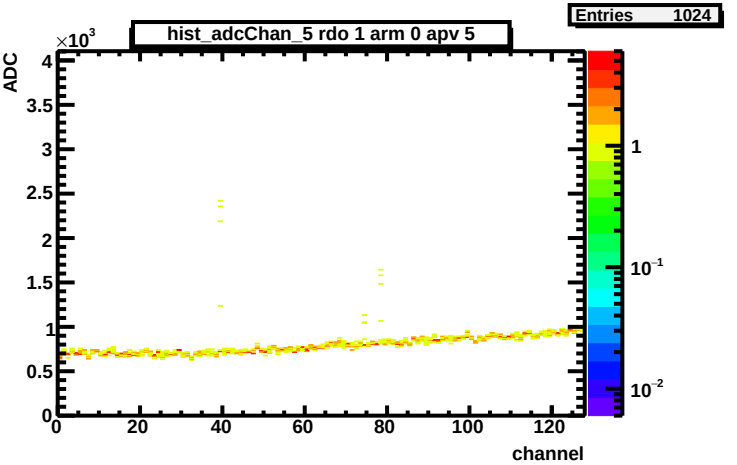
Entries 1024
ADC
channel


hist_adcChan_5 rdo 1 arm 0 apv 5
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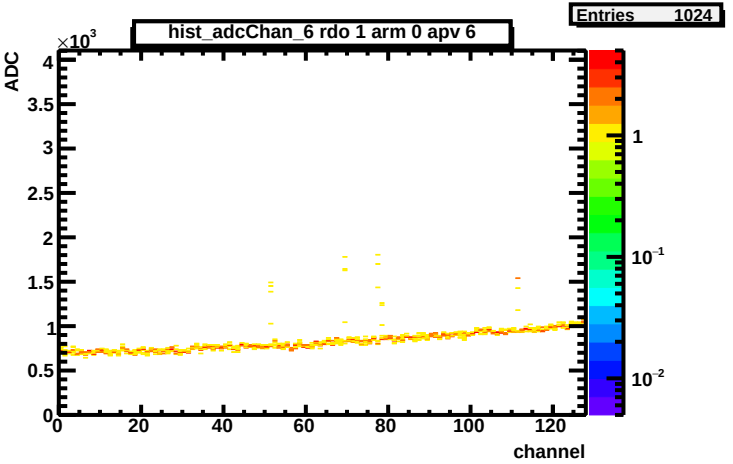
ADC
channel


hist_adcChan_6 rdo 1 arm 0 apv 6
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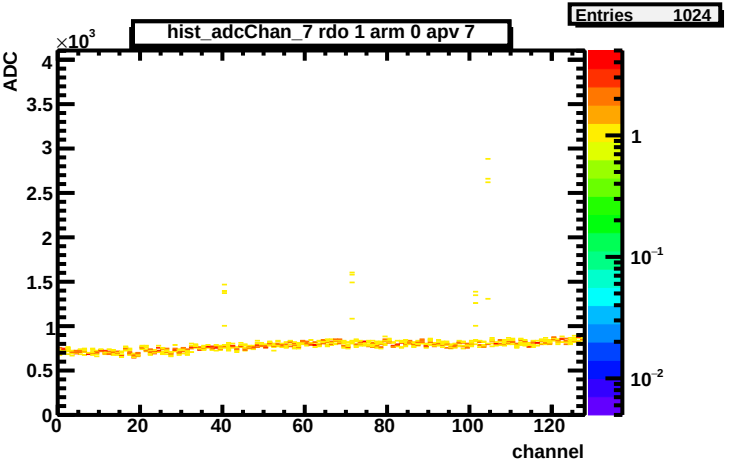
ADC
×10^3
channel


hist_adcChan_7 rdo 1 arm 0 apv 7
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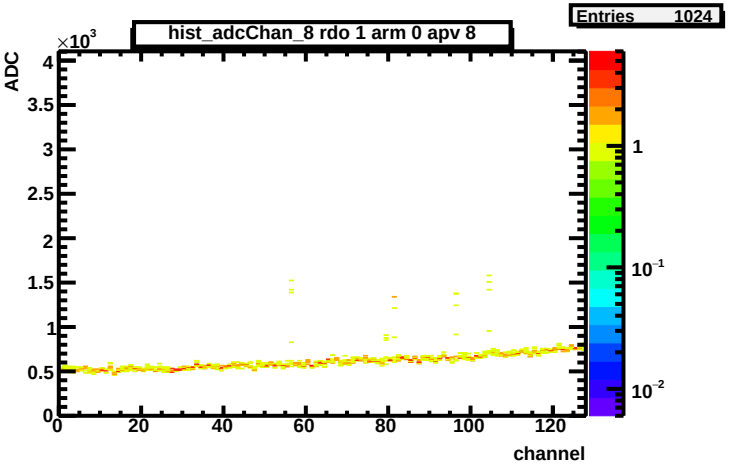
channel


hist_adcChan_8 rdo 1 arm 0 apv 8
Entries 1024
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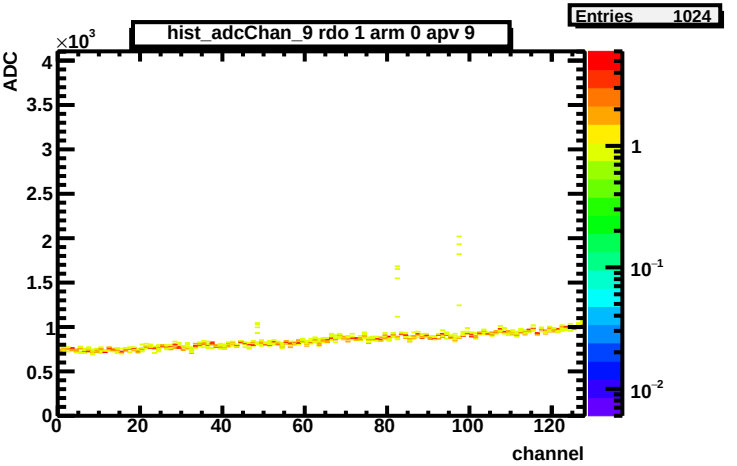
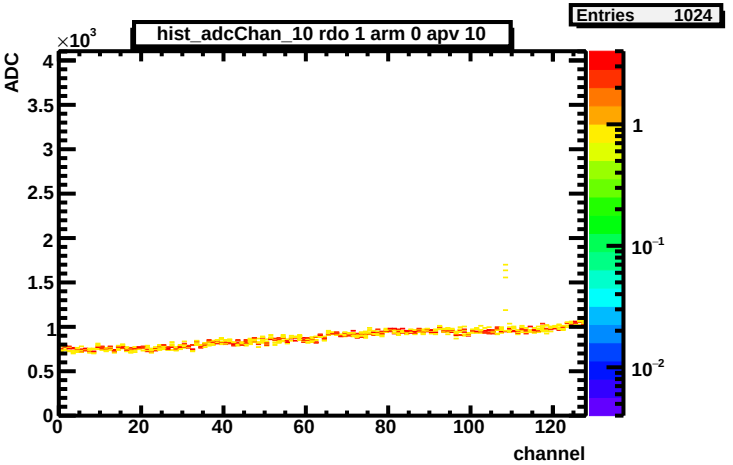
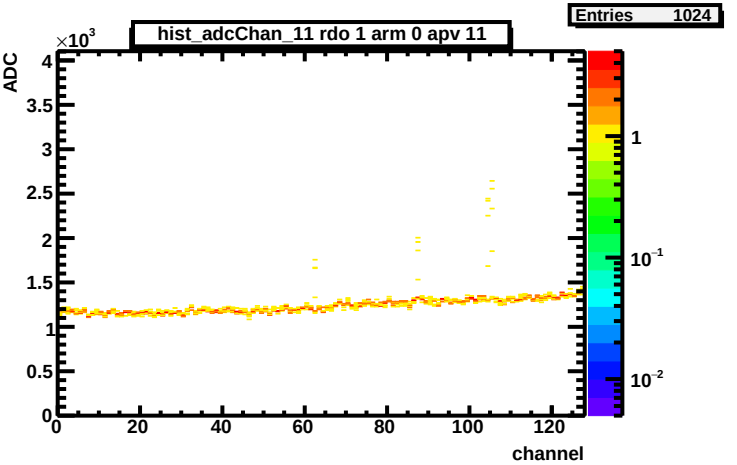
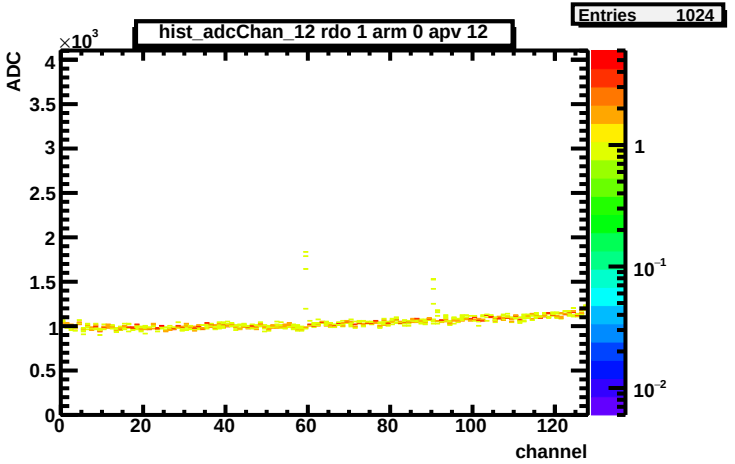
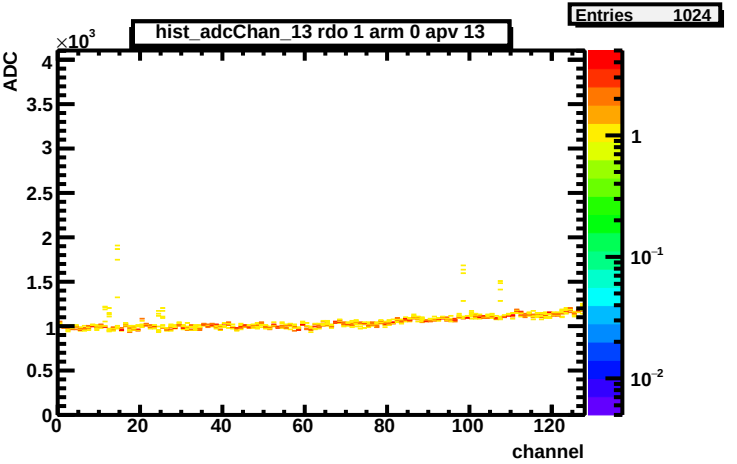
×10^3
channel

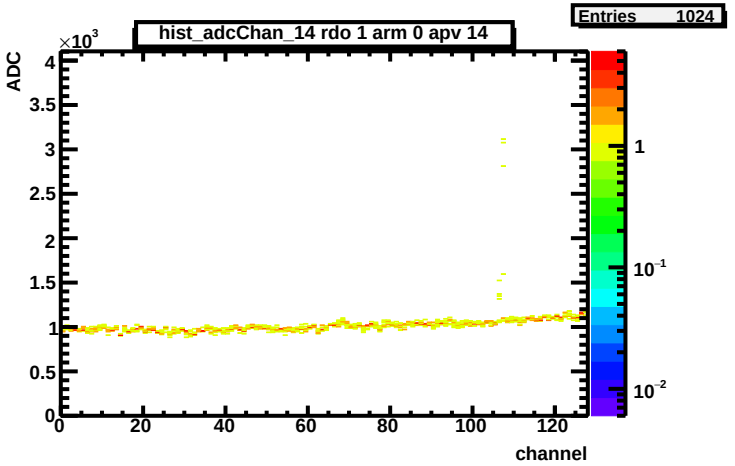

hist_adcChan_9 rdo 1 arm 0 apv 9
Entries 1024
ADC
×10^3
channel

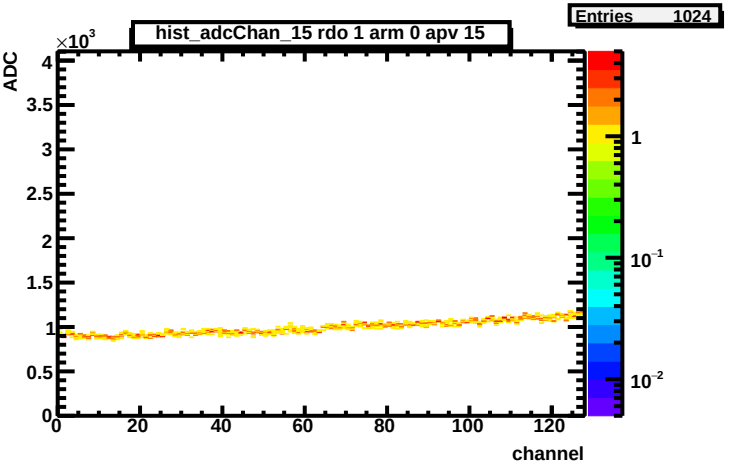

hist_adcChan_10 rdo 1 arm 0 apv 10
Entries 1024
ADC
×10^3
channel

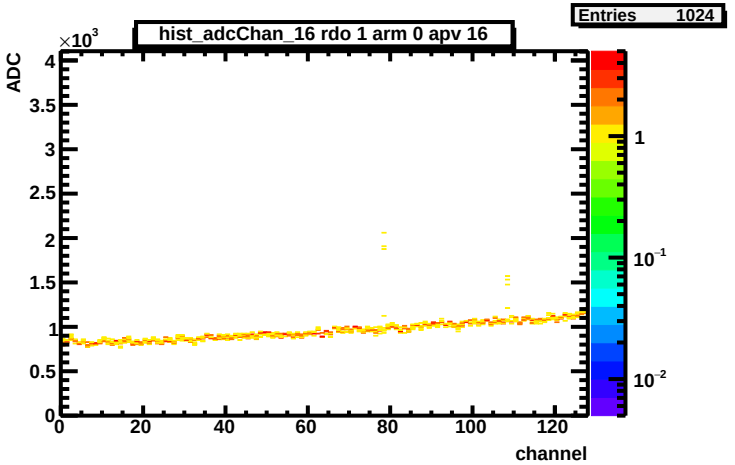

hist_adcChan_11 rdo 1 arm 0 apv 11
Entries 1024
ADC
×10^3
channel

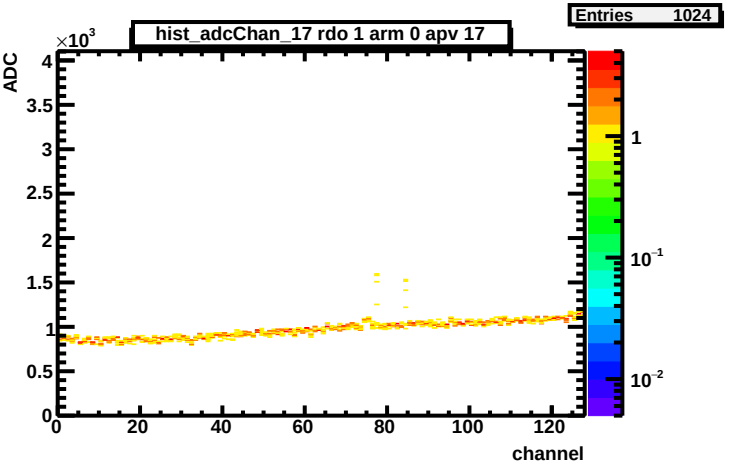

hist_adcChan_12 rdo 1 arm 0 apv 12
Entries 1024
ADC
channel

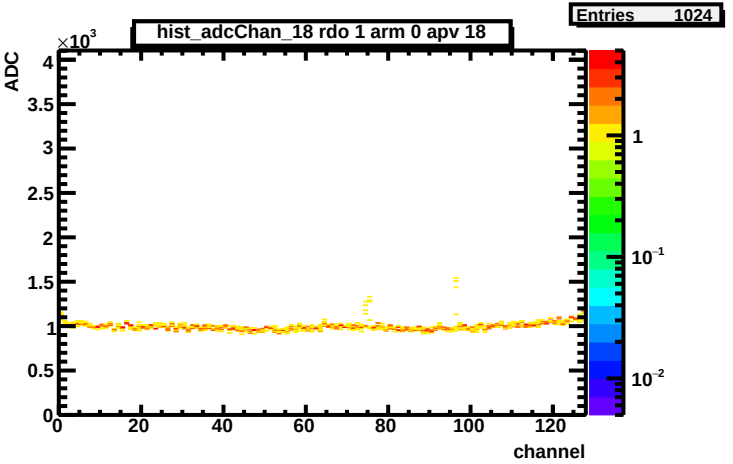

hist_adcChan_13 rdo 1 arm 0 apv 13
Entries 1024
ADC
channel

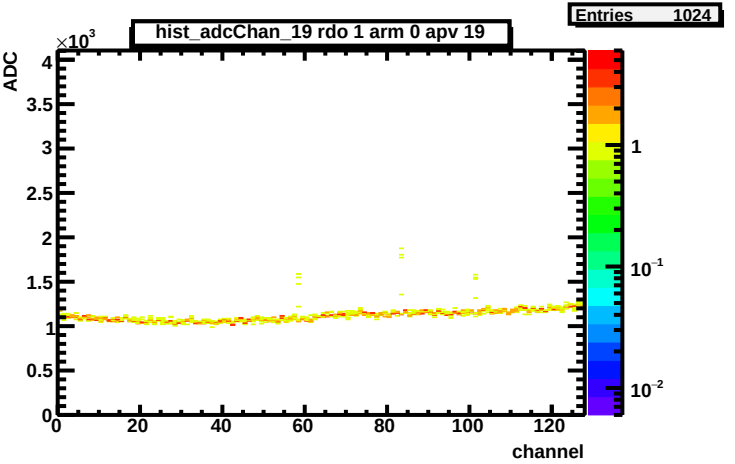

hist_adcChan_14 rdo 1 arm 0 apv 14
Entries 1024
ADC
channel

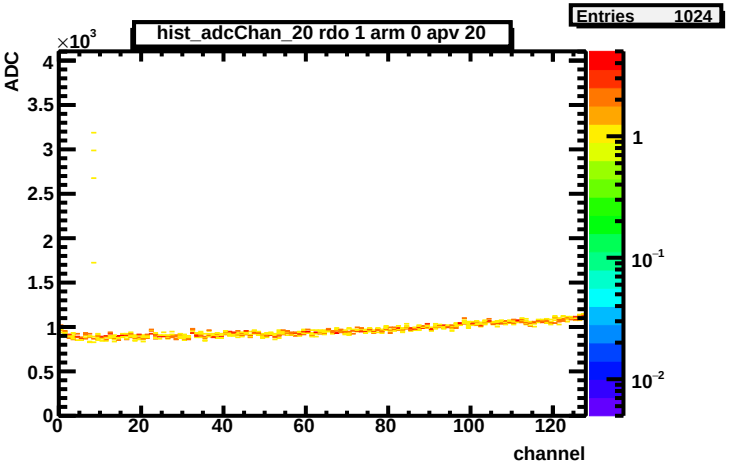

hist_adcChan_15 rdo 1 arm 0 apv 15
Entries 1024
ADC
channel

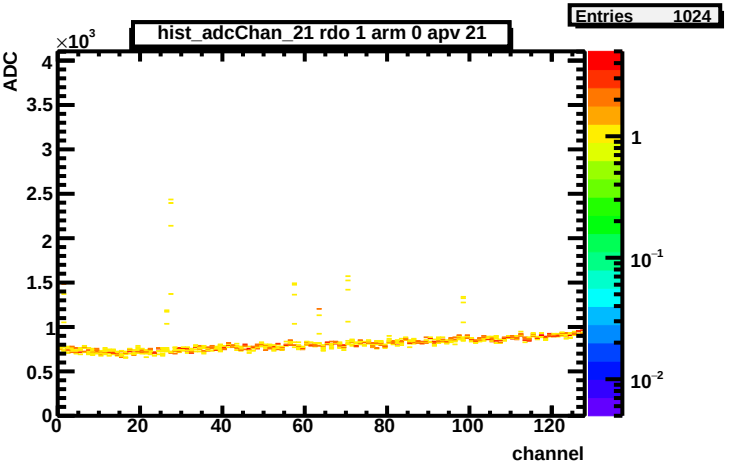

hist_adcChan_16 rdo 1 arm 0 apv 16
Entries 1024
ADC
channel

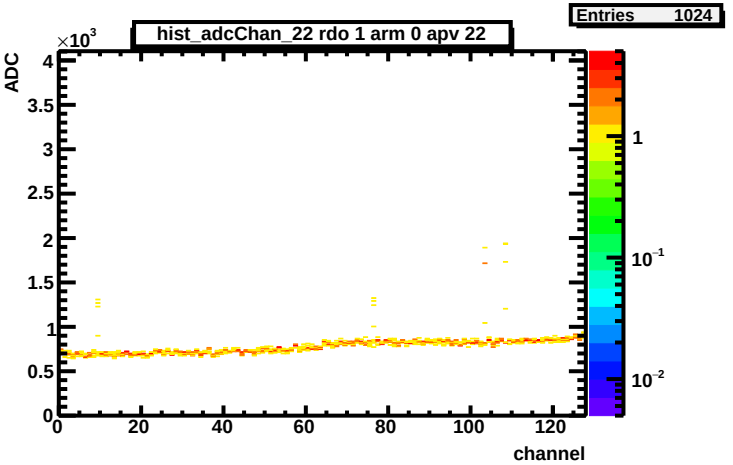

hist_adcChan_17 rdo 1 arm 0 apv 17
Entries 1024
ADC
channel

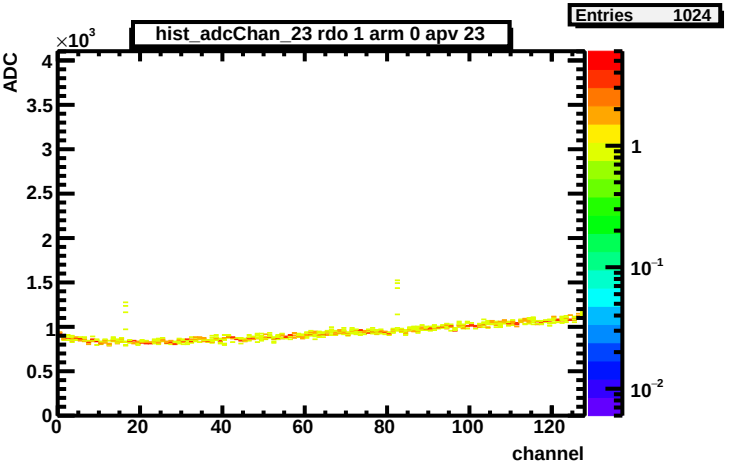

hist_adcChan_18 rdo 1 arm 0 apv 18
Entries 1024
ADC
channel

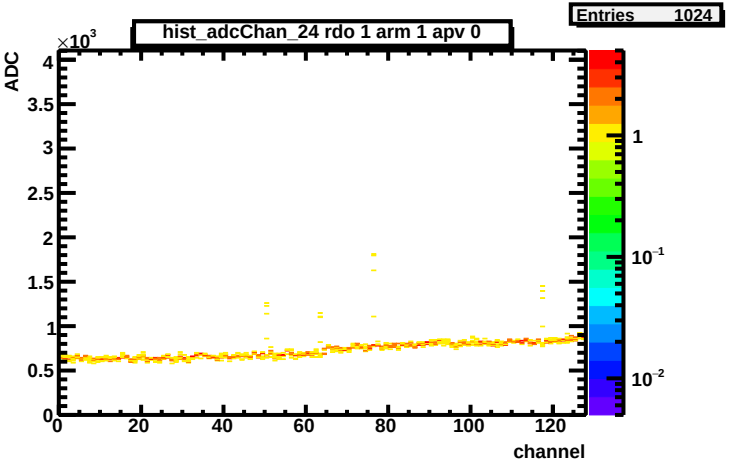

hist_adcChan_19 rdo 1 arm 0 apv 19
Entries 1024
ADC
channel


hist_adcChan_20 rdo 1 arm 0 apv 20
Entries 1024
ADC
channel


hist_adcChan_21 rdo 1 arm 0 apv 21
Entries 1024
ADC
channel


hist_adcChan_22 rdo 1 arm 0 apv 22
Entries 1024
ADC
channel


hist_adcChan_23 rdo 1 arm 0 apv 23
Entries 1024
ADC
channel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Entries 1024
ADC
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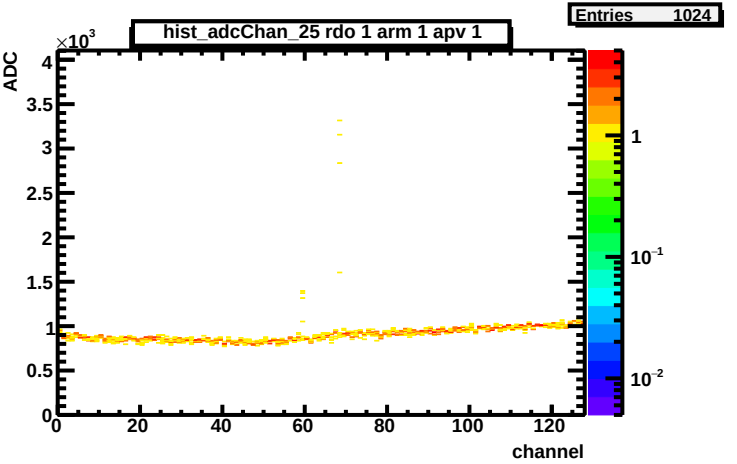
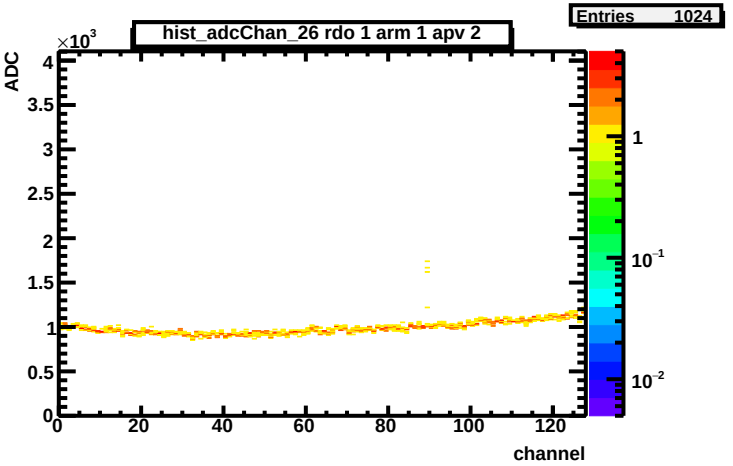
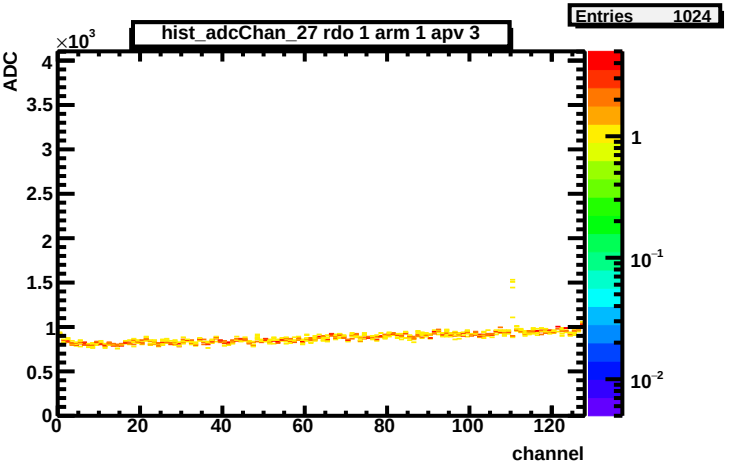
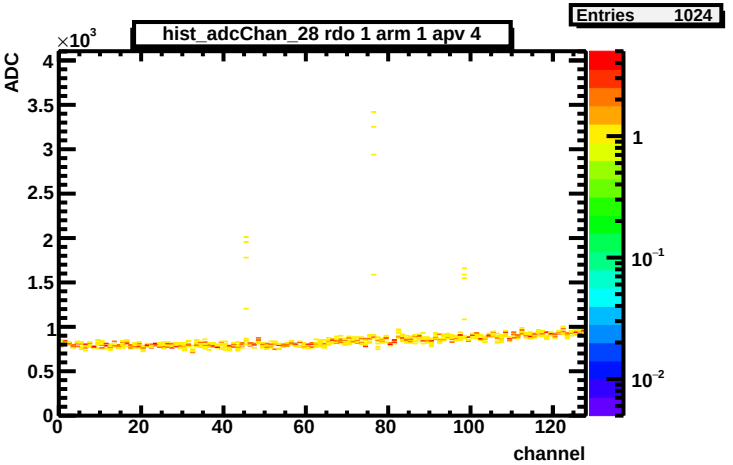
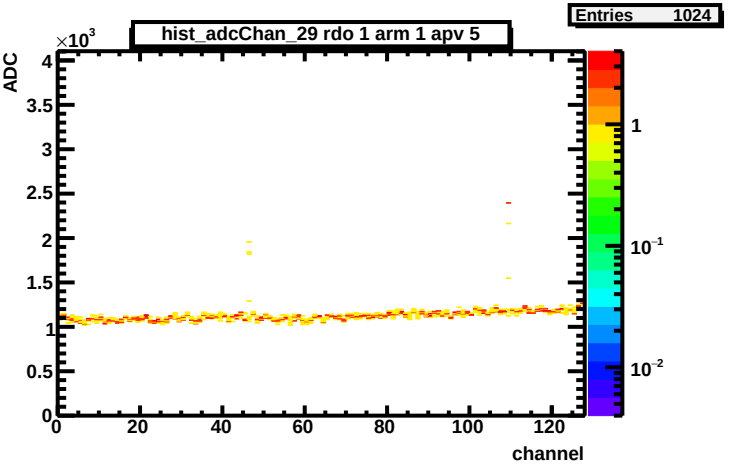
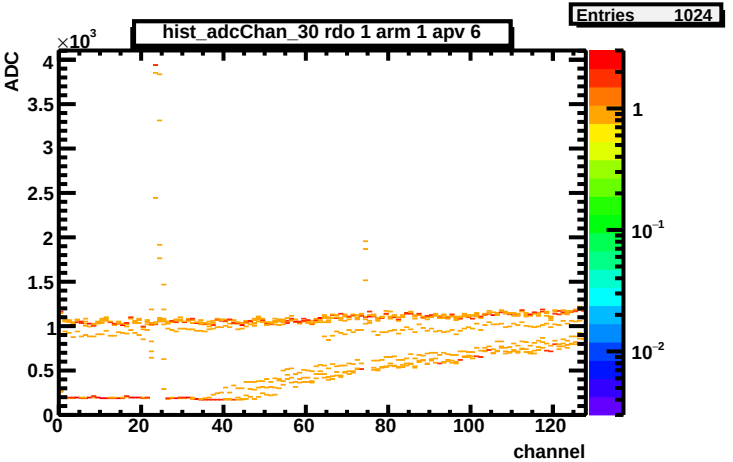

hist_adcChan_25 rdo 1 arm 1 apv 1
Entries 1024
ADC
channel

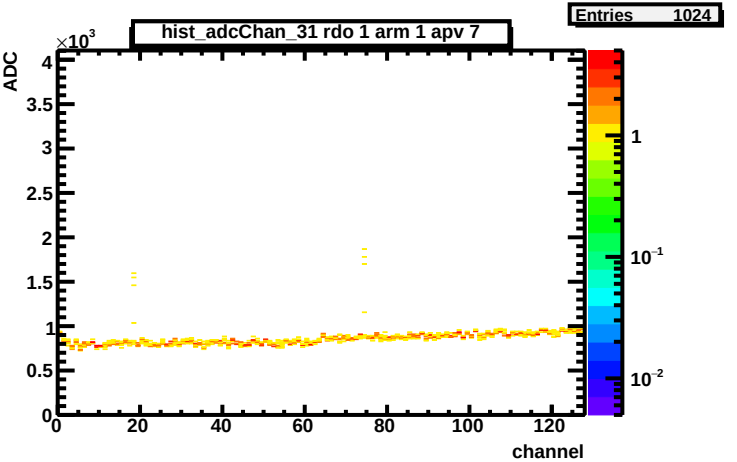

hist_adcChan_26 rdo 1 arm 1 apv 2
Entries 1024
ADC
channel

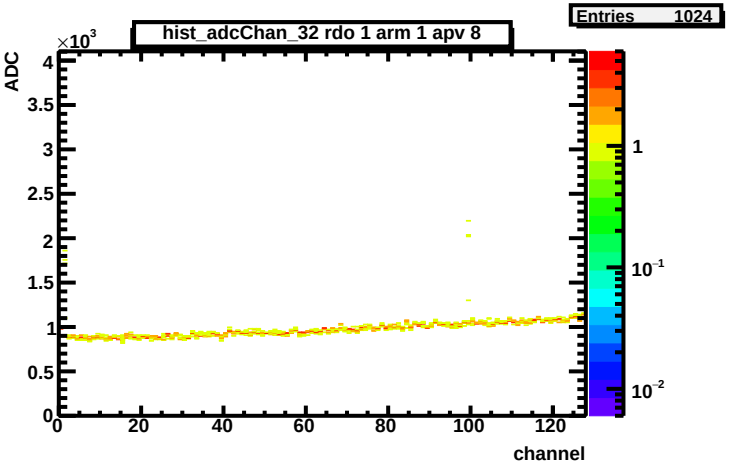

hist_adcChan_27 rdo 1 arm 1 apv 3
Entries 1024
ADC
channel

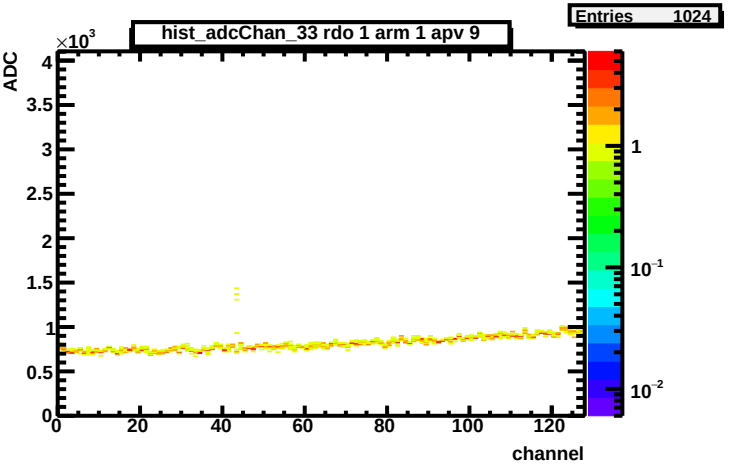

hist_adcChan_28 rdo 1 arm 1 apv 4
Entries 1024
ADC
channel

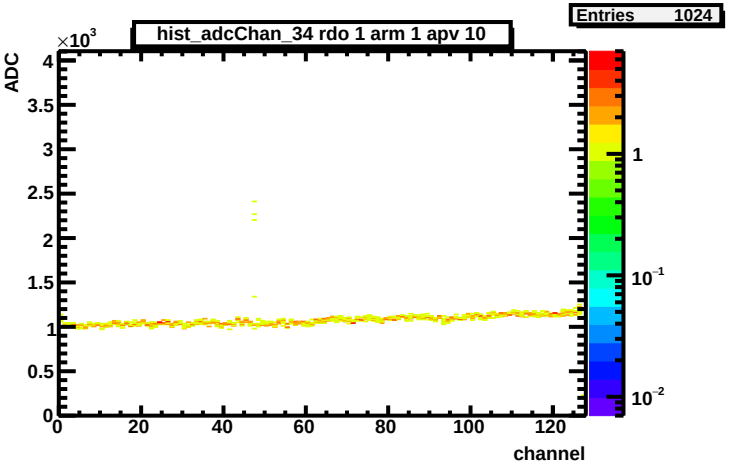

hist_adcChan_29 rdo 1 arm 1 apv 5
Entries 1024
ADC
channel

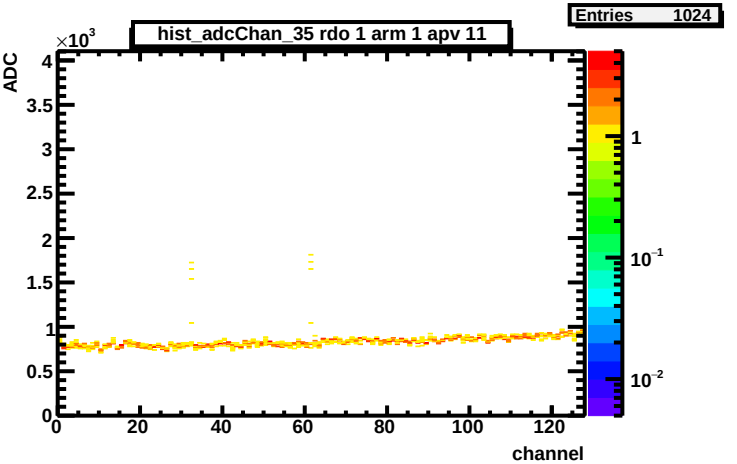

hist_adcChan_30 rdo 1 arm 1 apv 6
Entries 1024
ADC
×10^3
channel

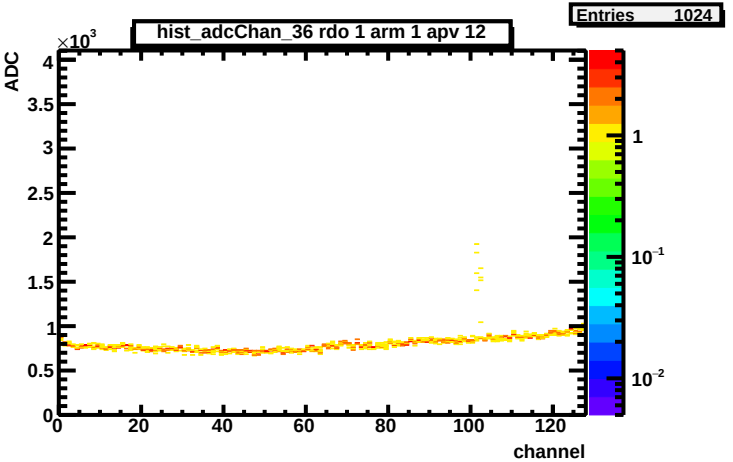

hist_adcChan_31 rdo 1 arm 1 apv 7
Entries 1024
ADC
×10^3
channel

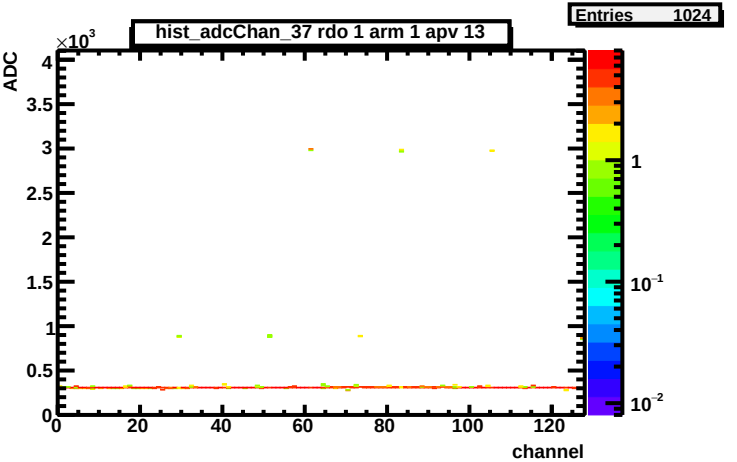

hist_adcChan_32 rdo 1 arm 1 apv 8
Entries 1024
ADC
×10^3
channel

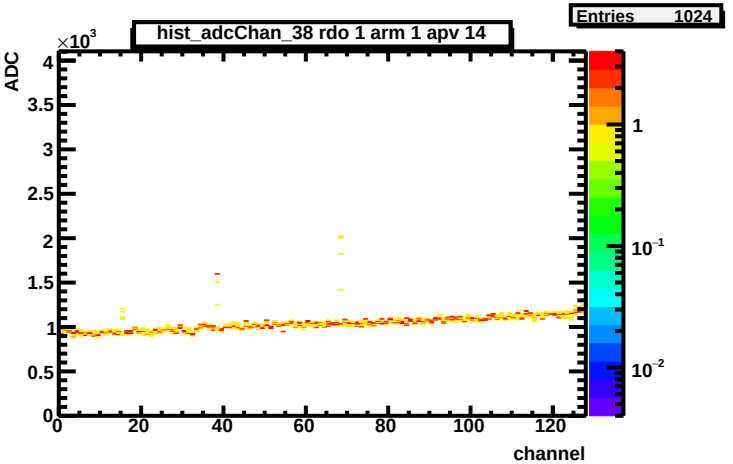

hist_adcChan_33 rdo 1 arm 1 apv 9
Entries 1024
ADC
×10^3
channel

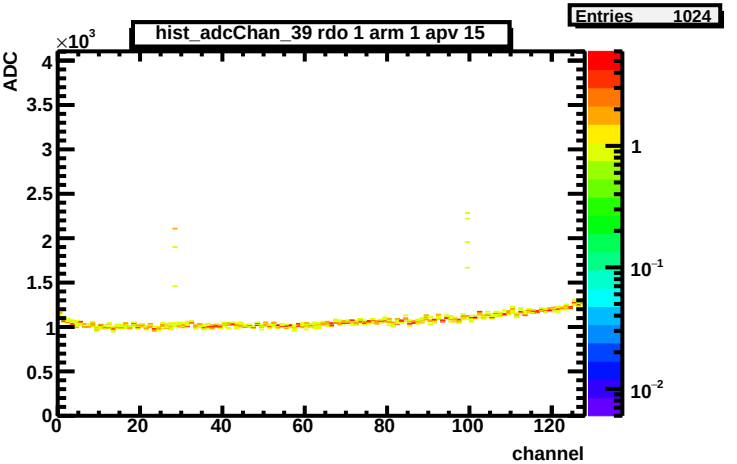

hist_adcChan_34 rdo 1 arm 1 apv 10
Entries 1024
ADC
×10^3
channel

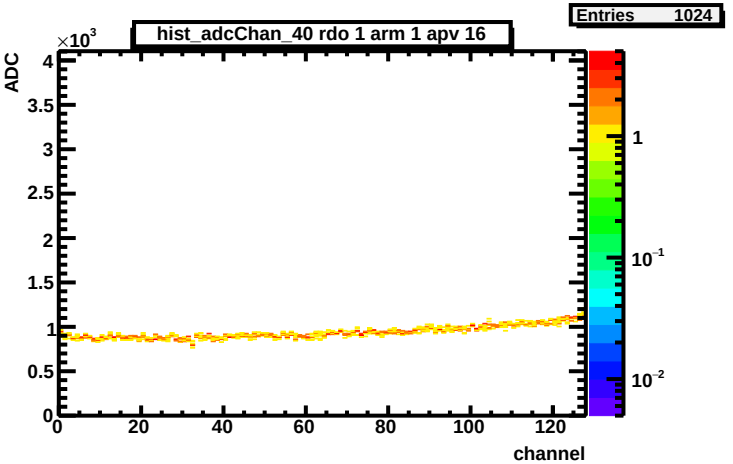

hist_adcChan_35 rdo 1 arm 1 apv 11
Entries 1024
ADC
×10^3
channel

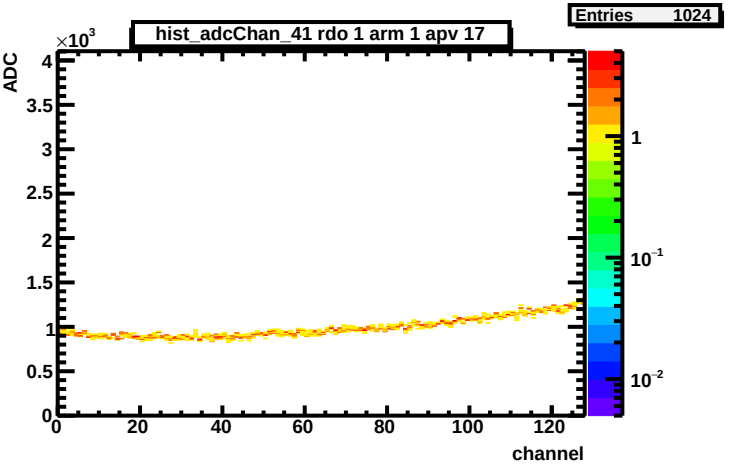

hist_adcChan_36 rdo 1 arm 1 apv 12
Entries 1024
ADC
×10^3
channel

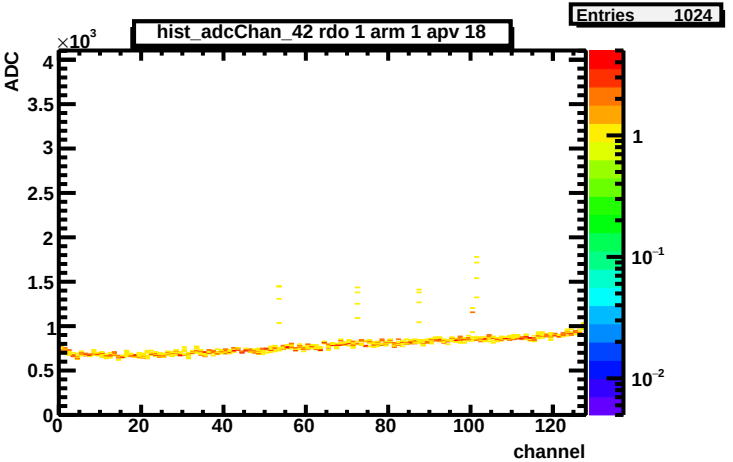

hist_adcChan_37 rdo 1 arm 1 apv 13
Entries 1024
ADC
×10^3
channel

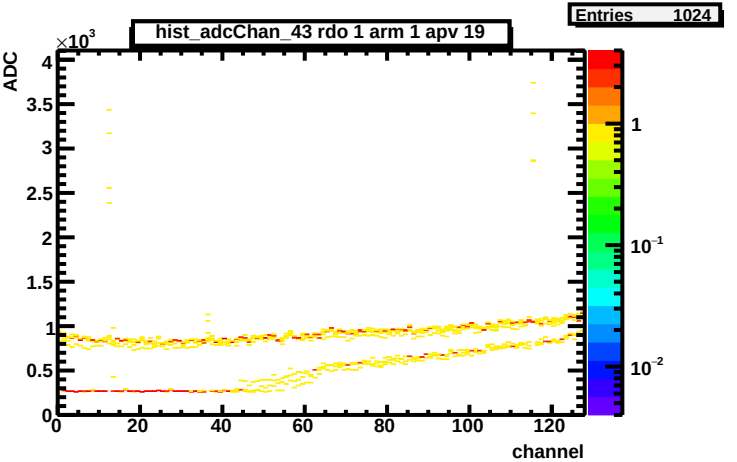

hist_adcChan_38 rdo 1 arm 1 apv 14
Entries 1024
ADC
×10^3
channel

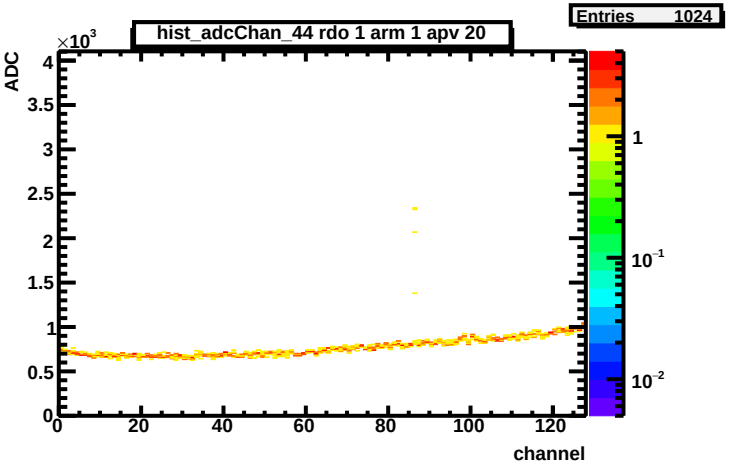

hist_adcChan_39 rdo 1 arm 1 apv 15
Entries 1024
ADC
×10^3
channel

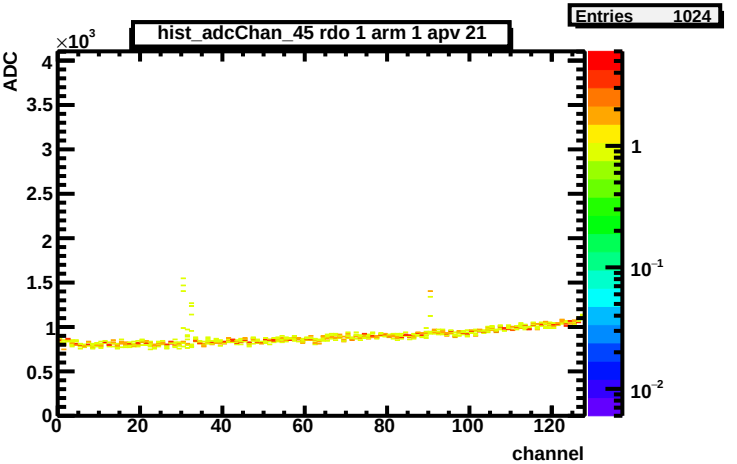

hist_adcChan_40 rdo 1 arm 1 apv 16
Entries 1024
ADC
×10^3
channel

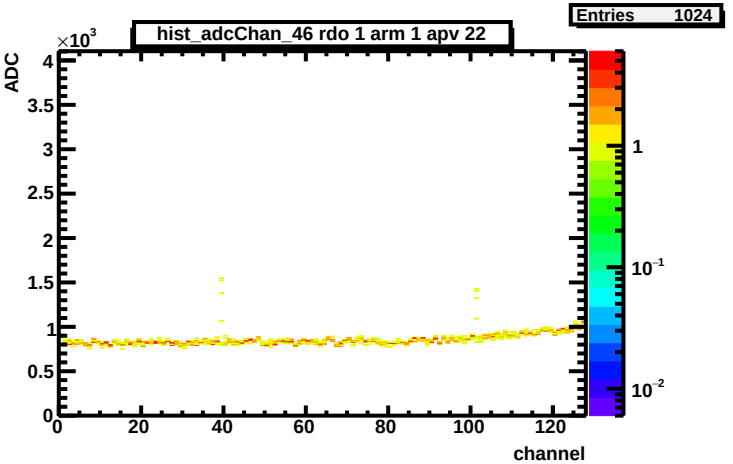

hist_adcChan_41 rdo 1 arm 1 apv 17
Entries 1024
ADC
×10^3
channel

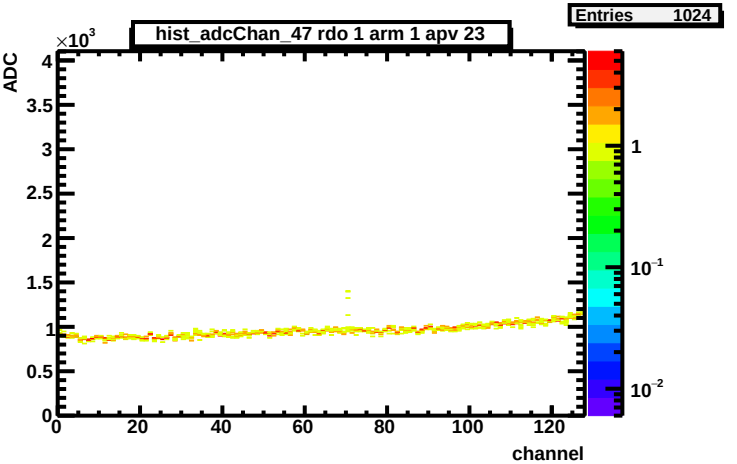

hist_adcChan_42 rdo 1 arm 1 apv 18
Entries 1024
ADC
×10^3
channel

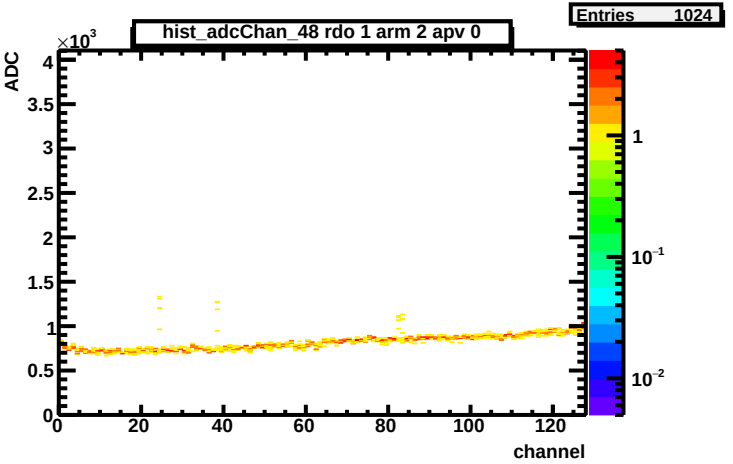

hist_adcChan_43 rdo 1 arm 1 apv 19
Entries 1024
ADC
×10^3
channel


hist_adcChan_44 rdo 1 arm 1 apv 20
Entries 1024
ADC
×10^3
channel


hist_adcChan_45 rdo 1 arm 1 apv 21
Entries 1024
ADC
×10^3
channel


hist_adcChan_46 rdo 1 arm 1 apv 22
Entries 1024
ADC
×10^3
channel


hist_adcChan_47 rdo 1 arm 1 apv 23
Entries 1024
ADC
×10^3
channel


hist_adcChan_48 rdo 1 arm 2 apv 0
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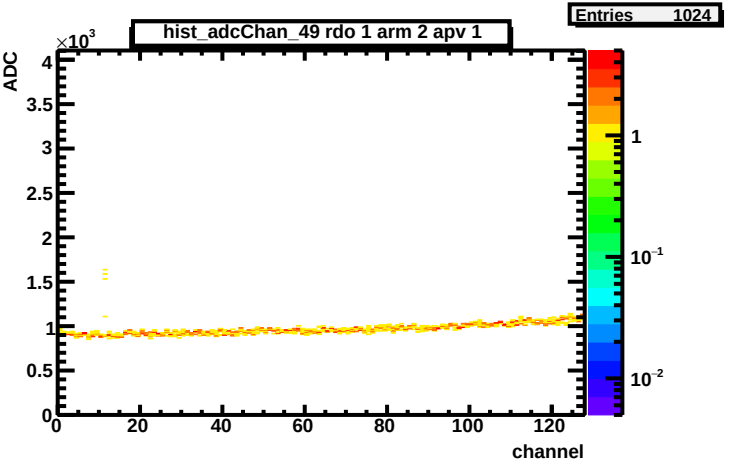
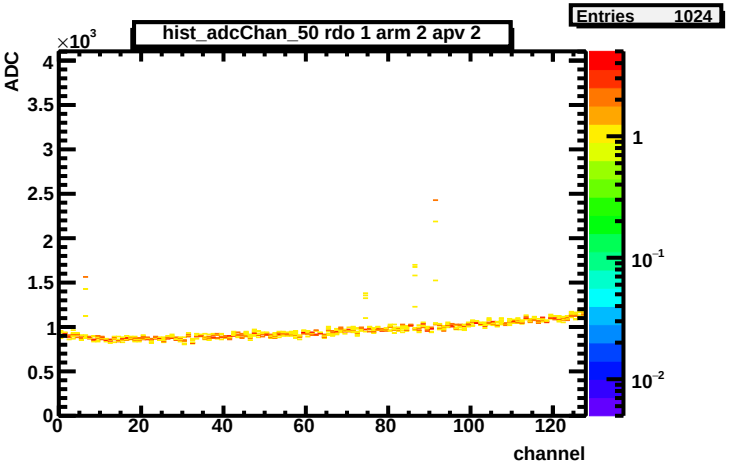
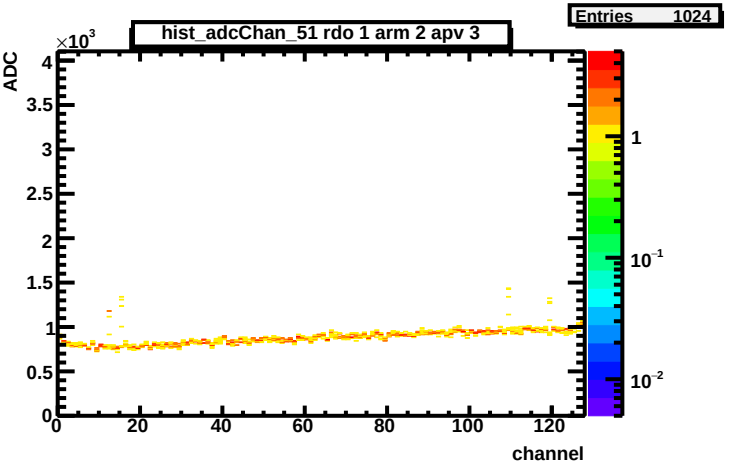
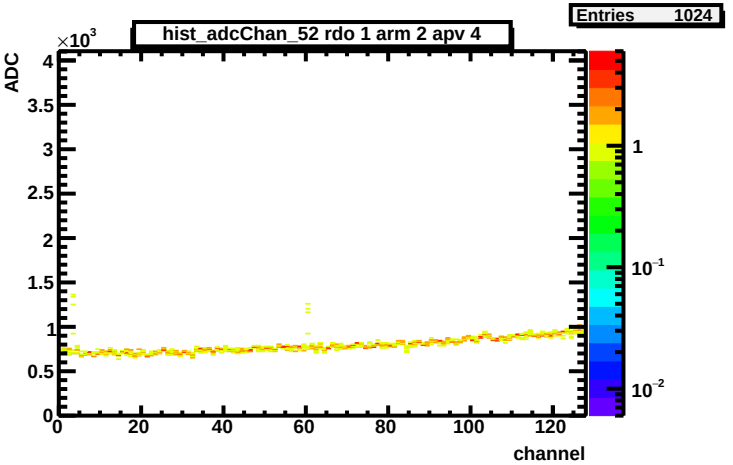
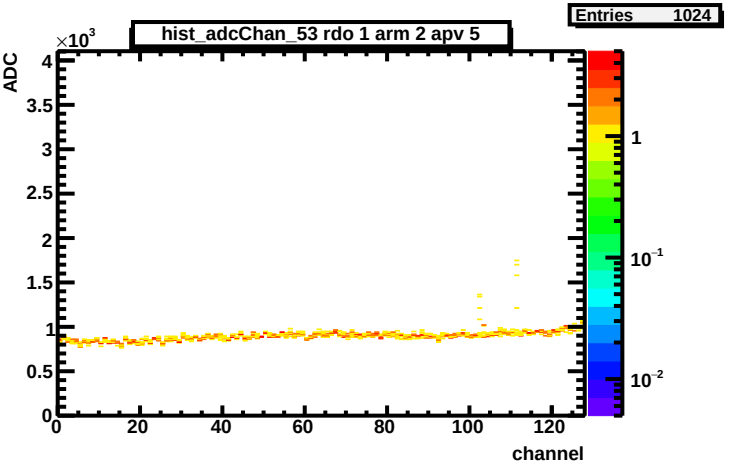
ADC
channel

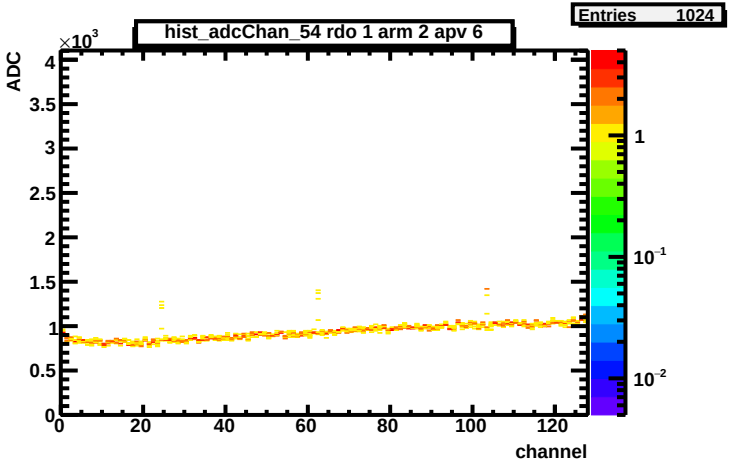

hist_adcChan_49 rdo 1 arm 2 apv 1
Entries 1024
ADC
channel

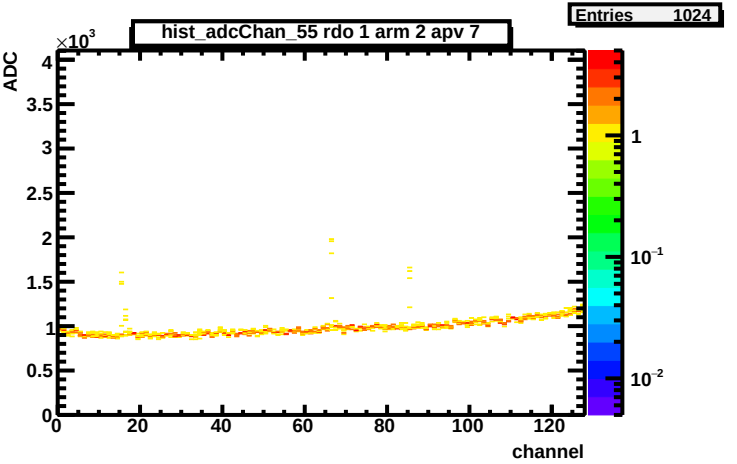

hist_adcChan_50 rdo 1 arm 2 apv 2
Entries 1024
ADC
channel

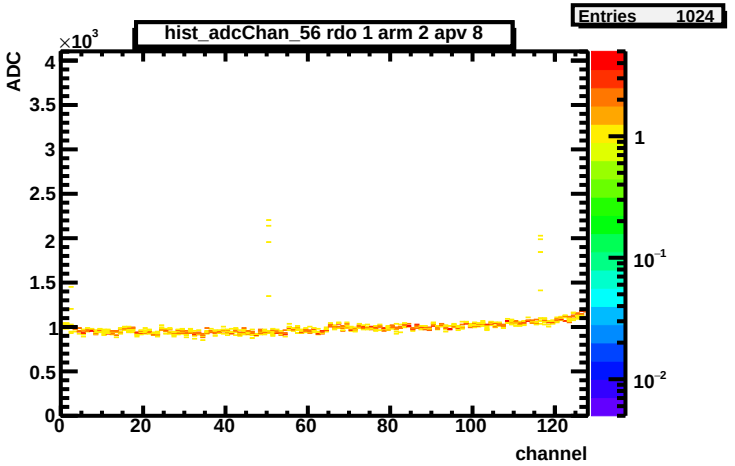

hist_adcChan_51 rdo 1 arm 2 apv 3
Entries 1024
ADC
channel

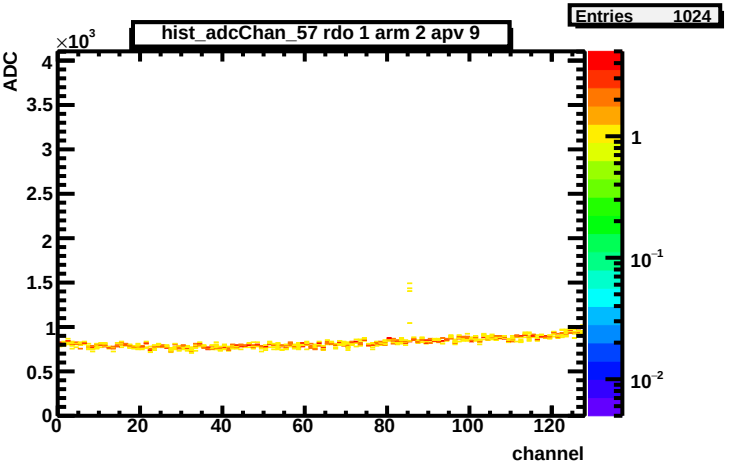

hist_adcChan_52 rdo 1 arm 2 apv 4
Entries 1024
ADC
channel

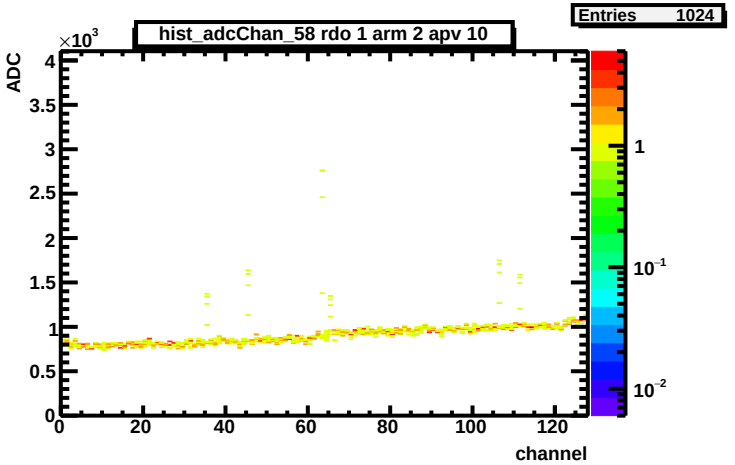

hist_adcChan_53 rdo 1 arm 2 apv 5
Entries 1024
ADC
channel

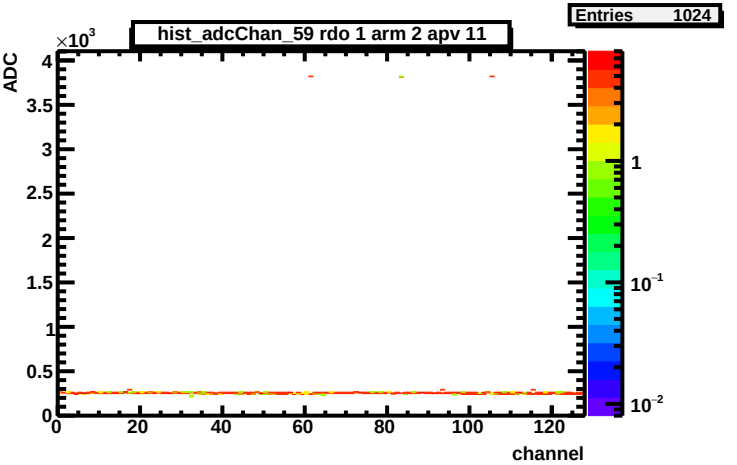

hist_adcChan_54 rdo 1 arm 2 apv 6
Entries 1024
ADC
×10^3
channel

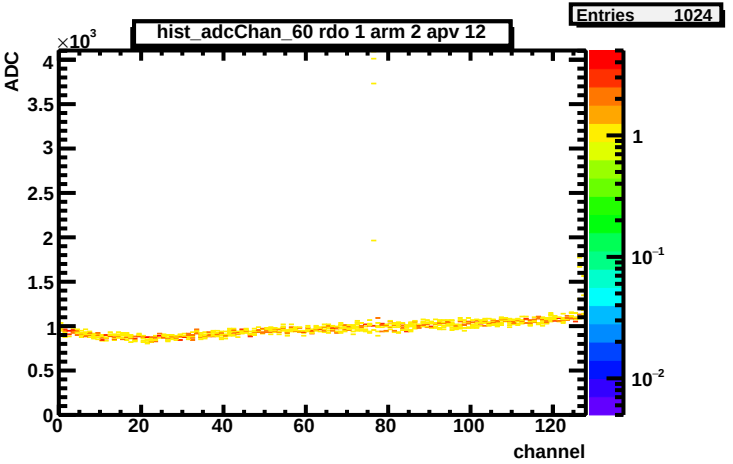

hist_adcChan_55 rdo 1 arm 2 apv 7
Entries 1024
ADC
×10^3
channel

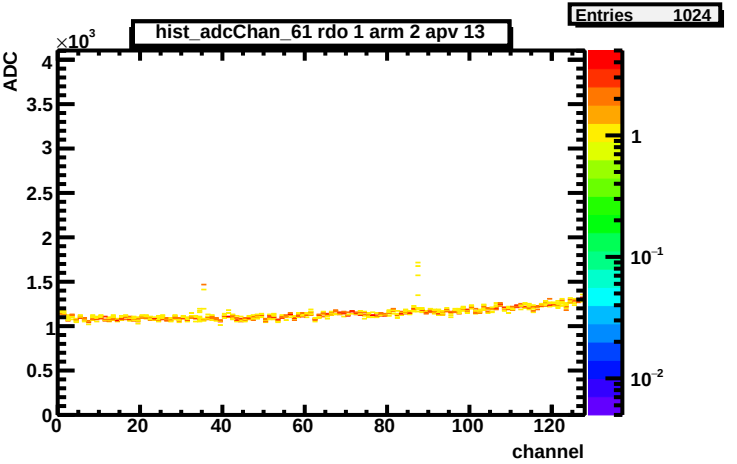

hist_adcChan_56 rdo 1 arm 2 apv 8
Entries 1024
ADC
×10^3
channel

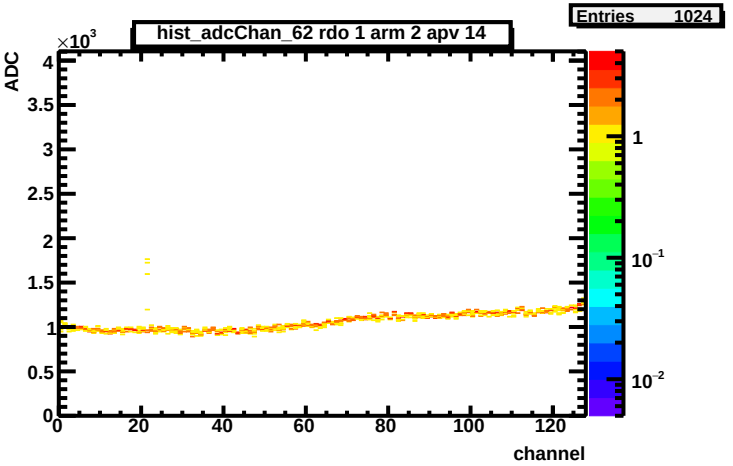

hist_adcChan_57 rdo 1 arm 2 apv 9
Entries 1024
ADC
×10^3
channel

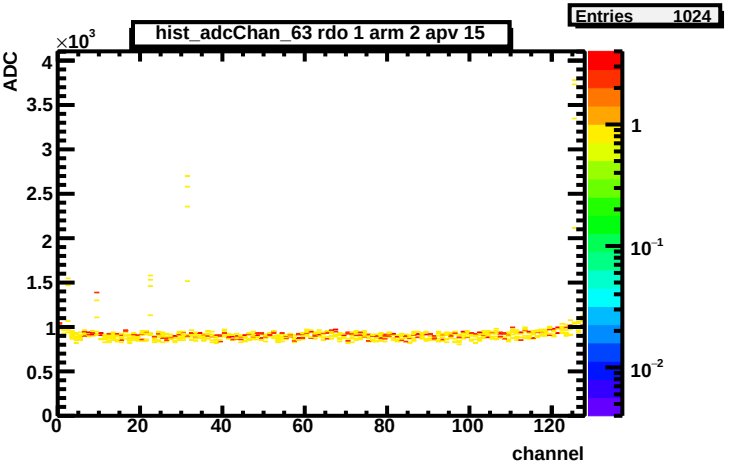

hist_adcChan_58 rdo 1 arm 2 apv 10
Entries 1024
ADC
×10^3
channel

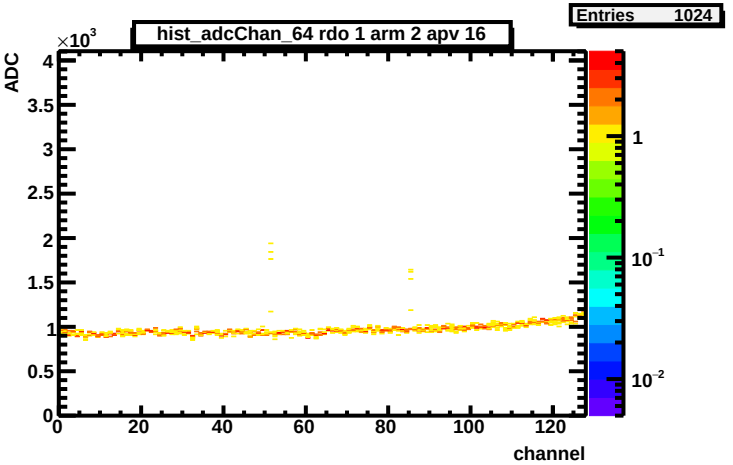

hist_adcChan_59 rdo 1 arm 2 apv 11
Entries 1024
ADC
×10^3
channel

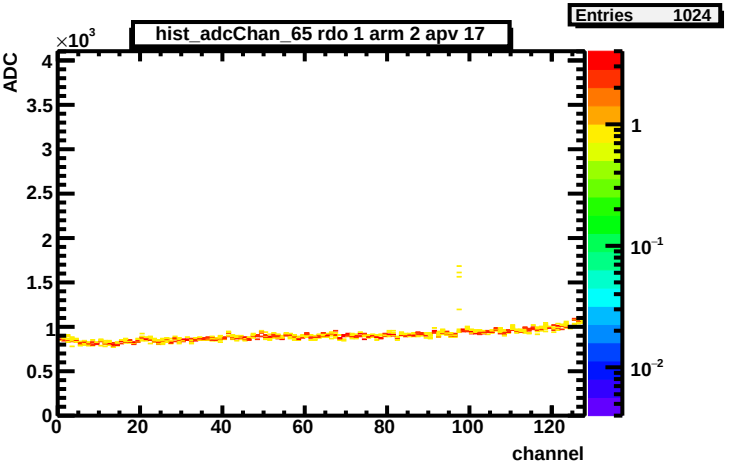

hist_adcChan_60 rdo 1 arm 2 apv 12
Entries 1024
ADC
×10³
channel

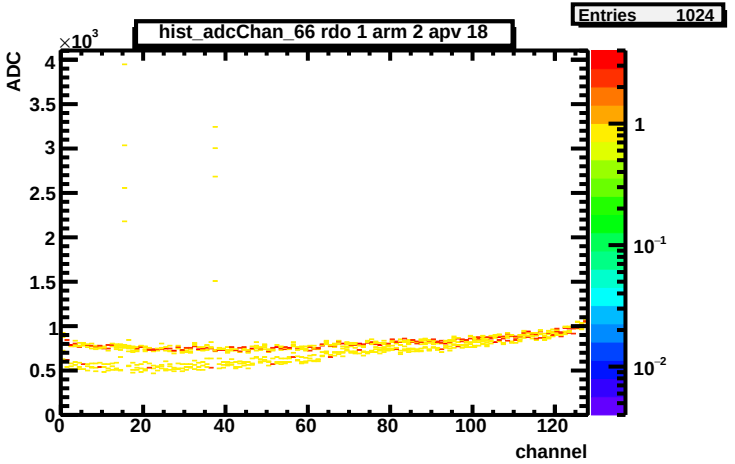

hist_adcChan_61 rdo 1 arm 2 apv 13
Entries 1024
ADC
×10³
channel

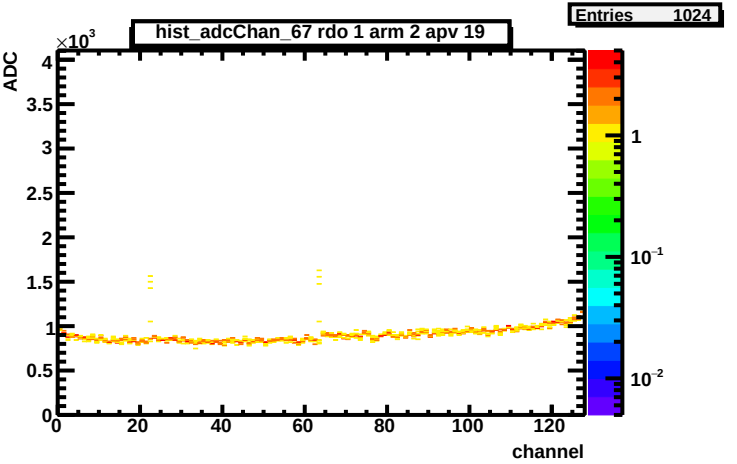

hist_adcChan_62 rdo 1 arm 2 apv 14
Entries 1024
ADC
×10³
channel

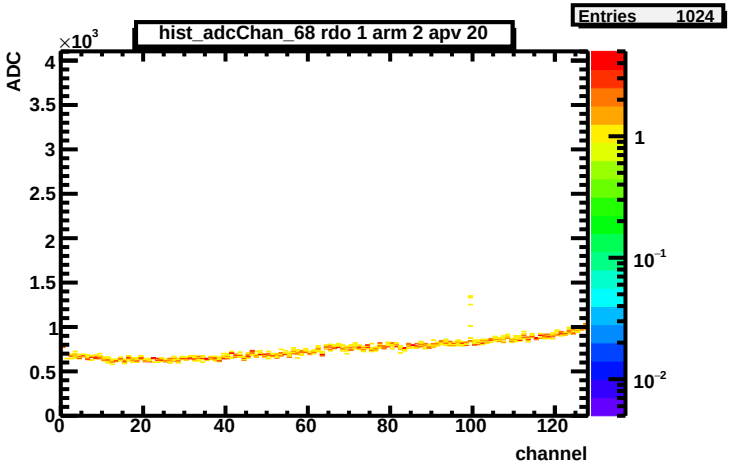

hist_adcChan_63 rdo 1 arm 2 apv 15
Entries 1024
ADC
×10³
channel

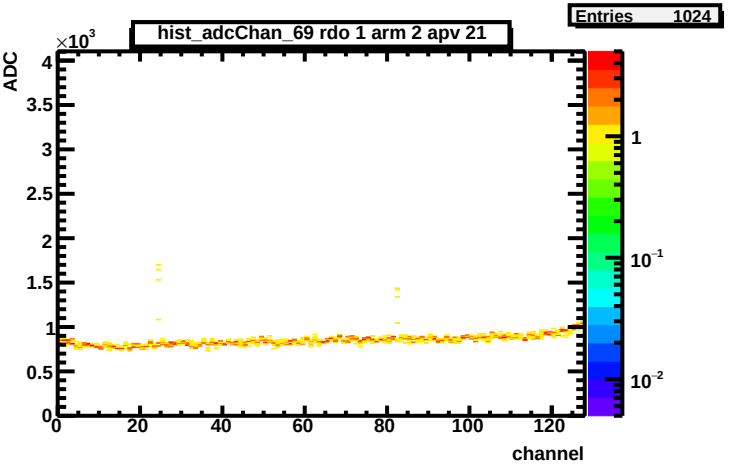

hist_adcChan_64 rdo 1 arm 2 apv 16
Entries 1024
ADC
×10³
channel

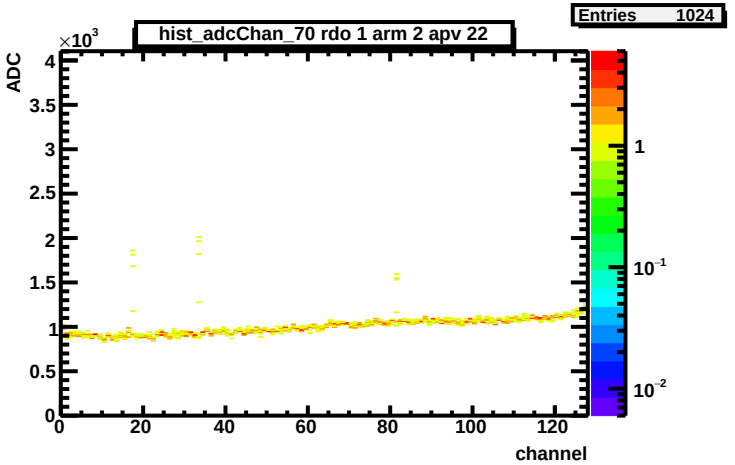

hist_adcChan_65 rdo 1 arm 2 apv 17
Entries 1024
ADC
×10³
channel

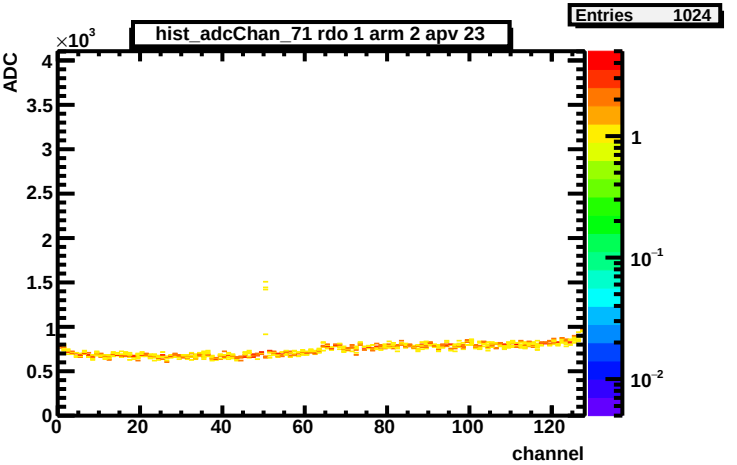

hist_adcChan_66 rdo 1 arm 2 apv 18
Entries 1024
ADC
channel

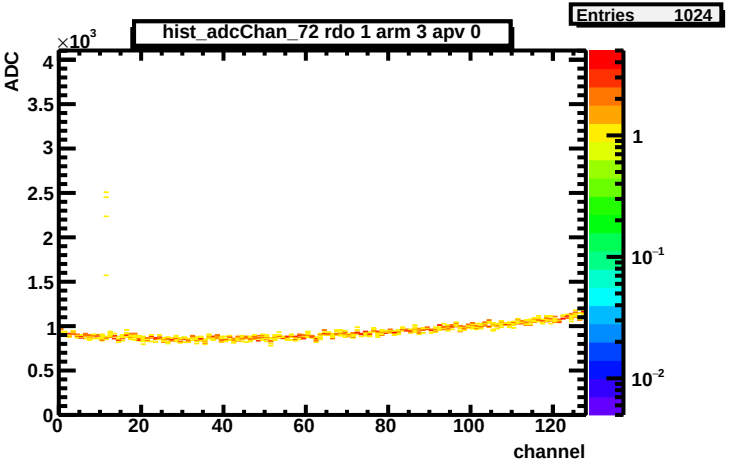

hist_adcChan_67 rdo 1 arm 2 apv 19
Entries 1024
ADC
channel

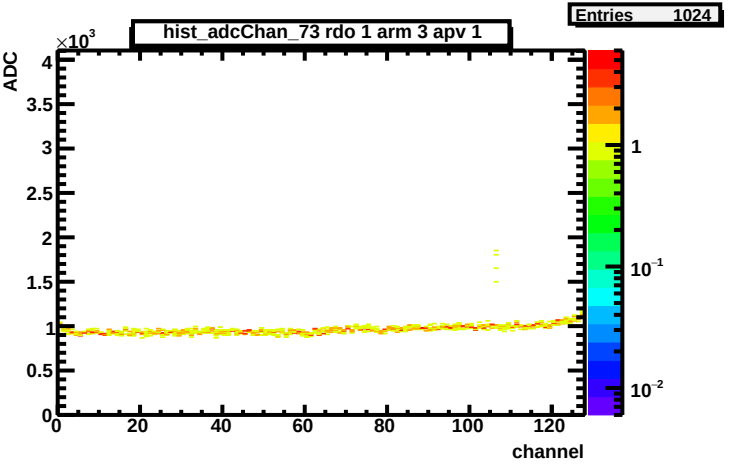

hist_adcChan_68 rdo 1 arm 2 apv 20
Entries 1024
ADC
channel

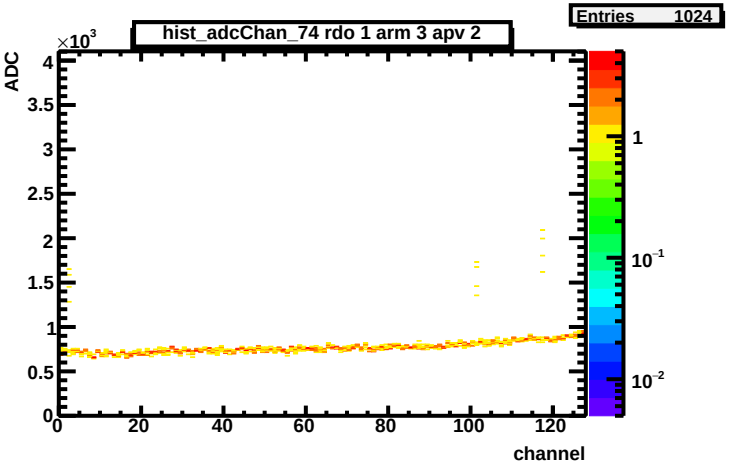

hist_adcChan_69 rdo 1 arm 2 apv 21
Entries 1024
ADC
channel

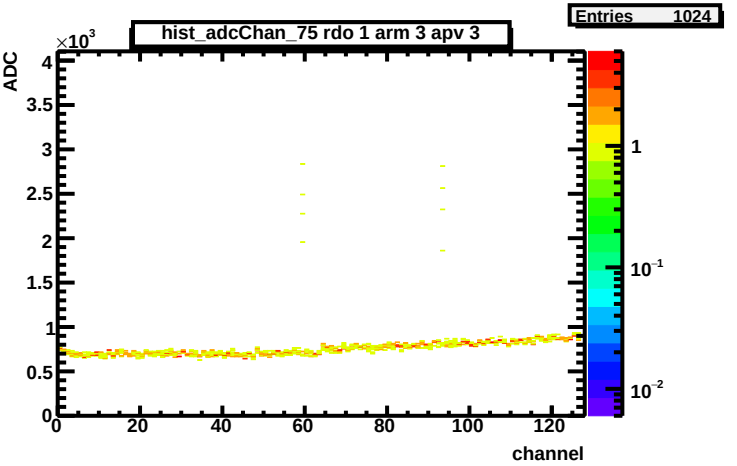

hist_adcChan_70 rdo 1 arm 2 apv 22
Entries 1024
ADC
channel

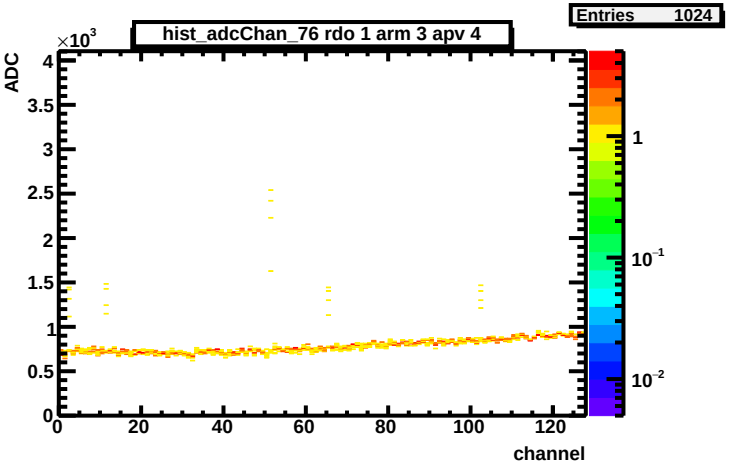

hist_adcChan_71 rdo 1 arm 2 apv 23
Entries 1024
ADC
channel

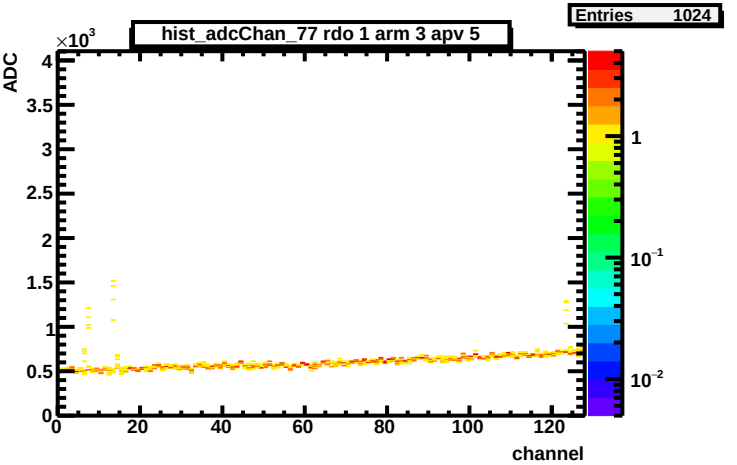

hist_adcChan_72 rdo 1 arm 3 apv 0
Entries 1024
ADC
×10^3
channel

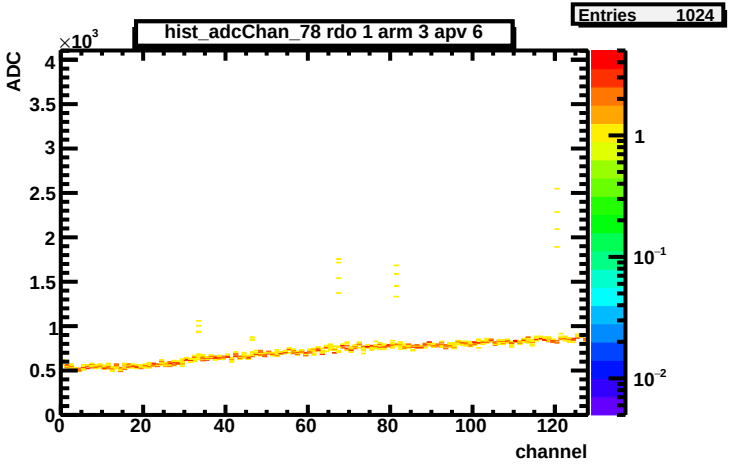

hist_adcChan_73 rdo 1 arm 3 apv 1
Entries 1024
ADC
×10^3
channel

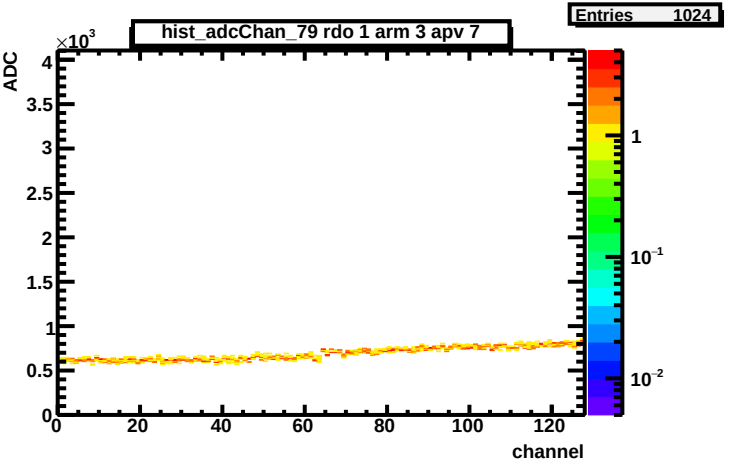

hist_adcChan_74 rdo 1 arm 3 apv 2
Entries 1024
ADC
×10^3
channel

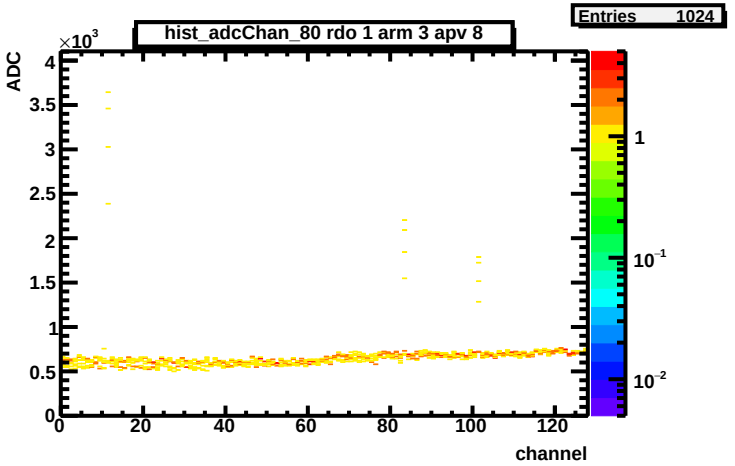

hist_adcChan_75 rdo 1 arm 3 apv 3
Entries 1024
ADC
×10^3
channel

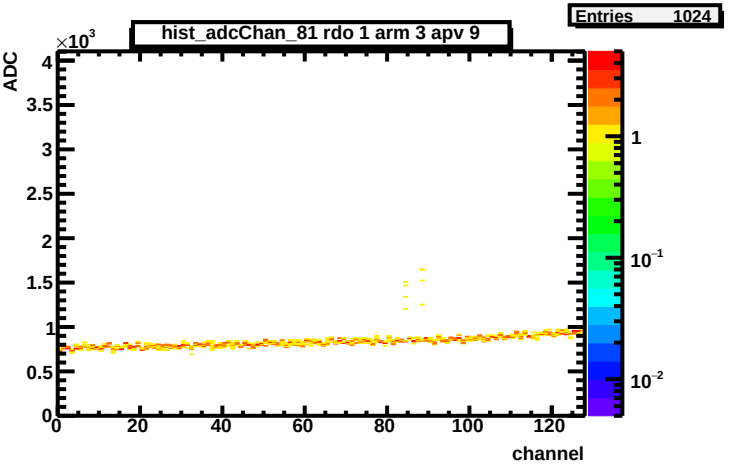

hist_adcChan_76 rdo 1 arm 3 apv 4
Entries 1024
ADC
×10^3
channel

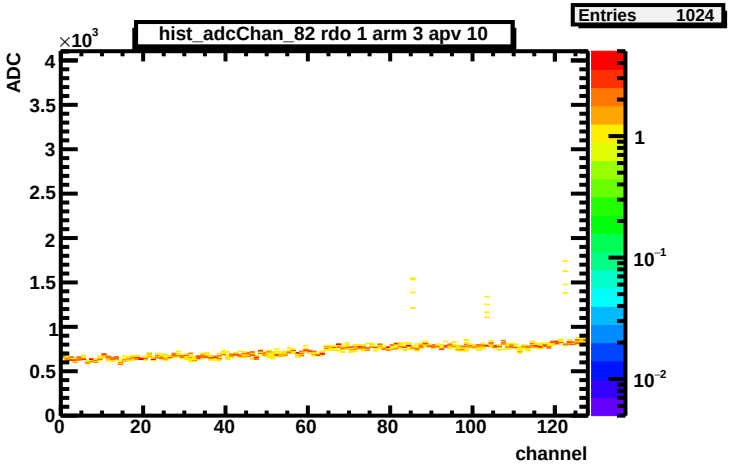

hist_adcChan_77 rdo 1 arm 3 apv 5
Entries 1024
ADC
×10^3
channel

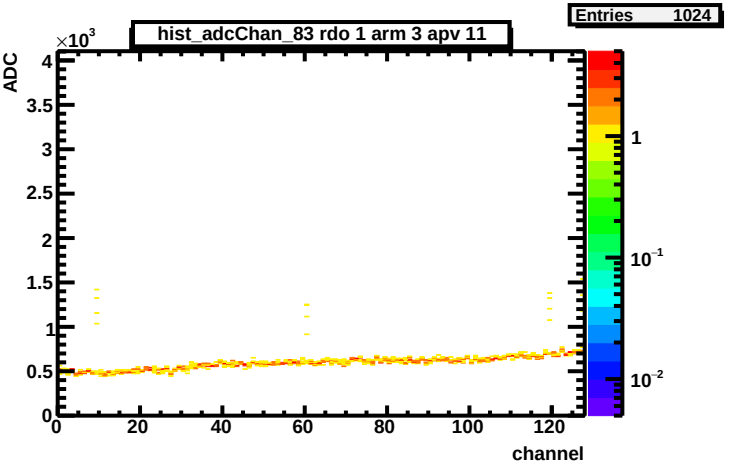

hist_adcChan_78 rdo 1 arm 3 apv 6
Entries 1024
ADC
channel

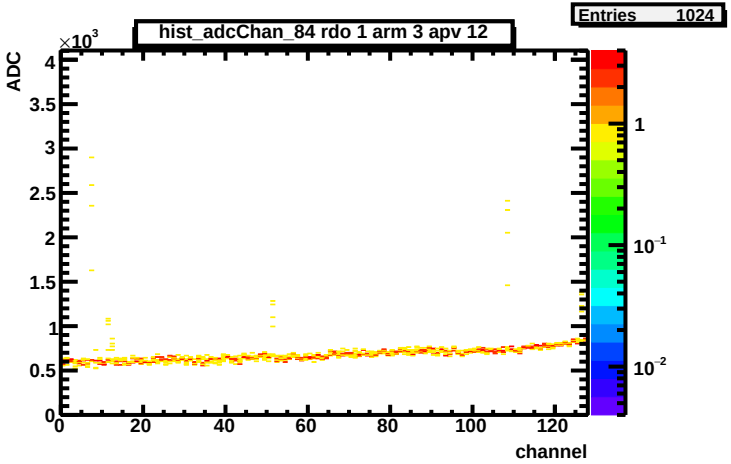

hist_adcChan_79 rdo 1 arm 3 apv 7
Entries 1024
ADC
channel

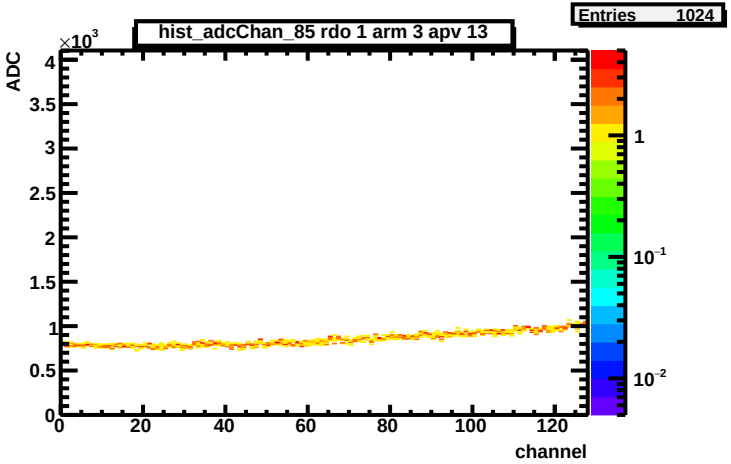

hist_adcChan_80 rdo 1 arm 3 apv 8
Entries 1024
ADC
channel

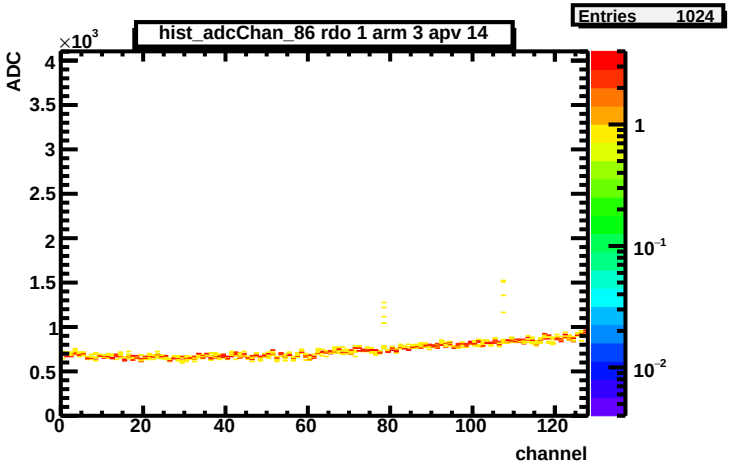

hist_adcChan_81 rdo 1 arm 3 apv 9
Entries 1024
ADC
channel

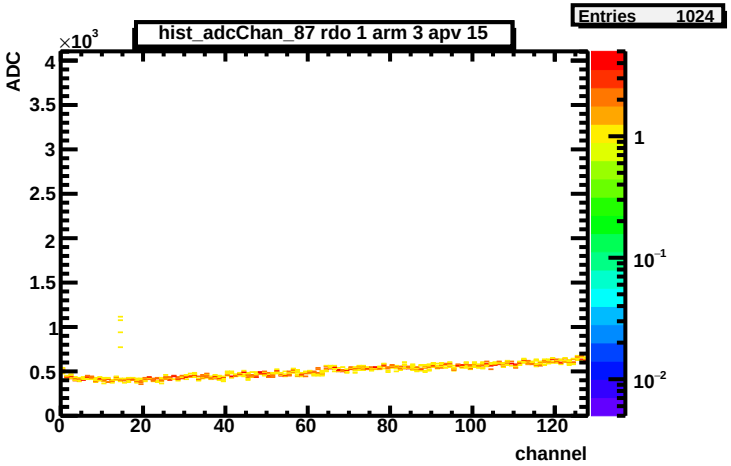

hist_adcChan_82 rdo 1 arm 3 apv 10
Entries 1024
ADC
channel

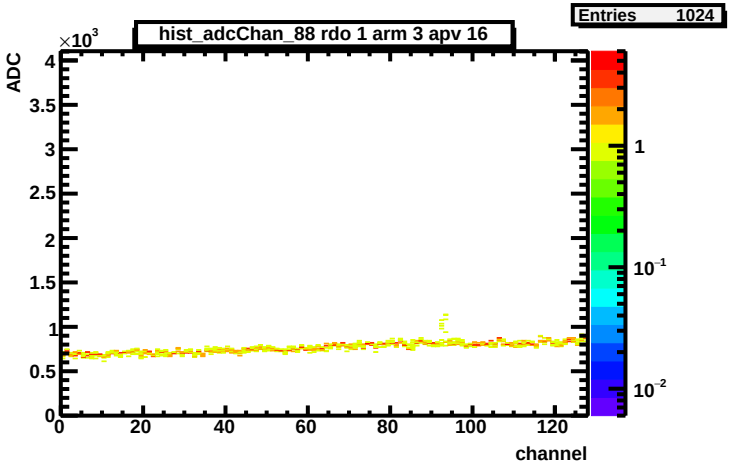

hist_adcChan_83 rdo 1 arm 3 apv 11
Entries 1024
ADC
channel

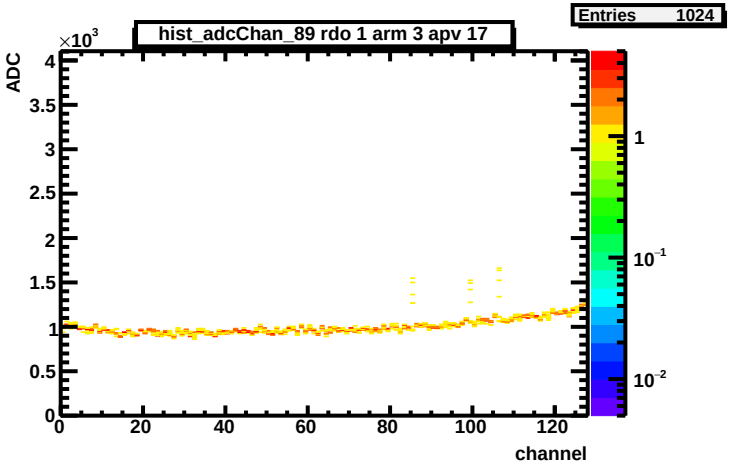

hist_adcChan_84 rdo 1 arm 3 apv 12
Entries 1024
ADC
×10^3
channel

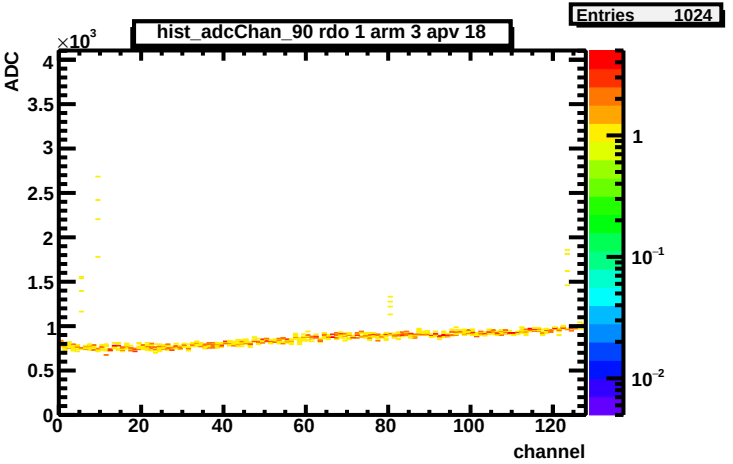

hist_adcChan_85 rdo 1 arm 3 apv 13
Entries 1024
ADC
×10^3
channel

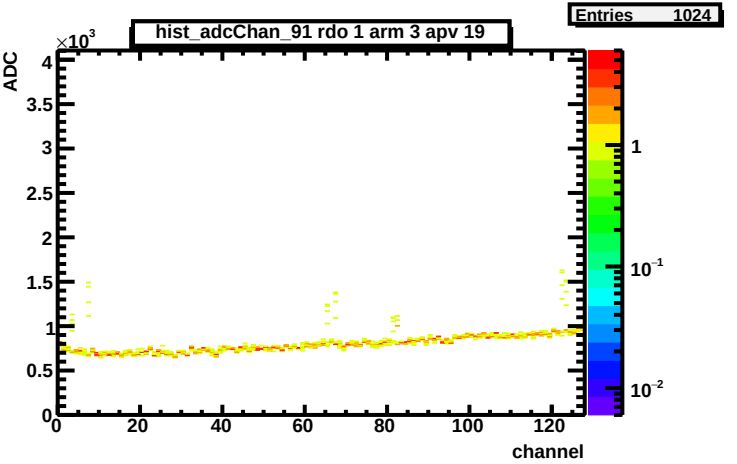

hist_adcChan_86 rdo 1 arm 3 apv 14
Entries 1024
ADC
×10^3
channel

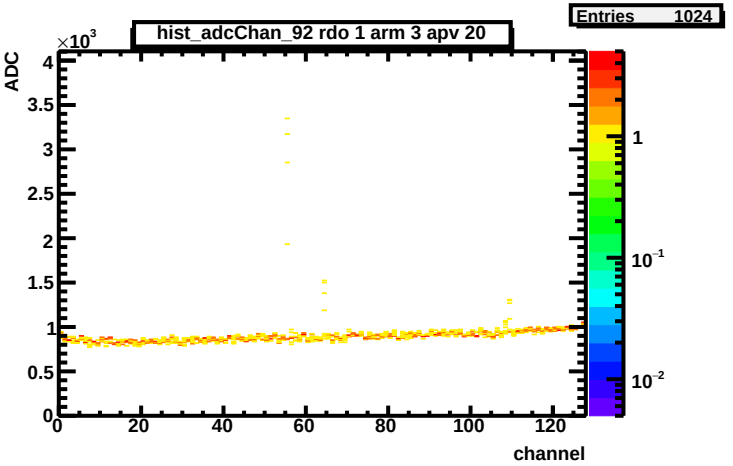

hist_adcChan_87 rdo 1 arm 3 apv 15
Entries 1024
ADC
×10^3
channel

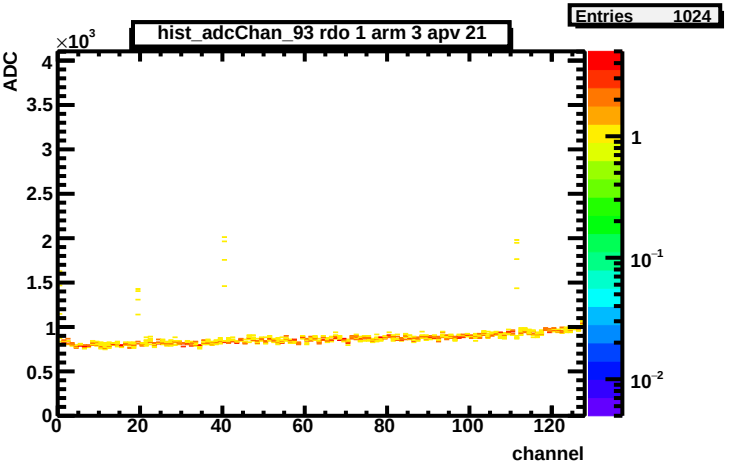

hist_adcChan_88 rdo 1 arm 3 apv 16
Entries 1024
ADC
×10^3
channel

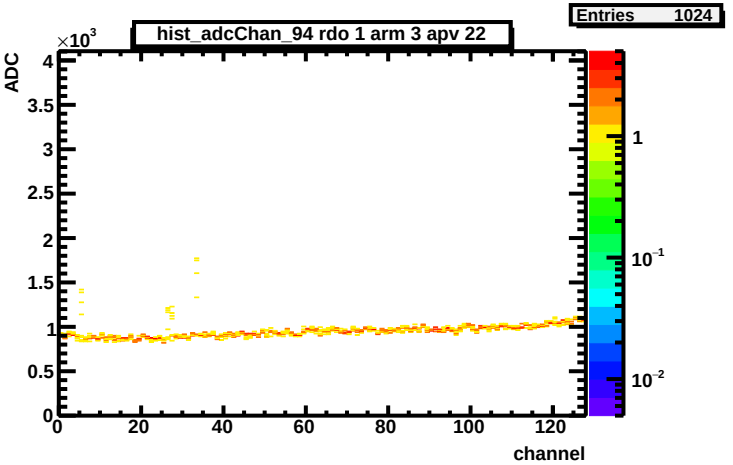

hist_adcChan_89 rdo 1 arm 3 apv 17
Entries 1024
ADC
×10^3
channel

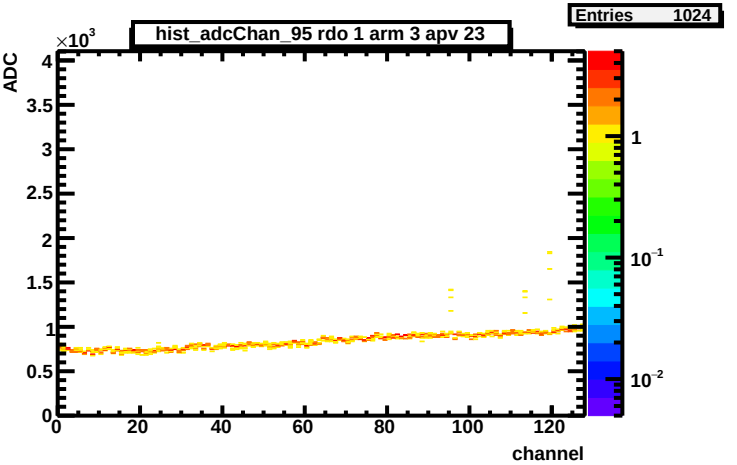

hist_adcChan_90 rdo 1 arm 3 apv 18
Entries 1024
ADC
×10^3
channel

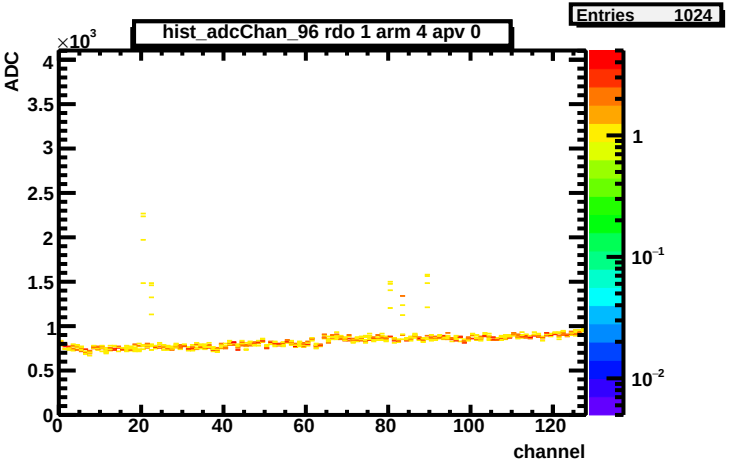

hist_adcChan_91 rdo 1 arm 3 apv 19
Entries 1024
ADC
×10^3
channel

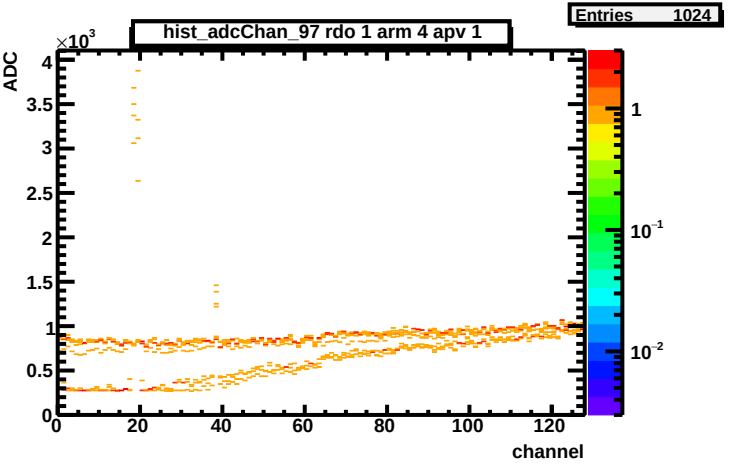

hist_adcChan_92 rdo 1 arm 3 apv 20
Entries 1024
ADC
×10^3
channel

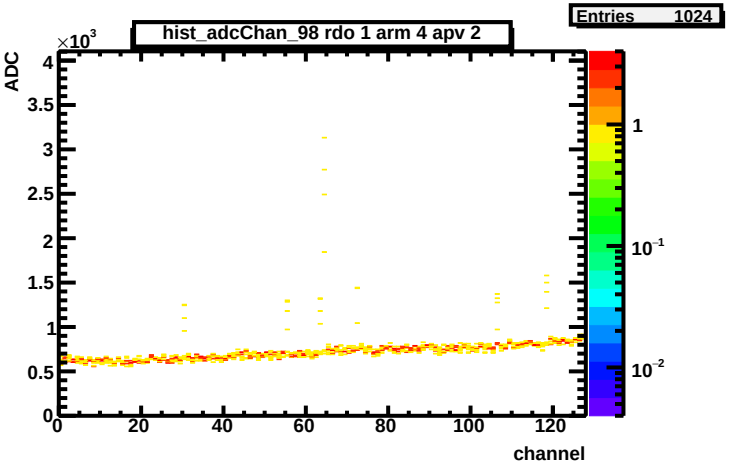

hist_adcChan_93 rdo 1 arm 3 apv 21
Entries 1024
ADC
×10^3
channel

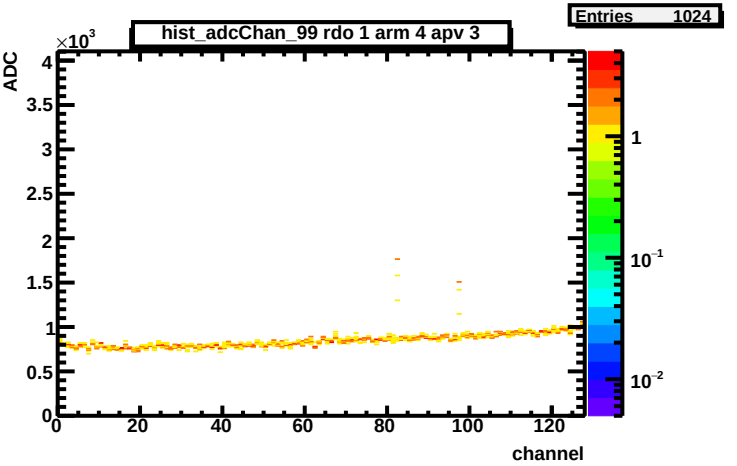

hist_adcChan_94 rdo 1 arm 3 apv 22
Entries 1024
ADC
×10^3
channel

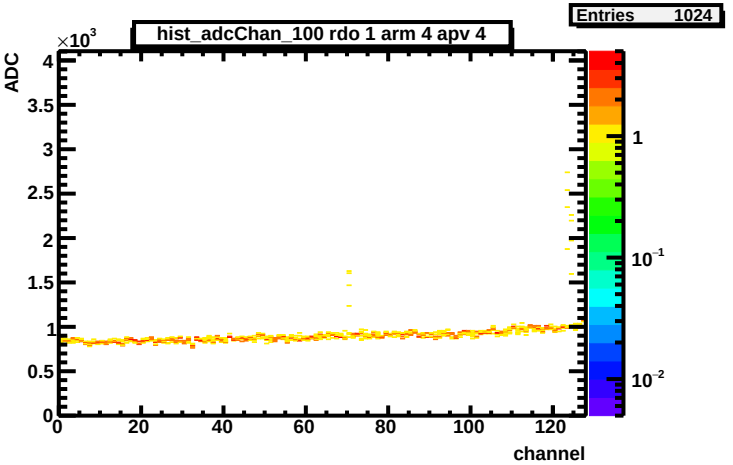

hist_adcChan_95 rdo 1 arm 3 apv 23
Entries 1024
ADC
×10^3
channel

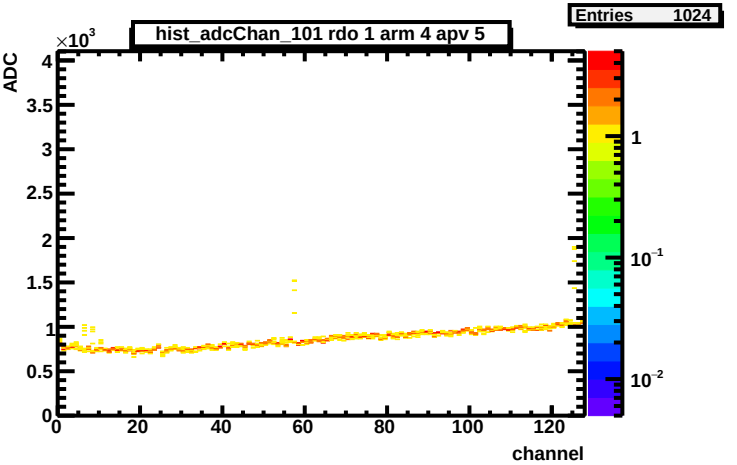

hist_adcChan_96 rdo 1 arm 4 apv 0
Entries 1024
ADC
×10^3
channel

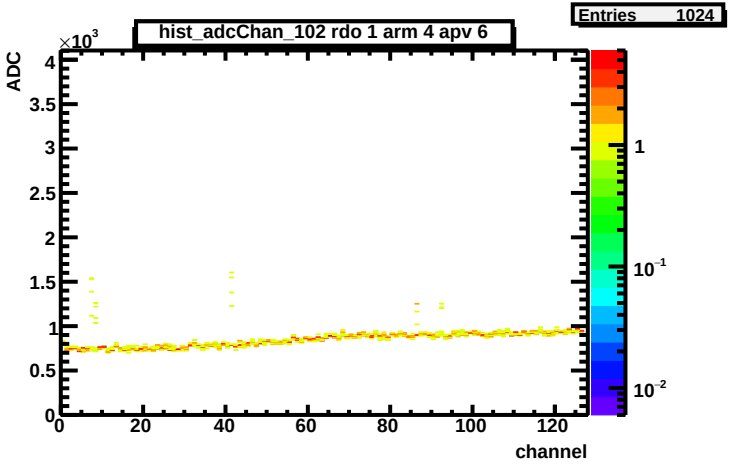

hist_adcChan_97 rdo 1 arm 4 apv 1
Entries 1024
ADC
×10^3
channel

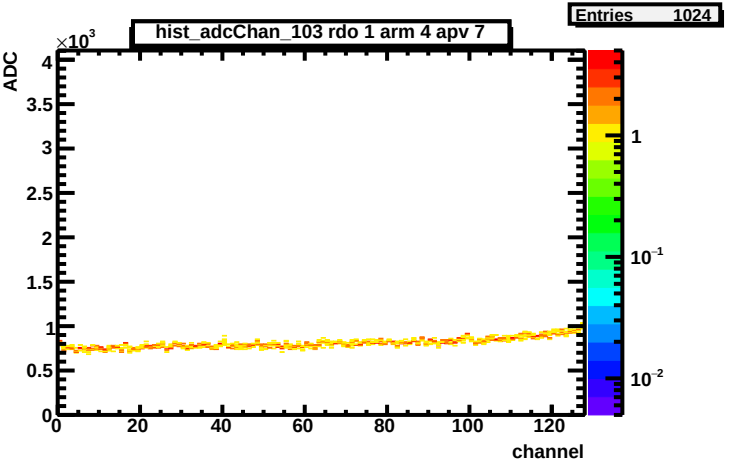

hist_adcChan_98 rdo 1 arm 4 apv 2
Entries 1024
ADC
×10^3
channel

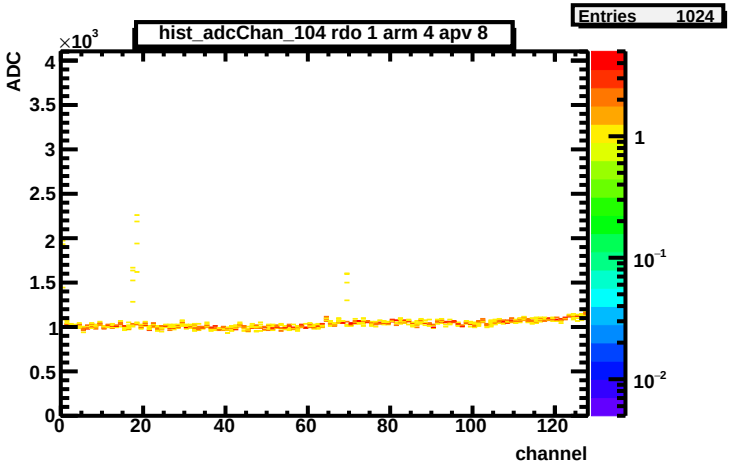

hist_adcChan_99 rdo 1 arm 4 apv 3
Entries 1024
ADC
×10^3
channel

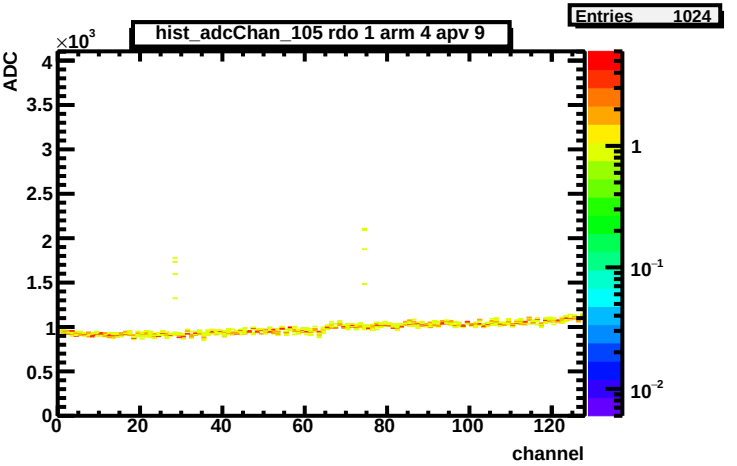

hist_adcChan_100 rdo 1 arm 4 apv 4
Entries 1024
ADC
×10^3
channel

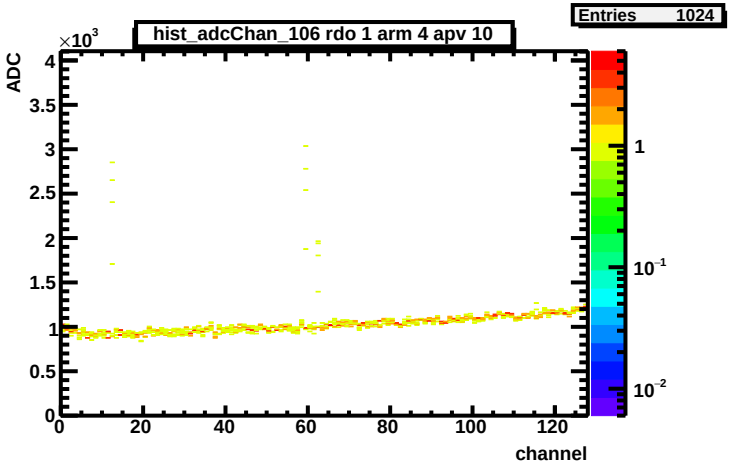

hist_adcChan_101 rdo 1 arm 4 apv 5
Entries 1024
ADC
×10^3
channel

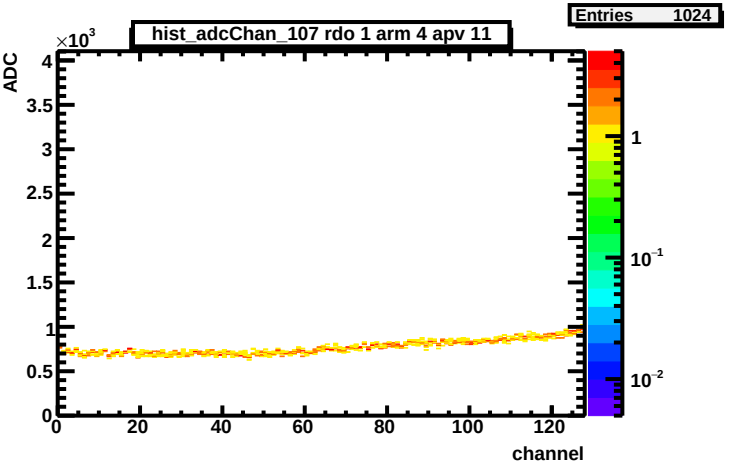

hist_adcChan_102 rdo 1 arm 4 apv 6
Entries 1024
ADC
channel

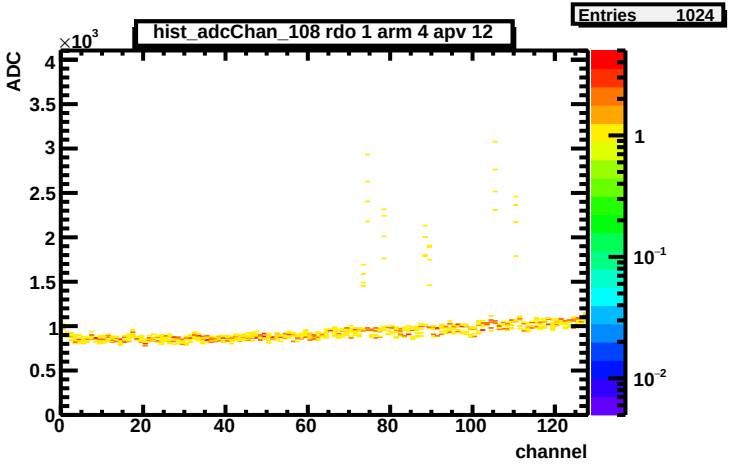

hist_adcChan_103 rdo 1 arm 4 apv 7
Entries 1024
ADC
channel

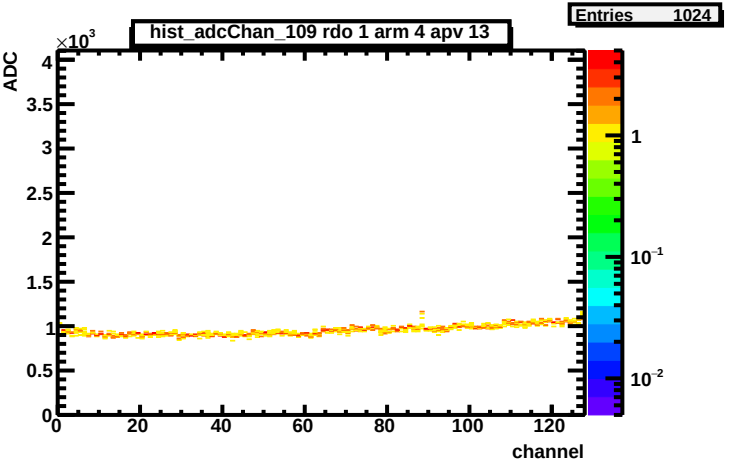

hist_adcChan_104 rdo 1 arm 4 apv 8
Entries 1024
ADC
channel

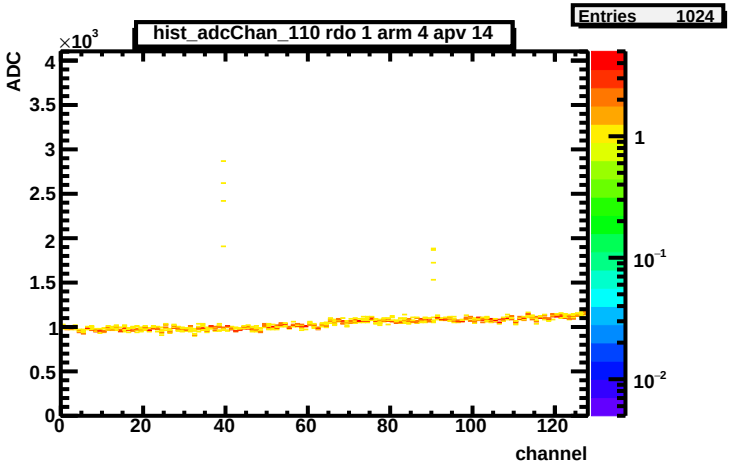

hist_adcChan_105 rdo 1 arm 4 apv 9
Entries 1024
ADC
channel

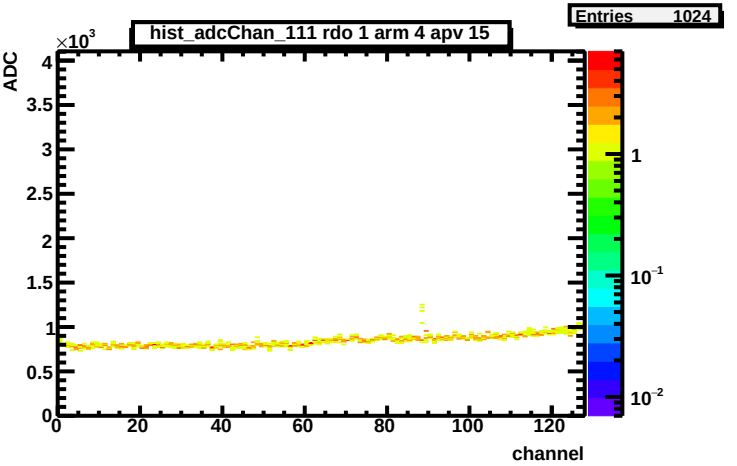

hist_adcChan_106 rdo 1 arm 4 apv 10
Entries 1024
ADC
channel

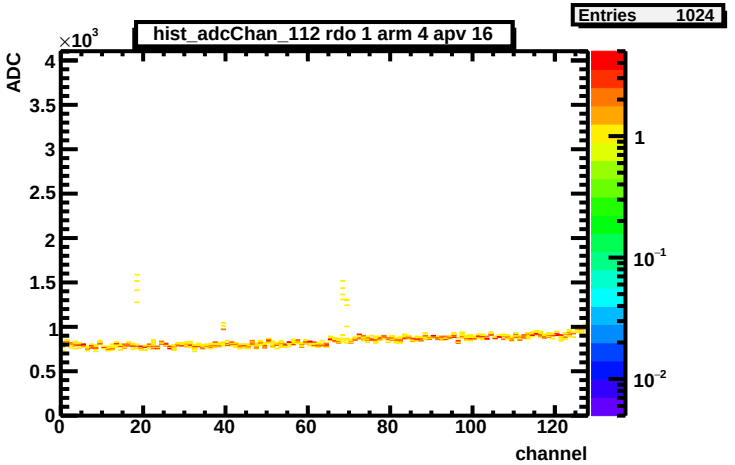

hist_adcChan_107 rdo 1 arm 4 apv 11
Entries 1024
ADC
channel

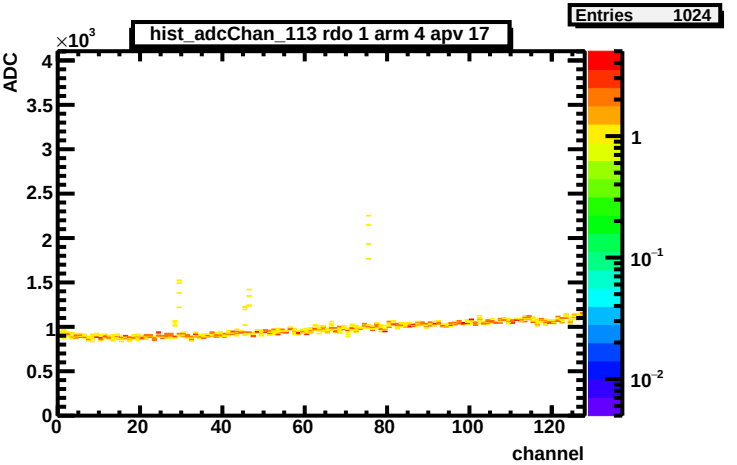

hist_adcChan_108 rdo 1 arm 4 apv 12
Entries 1024
ADC
×10^3
channel

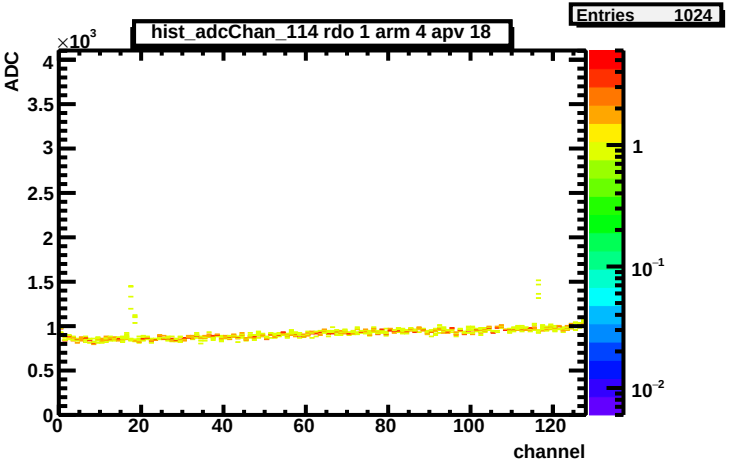

hist_adcChan_109 rdo 1 arm 4 apv 13
Entries 1024
ADC
×10^3
channel

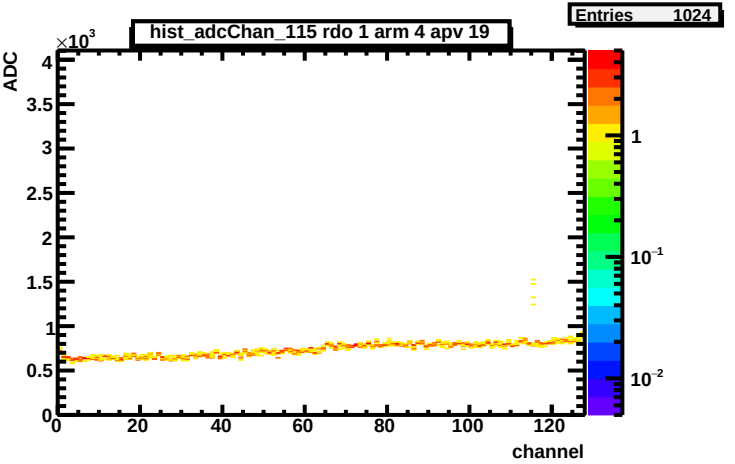

hist_adcChan_110 rdo 1 arm 4 apv 14
Entries 1024
ADC
×10^3
channel

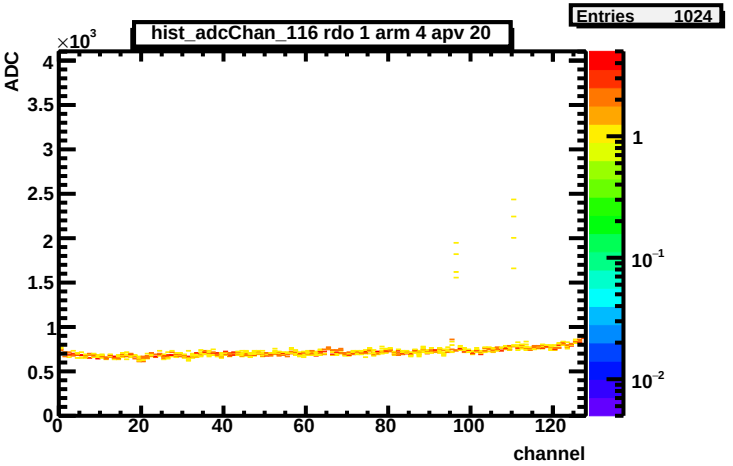

hist_adcChan_111 rdo 1 arm 4 apv 15
Entries 1024
ADC
×10^3
channel

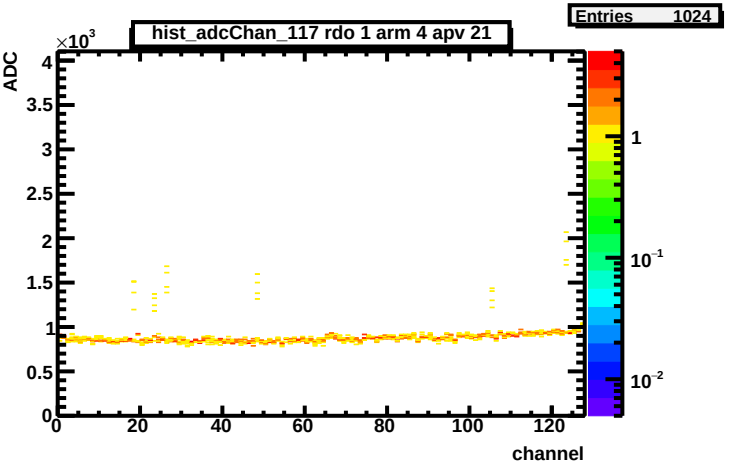

hist_adcChan_112 rdo 1 arm 4 apv 16
Entries 1024
ADC
×10^3
channel

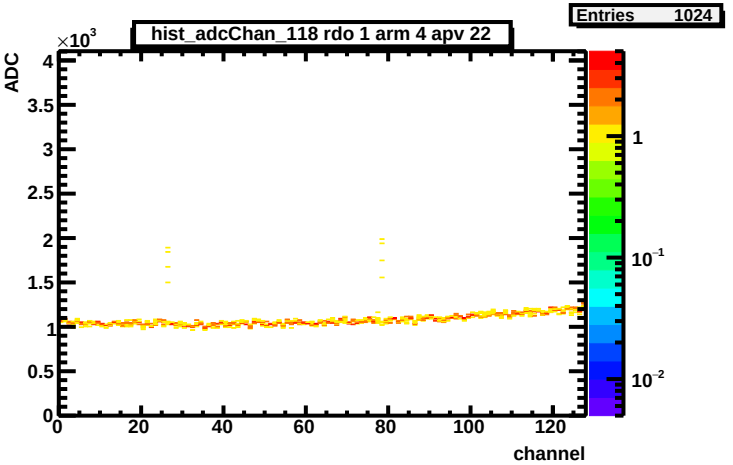

hist_adcChan_113 rdo 1 arm 4 apv 17
Entries 1024
ADC
×10^3
channel

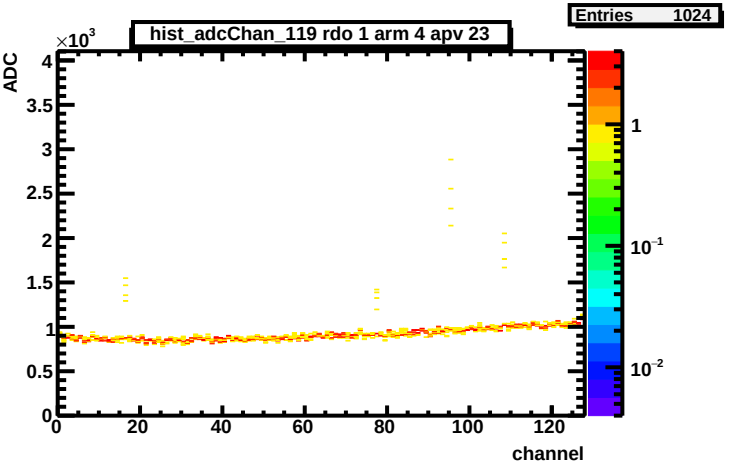

hist_adcChan_114 rdo 1 arm 4 apv 18
Entries 1024
ADC
channel

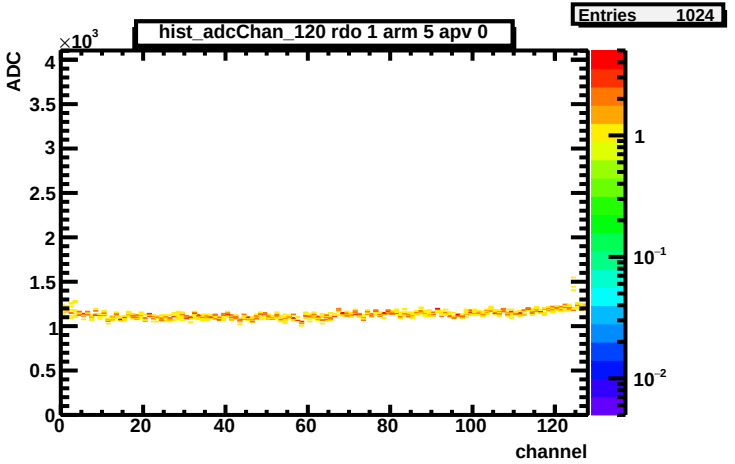

hist_adcChan_115 rdo 1 arm 4 apv 19
Entries 1024
ADC
channel

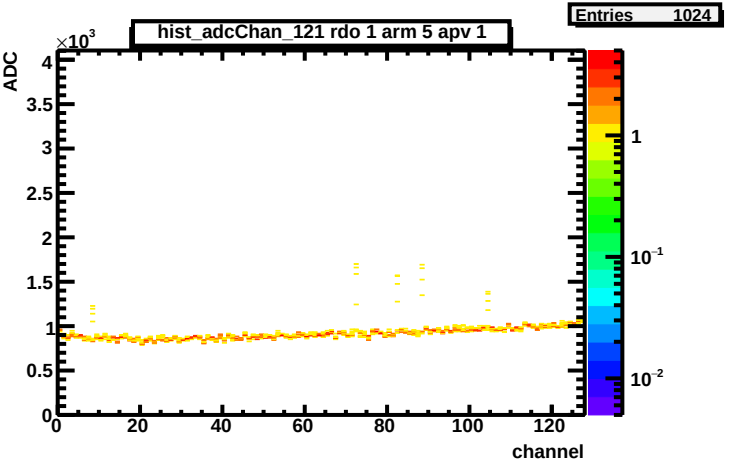

hist_adcChan_116 rdo 1 arm 4 apv 20
Entries 1024
ADC
channel

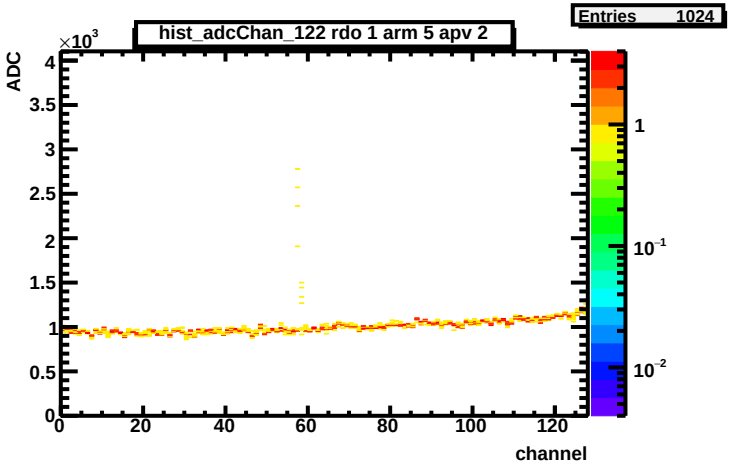

hist_adcChan_117 rdo 1 arm 4 apv 21
Entries 1024
ADC
channel

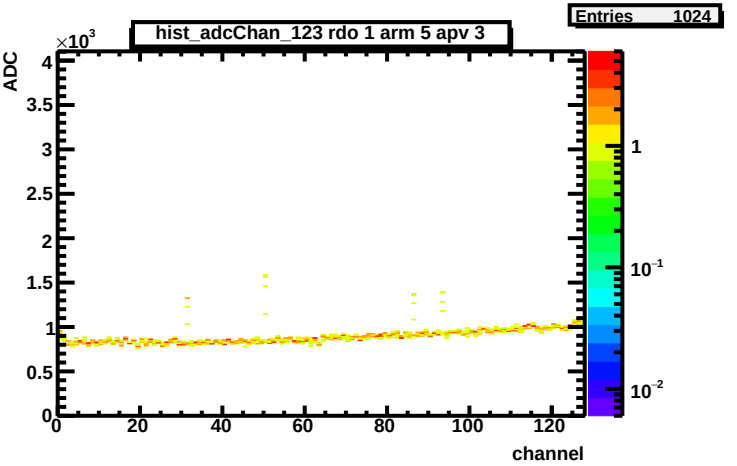

hist_adcChan_118 rdo 1 arm 4 apv 22
Entries 1024
ADC
channel

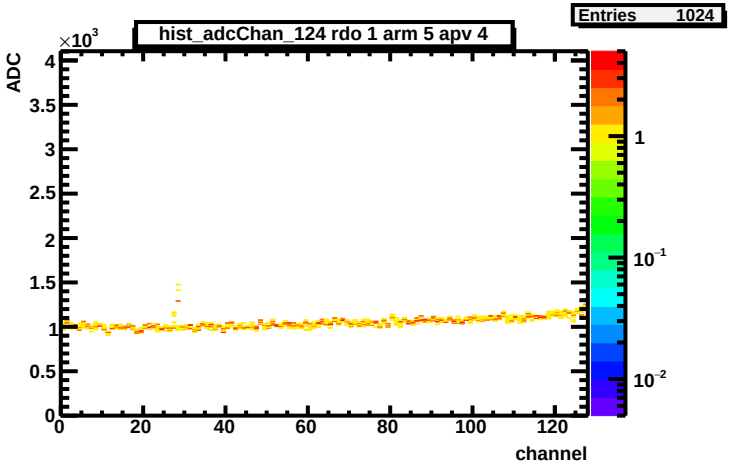

hist_adcChan_119 rdo 1 arm 4 apv 23
Entries 1024
ADC
channel

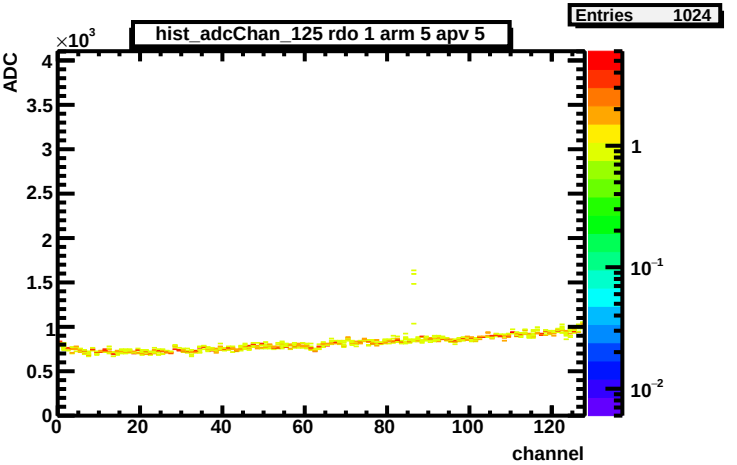

hist_adcChan_120 rdo 1 arm 5 apv 0
Entries 1024
ADC
×10^3
channel

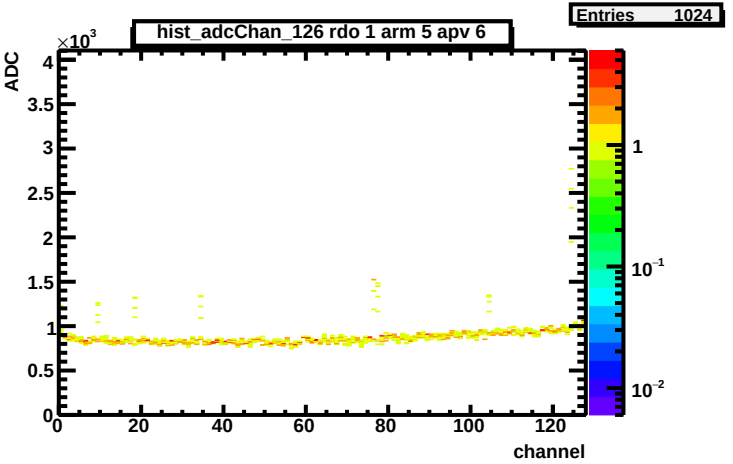

hist_adcChan_121 rdo 1 arm 5 apv 1
Entries 1024
ADC
×10^3
channel

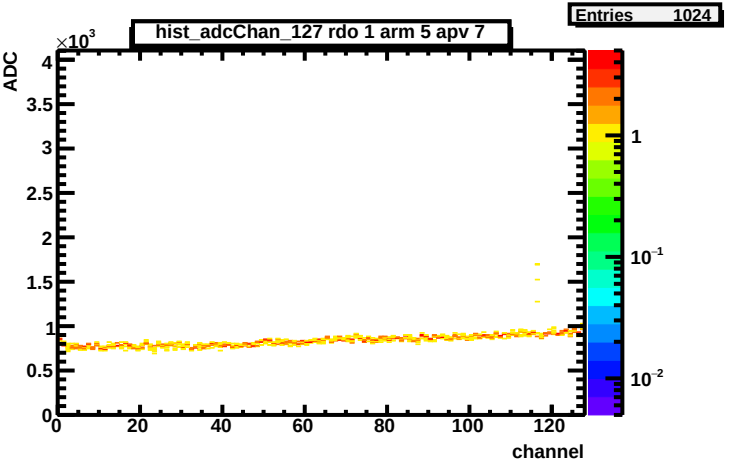

hist_adcChan_122 rdo 1 arm 5 apv 2
Entries 1024
ADC
×10^3
channel

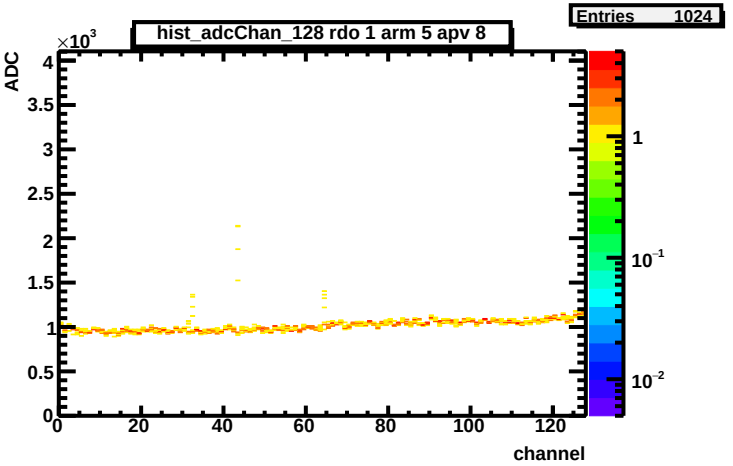

hist_adcChan_123 rdo 1 arm 5 apv 3
Entries 1024
ADC
×10^3
channel

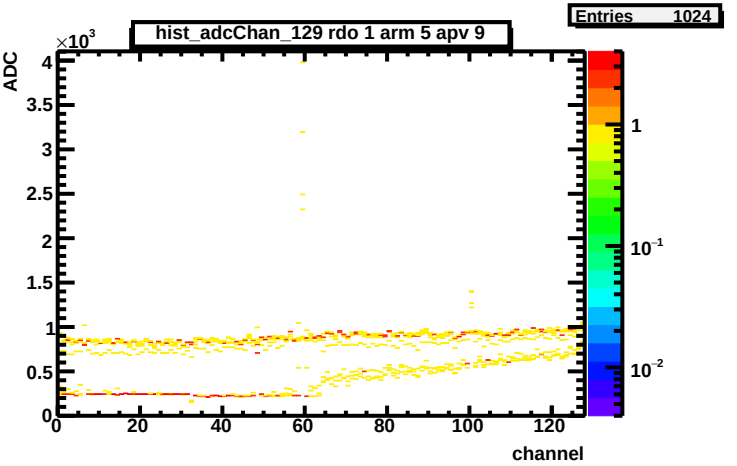

hist_adcChan_124 rdo 1 arm 5 apv 4
Entries 1024
ADC
×10^3
channel

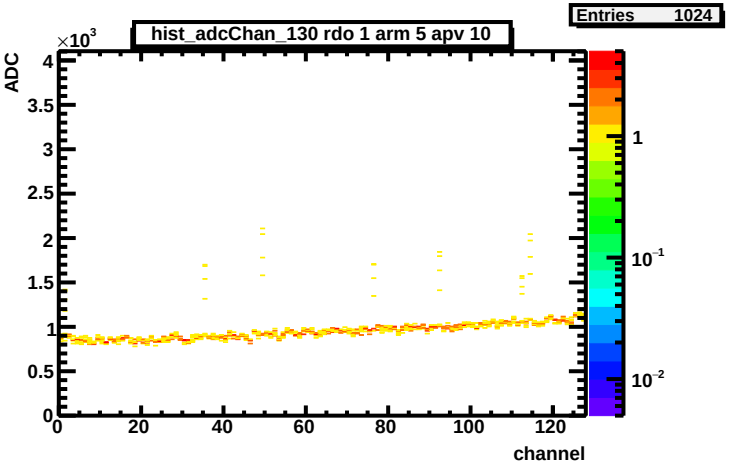

hist_adcChan_125 rdo 1 arm 5 apv 5
Entries 1024
ADC
×10^3
channel

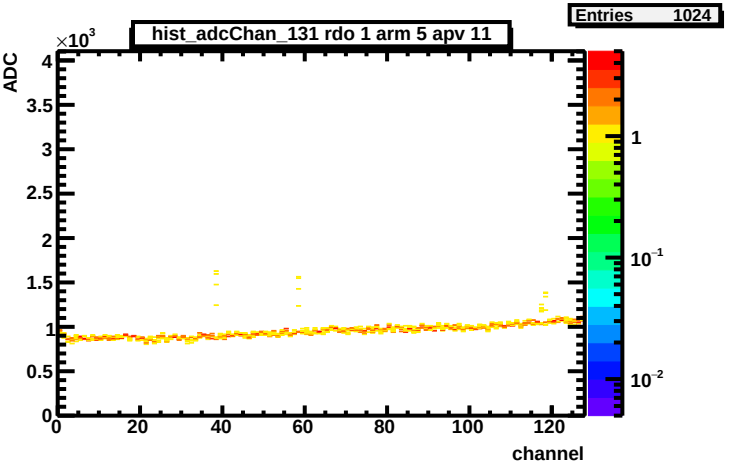

hist_adcChan_126 rdo 1 arm 5 apv 6
Entries 1024
ADC
×10^3
channel

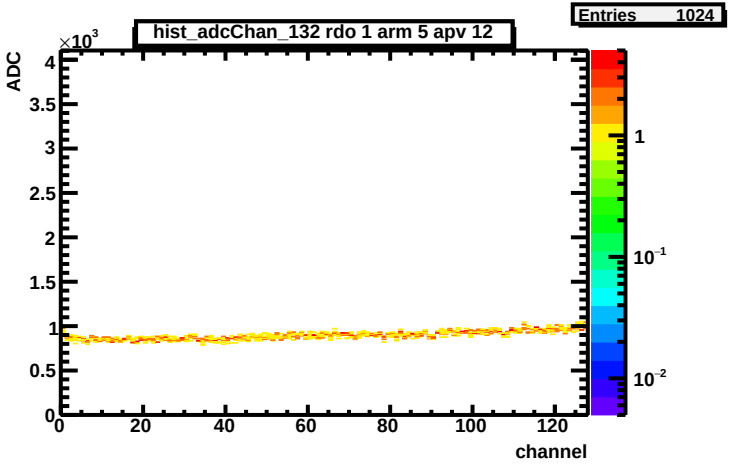

hist_adcChan_127 rdo 1 arm 5 apv 7
Entries 1024
ADC
×10^3
channel

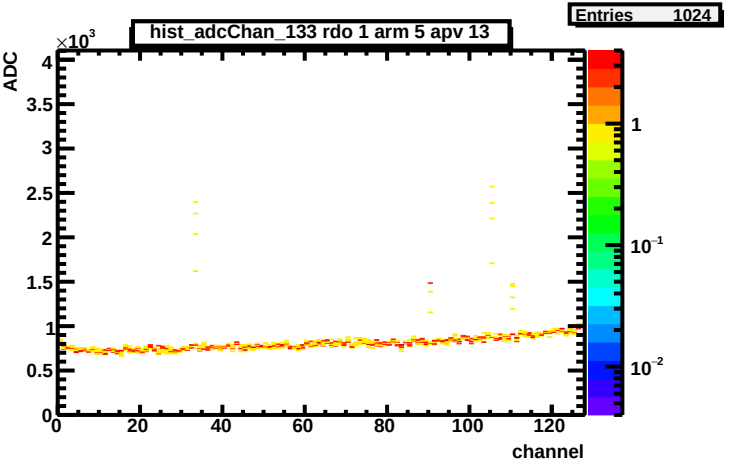

hist_adcChan_128 rdo 1 arm 5 apv 8
Entries 1024
ADC
×10^3
channel

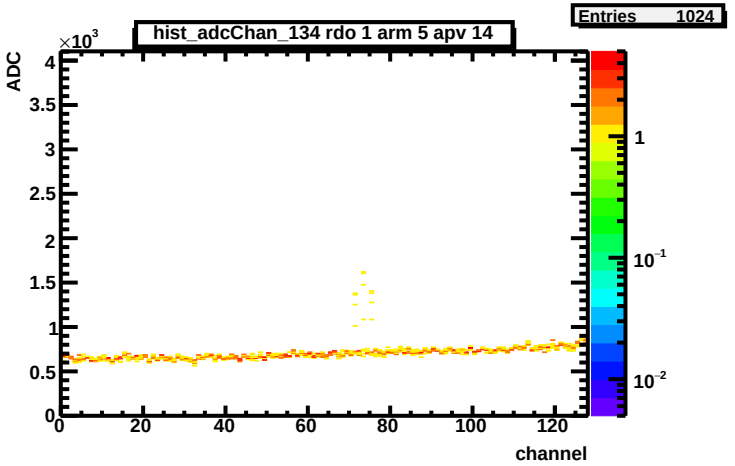

hist_adcChan_129 rdo 1 arm 5 apv 9
Entries 1024
ADC
×10^3
channel

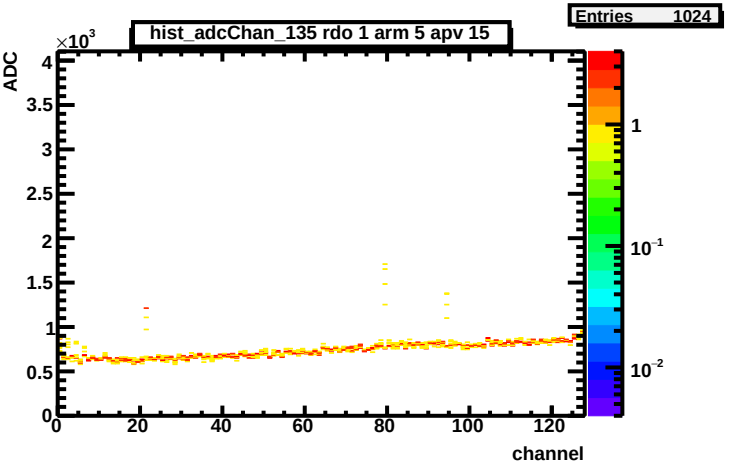

hist_adcChan_130 rdo 1 arm 5 apv 10
Entries 1024
ADC
×10^3
channel

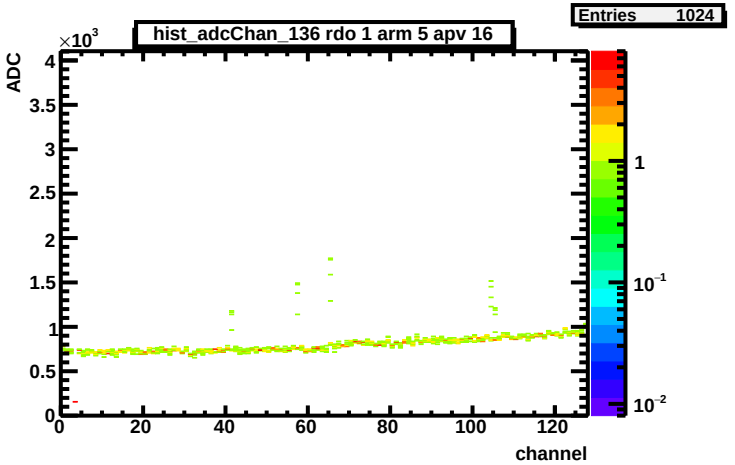

hist_adcChan_131 rdo 1 arm 5 apv 11
Entries 1024
ADC
×10^3
channel

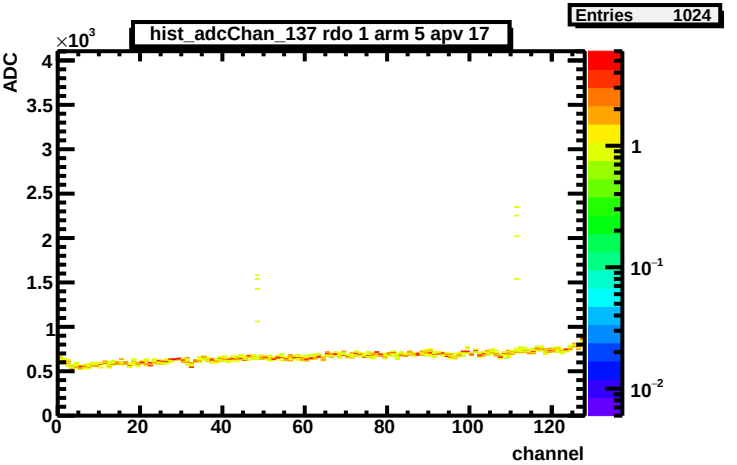

hist_adcChan_132 rdo 1 arm 5 apv 12
Entries 1024
ADC
channel

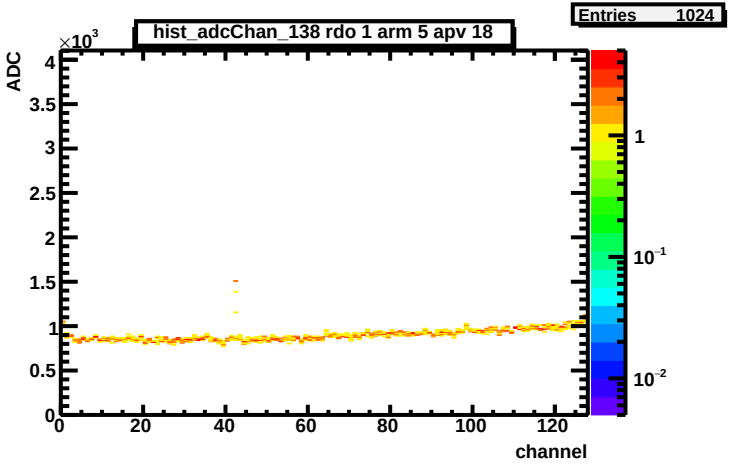

hist_adcChan_133 rdo 1 arm 5 apv 13
Entries 1024
ADC
channel

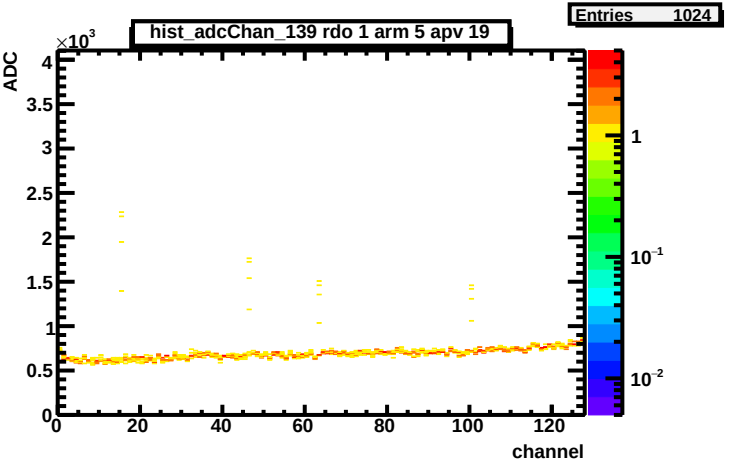

hist_adcChan_134 rdo 1 arm 5 apv 14
Entries 1024
ADC
channel

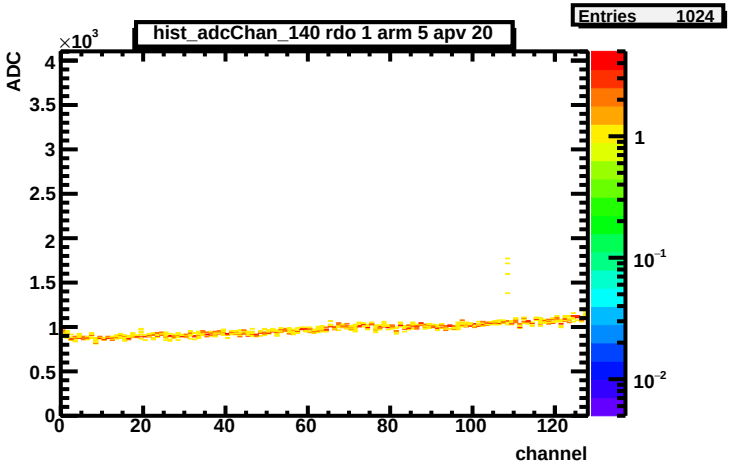

hist_adcChan_135 rdo 1 arm 5 apv 15
Entries 1024
ADC
channel

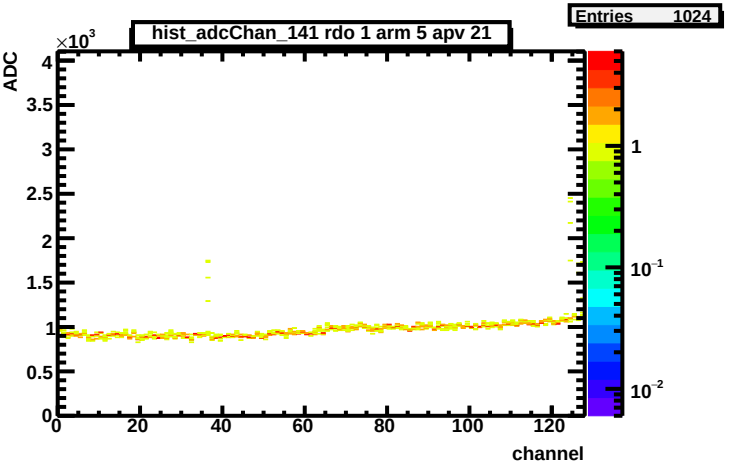

hist_adcChan_136 rdo 1 arm 5 apv 16
Entries 1024
ADC
channel

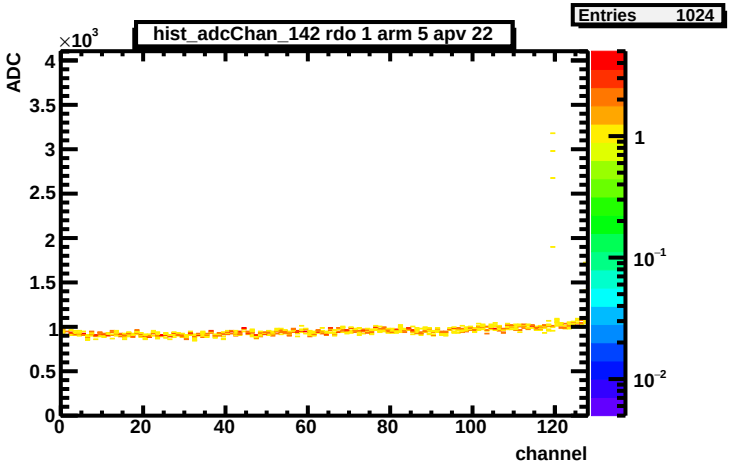

hist_adcChan_137 rdo 1 arm 5 apv 17
Entries 1024
ADC
channel

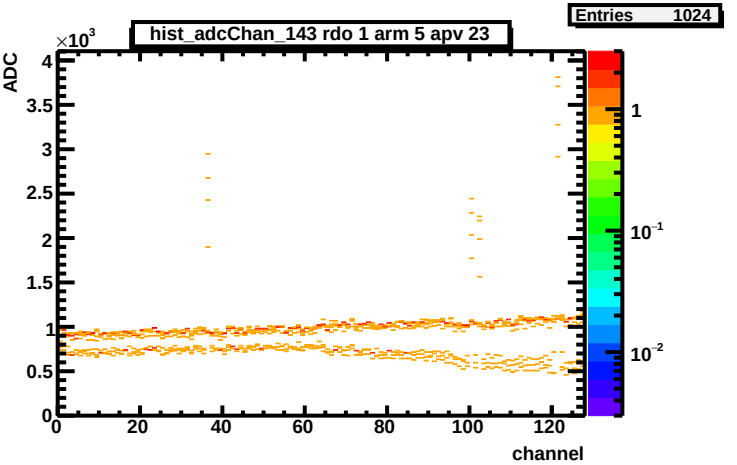

hist_adcChan_138 rdo 1 arm 5 apv 18
Entries 1024
ADC
×10^3
channel

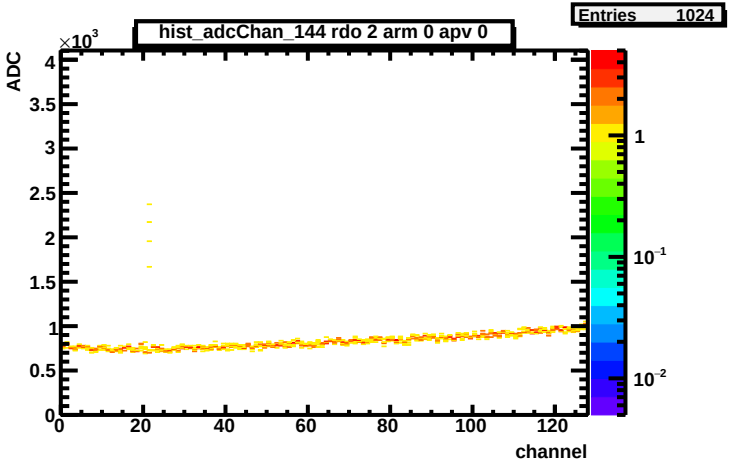

hist_adcChan_139 rdo 1 arm 5 apv 19
Entries 1024
ADC
×10^3
channel

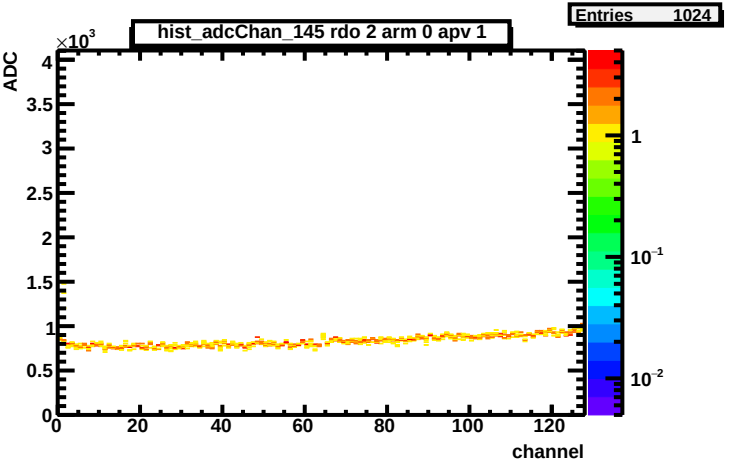

hist_adcChan_140 rdo 1 arm 5 apv 20
Entries 1024
ADC
×10^3
channel

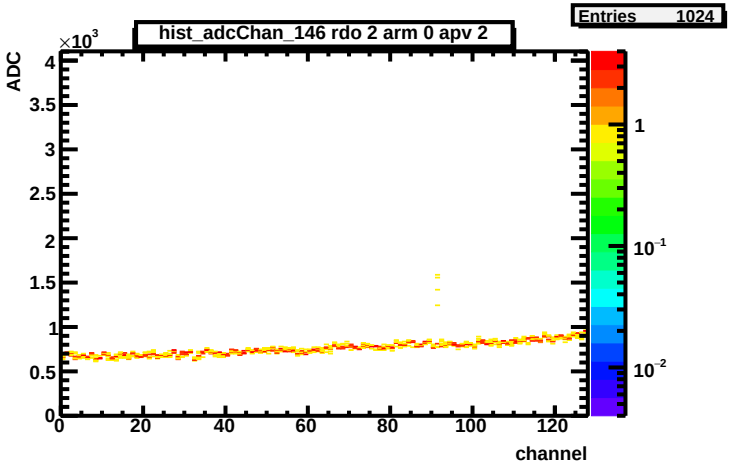

hist_adcChan_141 rdo 1 arm 5 apv 21
Entries 1024
ADC
×10^3
channel

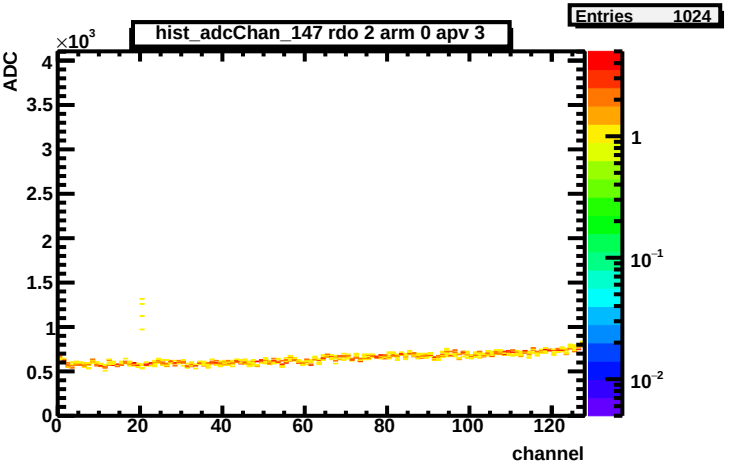

hist_adcChan_142 rdo 1 arm 5 apv 22
Entries 1024
ADC
×10^3
channel

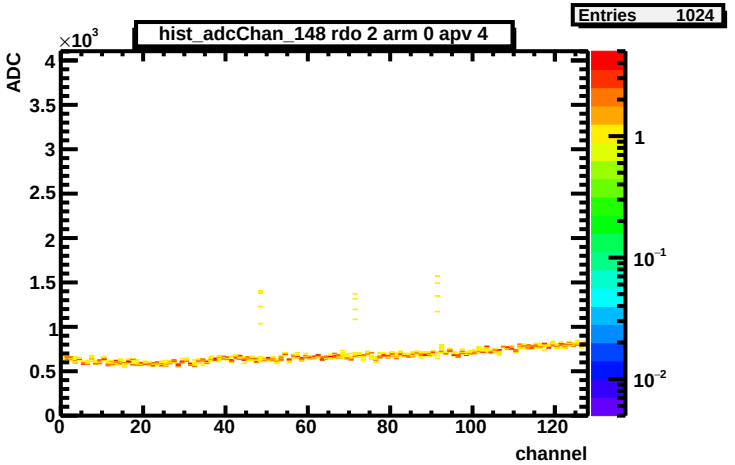

hist_adcChan_143 rdo 1 arm 5 apv 23
Entries 1024
ADC
×10^3
channel

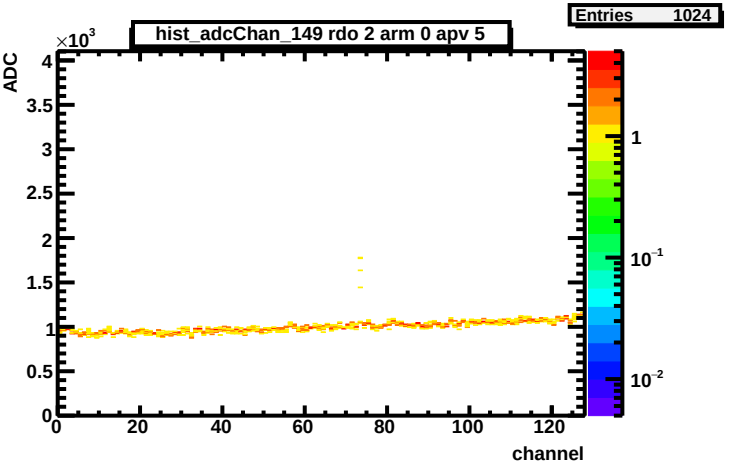

hist_adcChan_144 rdo 2 arm 0 apv 0
Entries 1024
ADC
channel

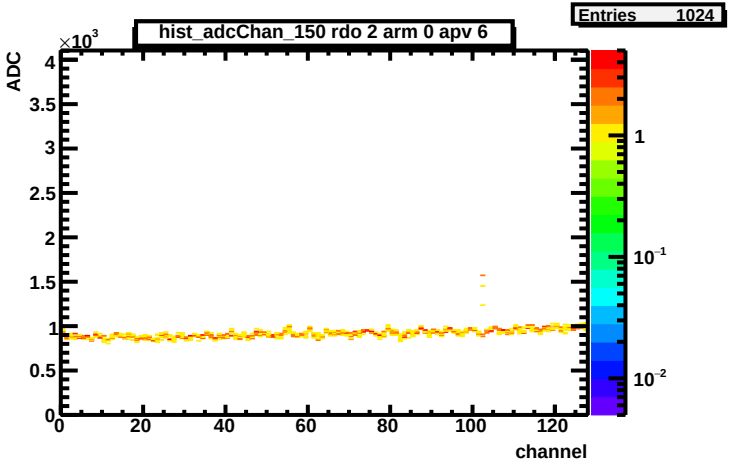

hist_adcChan_145 rdo 2 arm 0 apv 1
Entries 1024
ADC
channel

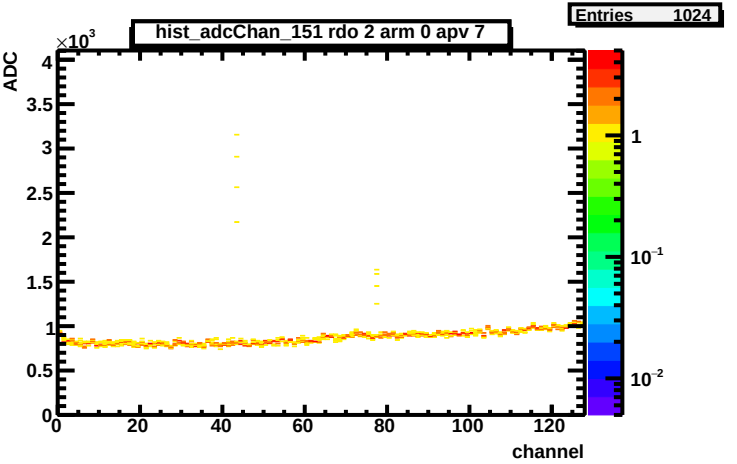

hist_adcChan_146 rdo 2 arm 0 apv 2
Entries 1024
ADC
channel

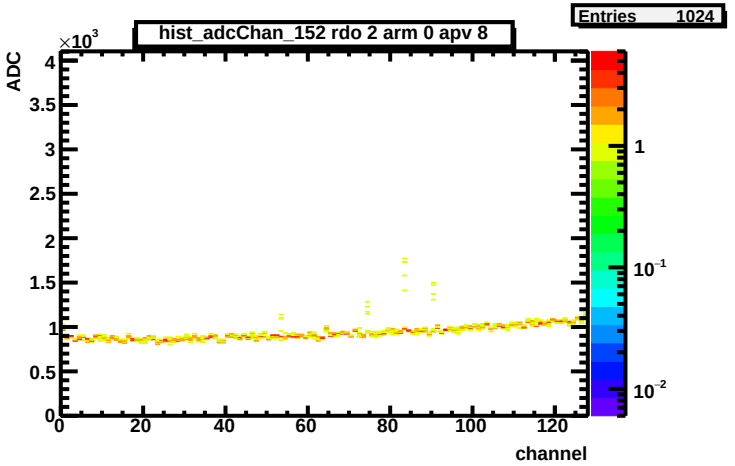

hist_adcChan_147 rdo 2 arm 0 apv 3
Entries 1024
ADC
channel

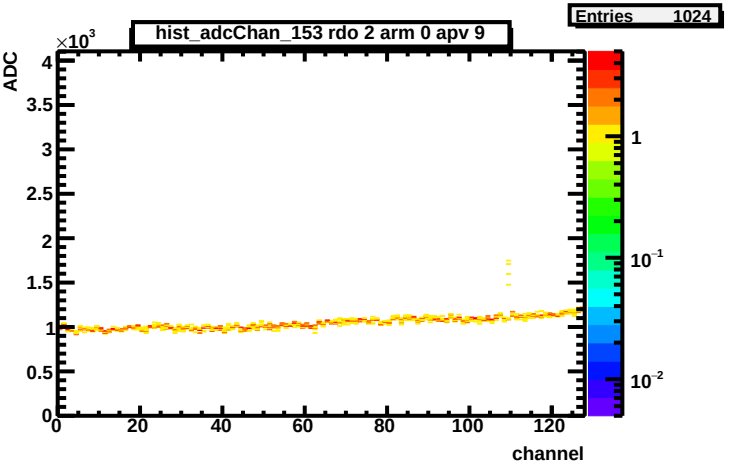

hist_adcChan_148 rdo 2 arm 0 apv 4
Entries 1024
ADC
channel

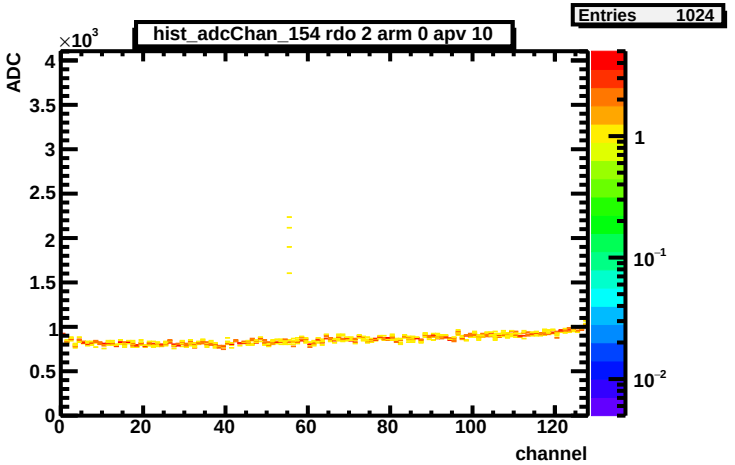

hist_adcChan_149 rdo 2 arm 0 apv 5
Entries 1024
ADC
channel

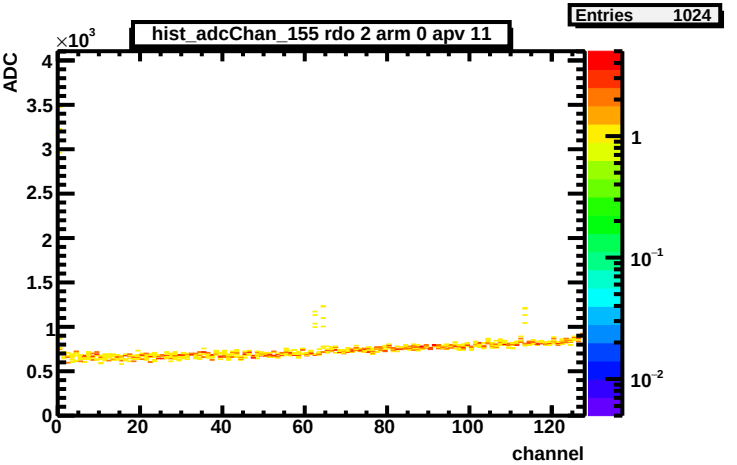

hist_adcChan_150 rdo 2 arm 0 apv 6
Entries 1024
ADC
channel

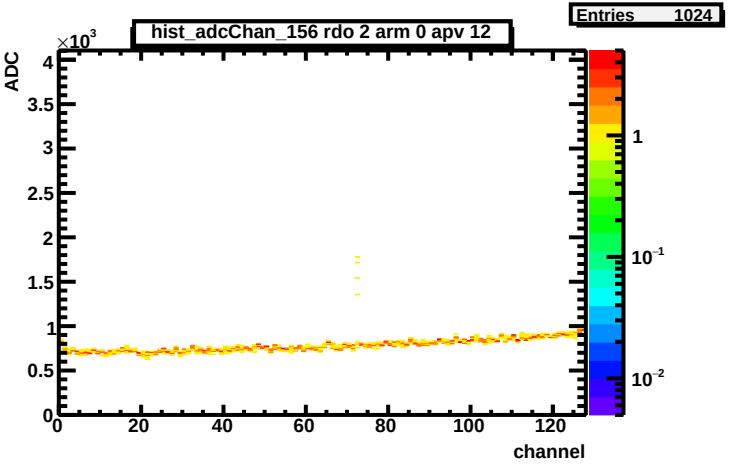

hist_adcChan_151 rdo 2 arm 0 apv 7
Entries 1024
ADC
channel

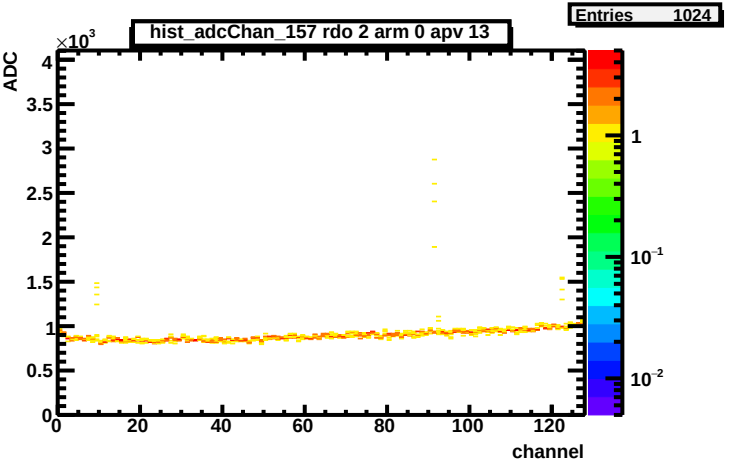

hist_adcChan_152 rdo 2 arm 0 apv 8
Entries 1024
ADC
channel

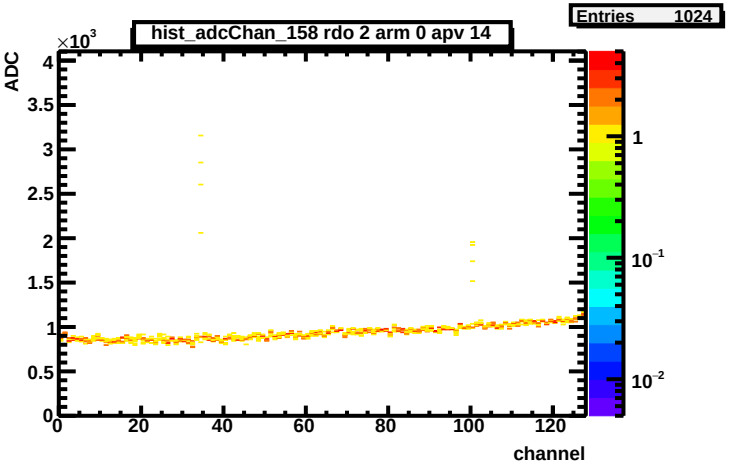

hist_adcChan_153 rdo 2 arm 0 apv 9
Entries 1024
ADC
channel

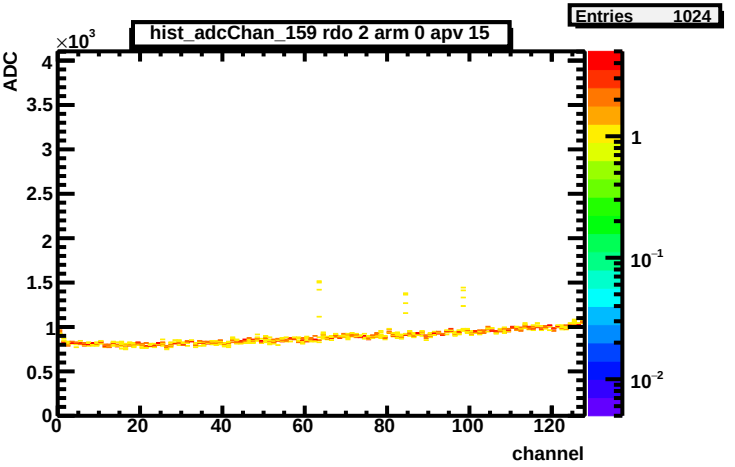

hist_adcChan_154 rdo 2 arm 0 apv 10
Entries 1024
ADC
channel

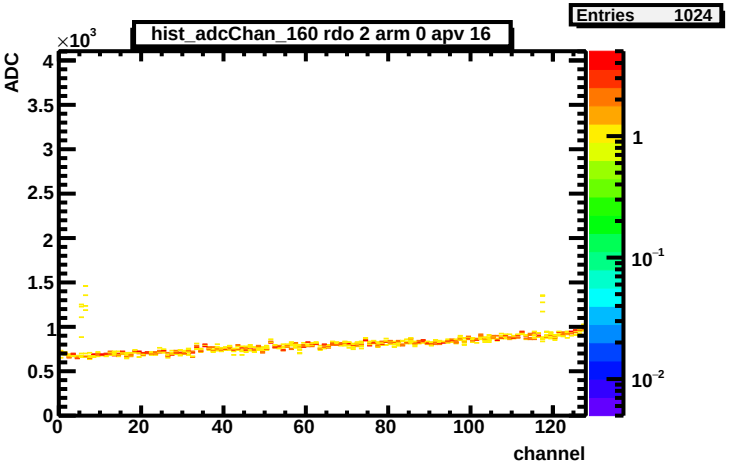

hist_adcChan_155 rdo 2 arm 0 apv 11
Entries 1024
ADC
channel

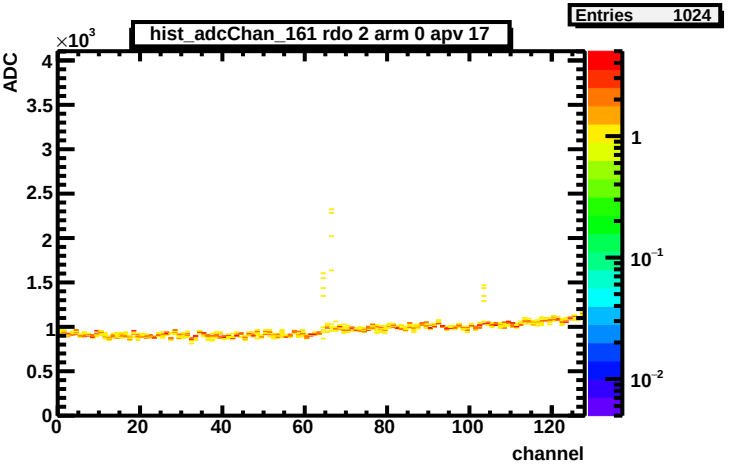

hist_adcChan_156 rdo 2 arm 0 apv 12
Entries 1024
ADC
channel

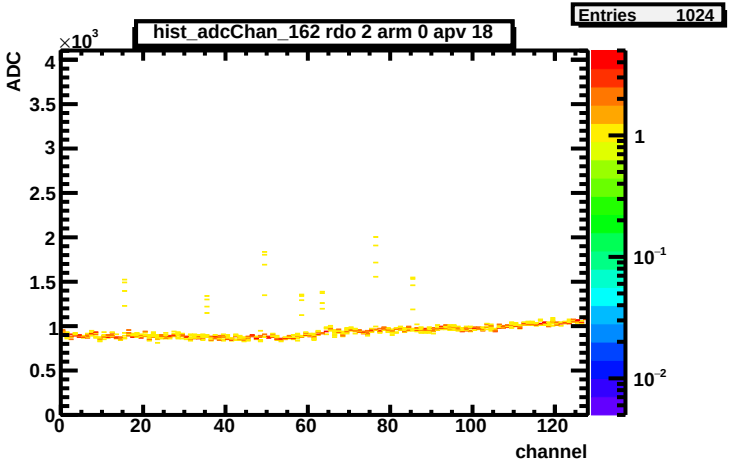

hist_adcChan_157 rdo 2 arm 0 apv 13
Entries 1024
ADC
channel

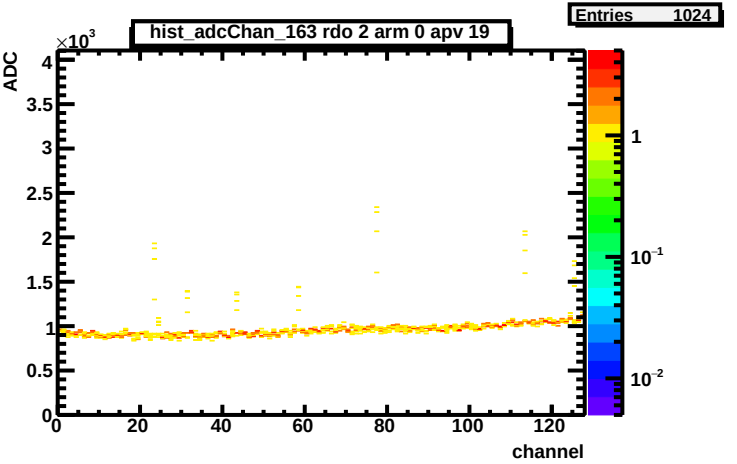

hist_adcChan_158 rdo 2 arm 0 apv 14
Entries 1024
ADC
channel

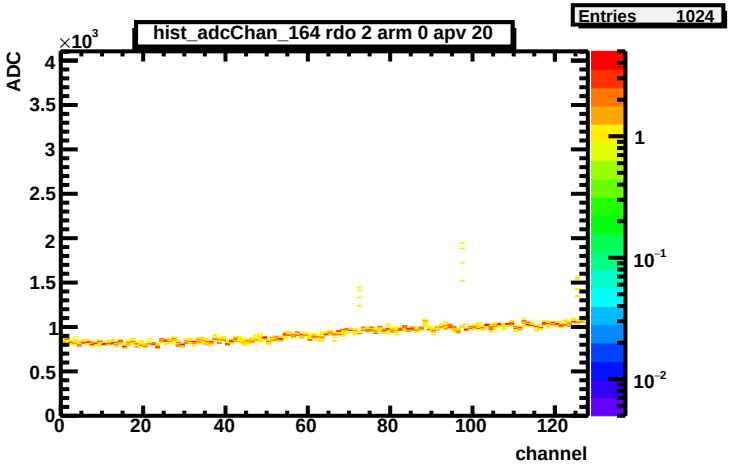

hist_adcChan_159 rdo 2 arm 0 apv 15
Entries 1024
ADC
channel

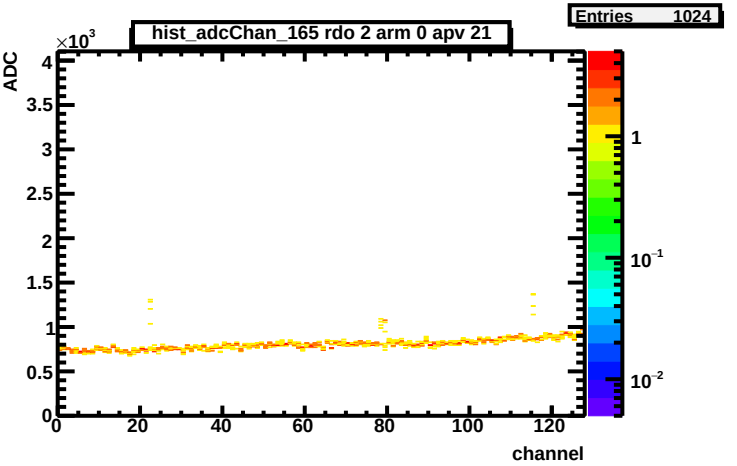

hist_adcChan_160 rdo 2 arm 0 apv 16
Entries 1024
ADC
channel

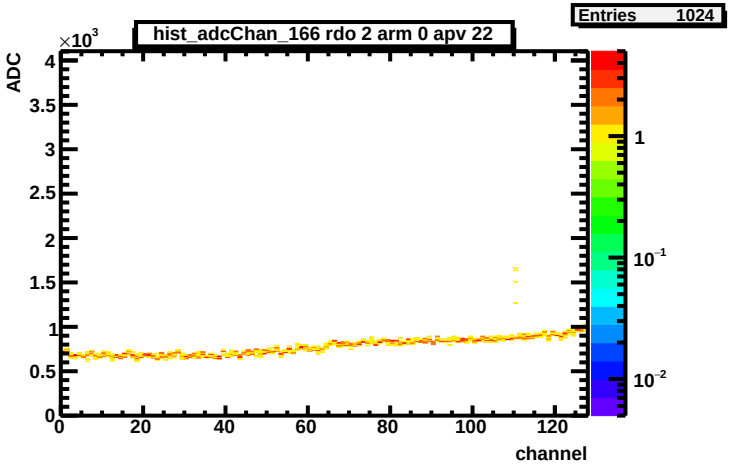

hist_adcChan_161 rdo 2 arm 0 apv 17
Entries 1024
ADC
channel

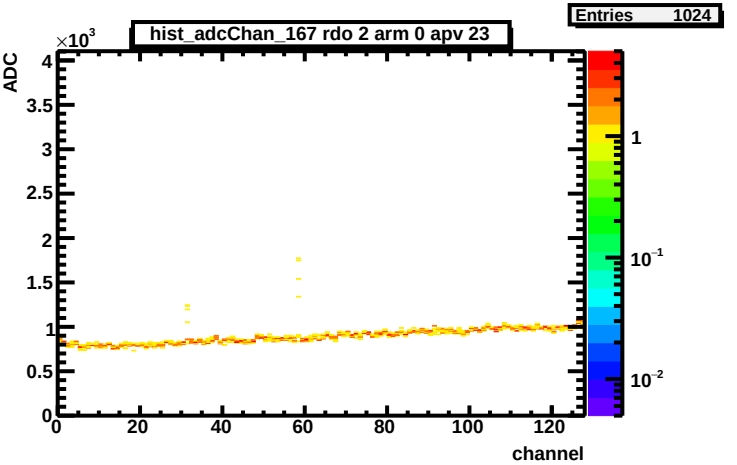

hist_adcChan_162 rdo 2 arm 0 apv 18
Entries 1024
ADC
channel

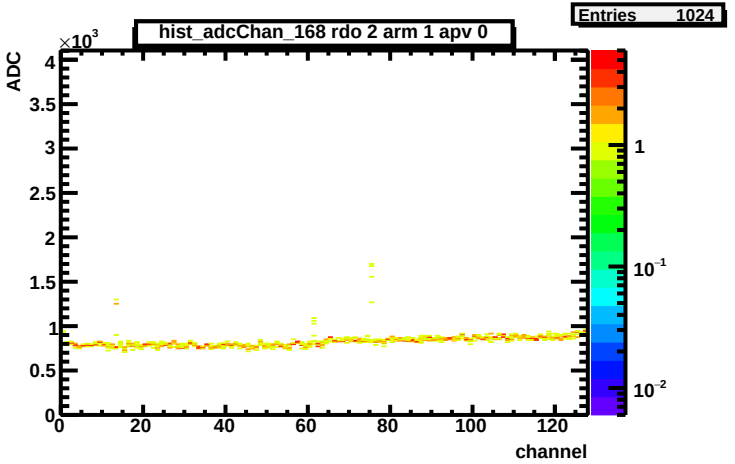

hist_adcChan_163 rdo 2 arm 0 apv 19
Entries 1024
ADC
channel

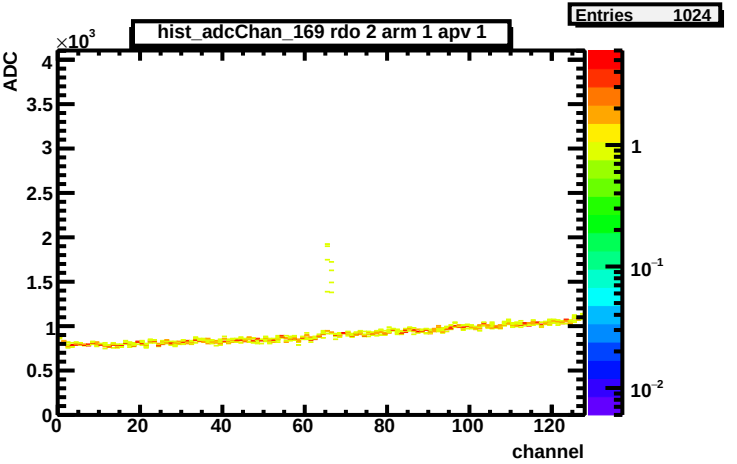

hist_adcChan_164 rdo 2 arm 0 apv 20
Entries 1024
ADC
channel

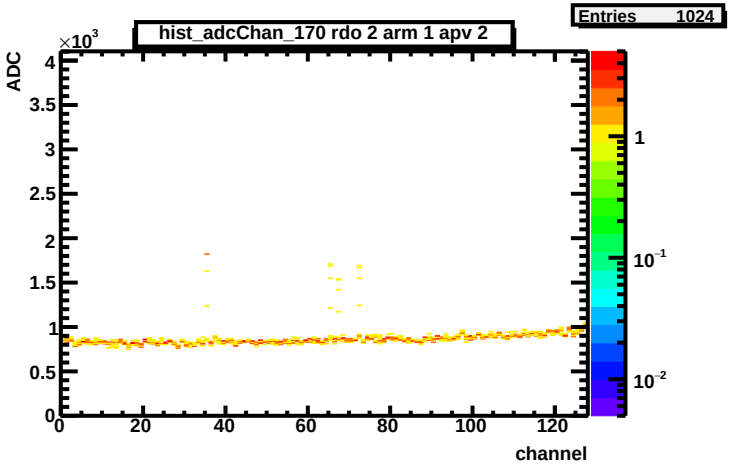

hist_adcChan_165 rdo 2 arm 0 apv 21
Entries 1024
ADC
channel

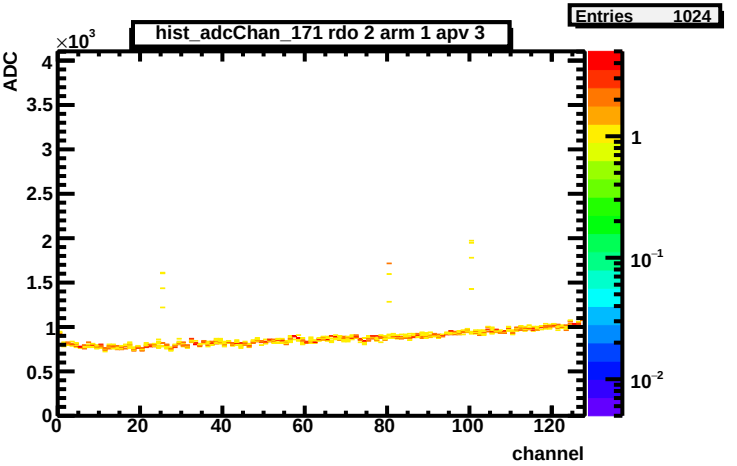

hist_adcChan_166 rdo 2 arm 0 apv 22
Entries 1024
ADC
channel

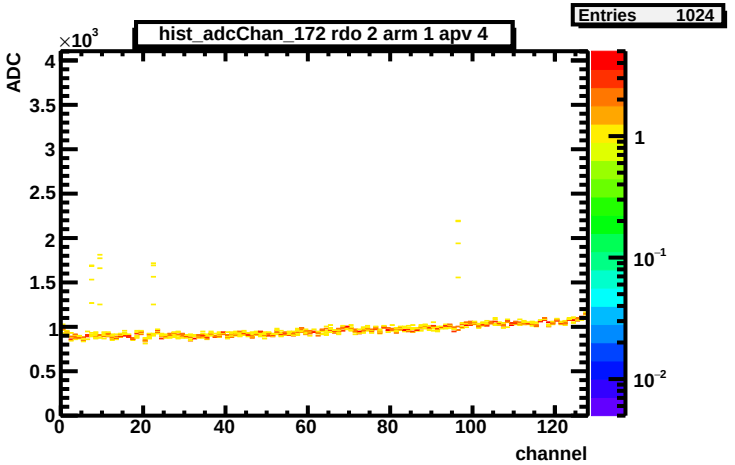

hist_adcChan_167 rdo 2 arm 0 apv 23
Entries 1024
ADC
channel

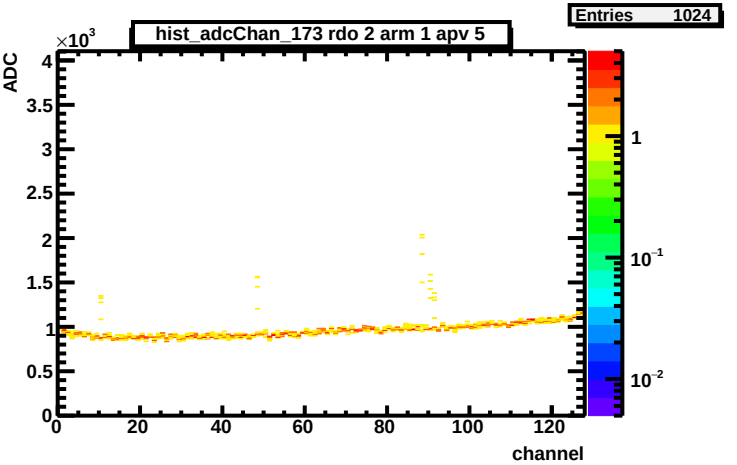

hist_adcChan_168 rdo 2 arm 1 apv 0
Entries 1024
ADC
×10^3
channel

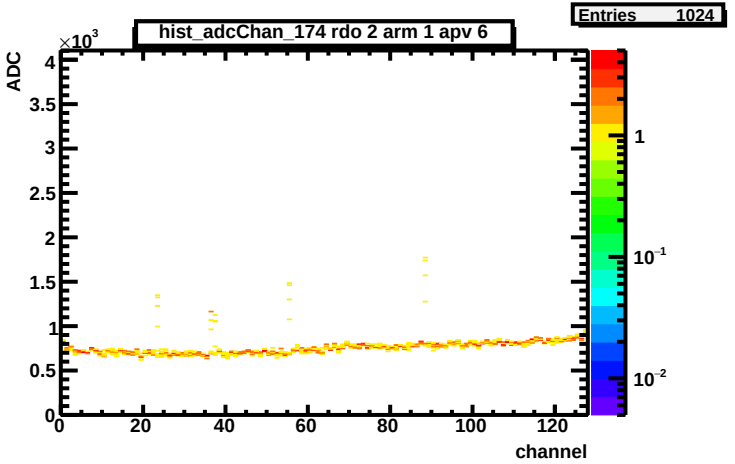

hist_adcChan_169 rdo 2 arm 1 apv 1
Entries 1024
ADC
×10^3
channel

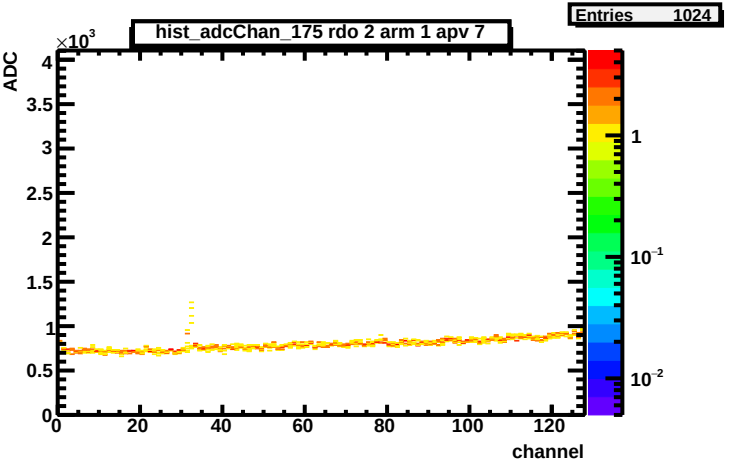

hist_adcChan_170 rdo 2 arm 1 apv 2
Entries 1024
ADC
×10^3
channel

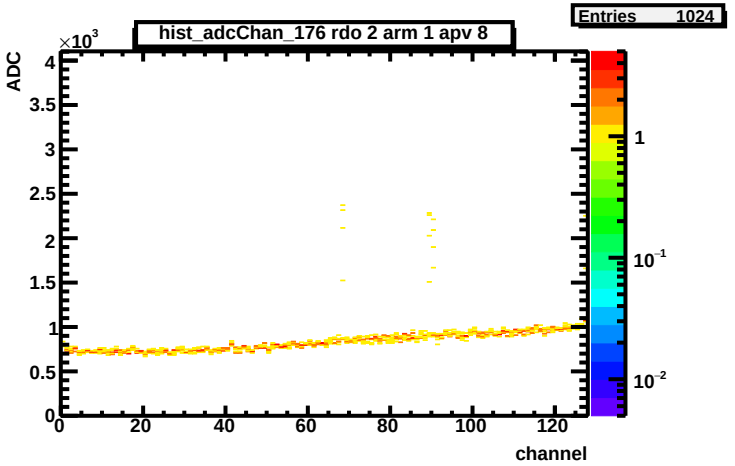

hist_adcChan_171 rdo 2 arm 1 apv 3
Entries 1024
ADC
×10^3
channel

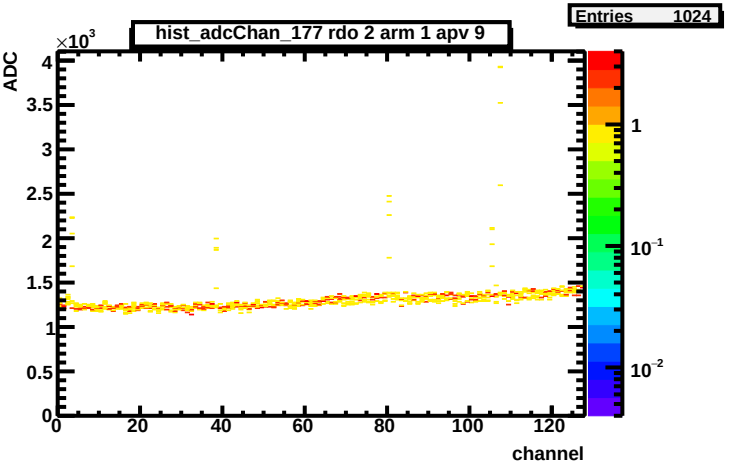

hist_adcChan_172 rdo 2 arm 1 apv 4
Entries 1024
ADC
×10^3
channel

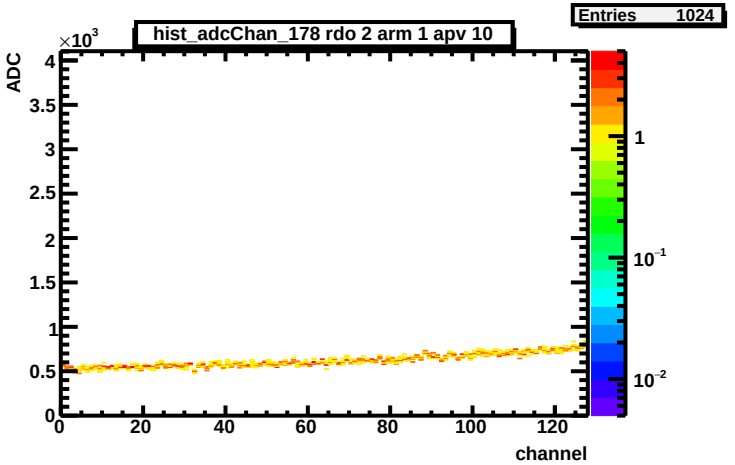

hist_adcChan_173 rdo 2 arm 1 apv 5
Entries 1024
ADC
×10^3
channel

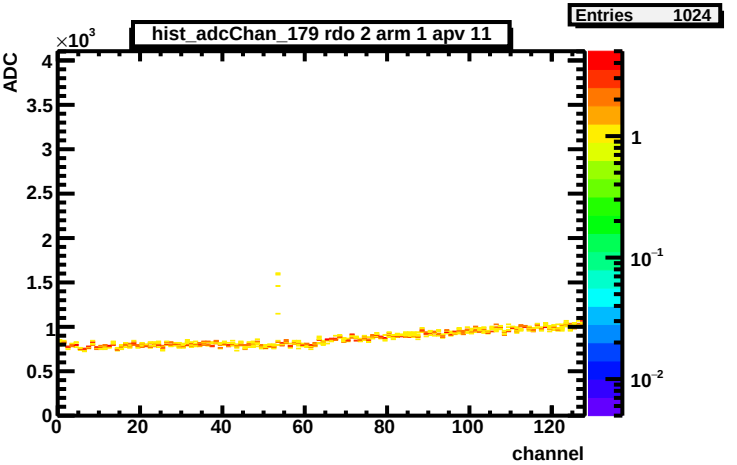

hist_adcChan_174 rdo 2 arm 1 apv 6
Entries 1024
ADC
×10^3
channel

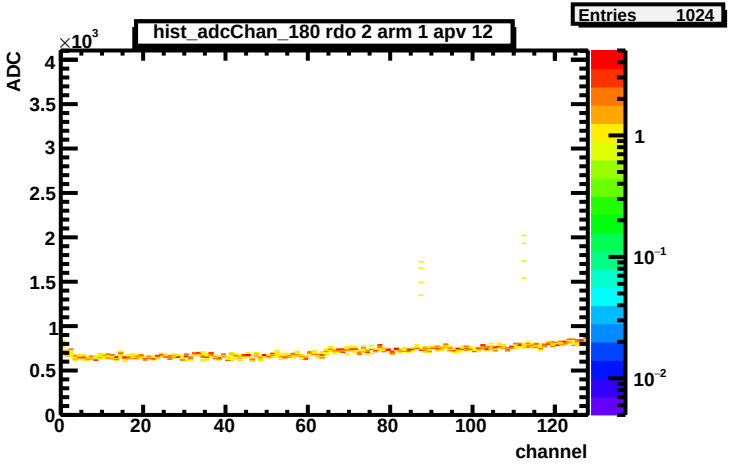

hist_adcChan_175 rdo 2 arm 1 apv 7
Entries 1024
ADC
×10^3
channel

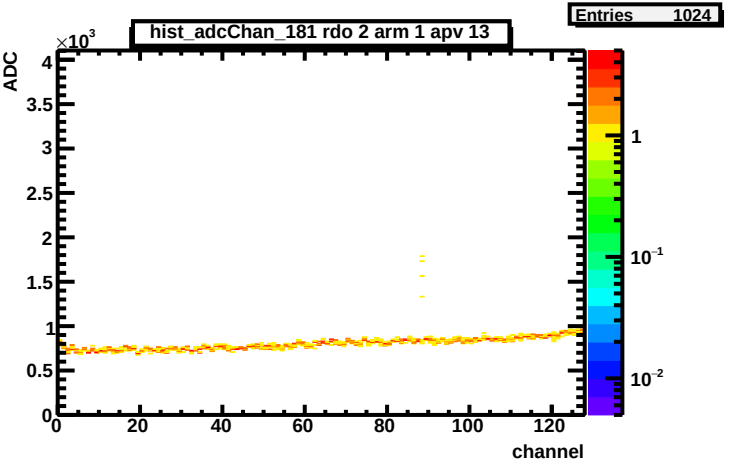

hist_adcChan_176 rdo 2 arm 1 apv 8
Entries 1024
ADC
×10^3
channel

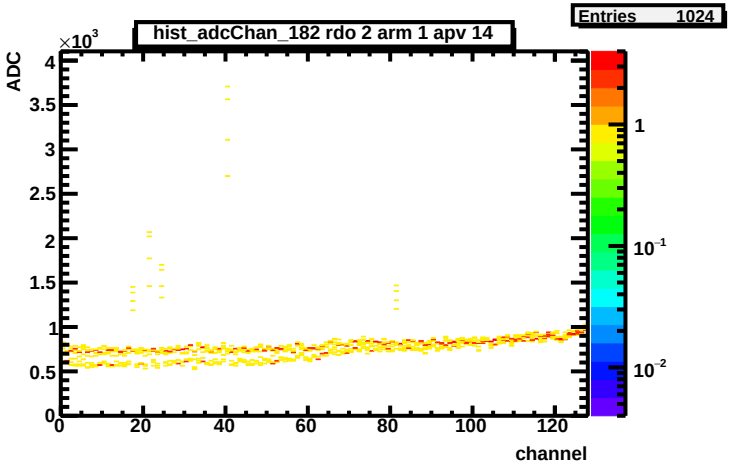

hist_adcChan_177 rdo 2 arm 1 apv 9
Entries 1024
ADC
×10^3
channel

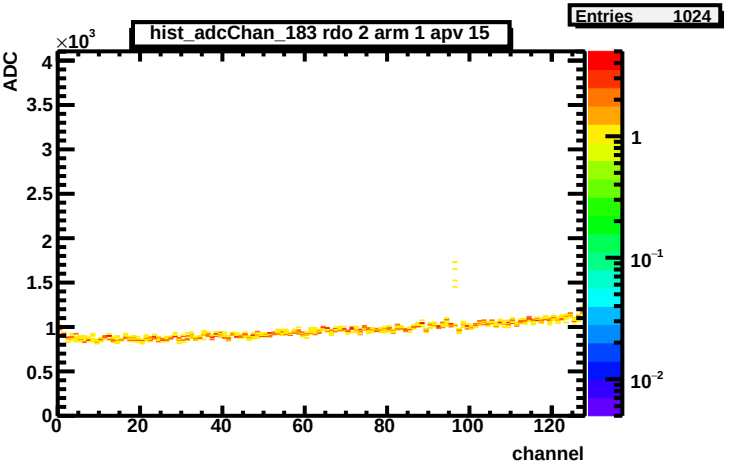

hist_adcChan_178 rdo 2 arm 1 apv 10
Entries 1024
ADC
×10^3
channel

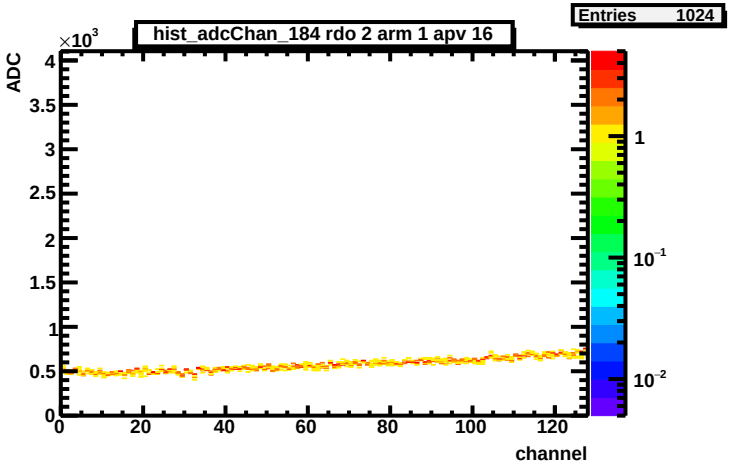

hist_adcChan_179 rdo 2 arm 1 apv 11
Entries 1024
ADC
×10^3
channel

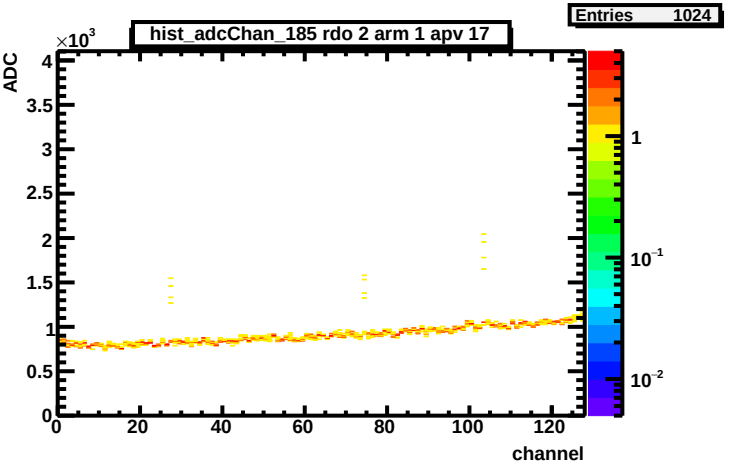

hist_adcChan_180 rdo 2 arm 1 apv 12
Entries 1024
ADC
×10^3
channel

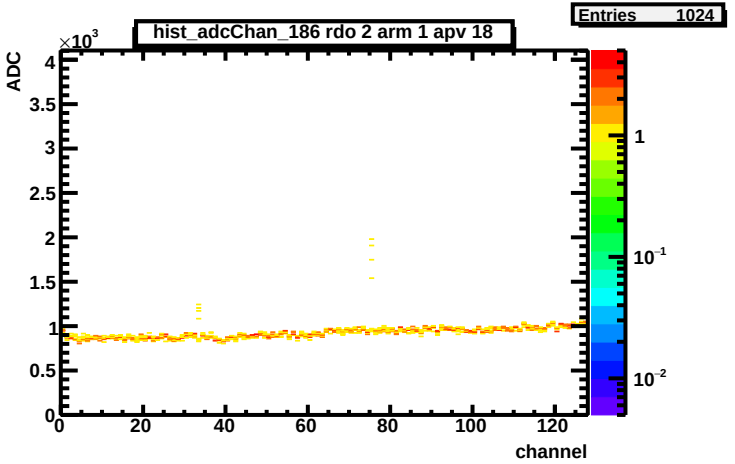

hist_adcChan_181 rdo 2 arm 1 apv 13
Entries 1024
ADC
×10^3
channel

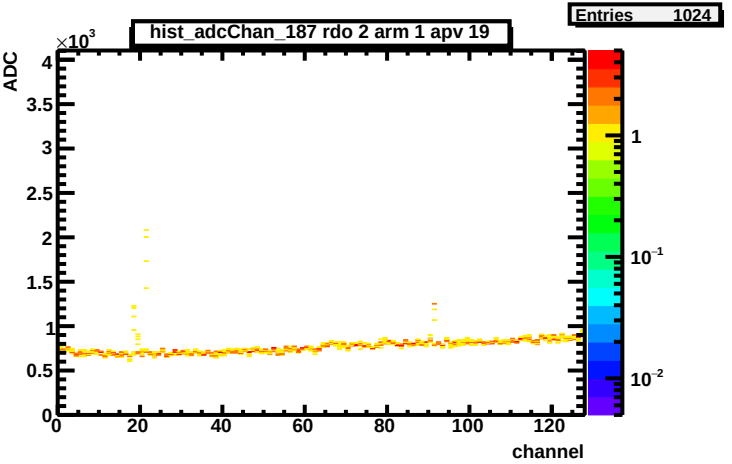

hist_adcChan_182 rdo 2 arm 1 apv 14
Entries 1024
ADC
×10^3
channel

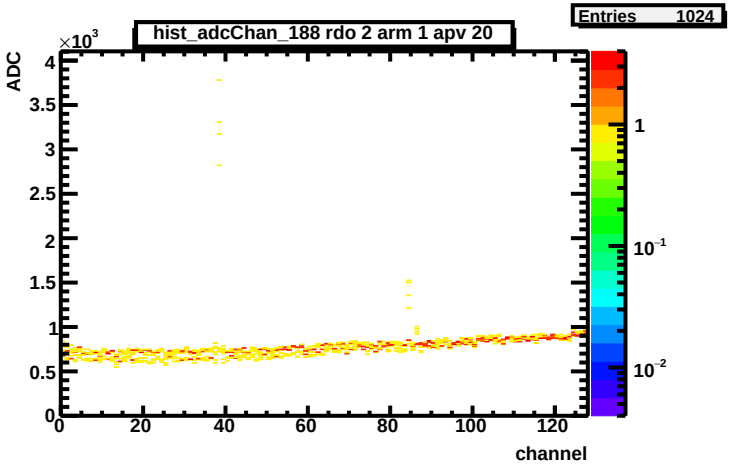

hist_adcChan_183 rdo 2 arm 1 apv 15
Entries 1024
ADC
×10^3
channel

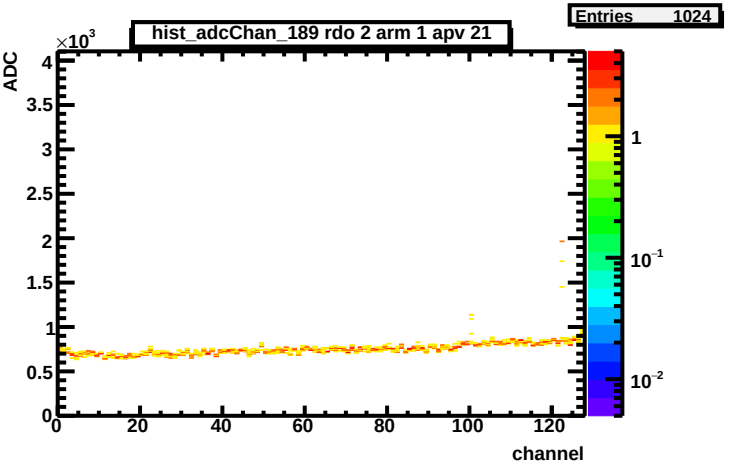

hist_adcChan_184 rdo 2 arm 1 apv 16
Entries 1024
ADC
×10^3
channel

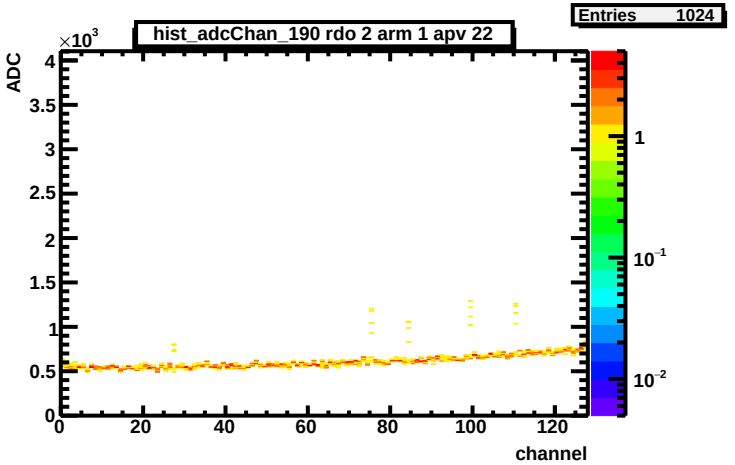

hist_adcChan_185 rdo 2 arm 1 apv 17
Entries 1024
ADC
×10^3
channel

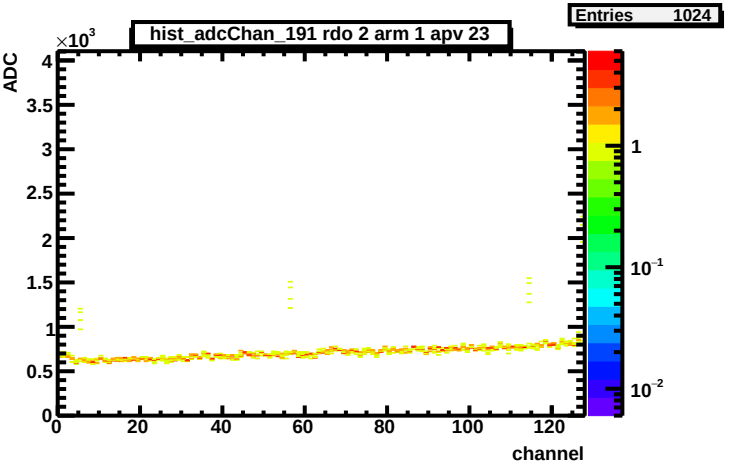

hist_adcChan_186 rdo 2 arm 1 apv 18
Entries 1024
ADC
channel

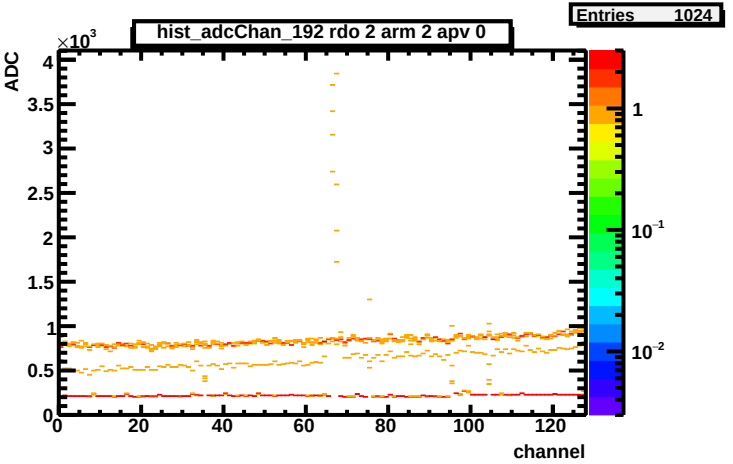

hist_adcChan_187 rdo 2 arm 1 apv 19
Entries 1024
ADC
channel

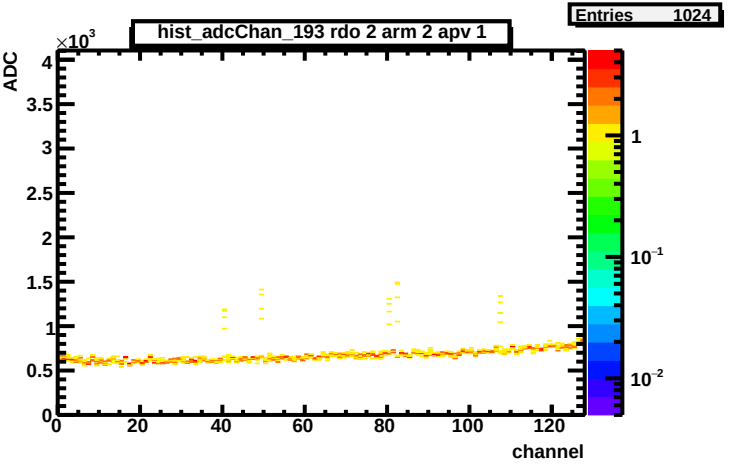

hist_adcChan_188 rdo 2 arm 1 apv 20
Entries 1024
ADC
channel

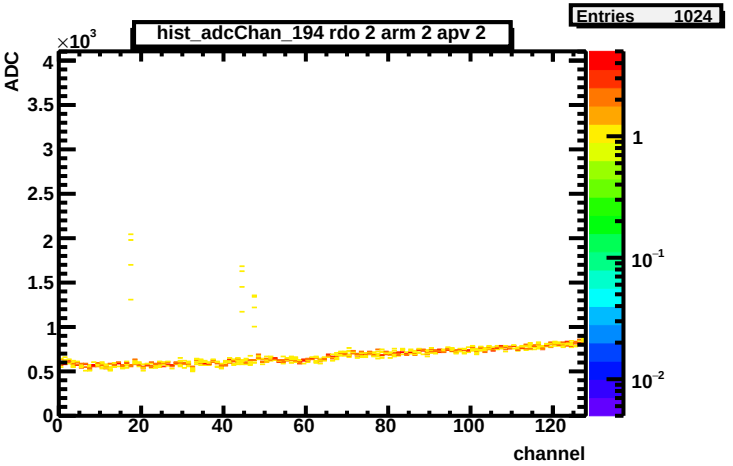

hist_adcChan_189 rdo 2 arm 1 apv 21
Entries 1024
ADC
channel

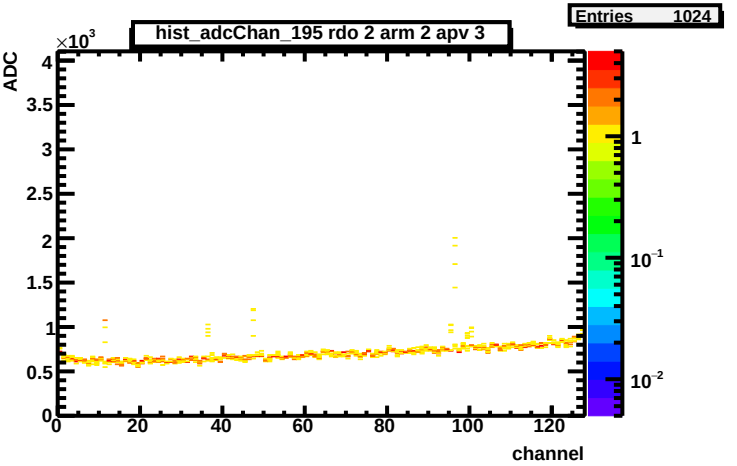

hist_adcChan_190 rdo 2 arm 1 apv 22
Entries 1024
ADC
channel

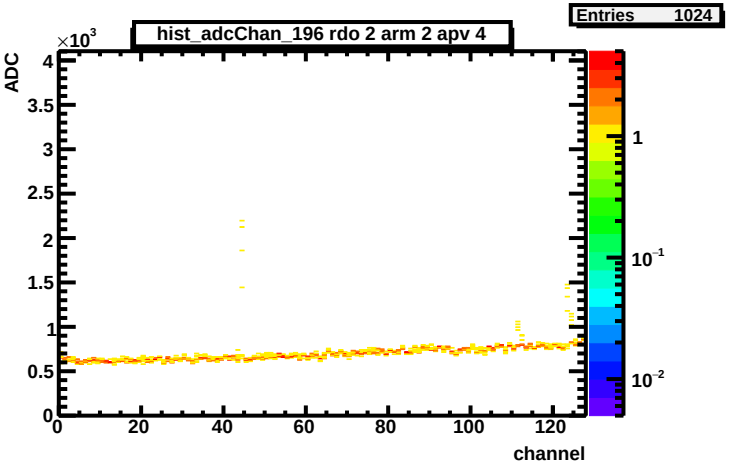

hist_adcChan_191 rdo 2 arm 1 apv 23
Entries 1024
ADC
channel

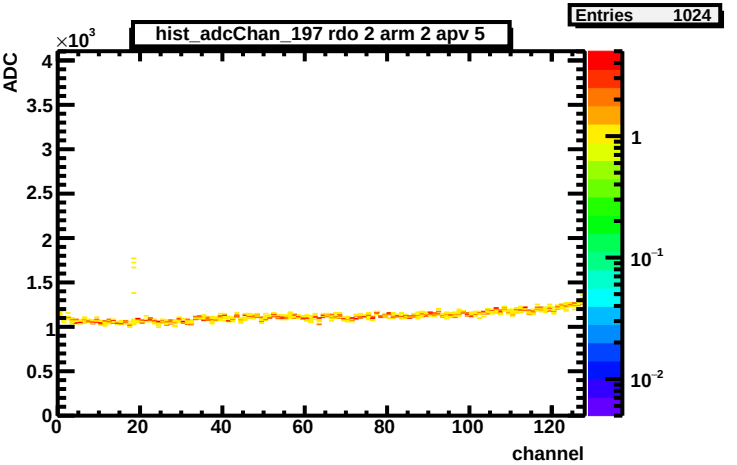

hist_adcChan_192 rdo 2 arm 2 apv 0
Entries 1024
ADC
×10^3
channel

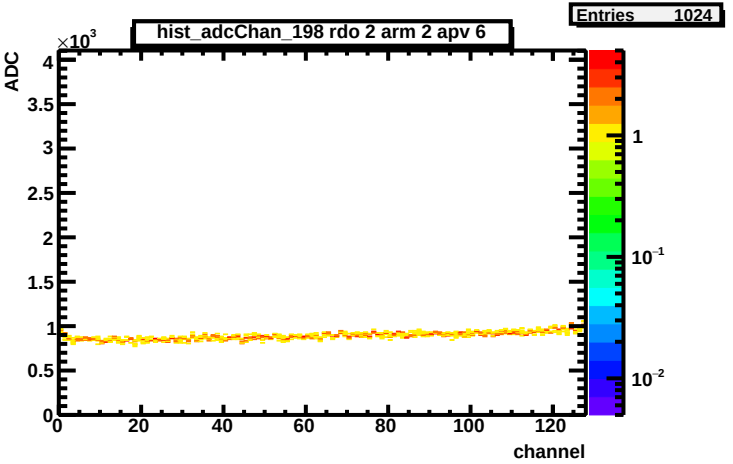

hist_adcChan_193 rdo 2 arm 2 apv 1
Entries 1024
ADC
×10^3
channel

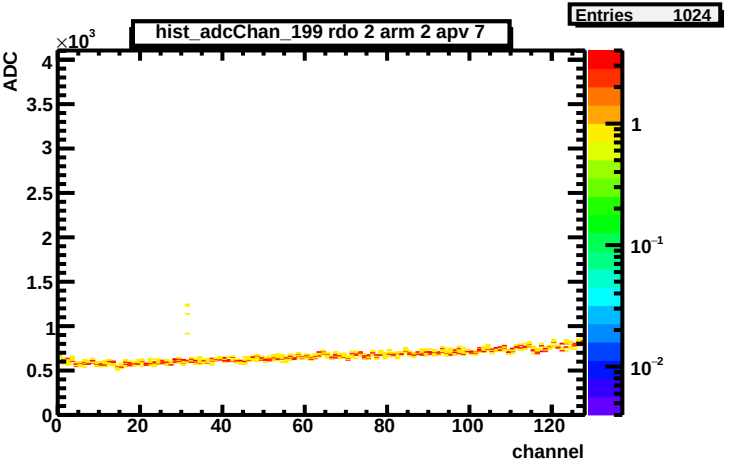

hist_adcChan_194 rdo 2 arm 2 apv 2
Entries 1024
ADC
×10^3
channel

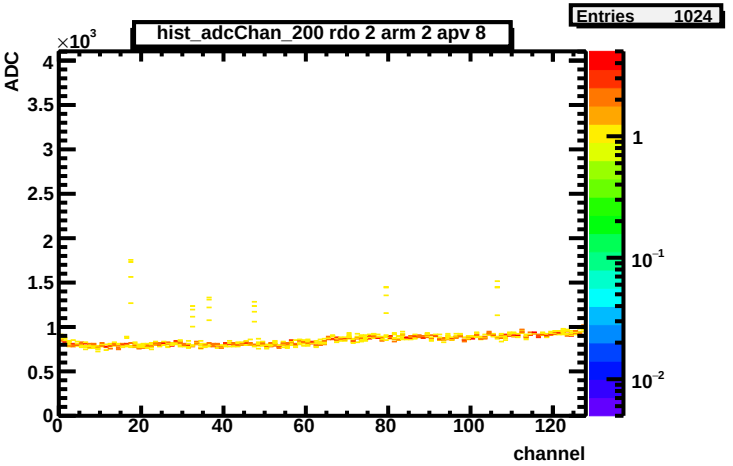

hist_adcChan_195 rdo 2 arm 2 apv 3
Entries 1024
ADC
×10^3
channel

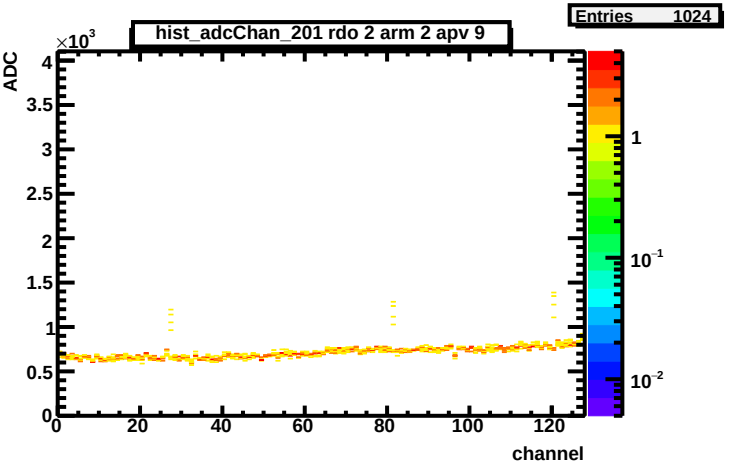

hist_adcChan_196 rdo 2 arm 2 apv 4
Entries 1024
ADC
×10^3
channel

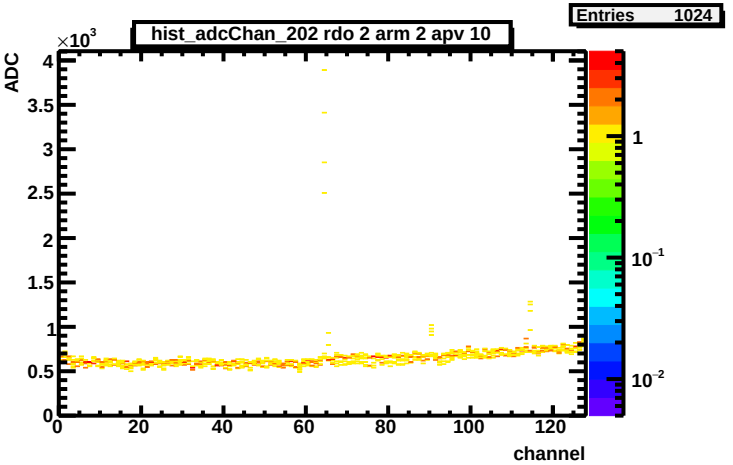

hist_adcChan_197 rdo 2 arm 2 apv 5
Entries 1024
ADC
×10^3
channel

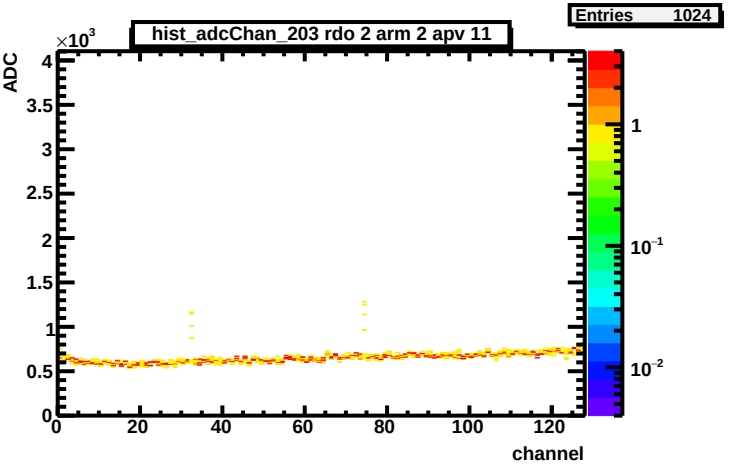

hist_adcChan_198 rdo 2 arm 2 apv 6
Entries 1024
ADC
channel

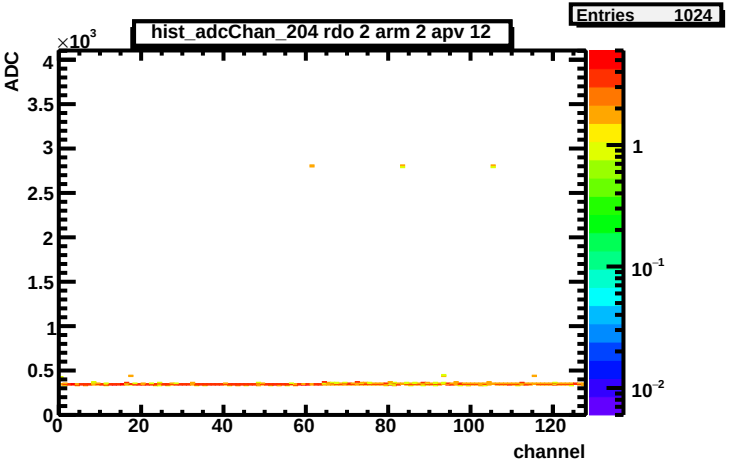

hist_adcChan_199 rdo 2 arm 2 apv 7
Entries 1024
ADC
channel

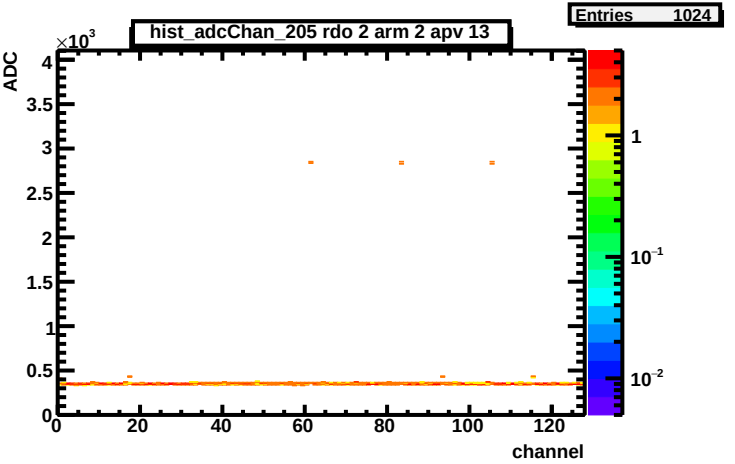

hist_adcChan_200 rdo 2 arm 2 apv 8
Entries 1024
ADC
channel

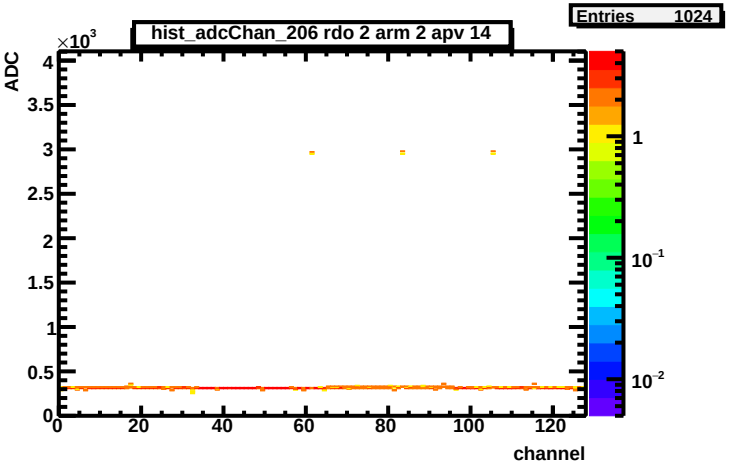

hist_adcChan_201 rdo 2 arm 2 apv 9
Entries 1024
ADC
channel

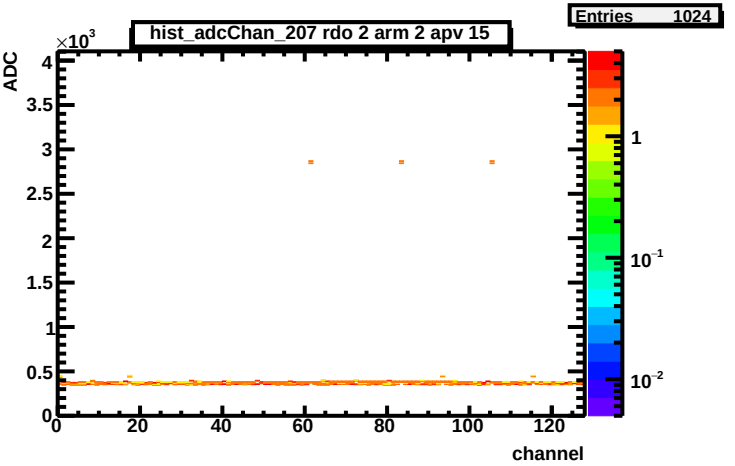

hist_adcChan_202 rdo 2 arm 2 apv 10
Entries 1024
ADC
channel

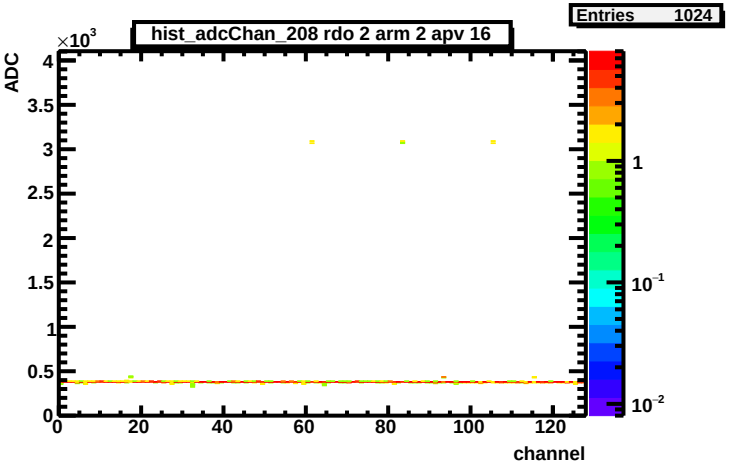

hist_adcChan_203 rdo 2 arm 2 apv 11
Entries 1024
ADC
channel

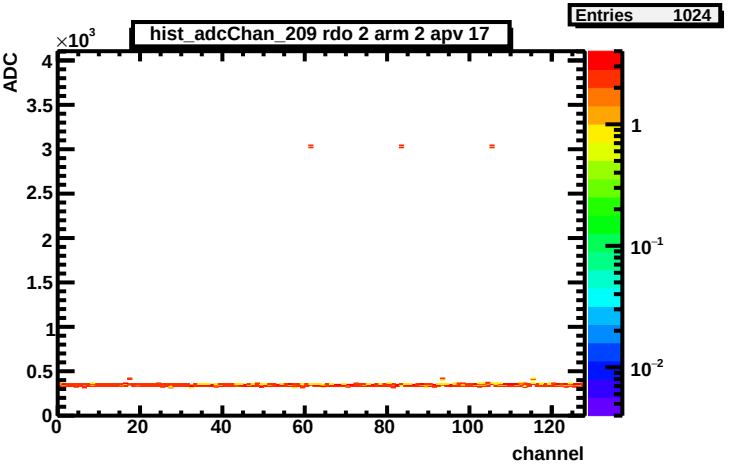

hist_adcChan_204 rdo 2 arm 2 apv 12
Entries 1024
ADC
channel

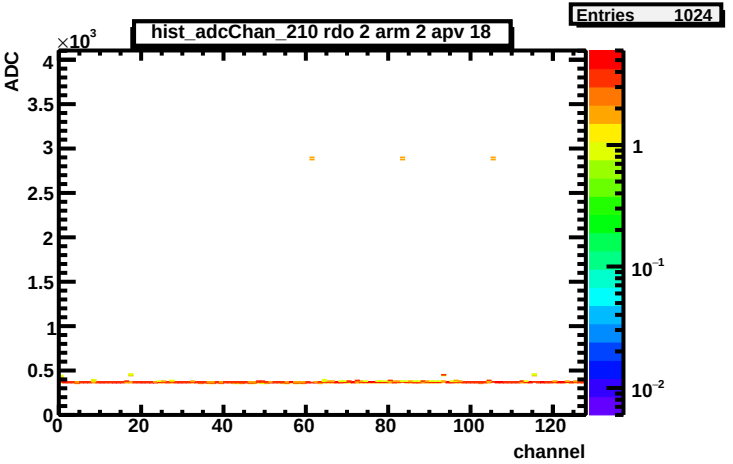

hist_adcChan_205 rdo 2 arm 2 apv 13
Entries 1024
ADC
channel

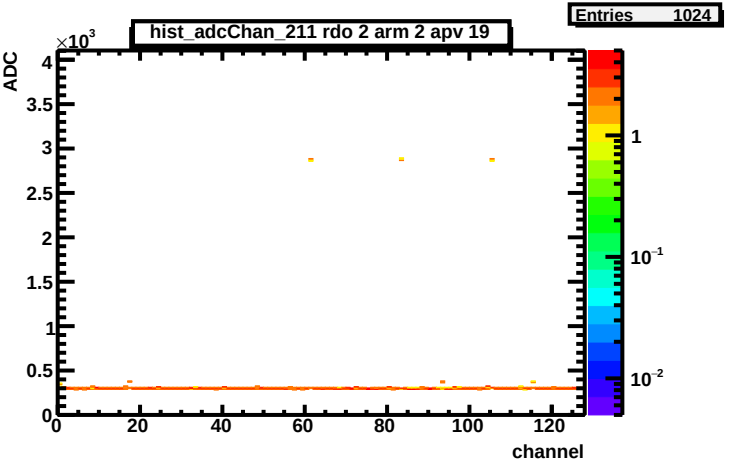

hist_adcChan_206 rdo 2 arm 2 apv 14
Entries 1024
ADC
channel

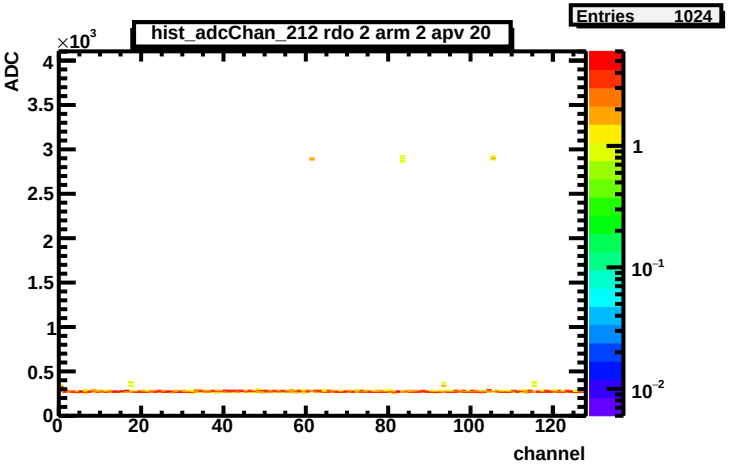

hist_adcChan_207 rdo 2 arm 2 apv 15
Entries 1024
ADC
channel

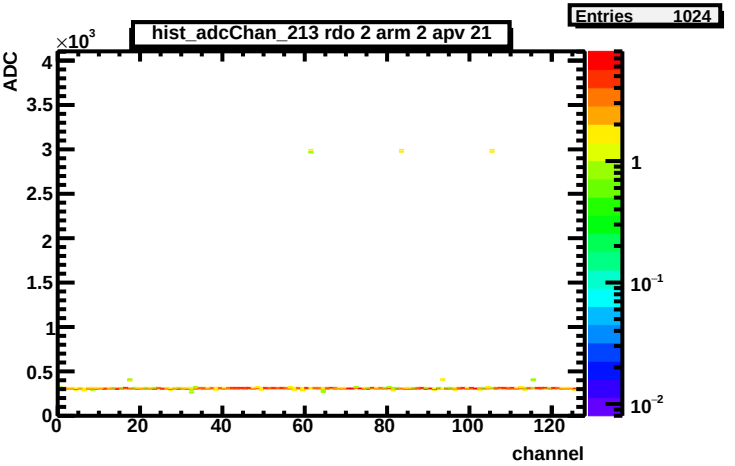

hist_adcChan_208 rdo 2 arm 2 apv 16
Entries 1024
ADC
channel

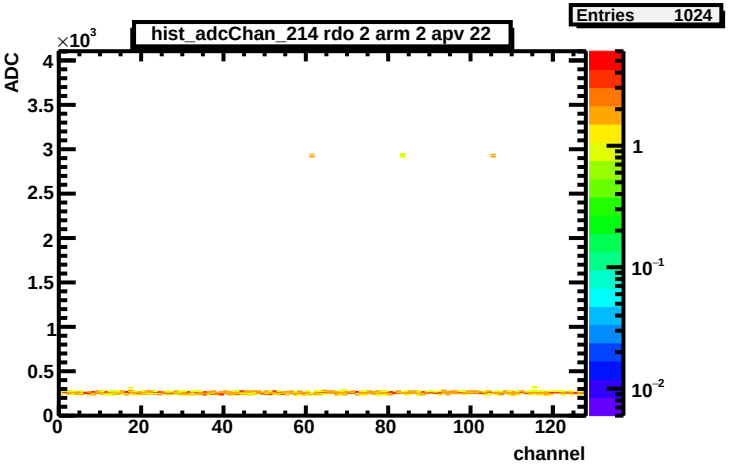

hist_adcChan_209 rdo 2 arm 2 apv 17
Entries 1024
ADC
channel

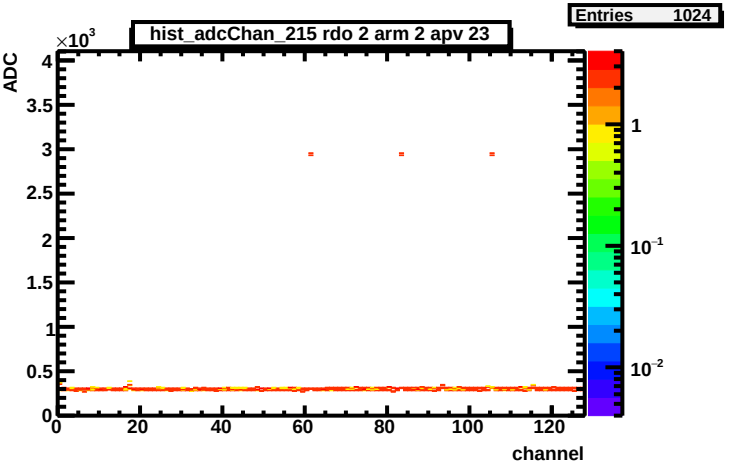

hist_adcChan_210 rdo 2 arm 2 apv 18
Entries 1024
ADC
channel

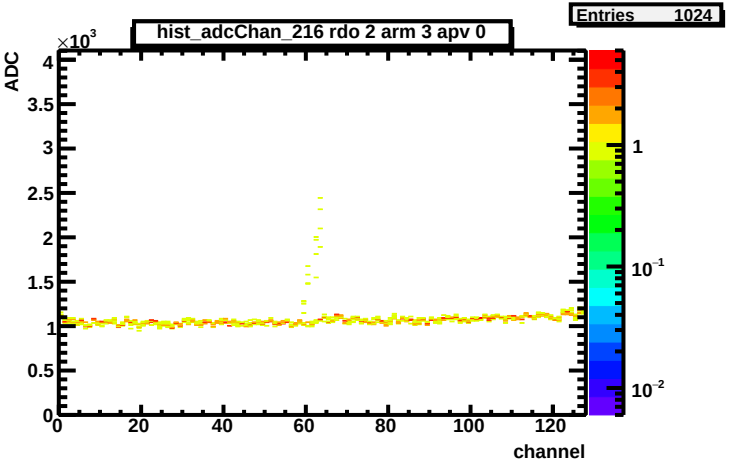

hist_adcChan_211 rdo 2 arm 2 apv 19
Entries 1024
ADC
channel

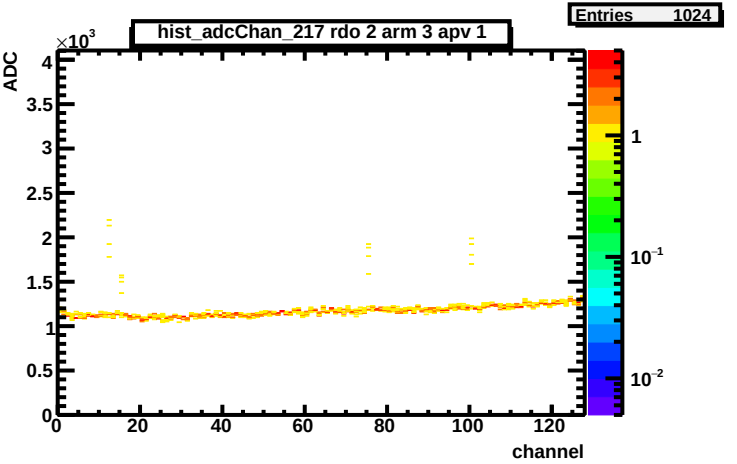

hist_adcChan_212 rdo 2 arm 2 apv 20
Entries 1024
ADC
channel

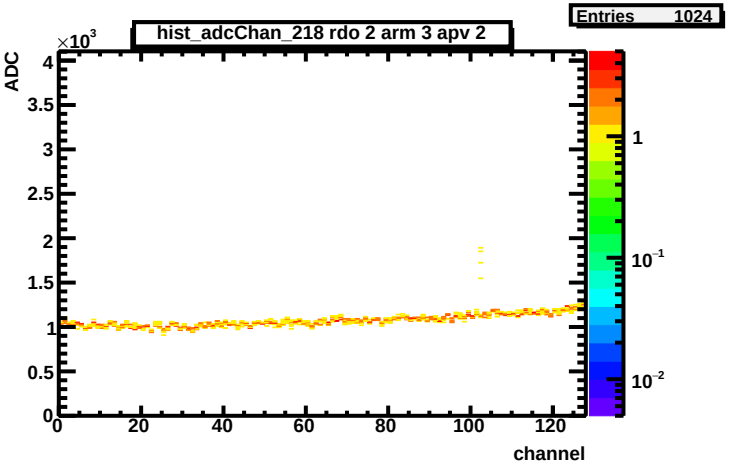

hist_adcChan_213 rdo 2 arm 2 apv 21
Entries 1024
ADC
channel

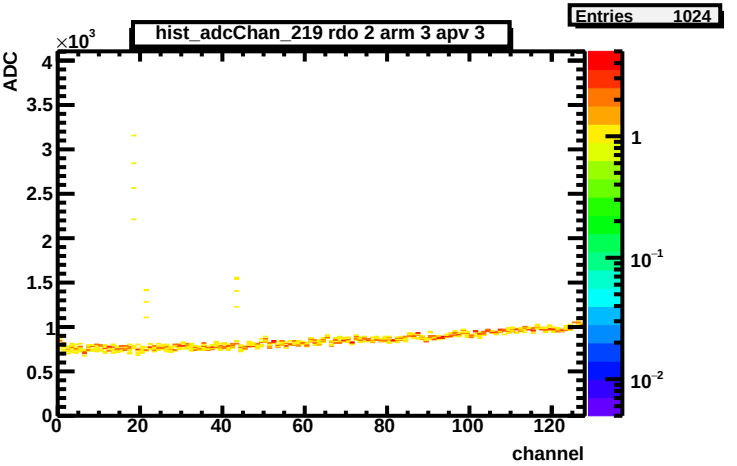

hist_adcChan_214 rdo 2 arm 2 apv 22
Entries 1024
ADC
channel

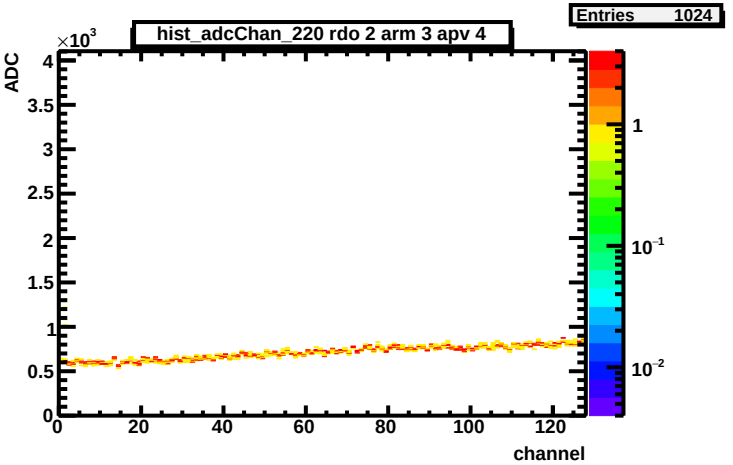

hist_adcChan_215 rdo 2 arm 2 apv 23
Entries 1024
ADC
channel

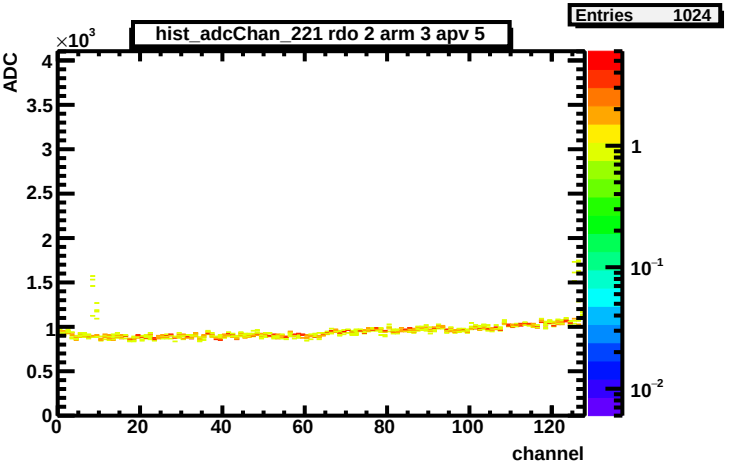

hist_adcChan_216 rdo 2 arm 3 apv 0
Entries 1024
ADC
channel

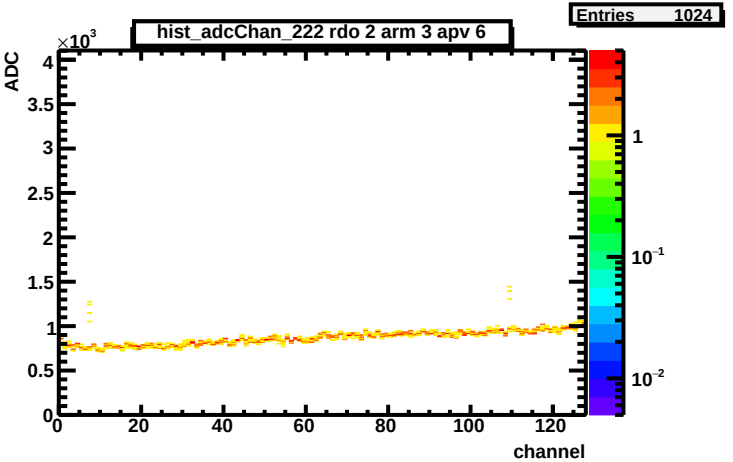

hist_adcChan_217 rdo 2 arm 3 apv 1
Entries 1024
ADC
channel

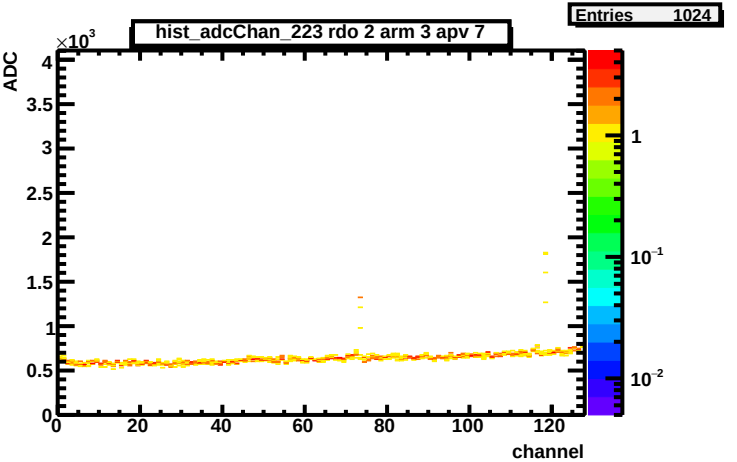

hist_adcChan_218 rdo 2 arm 3 apv 2
Entries 1024
ADC
channel

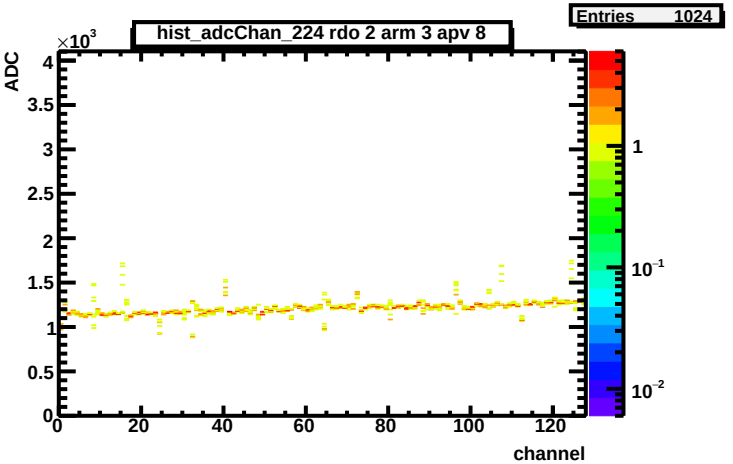

hist_adcChan_219 rdo 2 arm 3 apv 3
Entries 1024
ADC
channel

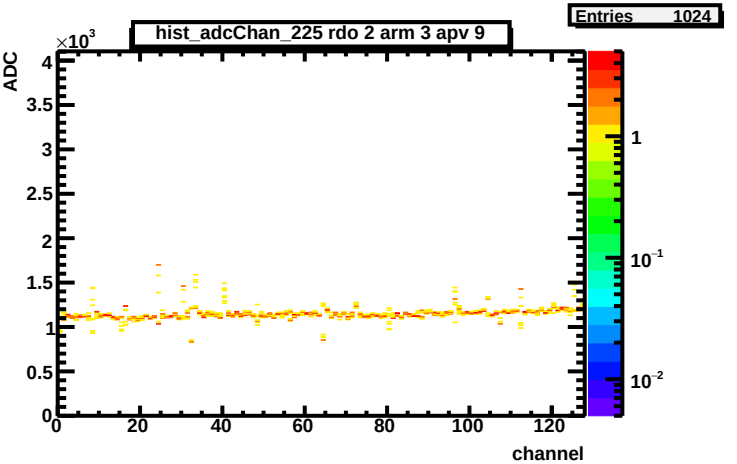

hist_adcChan_220 rdo 2 arm 3 apv 4
Entries 1024
ADC
channel

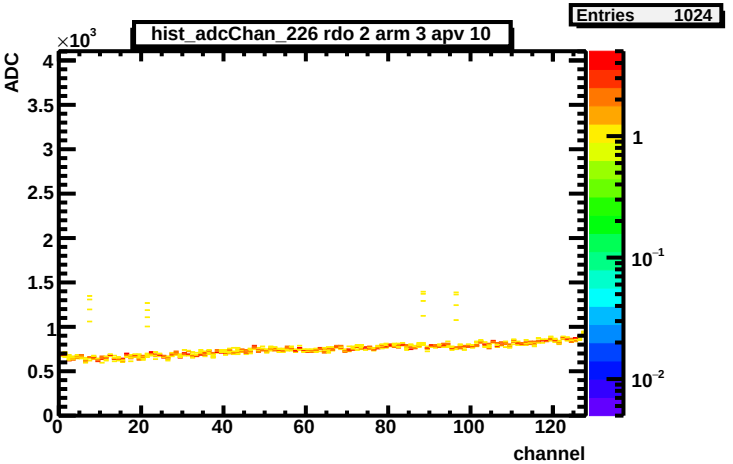

hist_adcChan_221 rdo 2 arm 3 apv 5
Entries 1024
ADC
channel

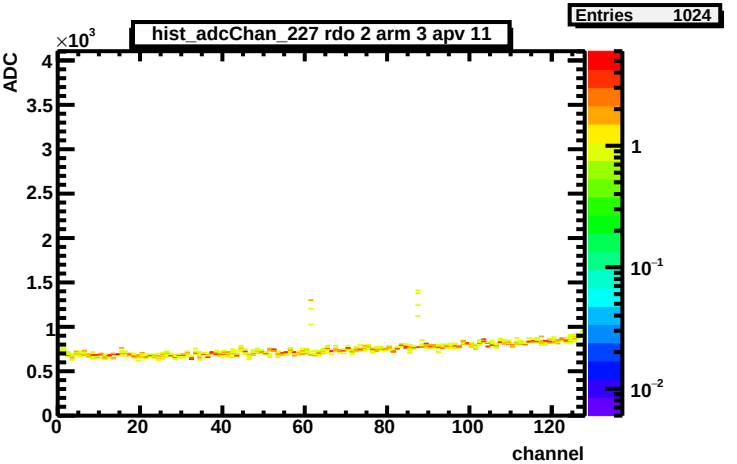

hist_adcChan_222 rdo 2 arm 3 apv 6
Entries 1024
ADC
channel

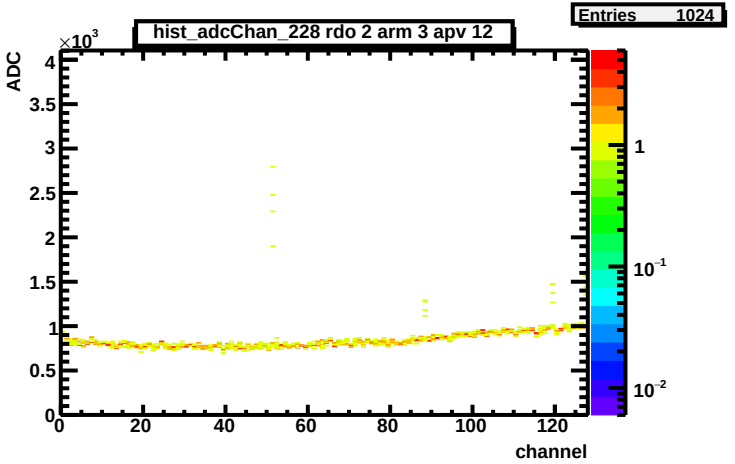

hist_adcChan_223 rdo 2 arm 3 apv 7
Entries 1024
ADC
channel

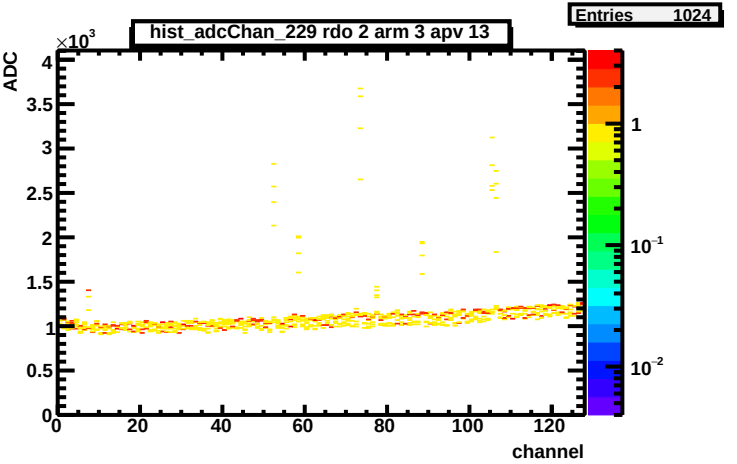

hist_adcChan_224 rdo 2 arm 3 apv 8
Entries 1024
ADC
channel

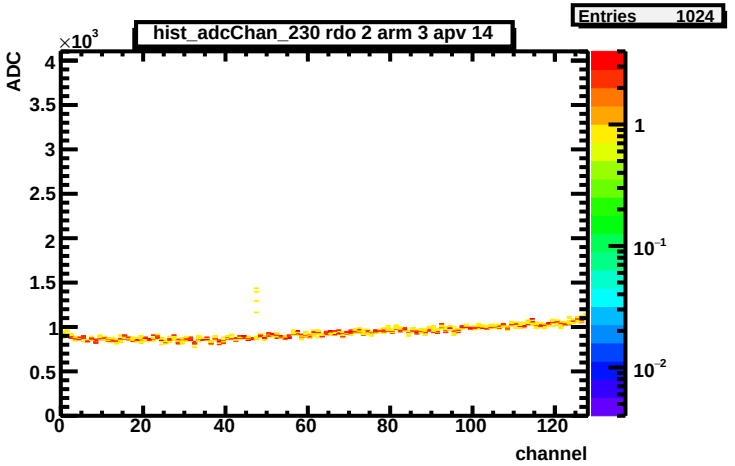

hist_adcChan_225 rdo 2 arm 3 apv 9
Entries 1024
ADC
channel

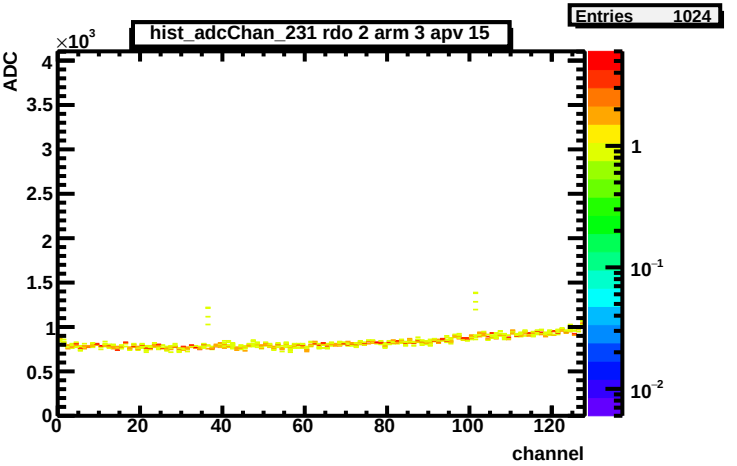

hist_adcChan_226 rdo 2 arm 3 apv 10
Entries 1024
ADC
channel

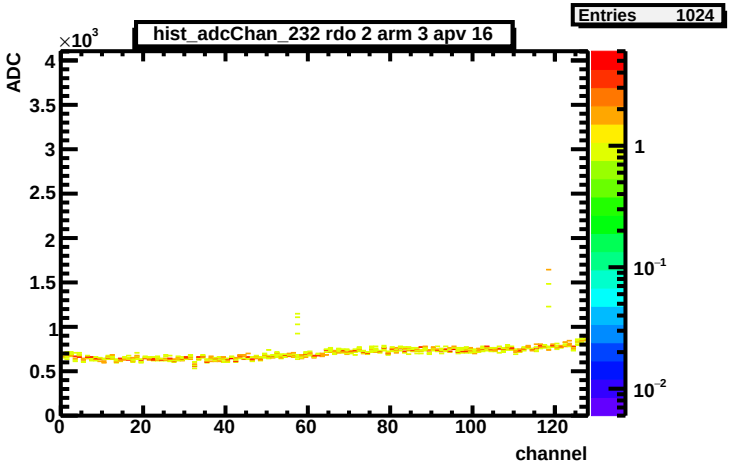

hist_adcChan_227 rdo 2 arm 3 apv 11
Entries 1024
ADC
channel

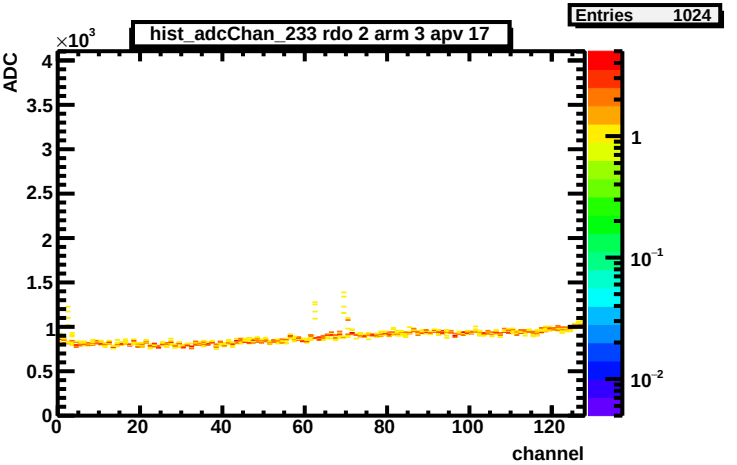

hist_adcChan_228 rdo 2 arm 3 apv 12
Entries 1024
ADC
channel

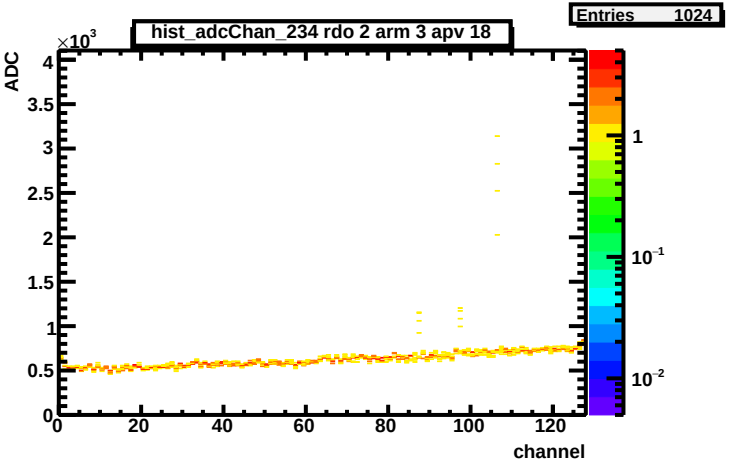

hist_adcChan_229 rdo 2 arm 3 apv 13
Entries 1024
ADC
channel

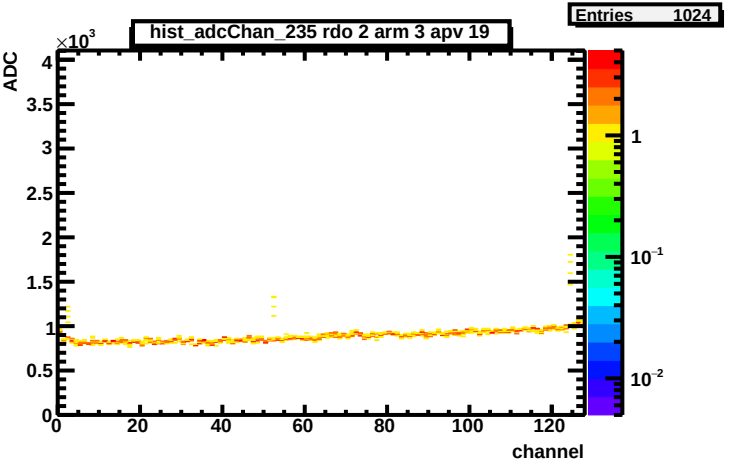

hist_adcChan_230 rdo 2 arm 3 apv 14
Entries 1024
ADC
channel

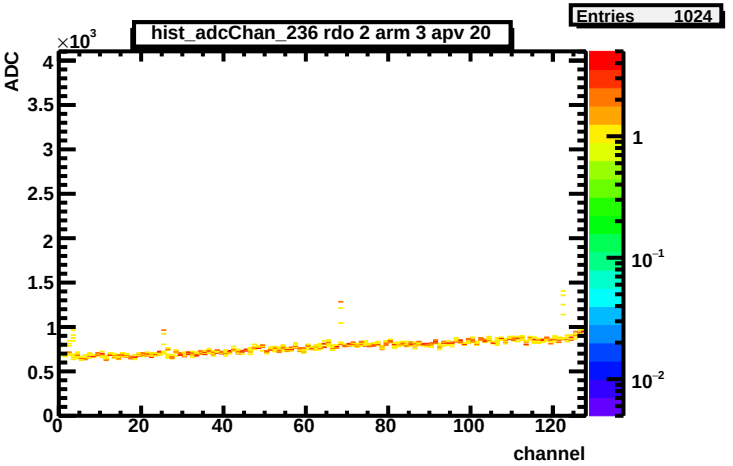

hist_adcChan_231 rdo 2 arm 3 apv 15
Entries 1024
ADC
channel

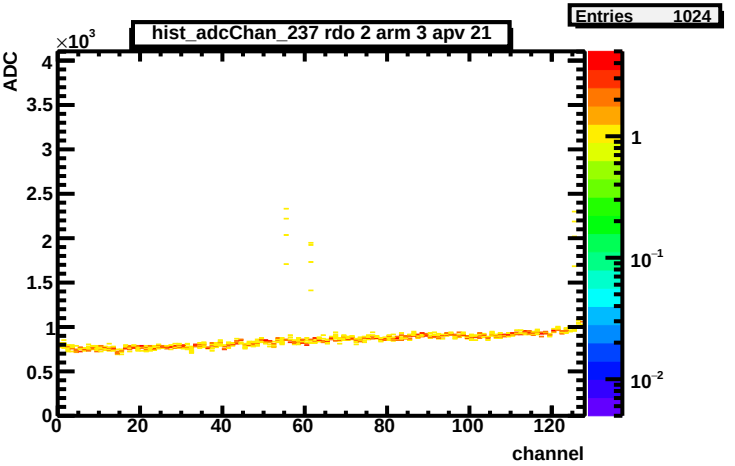

hist_adcChan_232 rdo 2 arm 3 apv 16
Entries 1024
ADC
channel

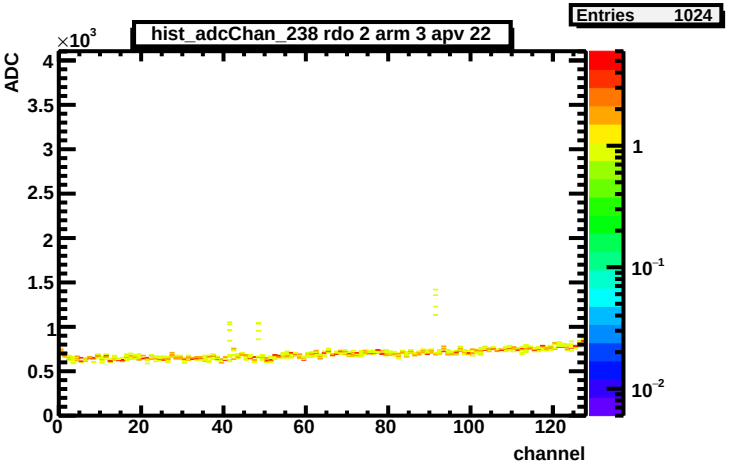

hist_adcChan_233 rdo 2 arm 3 apv 17
Entries 1024
ADC
channel

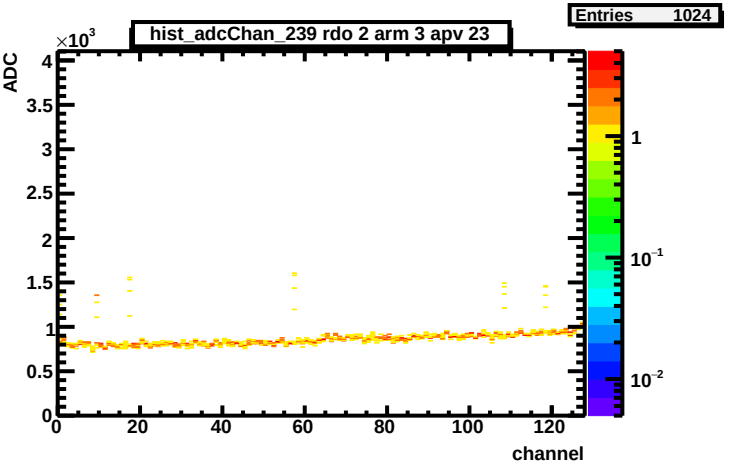

hist_adcChan_234 rdo 2 arm 3 apv 18
Entries 1024
ADC
channel

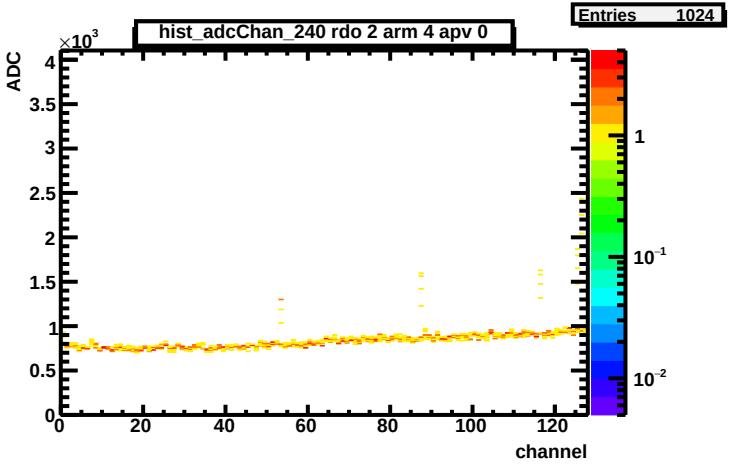

hist_adcChan_235 rdo 2 arm 3 apv 19
Entries 1024
ADC
channel

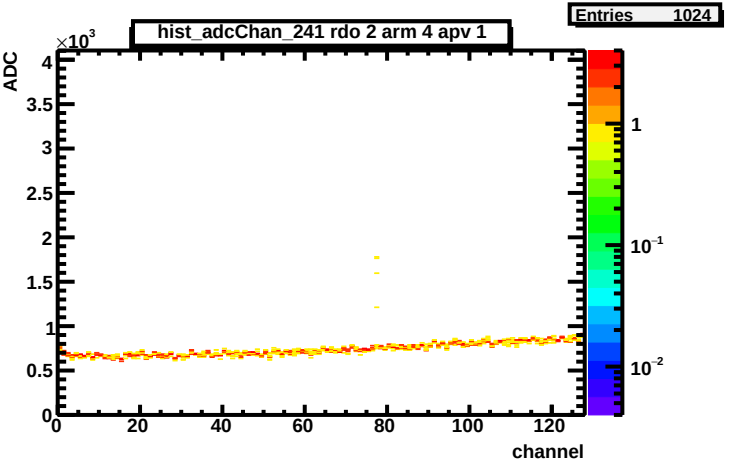

hist_adcChan_236 rdo 2 arm 3 apv 20
Entries 1024
ADC
channel

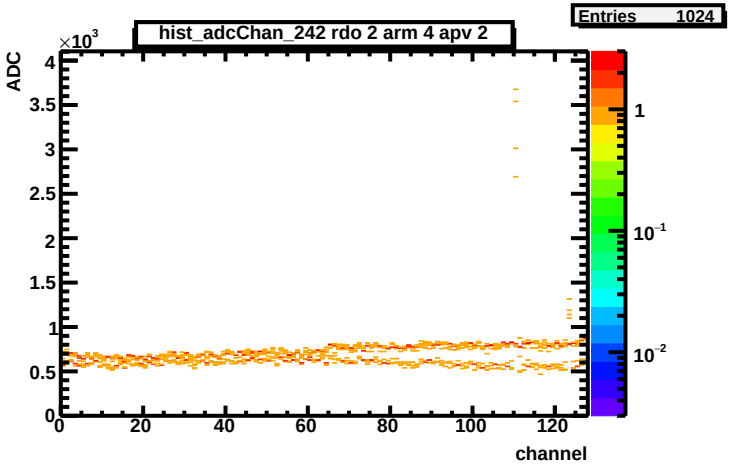

hist_adcChan_237 rdo 2 arm 3 apv 21
Entries 1024
ADC
channel

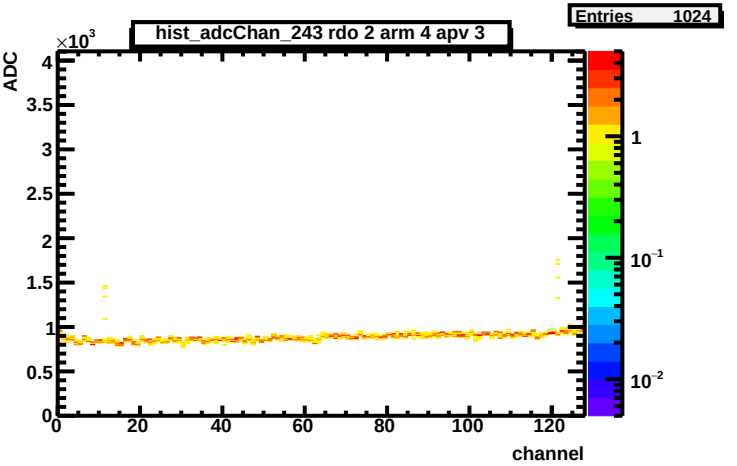

hist_adcChan_238 rdo 2 arm 3 apv 22
Entries 1024
ADC
channel

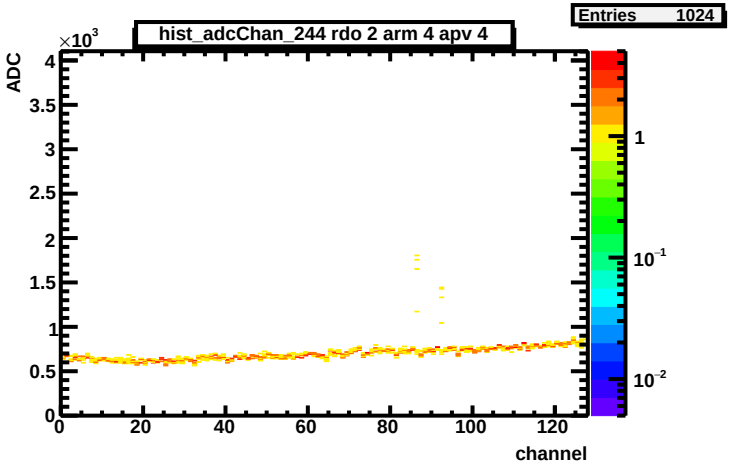

hist_adcChan_239 rdo 2 arm 3 apv 23
Entries 1024
ADC
channel

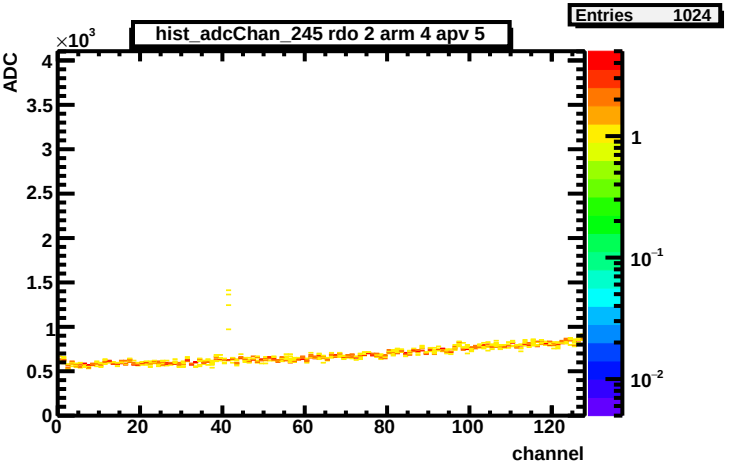

hist_adcChan_240 rdo 2 arm 4 apv 0
Entries 1024
ADC
×10^3
channel

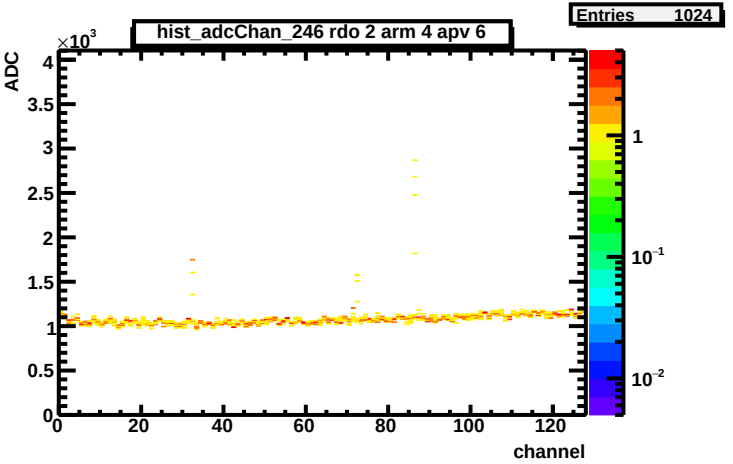

hist_adcChan_241 rdo 2 arm 4 apv 1
Entries 1024
ADC
×10^3
channel

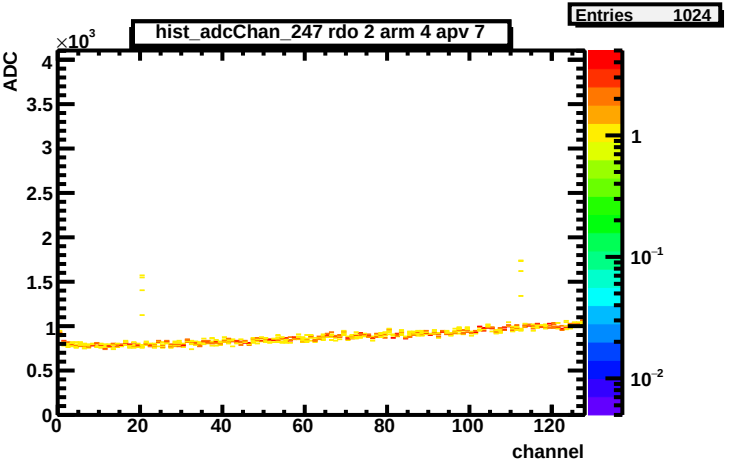

hist_adcChan_242 rdo 2 arm 4 apv 2
Entries 1024
ADC
×10^3
channel

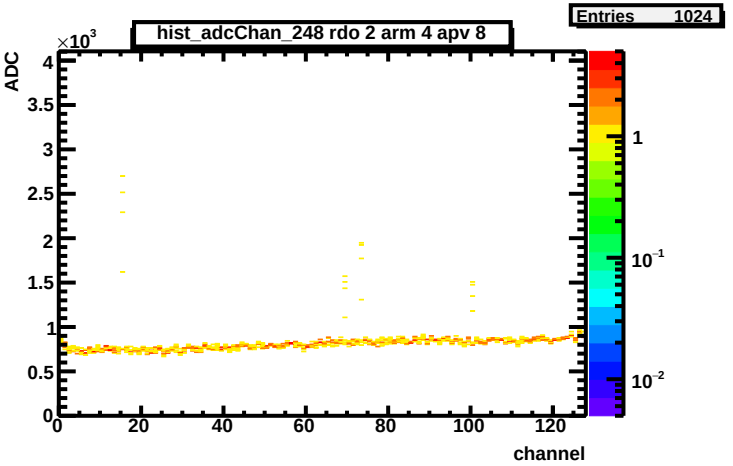

hist_adcChan_243 rdo 2 arm 4 apv 3
Entries 1024
ADC
×10^3
channel

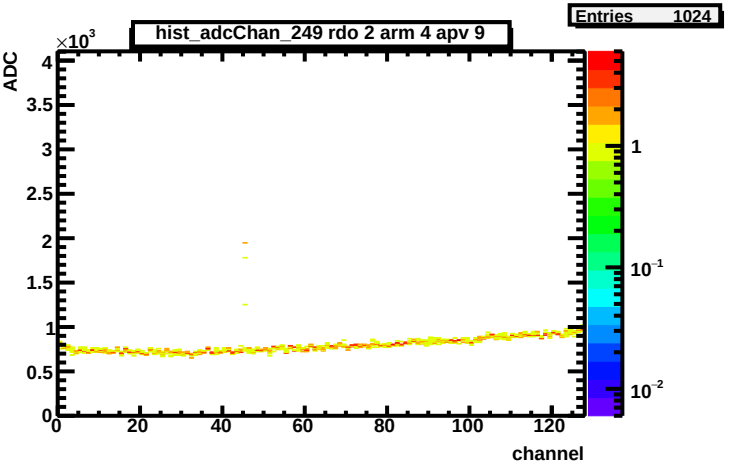

hist_adcChan_244 rdo 2 arm 4 apv 4
Entries 1024
ADC
×10^3
channel

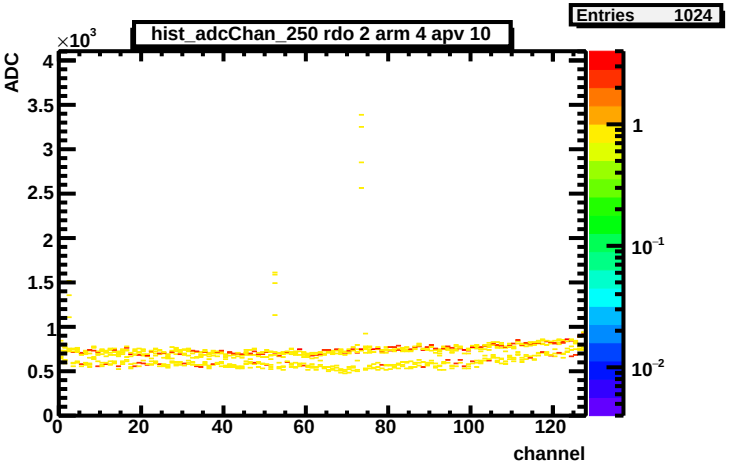

hist_adcChan_245 rdo 2 arm 4 apv 5
Entries 1024
ADC
×10^3
channel

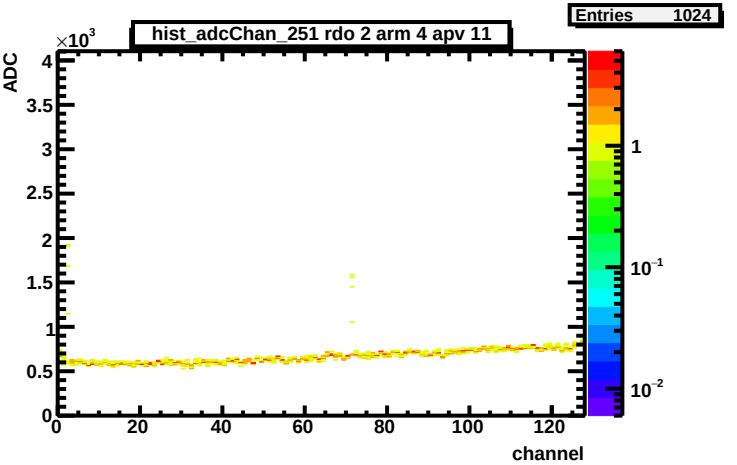

hist_adcChan_246 rdo 2 arm 4 apv 6
Entries 1024
ADC
channel

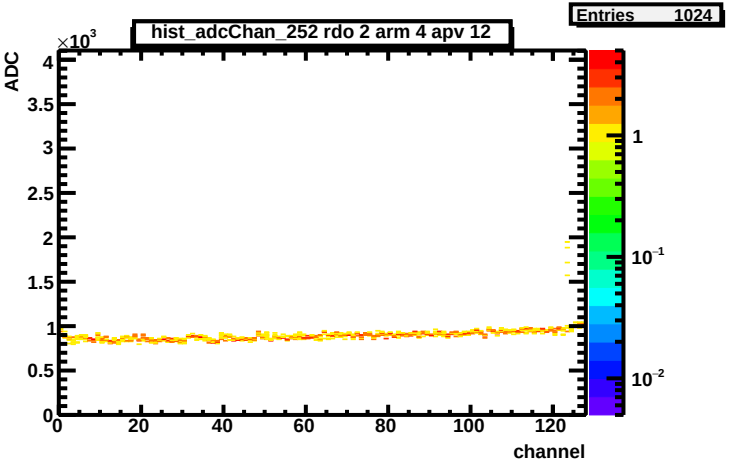

hist_adcChan_247 rdo 2 arm 4 apv 7
Entries 1024
ADC
channel

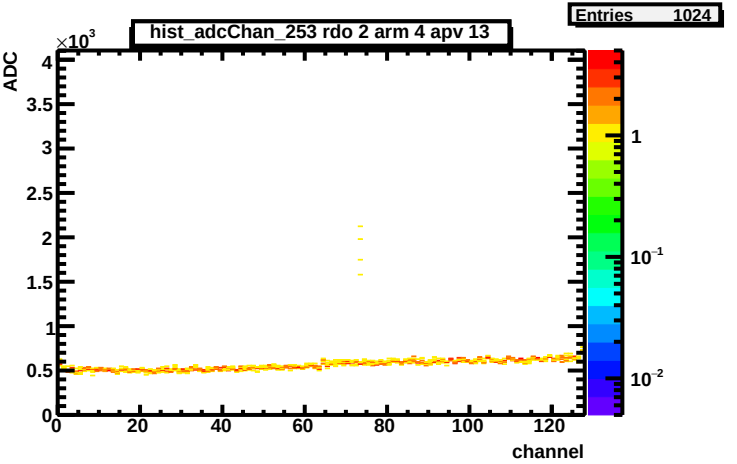

hist_adcChan_248 rdo 2 arm 4 apv 8
Entries 1024
ADC
channel

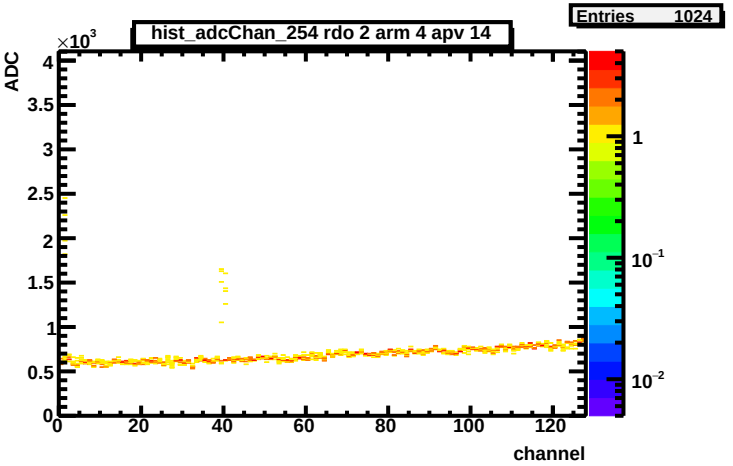

hist_adcChan_249 rdo 2 arm 4 apv 9
Entries 1024
ADC
channel

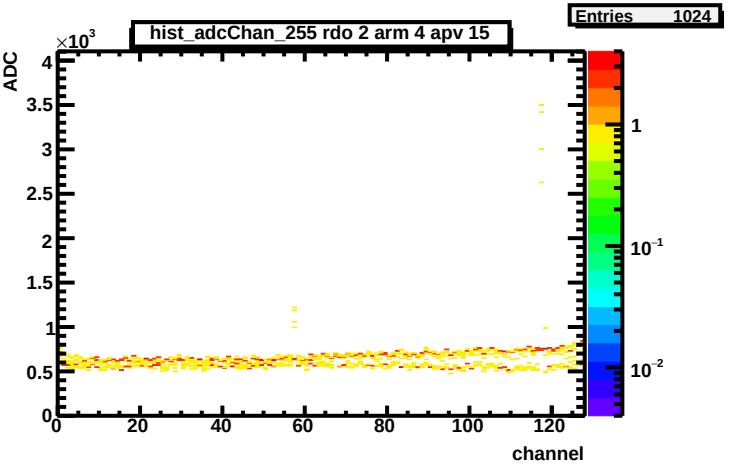

hist_adcChan_250 rdo 2 arm 4 apv 10
Entries 1024
ADC
channel

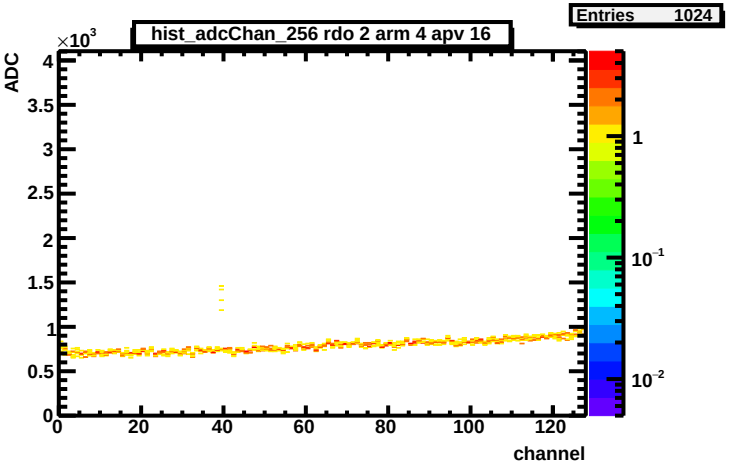

hist_adcChan_251 rdo 2 arm 4 apv 11
Entries 1024
ADC
channel

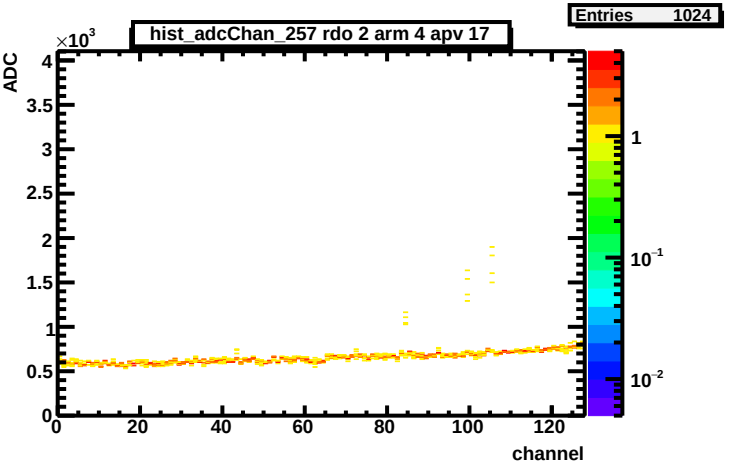

hist_adcChan_252 rdo 2 arm 4 apv 12
Entries 1024
ADC
×10^3
channel

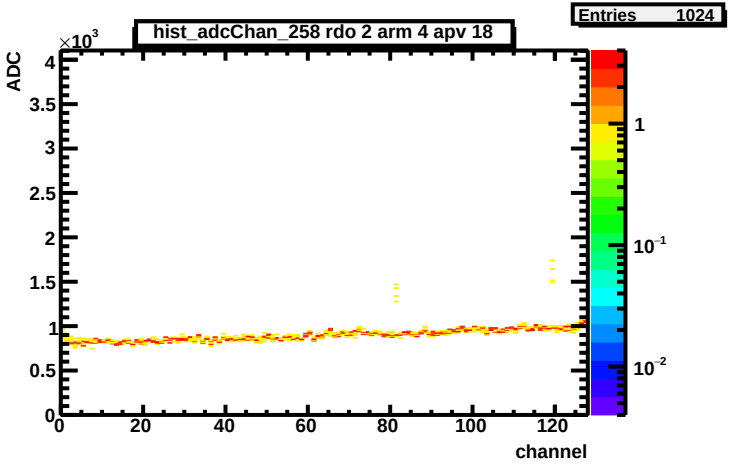

hist_adcChan_253 rdo 2 arm 4 apv 13
Entries 1024
ADC
×10^3
channel

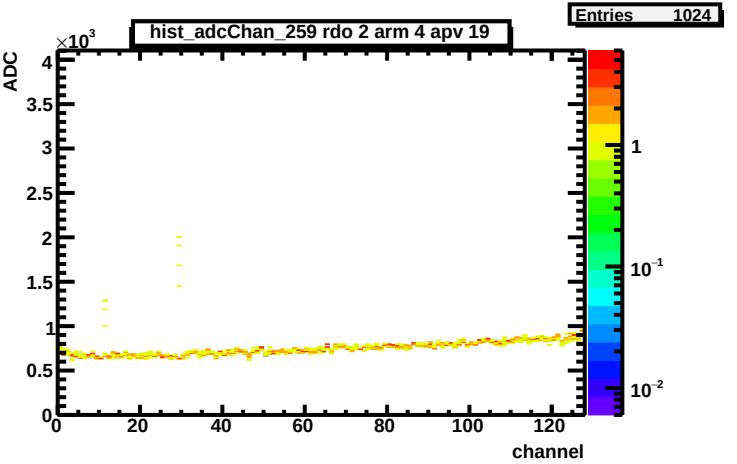

hist_adcChan_254 rdo 2 arm 4 apv 14
Entries 1024
ADC
×10^3
channel

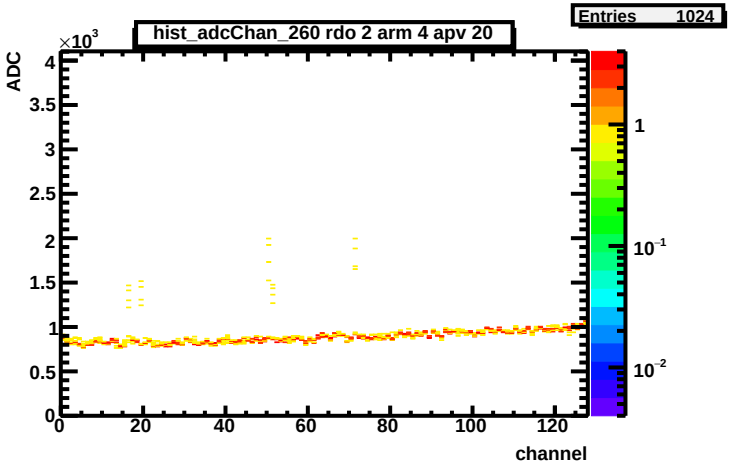

hist_adcChan_255 rdo 2 arm 4 apv 15
Entries 1024
ADC
×10^3
channel

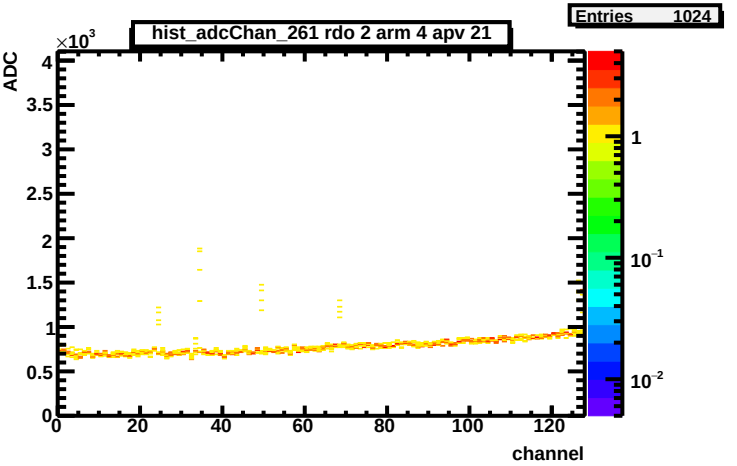

hist_adcChan_256 rdo 2 arm 4 apv 16
Entries 1024
ADC
×10^3
channel

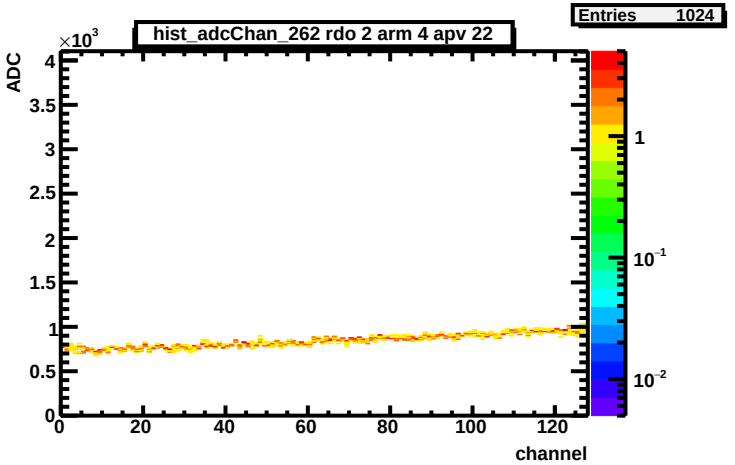

hist_adcChan_257 rdo 2 arm 4 apv 17
Entries 1024
ADC
×10^3
channel

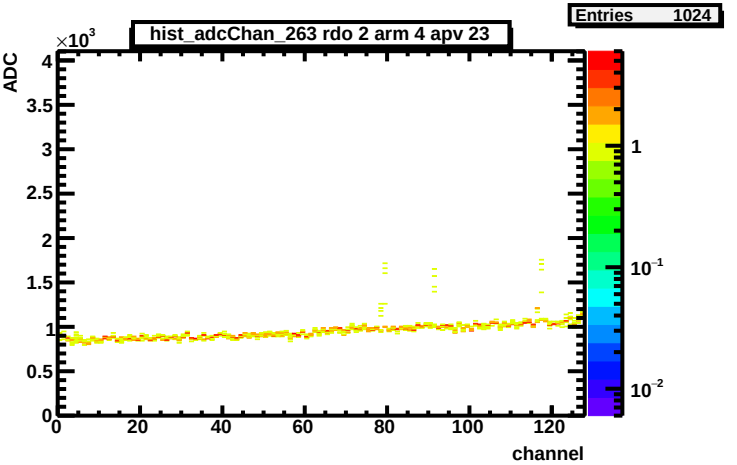

hist_adcChan_258 rdo 2 arm 4 apv 18
Entries 1024
ADC
channel

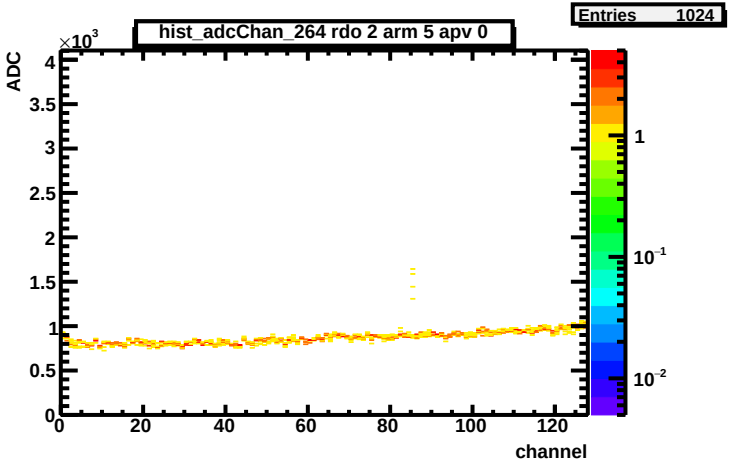

hist_adcChan_259 rdo 2 arm 4 apv 19
Entries 1024
ADC
channel

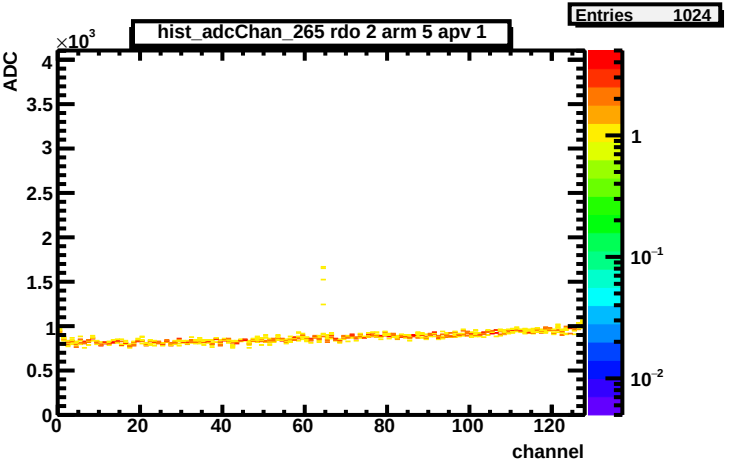

hist_adcChan_260 rdo 2 arm 4 apv 20
Entries 1024
ADC
channel

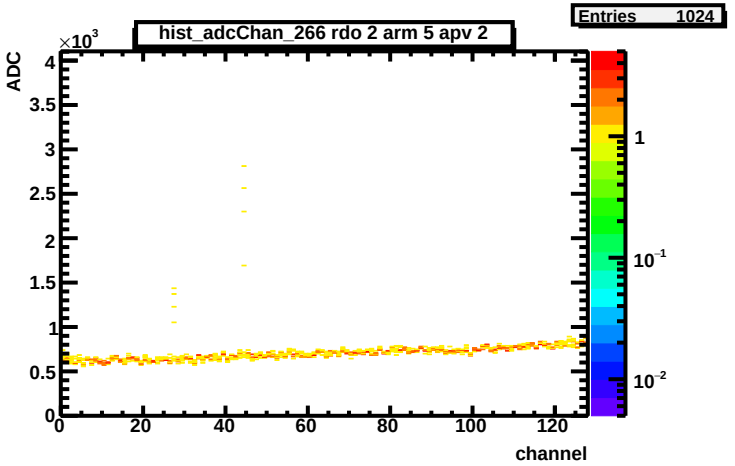

hist_adcChan_261 rdo 2 arm 4 apv 21
Entries 1024
ADC
channel

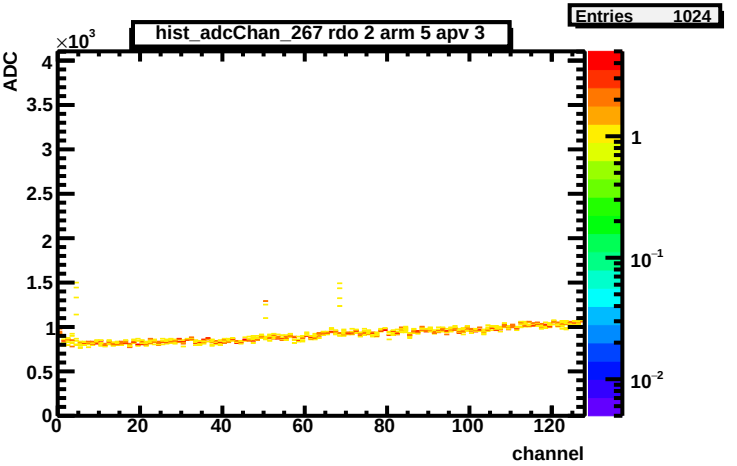

hist_adcChan_262 rdo 2 arm 4 apv 22
Entries 1024
ADC
channel

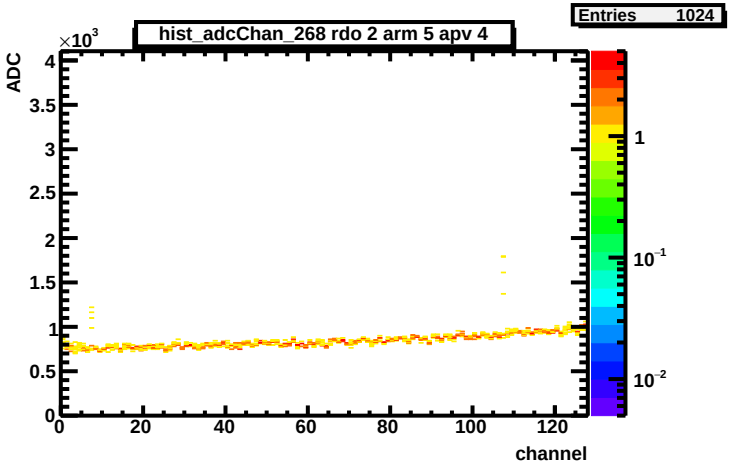

hist_adcChan_263 rdo 2 arm 4 apv 23
Entries 1024
ADC
channel

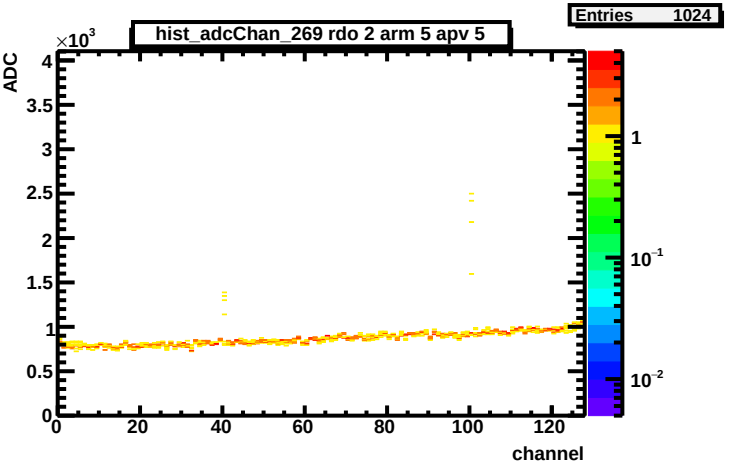

hist_adcChan_264 rdo 2 arm 5 apv 0
Entries 1024
ADC
×10^3
channel

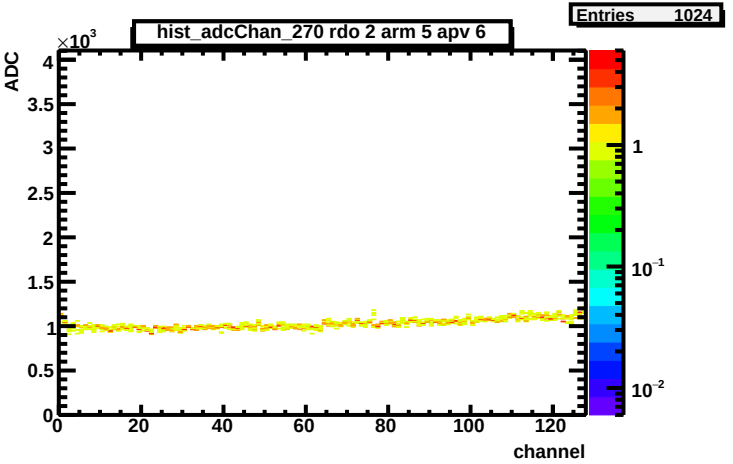

hist_adcChan_265 rdo 2 arm 5 apv 1
Entries 1024
ADC
×10^3
channel

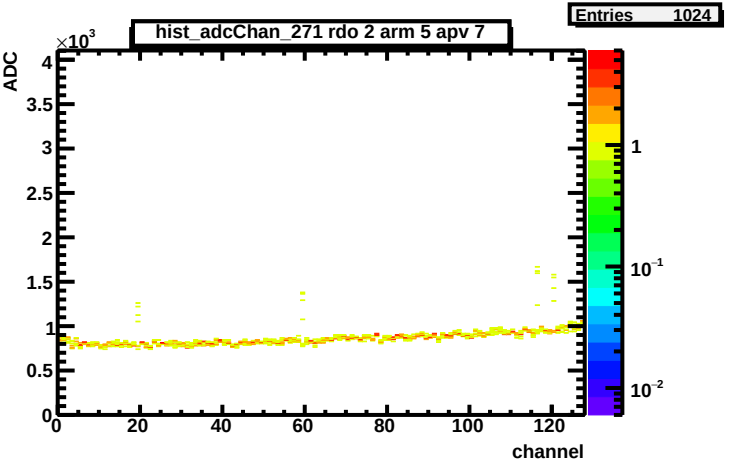

hist_adcChan_266 rdo 2 arm 5 apv 2
Entries 1024
ADC
×10^3
channel

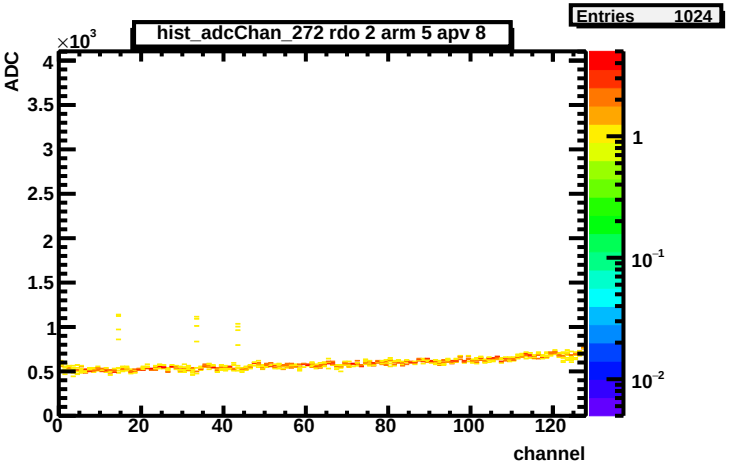

hist_adcChan_267 rdo 2 arm 5 apv 3
Entries 1024
ADC
×10^3
channel

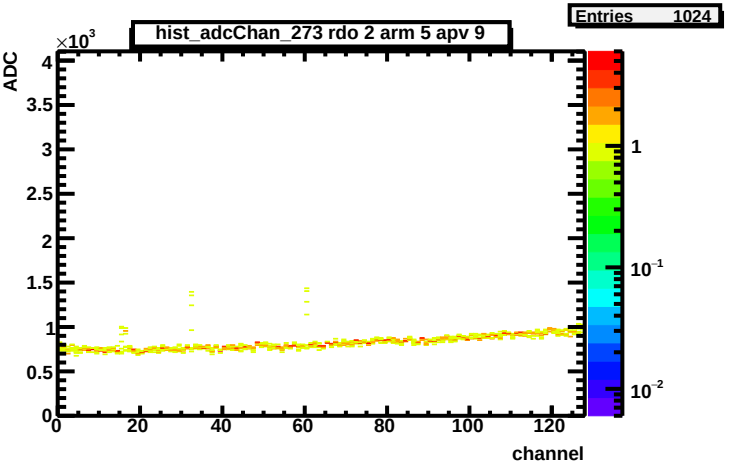

hist_adcChan_268 rdo 2 arm 5 apv 4
Entries 1024
ADC
×10^3
channel

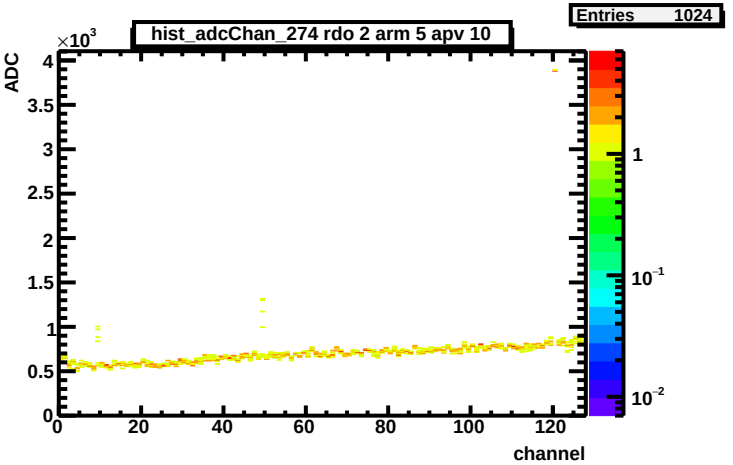

hist_adcChan_269 rdo 2 arm 5 apv 5
Entries 1024
ADC
×10^3
channel

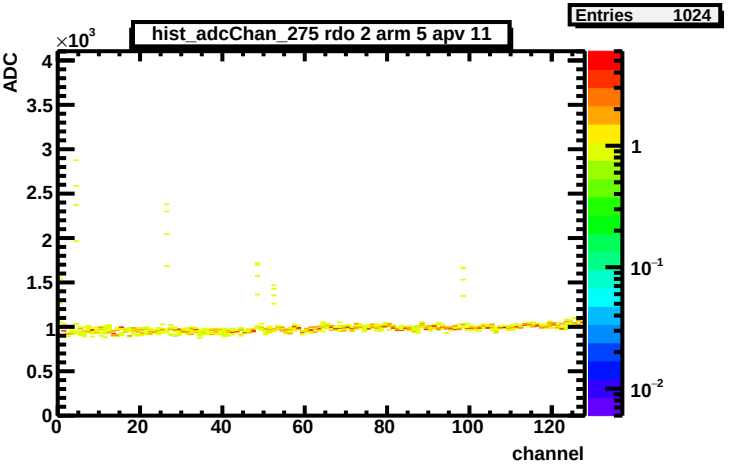

hist_adcChan_270 rdo 2 arm 5 apv 6
Entries 1024
ADC
channel

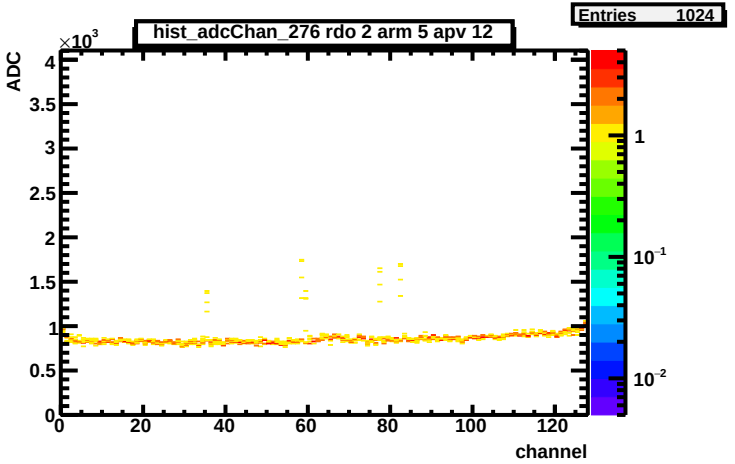

hist_adcChan_271 rdo 2 arm 5 apv 7
Entries 1024
ADC
channel

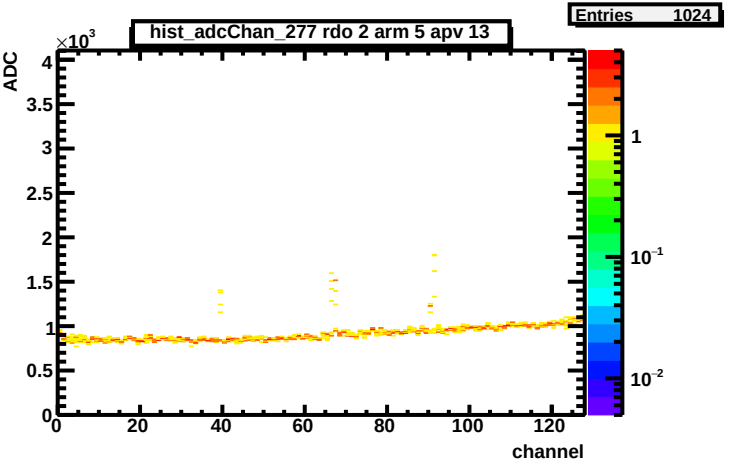

hist_adcChan_272 rdo 2 arm 5 apv 8
Entries 1024
ADC
channel

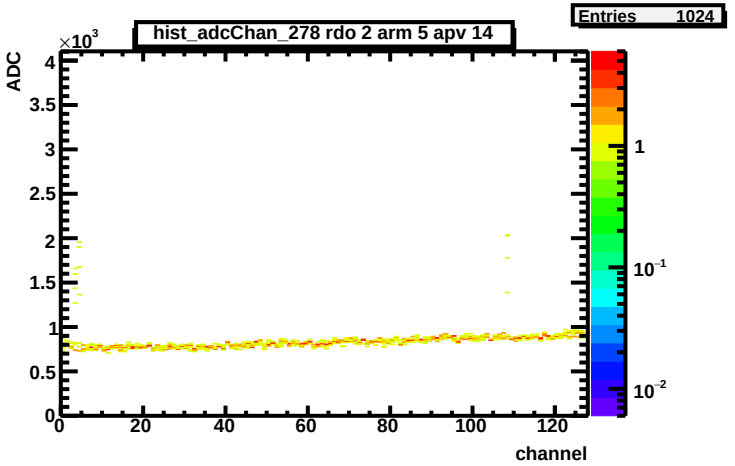

hist_adcChan_273 rdo 2 arm 5 apv 9
Entries 1024
ADC
channel

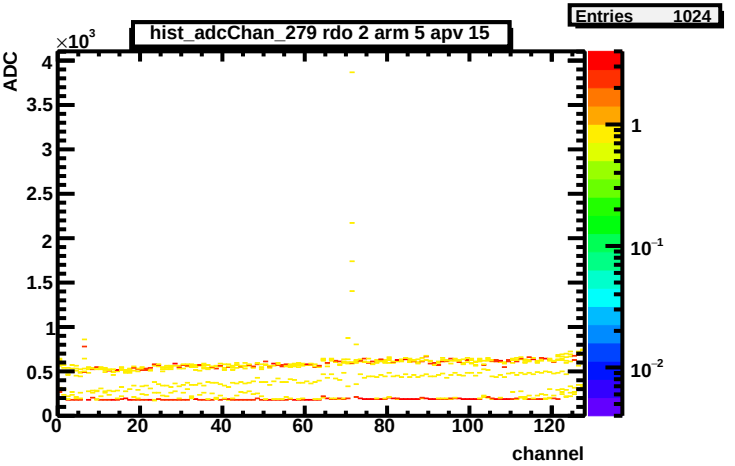

hist_adcChan_274 rdo 2 arm 5 apv 10
Entries 1024
ADC
channel

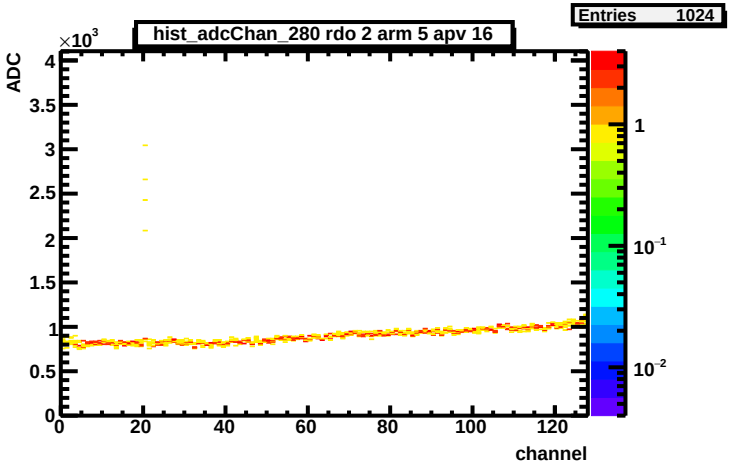

hist_adcChan_275 rdo 2 arm 5 apv 11
Entries 1024
ADC
channel

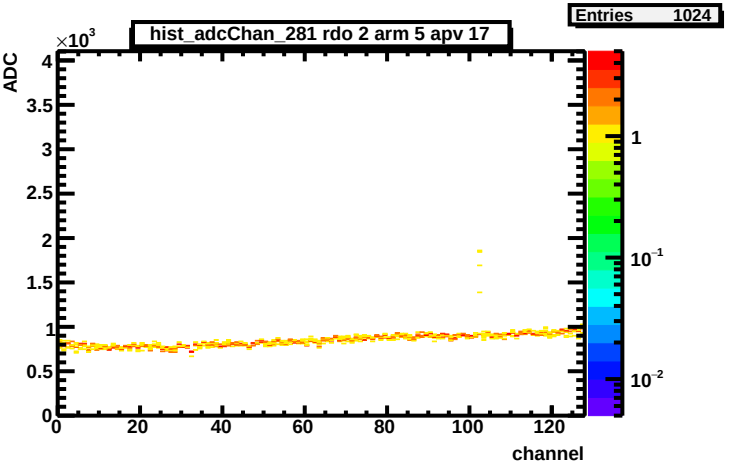

hist_adcChan_276 rdo 2 arm 5 apv 12
Entries 1024
ADC
channel

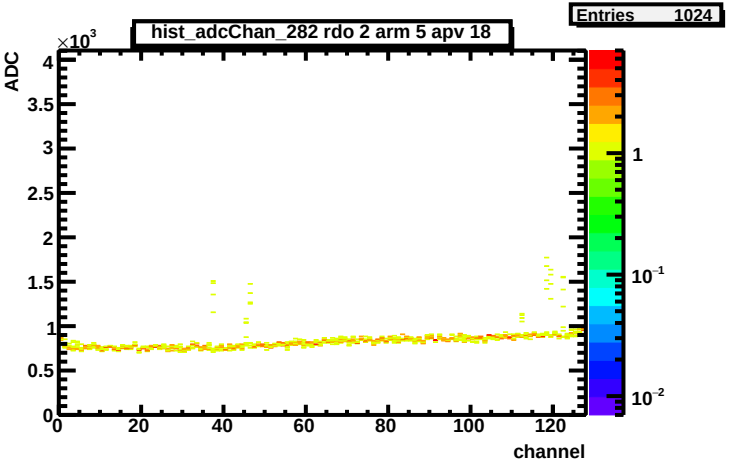

hist_adcChan_277 rdo 2 arm 5 apv 13
Entries 1024
ADC
channel

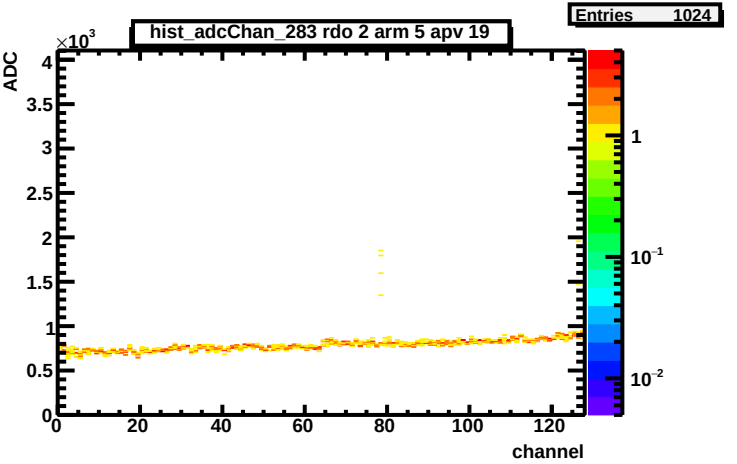

hist_adcChan_278 rdo 2 arm 5 apv 14
Entries 1024
ADC
channel

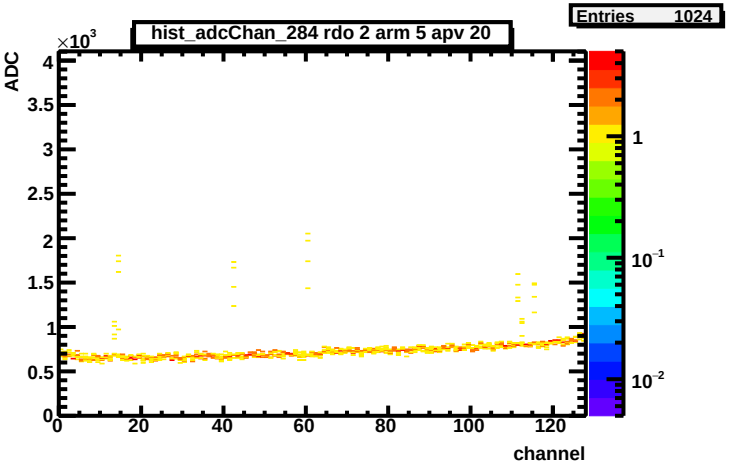

hist_adcChan_279 rdo 2 arm 5 apv 15
Entries 1024
ADC
channel

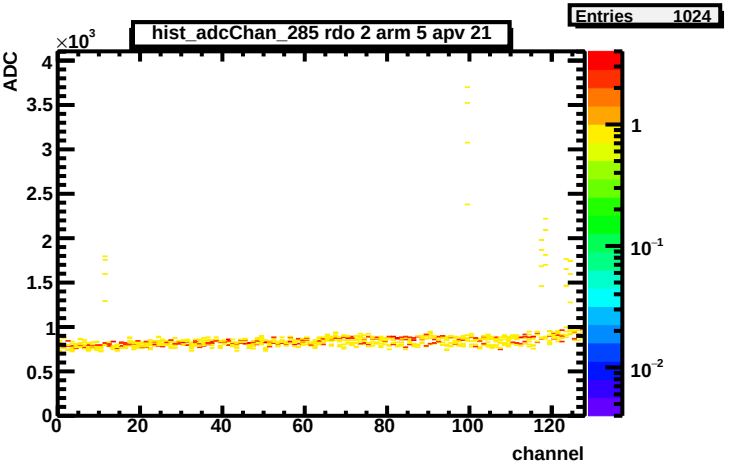

hist_adcChan_280 rdo 2 arm 5 apv 16
Entries 1024
ADC
channel

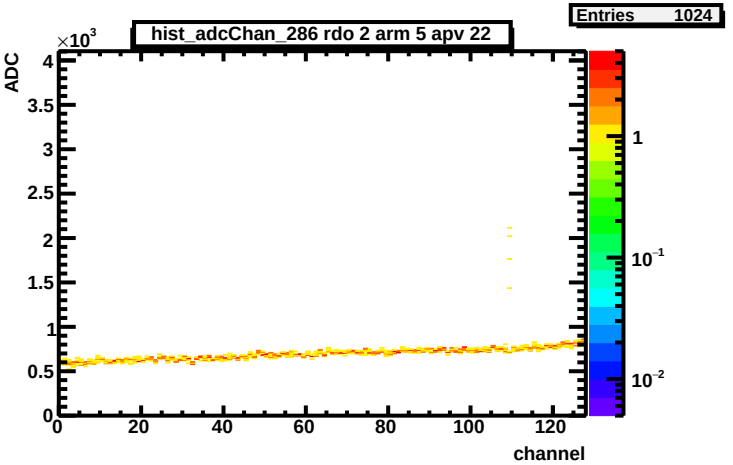

hist_adcChan_281 rdo 2 arm 5 apv 17
Entries 1024
ADC
channel

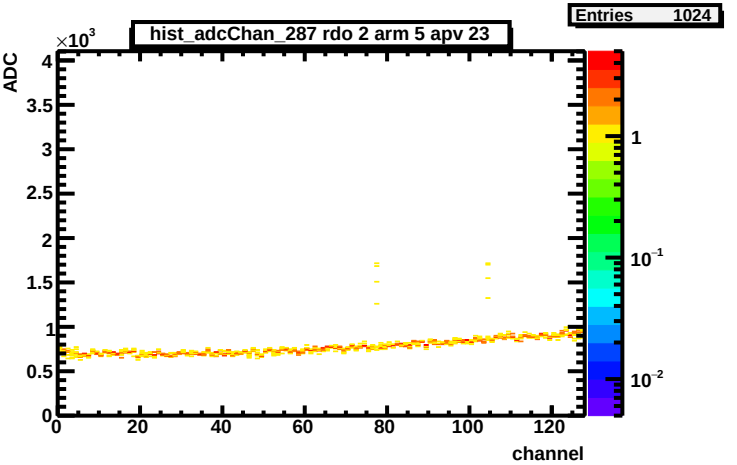

hist_adcChan_282 rdo 2 arm 5 apv 18
Entries 1024
ADC
×10^3
channel

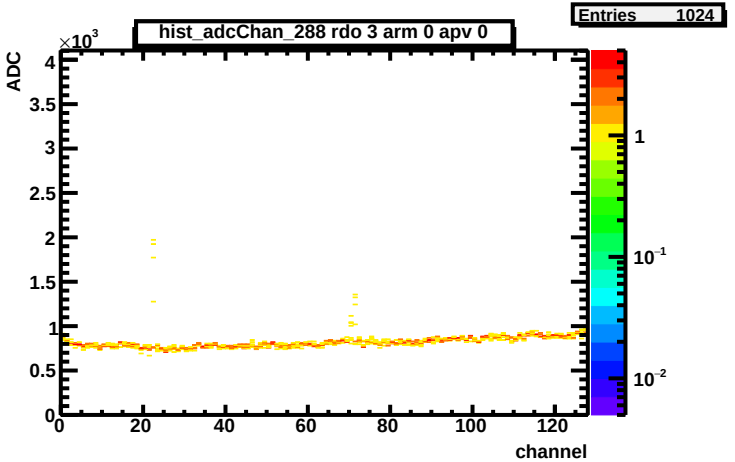

hist_adcChan_283 rdo 2 arm 5 apv 19
Entries 1024
ADC
×10^3
channel

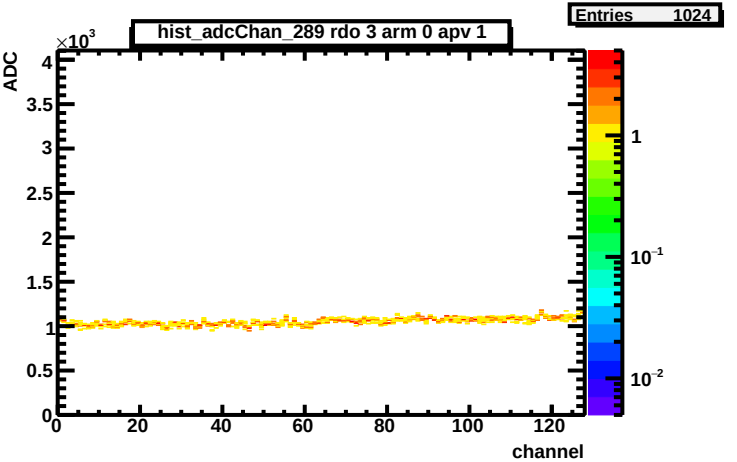

hist_adcChan_284 rdo 2 arm 5 apv 20
Entries 1024
ADC
×10^3
channel

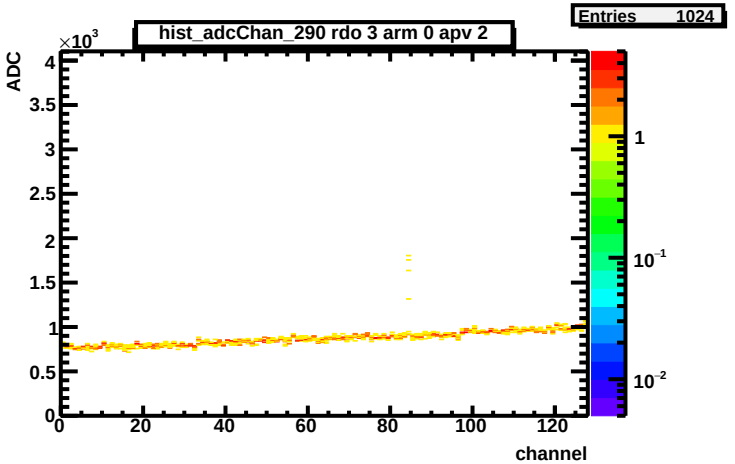

hist_adcChan_285 rdo 2 arm 5 apv 21
Entries 1024
ADC
×10^3
channel

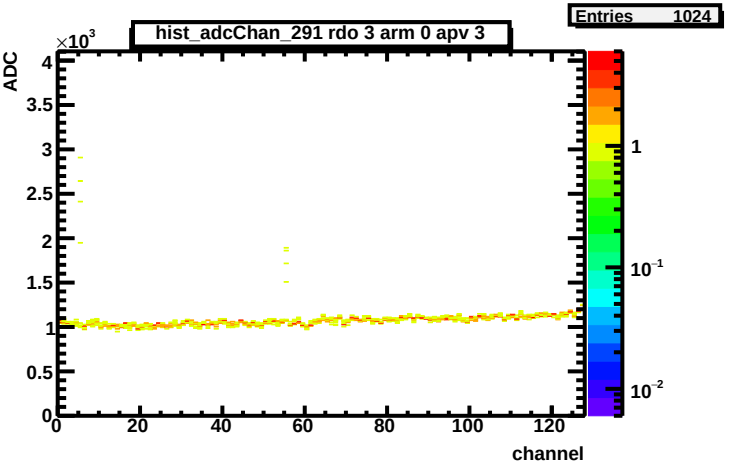

hist_adcChan_286 rdo 2 arm 5 apv 22
Entries 1024
ADC
×10^3
channel

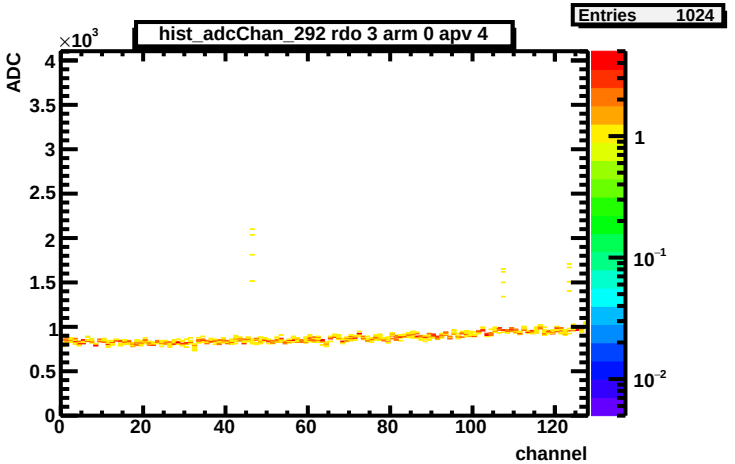

hist_adcChan_287 rdo 2 arm 5 apv 23
Entries 1024
ADC
×10^3
channel

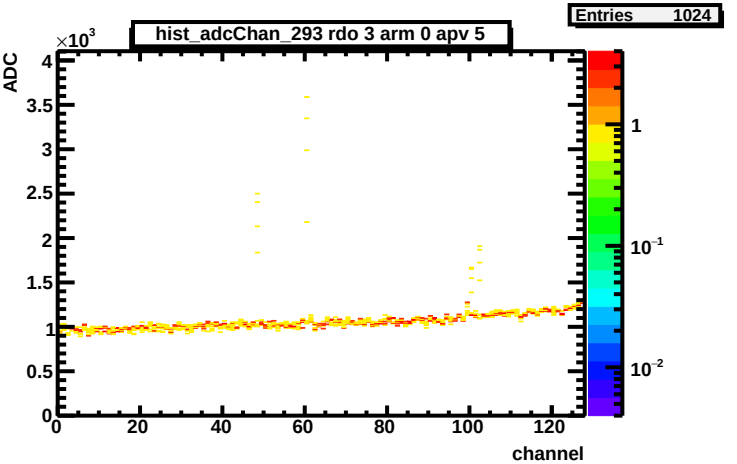

hist_adcChan_288 rdo 3 arm 0 apv 0
Entries 1024
ADC
channel

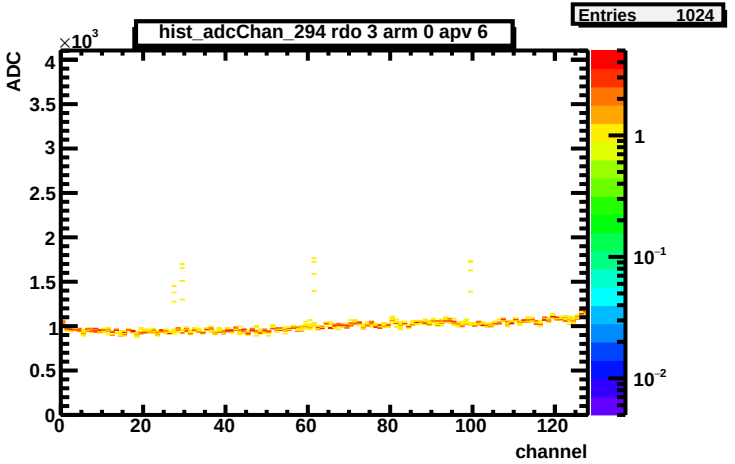

hist_adcChan_289 rdo 3 arm 0 apv 1
Entries 1024
ADC
channel

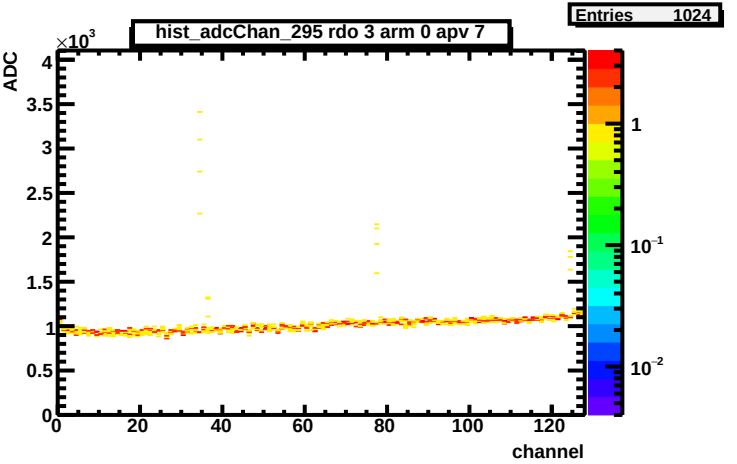

hist_adcChan_290 rdo 3 arm 0 apv 2
Entries 1024
ADC
channel

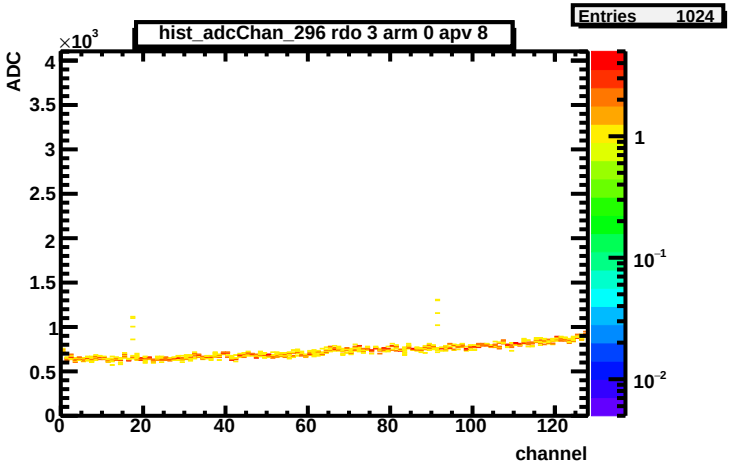

hist_adcChan_291 rdo 3 arm 0 apv 3
Entries 1024
ADC
channel

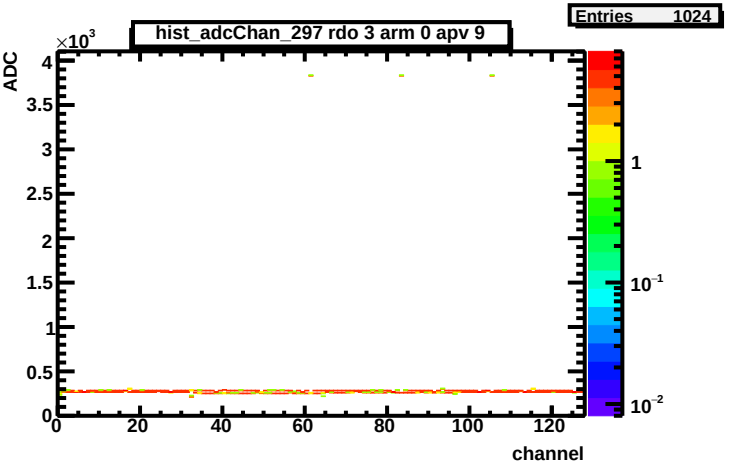

hist_adcChan_292 rdo 3 arm 0 apv 4
Entries 1024
ADC
channel

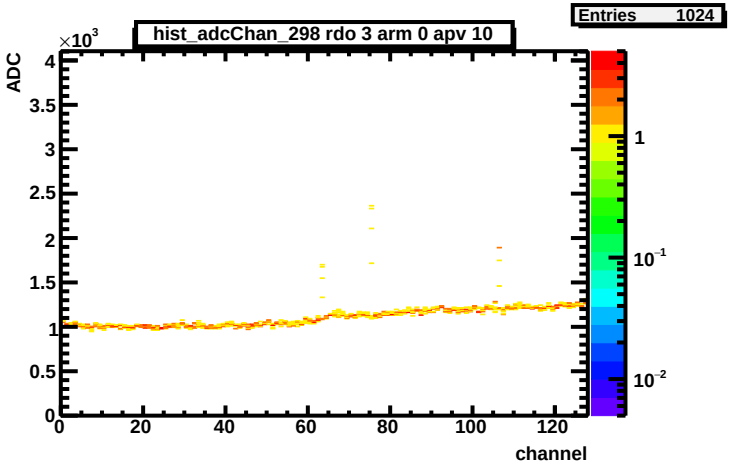

hist_adcChan_293 rdo 3 arm 0 apv 5
Entries 1024
ADC
channel

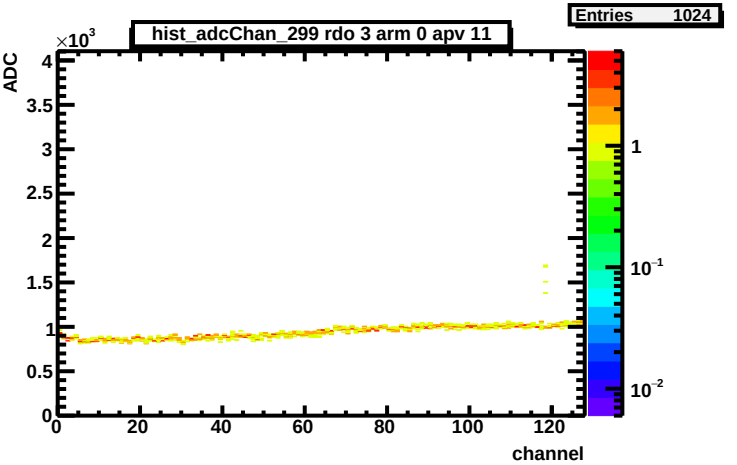

hist_adcChan_294 rdo 3 arm 0 apv 6
Entries 1024
ADC
channel

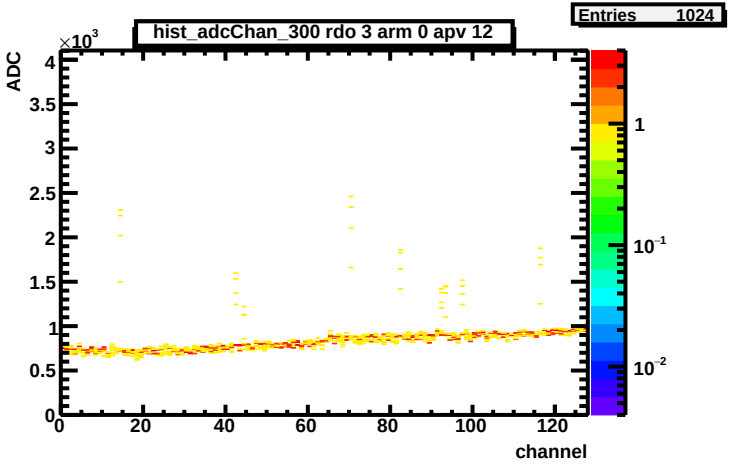

hist_adcChan_295 rdo 3 arm 0 apv 7
Entries 1024
ADC
channel

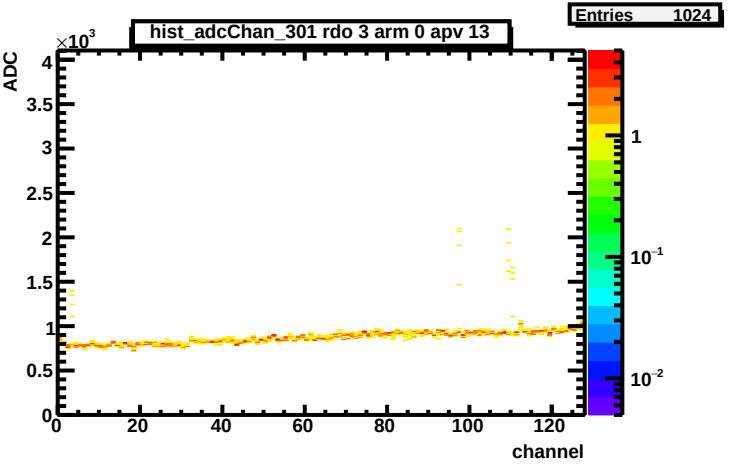

hist_adcChan_296 rdo 3 arm 0 apv 8
Entries 1024
ADC
channel

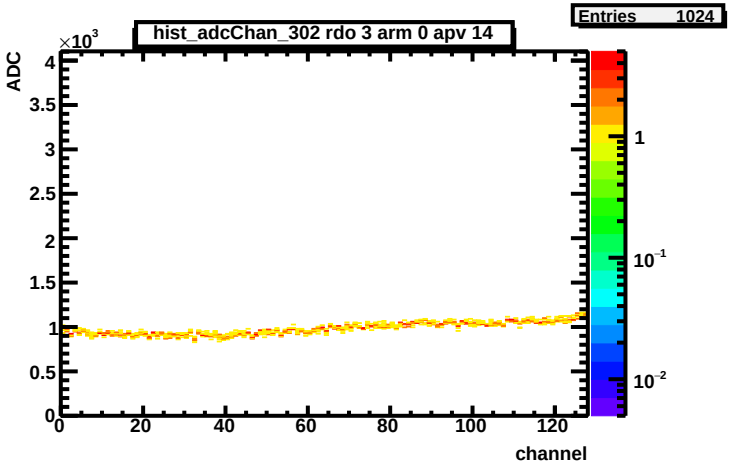

hist_adcChan_297 rdo 3 arm 0 apv 9
Entries 1024
ADC
channel

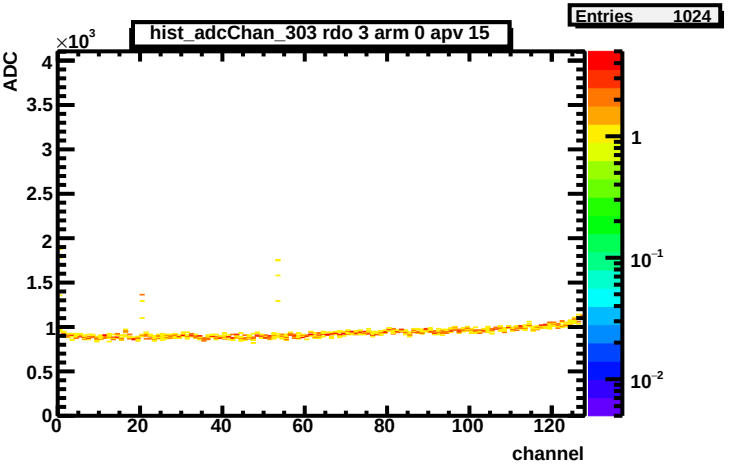

hist_adcChan_298 rdo 3 arm 0 apv 10
Entries 1024
ADC
channel

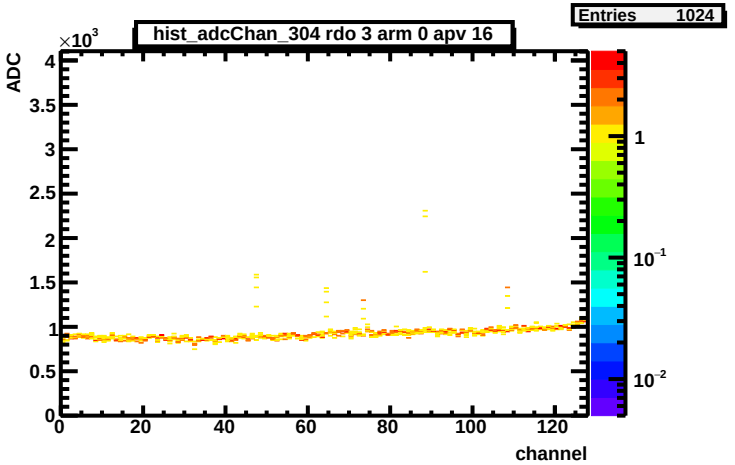

hist_adcChan_299 rdo 3 arm 0 apv 11
Entries 1024
ADC
channel

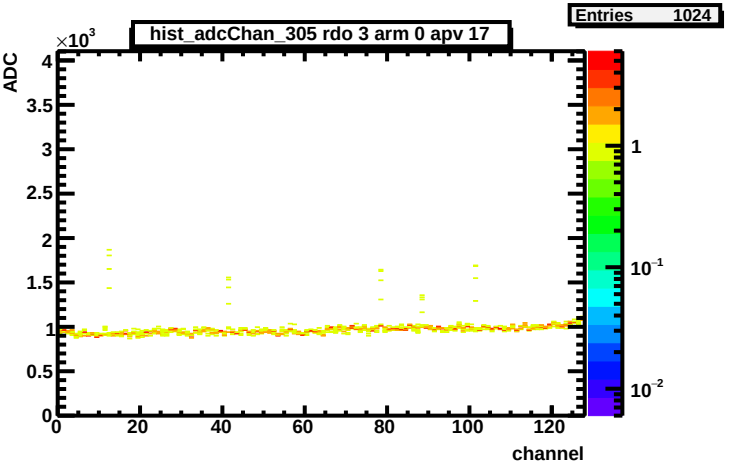

hist_adcChan_300 rdo 3 arm 0 apv 12
Entries 1024
ADC
×10^3
channel

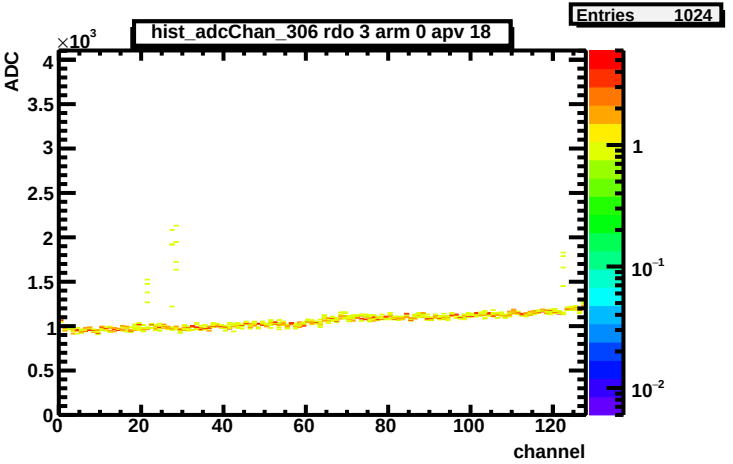

hist_adcChan_301 rdo 3 arm 0 apv 13
Entries 1024
ADC
×10^3
channel

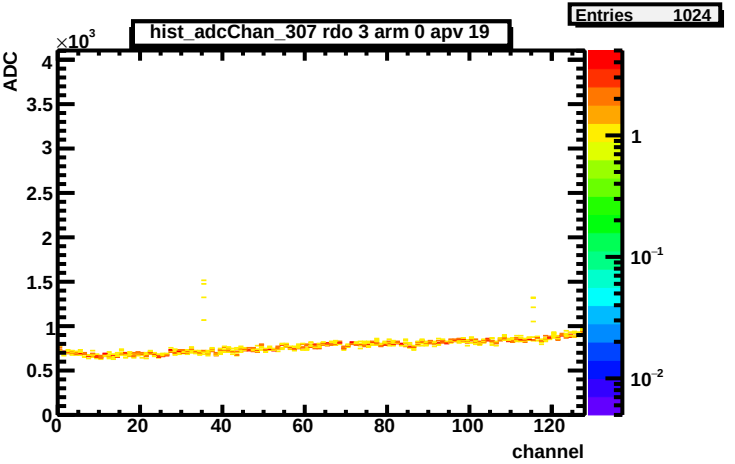

hist_adcChan_302 rdo 3 arm 0 apv 14
Entries 1024
ADC
×10^3
channel

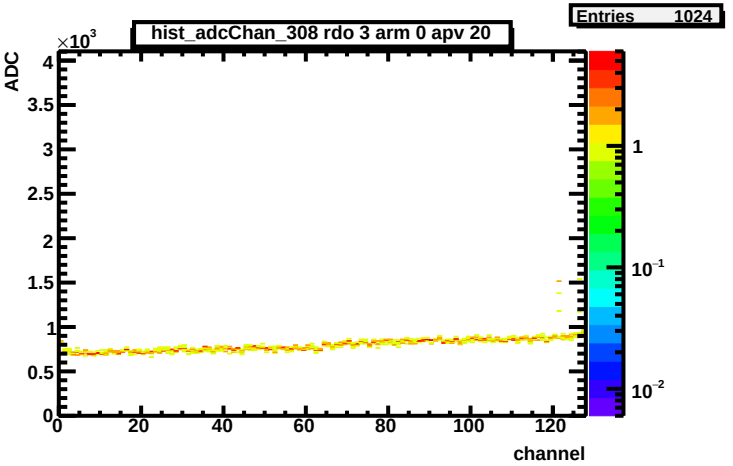

hist_adcChan_303 rdo 3 arm 0 apv 15
Entries 1024
ADC
×10^3
channel

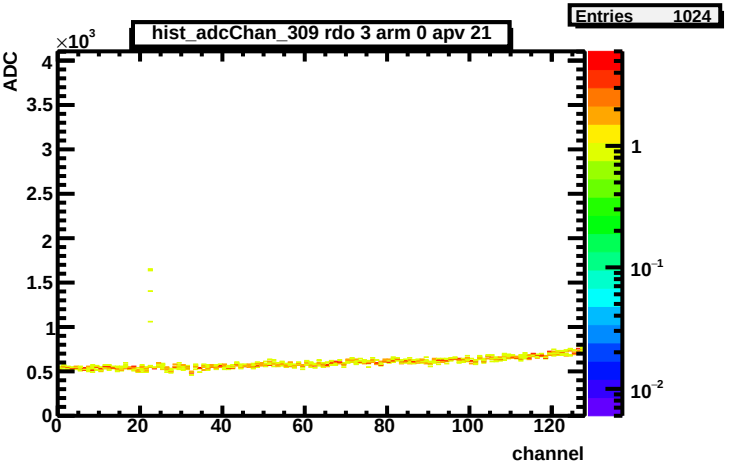

hist_adcChan_304 rdo 3 arm 0 apv 16
Entries 1024
ADC
×10^3
channel

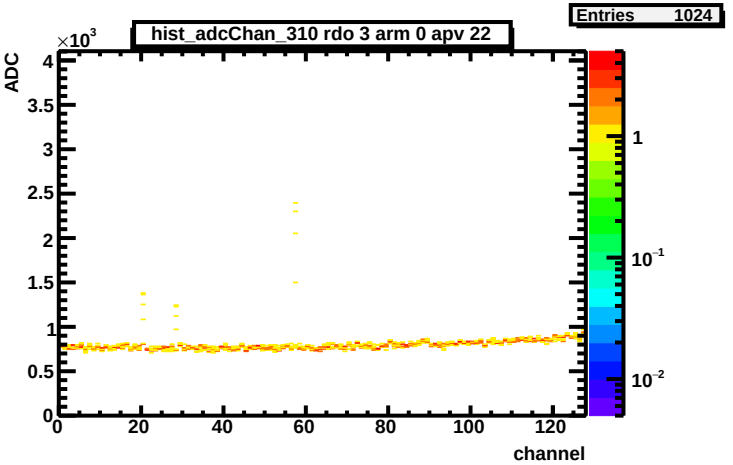

hist_adcChan_305 rdo 3 arm 0 apv 17
Entries 1024
ADC
×10^3
channel

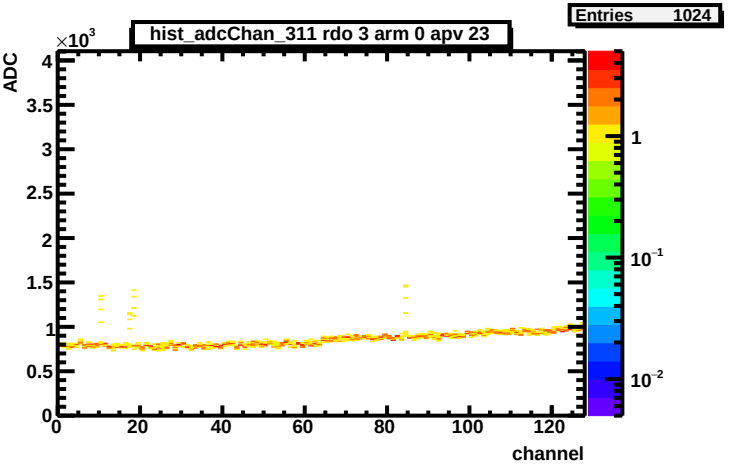

hist_adcChan_306 rdo 3 arm 0 apv 18
Entries 1024
ADC
channel

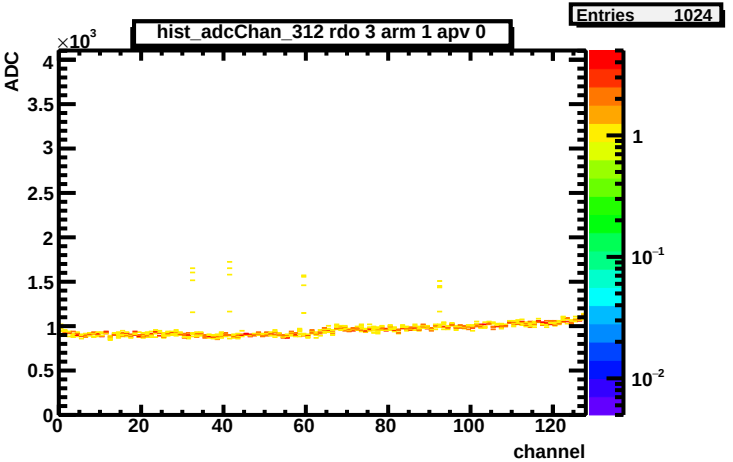

hist_adcChan_307 rdo 3 arm 0 apv 19
Entries 1024
ADC
channel

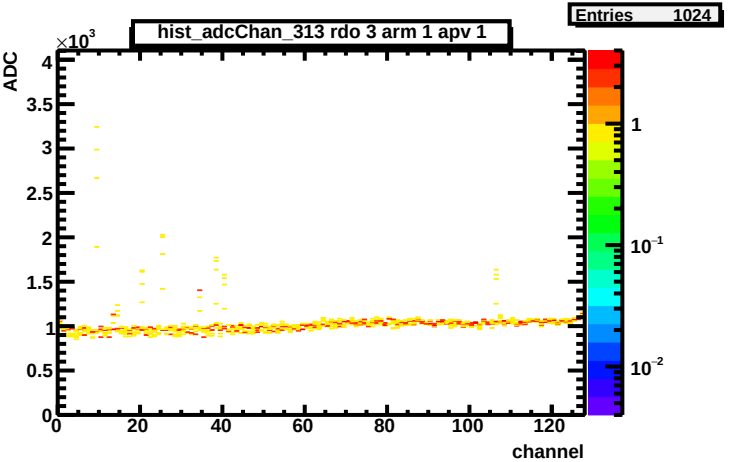

hist_adcChan_308 rdo 3 arm 0 apv 20
Entries 1024
ADC
channel

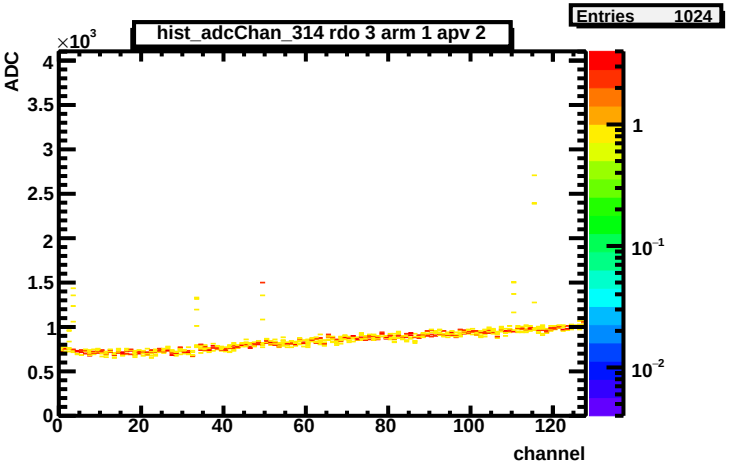

hist_adcChan_309 rdo 3 arm 0 apv 21
Entries 1024
ADC
channel

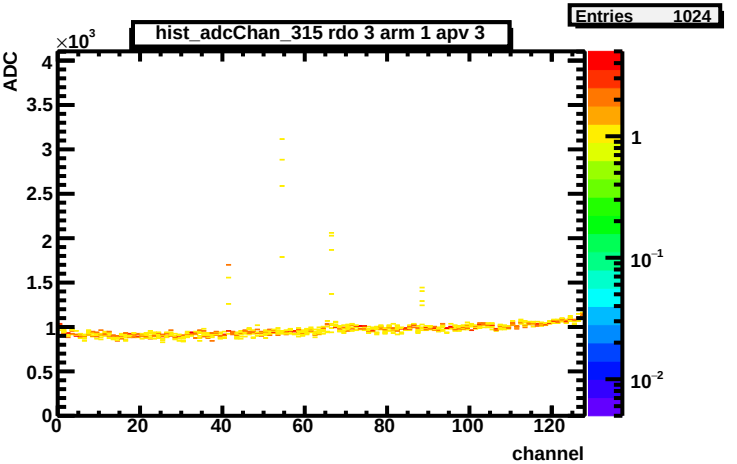

hist_adcChan_310 rdo 3 arm 0 apv 22
Entries 1024
ADC
channel

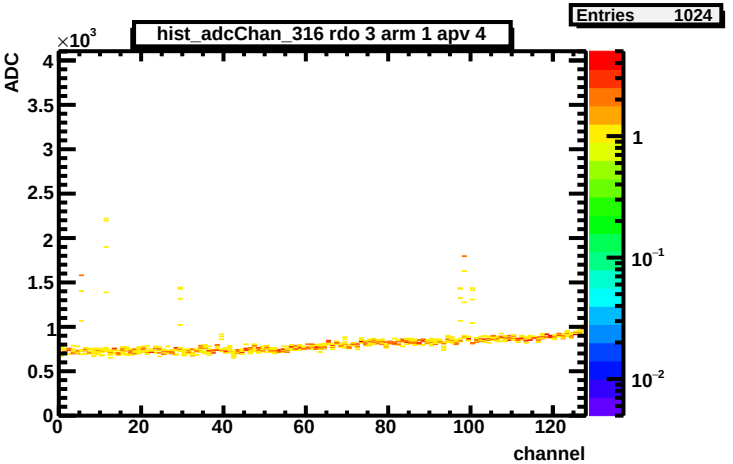

hist_adcChan_311 rdo 3 arm 0 apv 23
Entries 1024
ADC
channel

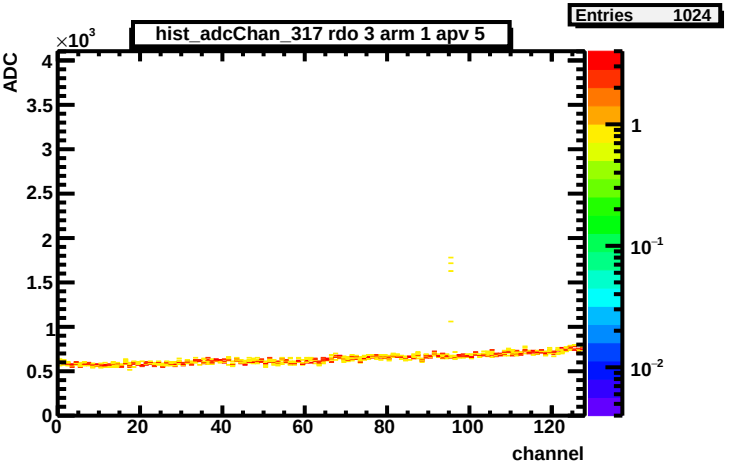

hist_adcChan_312 rdo 3 arm 1 apv 0
Entries 1024
ADC
×10^3
channel

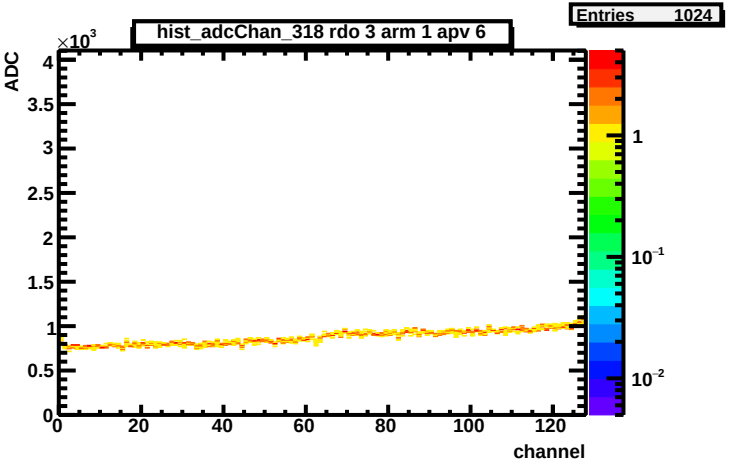

hist_adcChan_313 rdo 3 arm 1 apv 1
Entries 1024
ADC
×10^3
channel

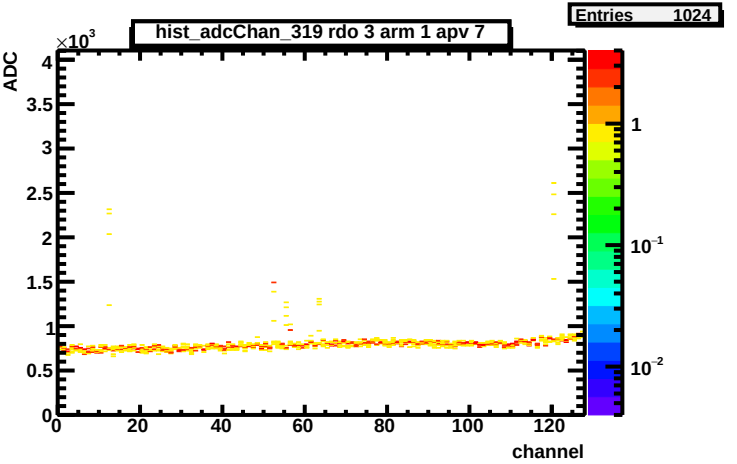

hist_adcChan_314 rdo 3 arm 1 apv 2
Entries 1024
ADC
×10^3
channel

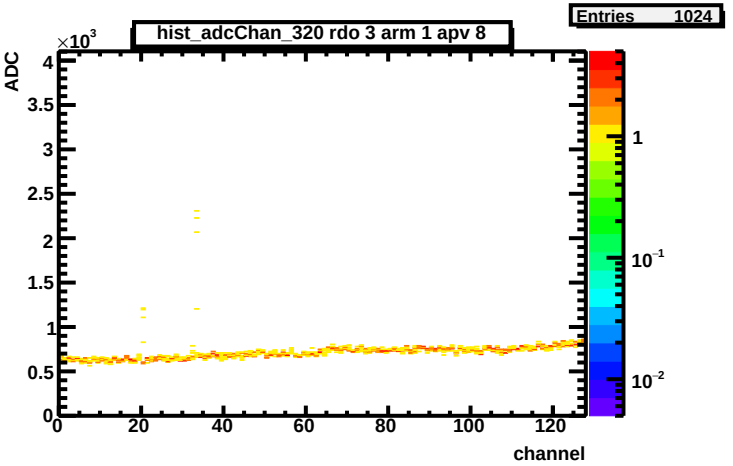

hist_adcChan_315 rdo 3 arm 1 apv 3
Entries 1024
ADC
×10^3
channel

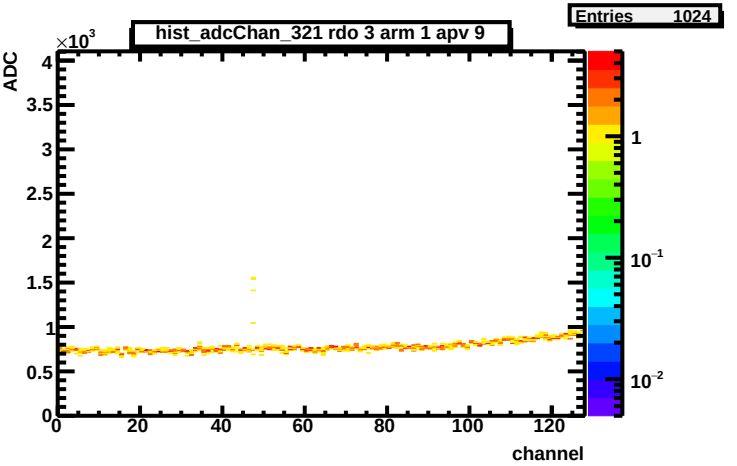

hist_adcChan_316 rdo 3 arm 1 apv 4
Entries 1024
ADC
×10^3
channel

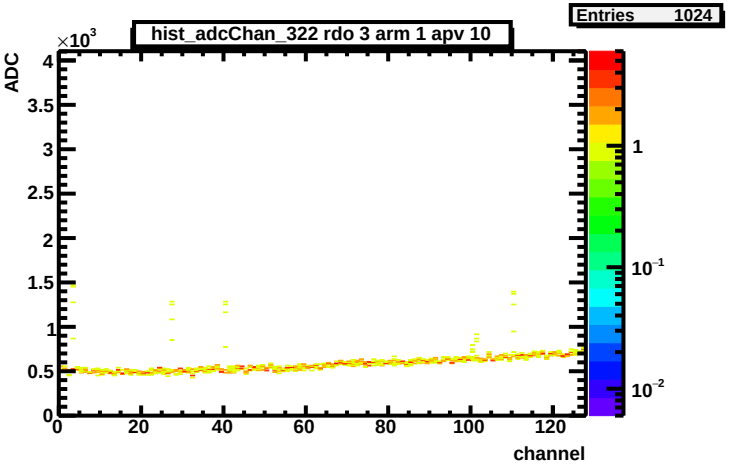

hist_adcChan_317 rdo 3 arm 1 apv 5
Entries 1024
ADC
×10^3
channel

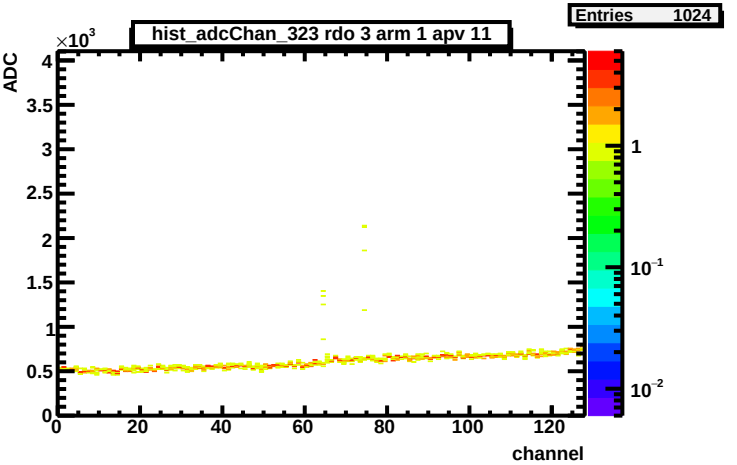

hist_adcChan_318 rdo 3 arm 1 apv 6
Entries 1024
ADC
×10³
channel

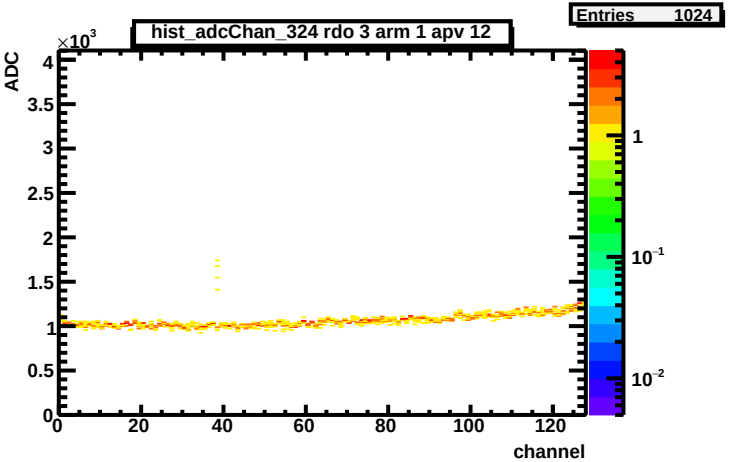

hist_adcChan_319 rdo 3 arm 1 apv 7
Entries 1024
ADC
×10³
channel

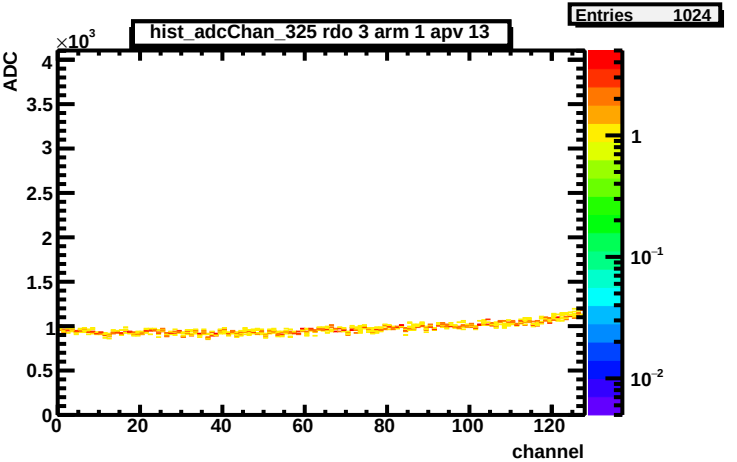

hist_adcChan_320 rdo 3 arm 1 apv 8
Entries 1024
ADC
×10³
channel

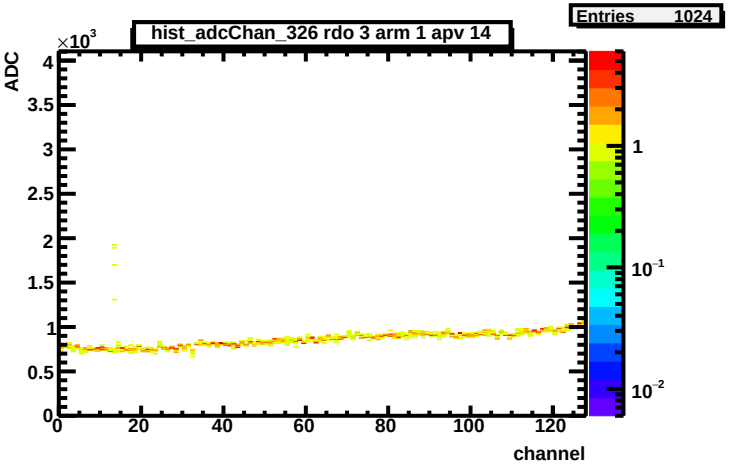

hist_adcChan_321 rdo 3 arm 1 apv 9
Entries 1024
ADC
×10³
channel

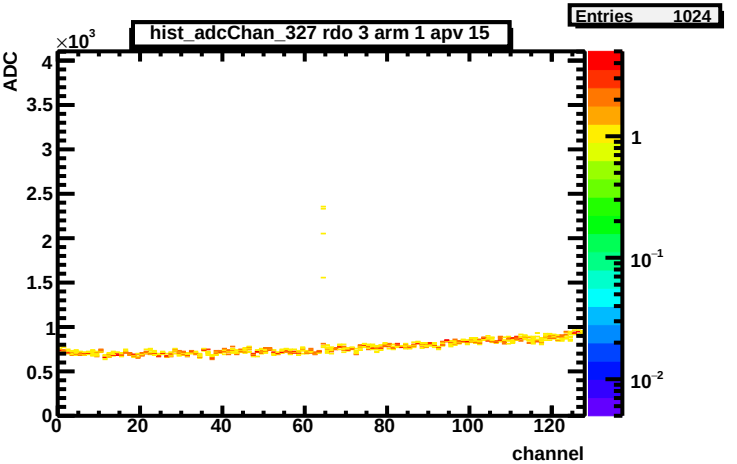

hist_adcChan_322 rdo 3 arm 1 apv 10
Entries 1024
ADC
×10³
channel

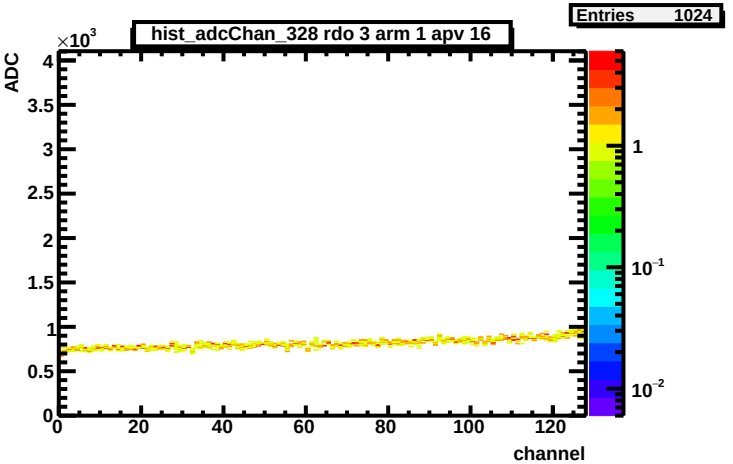

hist_adcChan_323 rdo 3 arm 1 apv 11
Entries 1024
ADC
×10³
channel

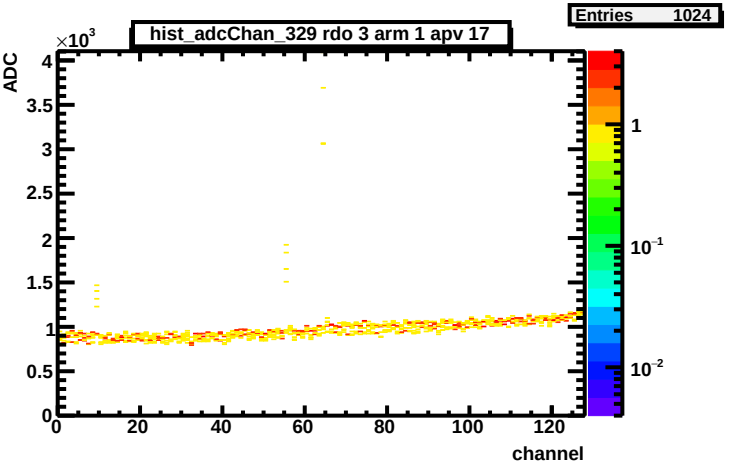

hist_adcChan_324 rdo 3 arm 1 apv 12
Entries 1024
ADC
×10^3
channel

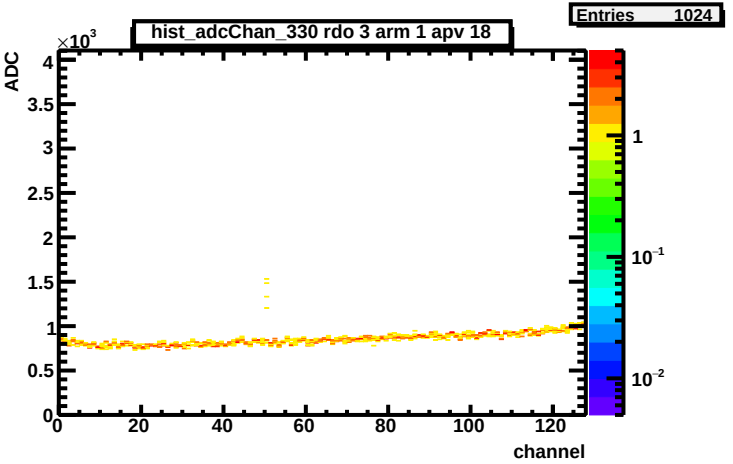

hist_adcChan_325 rdo 3 arm 1 apv 13
Entries 1024
ADC
×10^3
channel

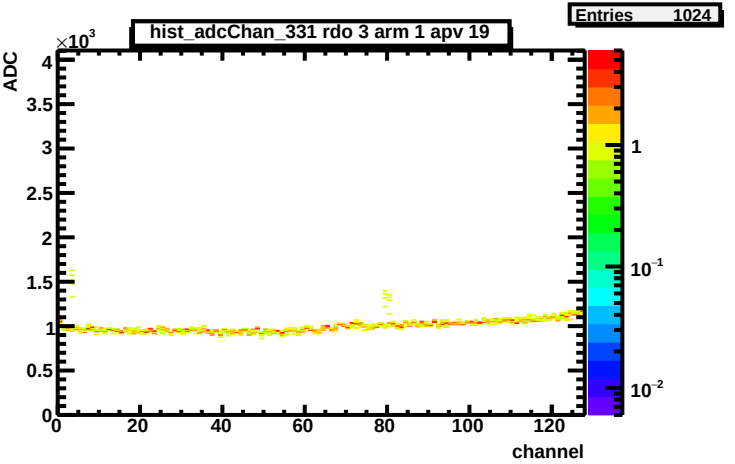

hist_adcChan_326 rdo 3 arm 1 apv 14
Entries 1024
ADC
×10^3
channel

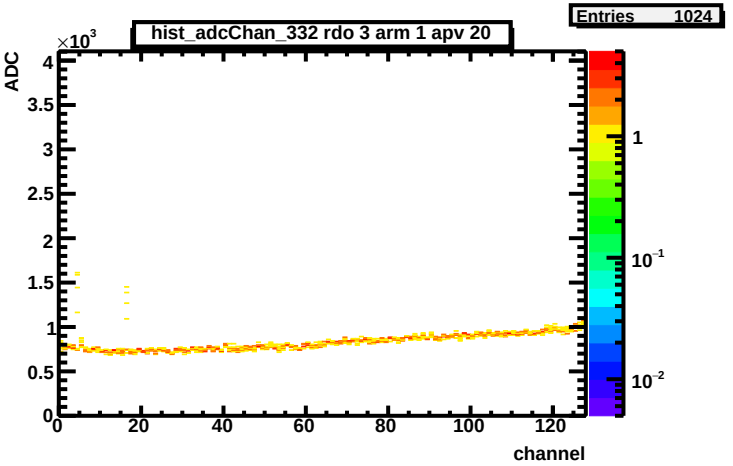

hist_adcChan_327 rdo 3 arm 1 apv 15
Entries 1024
ADC
×10^3
channel

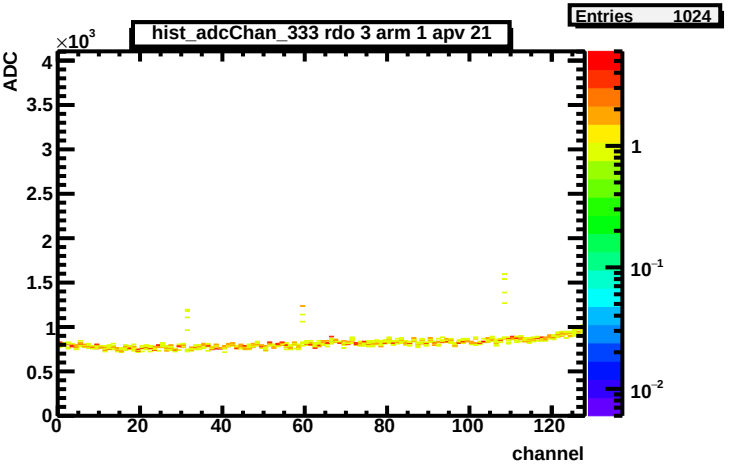

hist_adcChan_328 rdo 3 arm 1 apv 16
Entries 1024
ADC
×10^3
channel

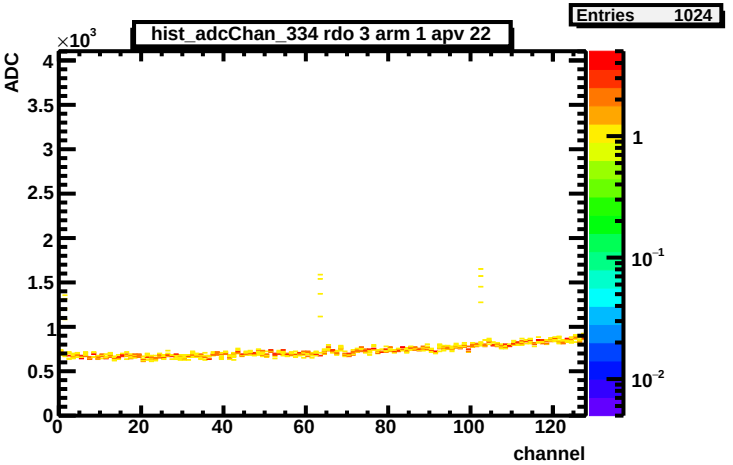

hist_adcChan_329 rdo 3 arm 1 apv 17
Entries 1024
ADC
×10^3
channel

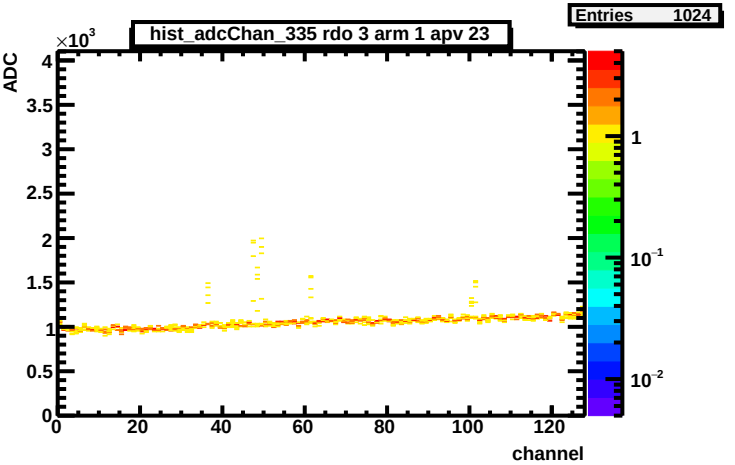

hist_adcChan_330 rdo 3 arm 1 apv 18
Entries 1024
ADC
channel

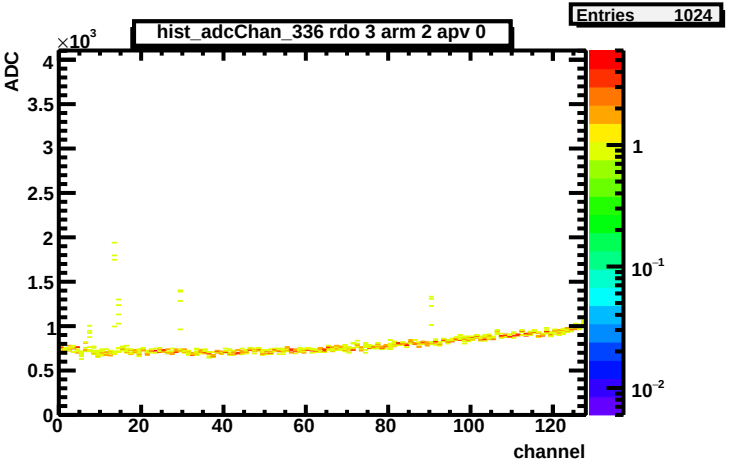

hist_adcChan_331 rdo 3 arm 1 apv 19
Entries 1024
ADC
channel

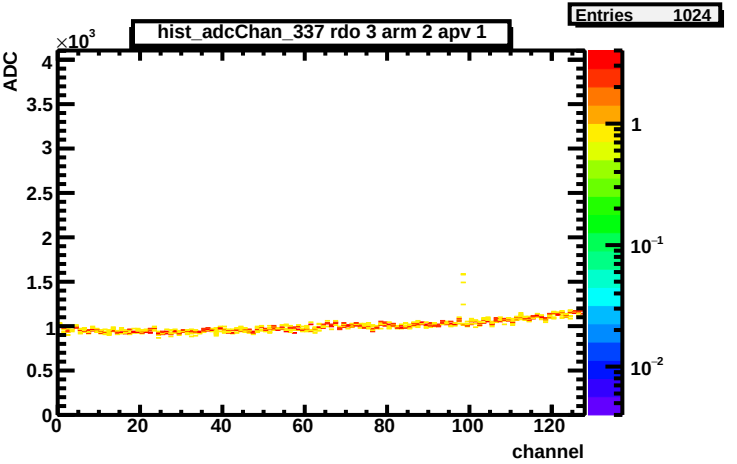

hist_adcChan_332 rdo 3 arm 1 apv 20
Entries 1024
ADC
channel

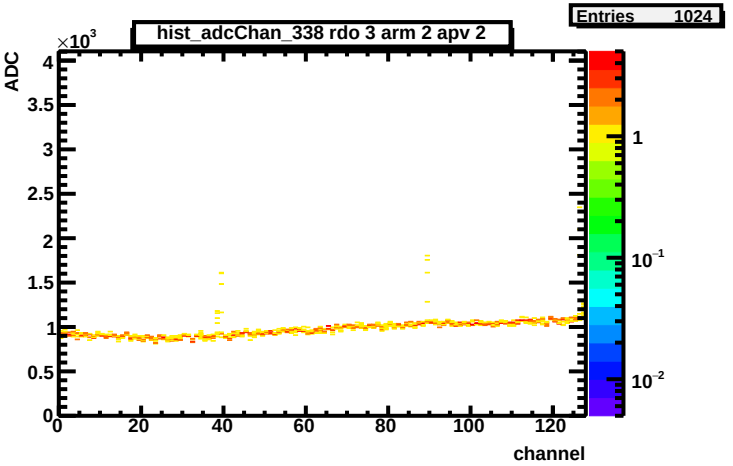

hist_adcChan_333 rdo 3 arm 1 apv 21
Entries 1024
ADC
channel

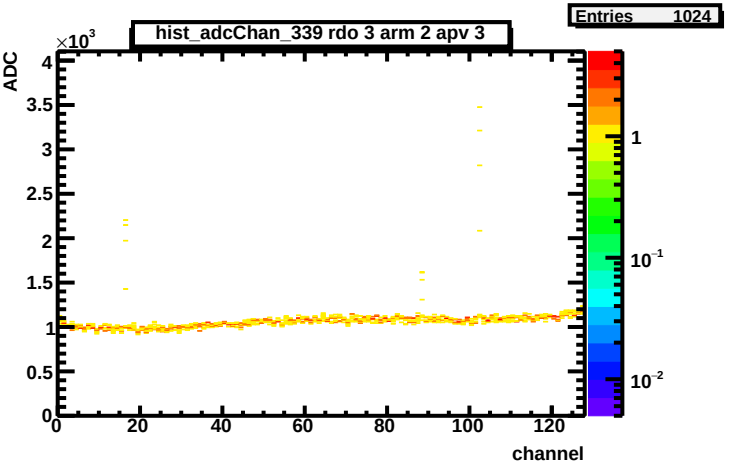

hist_adcChan_334 rdo 3 arm 1 apv 22
Entries 1024
ADC
channel

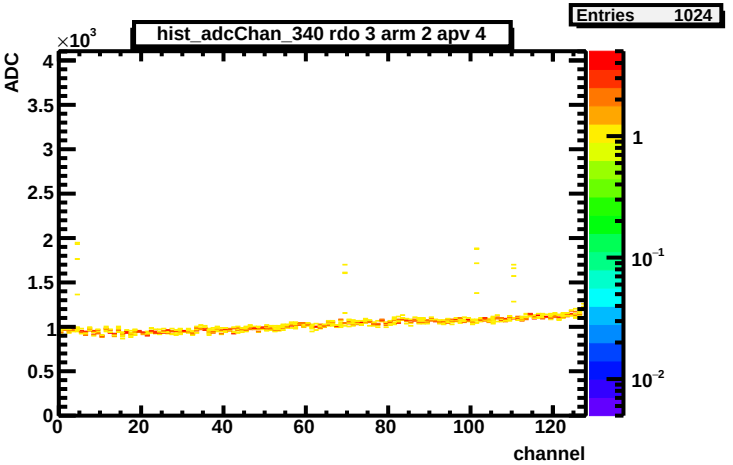

hist_adcChan_335 rdo 3 arm 1 apv 23
Entries 1024
ADC
channel

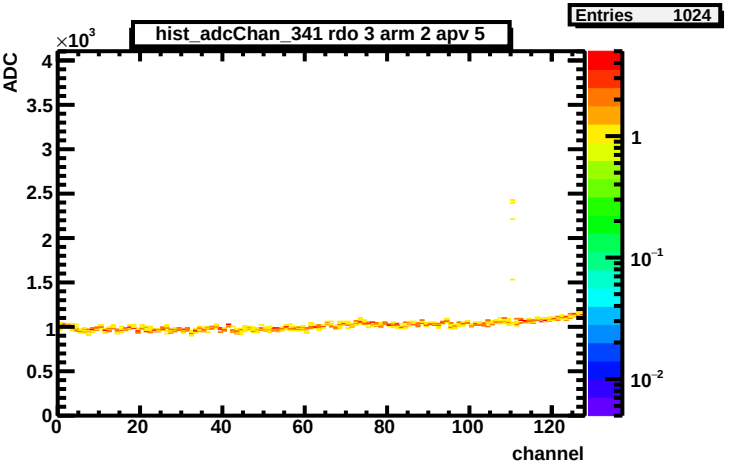

hist_adcChan_336 rdo 3 arm 2 apv 0
Entries 1024
ADC
channel

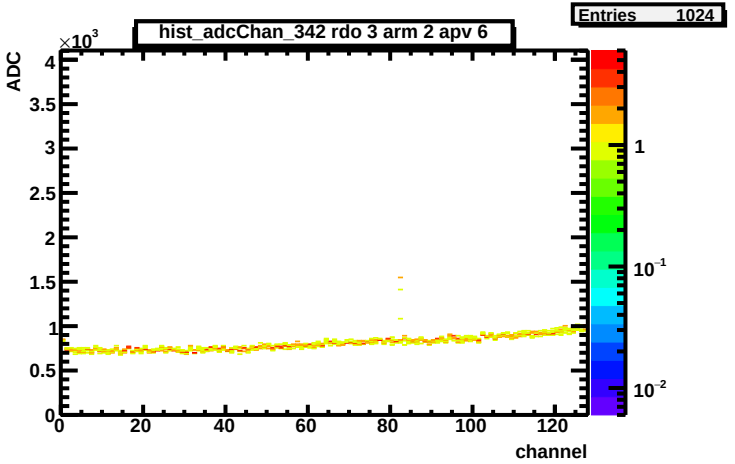

hist_adcChan_337 rdo 3 arm 2 apv 1
Entries 1024
ADC
channel

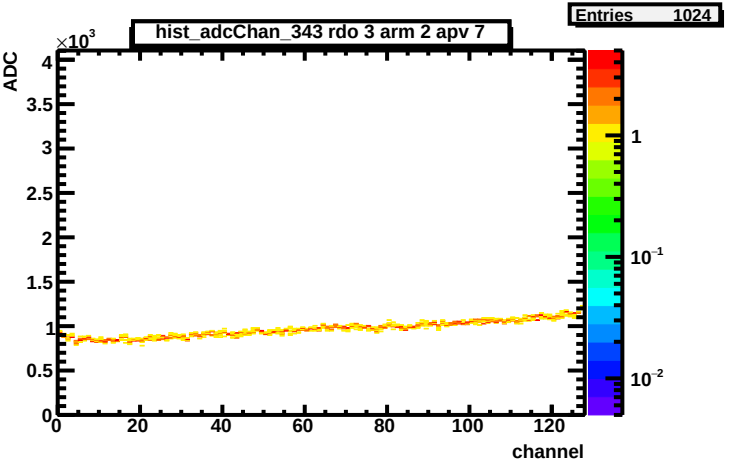

hist_adcChan_338 rdo 3 arm 2 apv 2
Entries 1024
ADC
channel

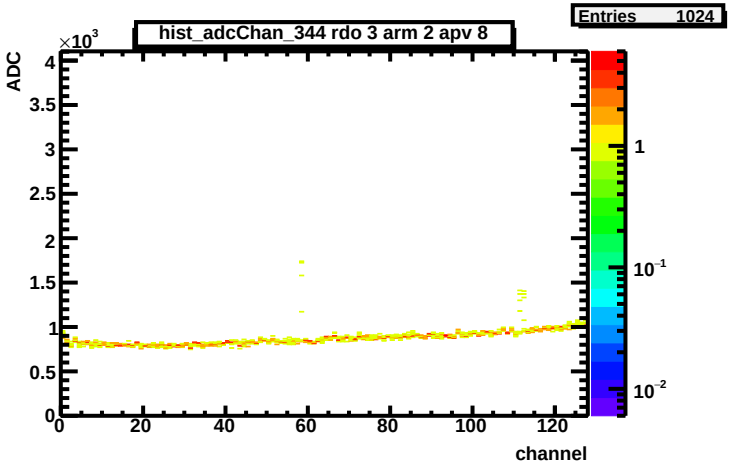

hist_adcChan_339 rdo 3 arm 2 apv 3
Entries 1024
ADC
channel

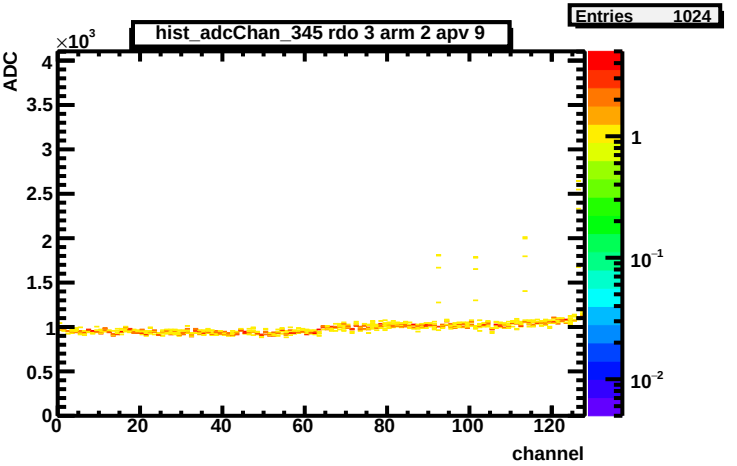

hist_adcChan_340 rdo 3 arm 2 apv 4
Entries 1024
ADC
channel

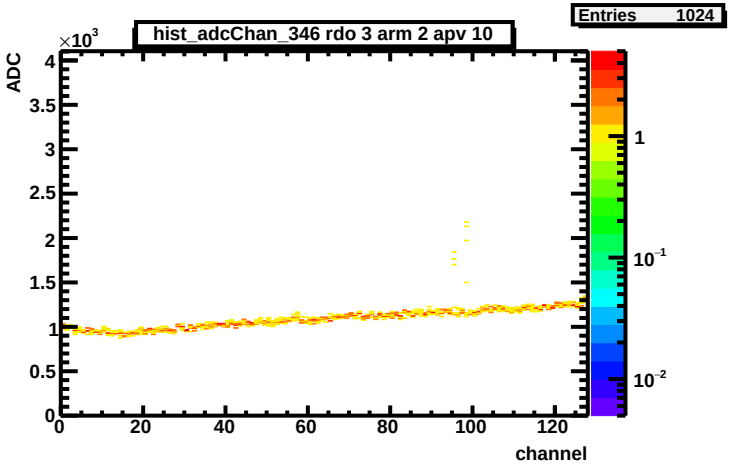

hist_adcChan_341 rdo 3 arm 2 apv 5
Entries 1024
ADC
channel

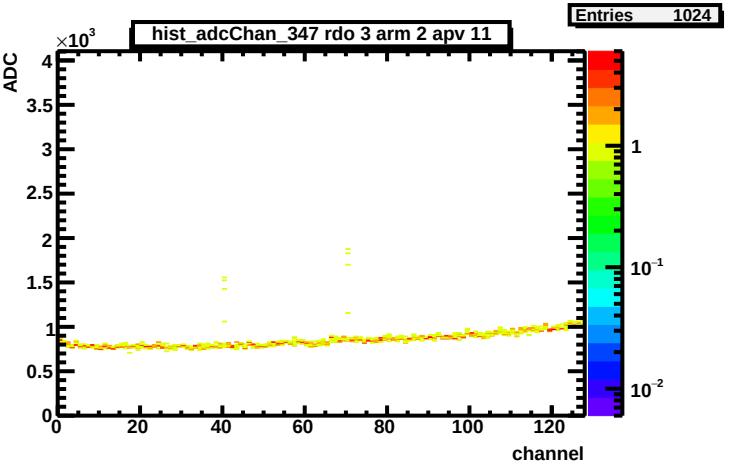

hist_adcChan_342 rdo 3 arm 2 apv 6
Entries 1024
ADC
channel

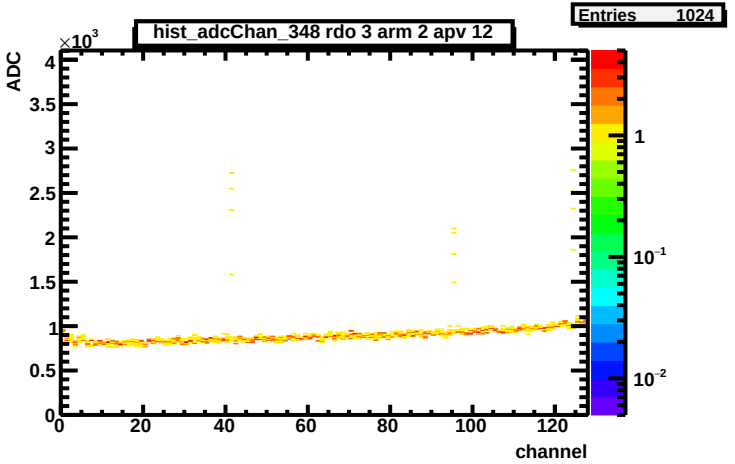

hist_adcChan_343 rdo 3 arm 2 apv 7
Entries 1024
ADC
channel

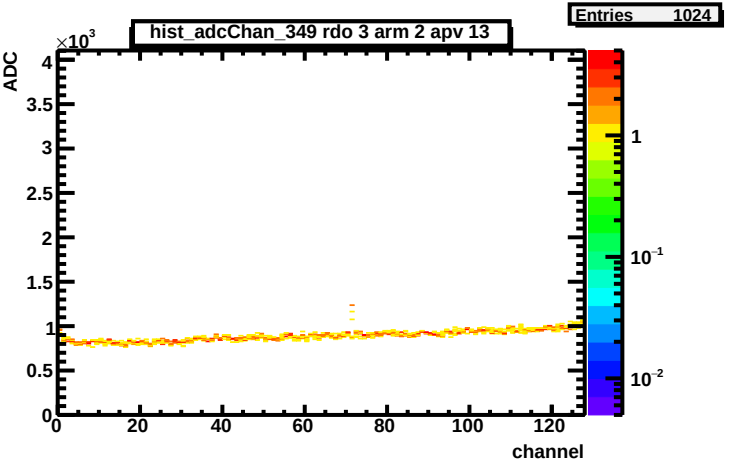

hist_adcChan_344 rdo 3 arm 2 apv 8
Entries 1024
ADC
channel

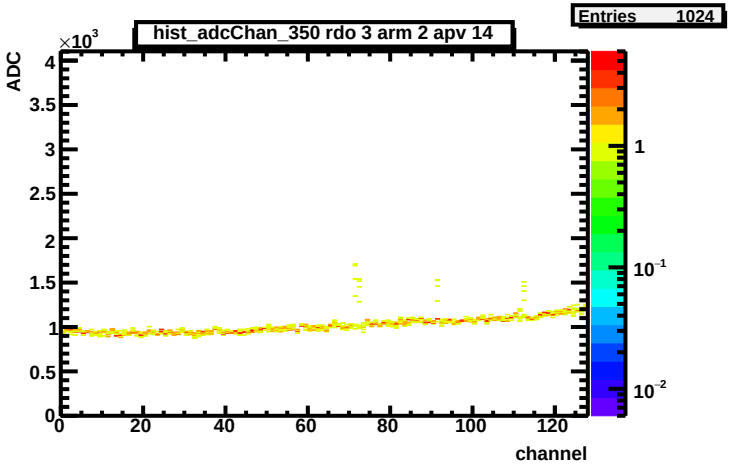

hist_adcChan_345 rdo 3 arm 2 apv 9
Entries 1024
ADC
channel

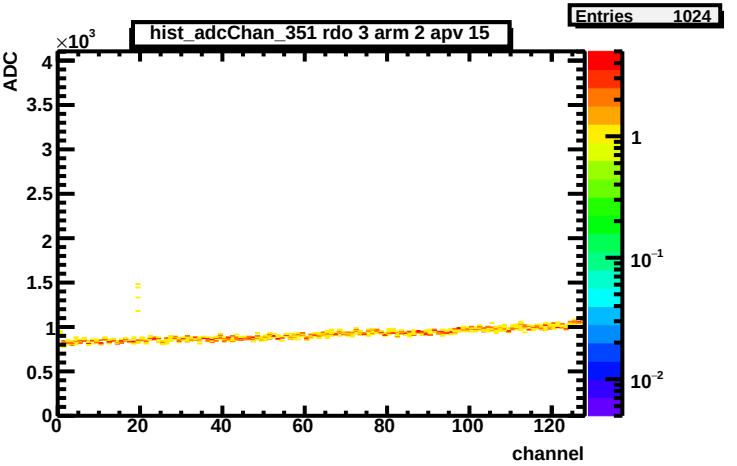

hist_adcChan_346 rdo 3 arm 2 apv 10
Entries 1024
ADC
channel

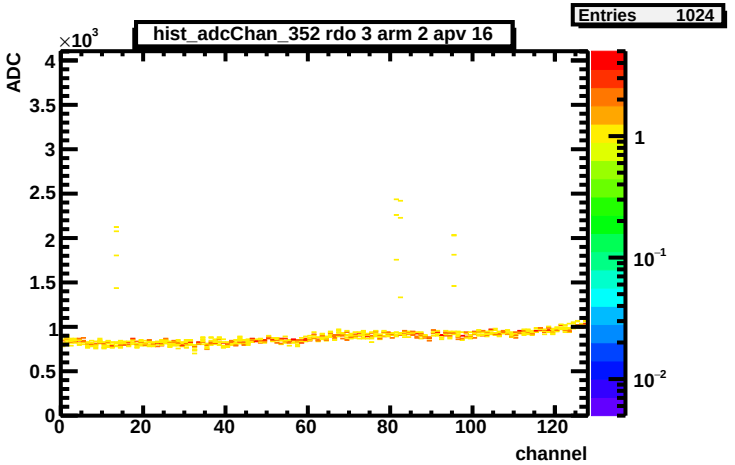

hist_adcChan_347 rdo 3 arm 2 apv 11
Entries 1024
ADC
channel

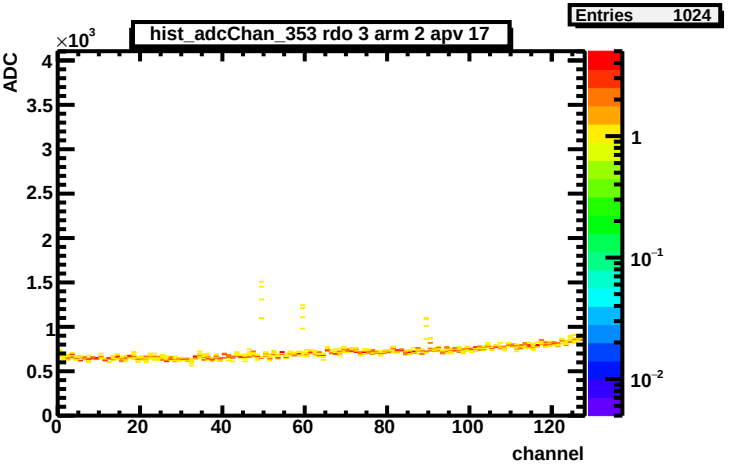

hist_adcChan_348 rdo 3 arm 2 apv 12
Entries 1024
ADC
×10³
channel

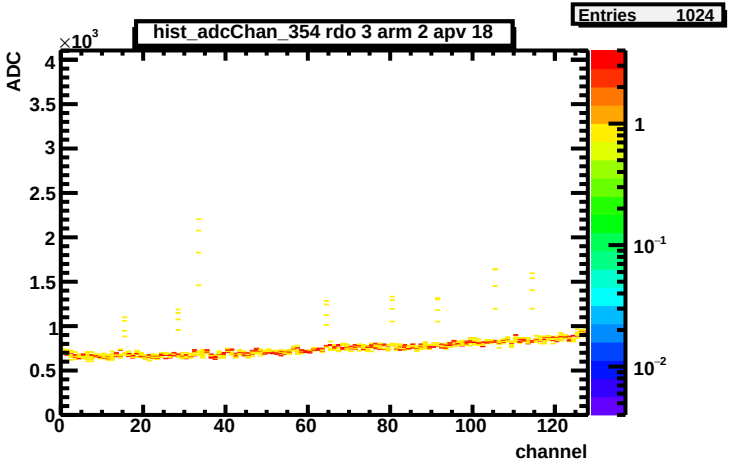

hist_adcChan_349 rdo 3 arm 2 apv 13
Entries 1024
ADC
×10³
channel

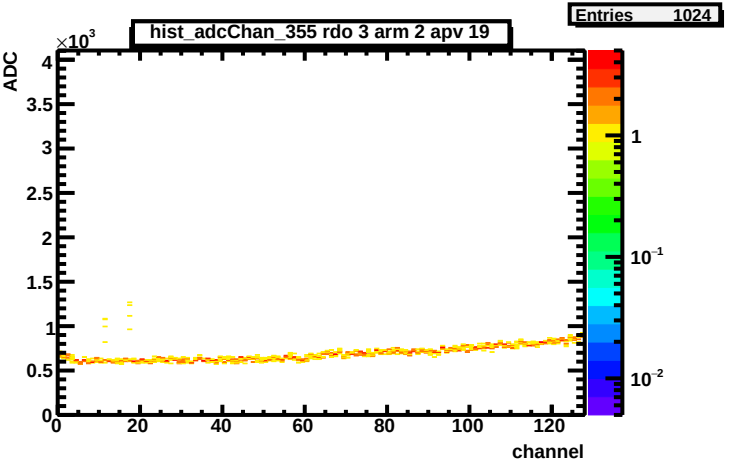

hist_adcChan_350 rdo 3 arm 2 apv 14
Entries 1024
ADC
×10³
channel

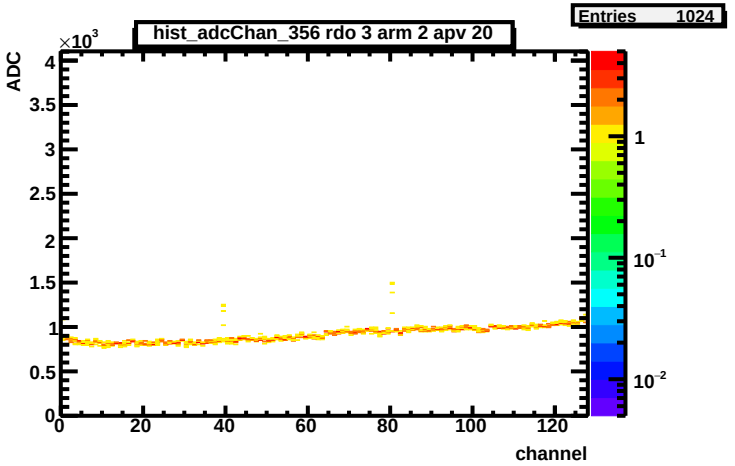

hist_adcChan_351 rdo 3 arm 2 apv 15
Entries 1024
ADC
×10³
channel

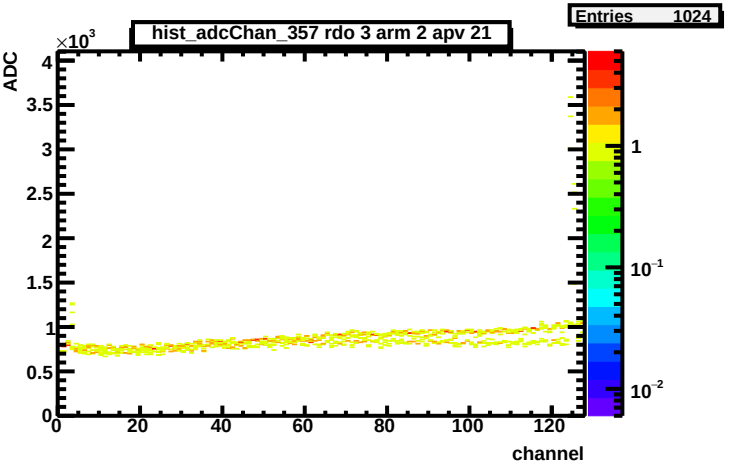

hist_adcChan_352 rdo 3 arm 2 apv 16
Entries 1024
ADC
×10³
channel

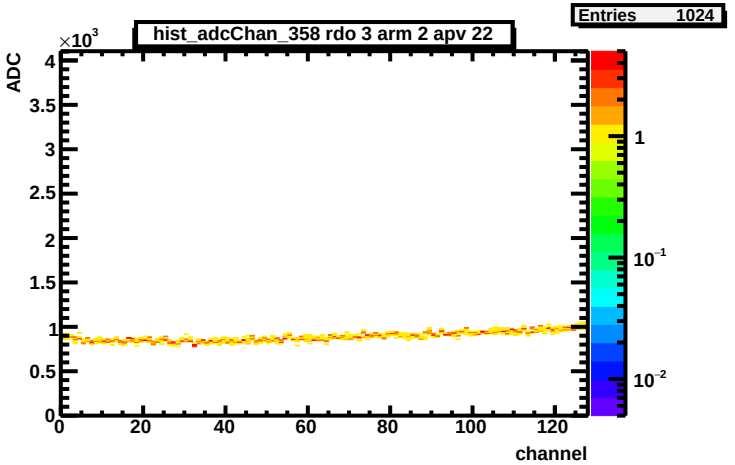

hist_adcChan_353 rdo 3 arm 2 apv 17
Entries 1024
ADC
×10³
channel

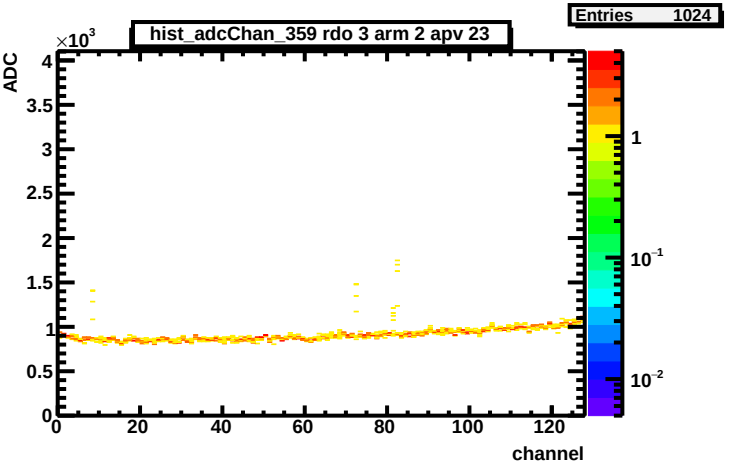

hist_adcChan_354 rdo 3 arm 2 apv 18
Entries 1024
ADC
channel

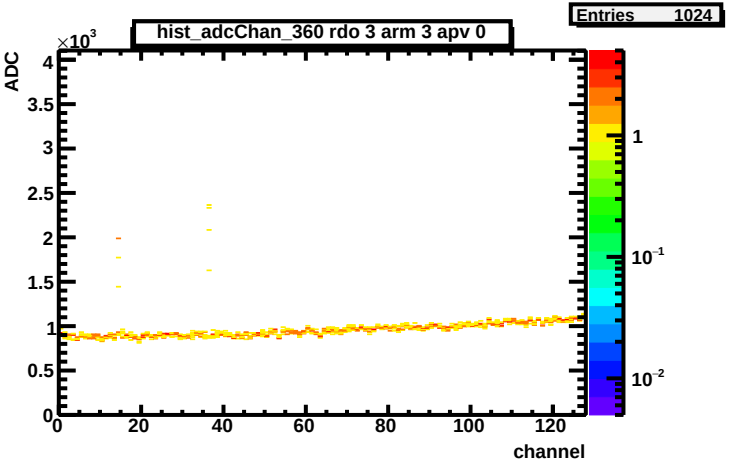

hist_adcChan_355 rdo 3 arm 2 apv 19
Entries 1024
ADC
channel

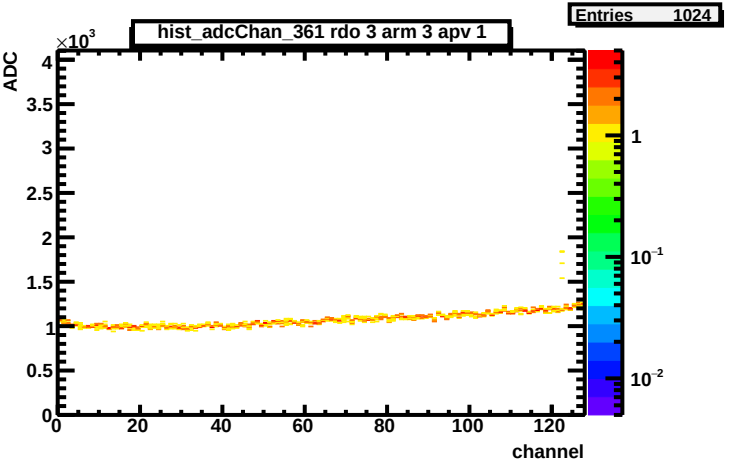

hist_adcChan_356 rdo 3 arm 2 apv 20
Entries 1024
ADC
channel

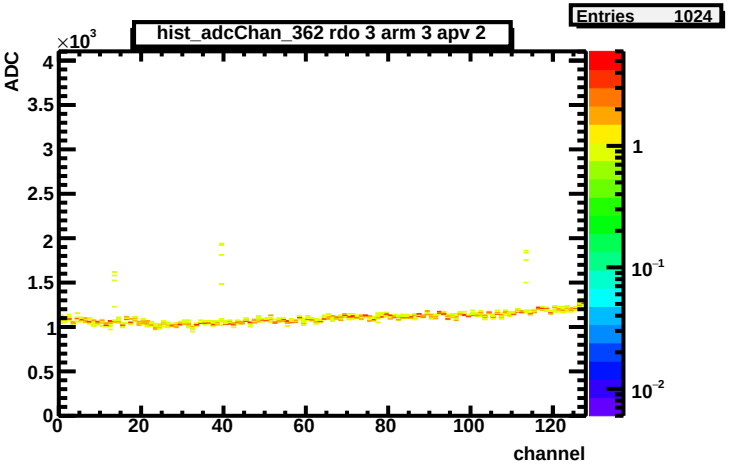

hist_adcChan_357 rdo 3 arm 2 apv 21
Entries 1024
ADC
channel

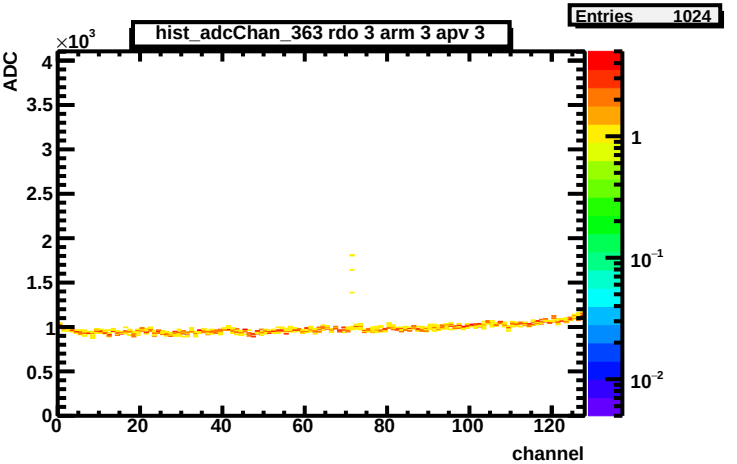

hist_adcChan_358 rdo 3 arm 2 apv 22
Entries 1024
ADC
channel

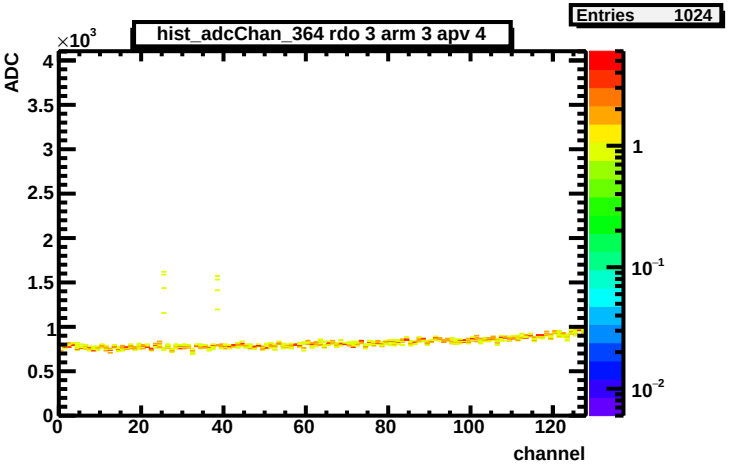

hist_adcChan_359 rdo 3 arm 2 apv 23
Entries 1024
ADC
channel

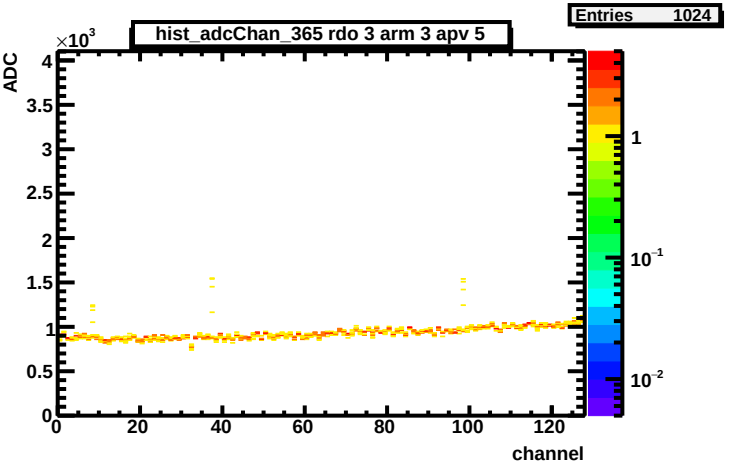

hist_adcChan_360 rdo 3 arm 3 apv 0
Entries 1024
ADC
×10^3
channel

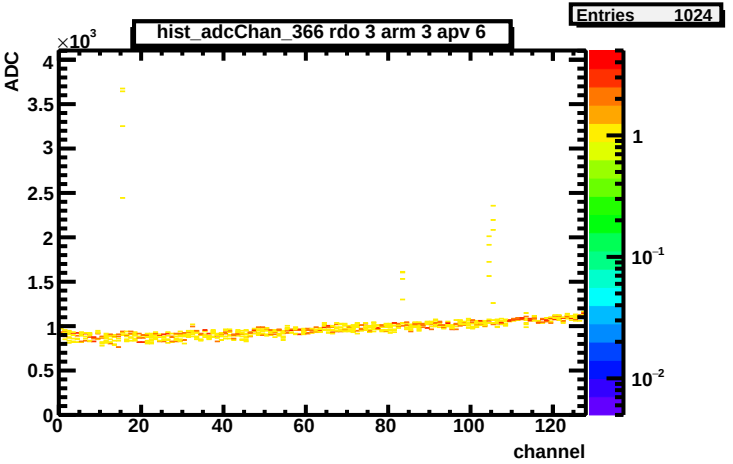

hist_adcChan_361 rdo 3 arm 3 apv 1
Entries 1024
ADC
×10^3
channel

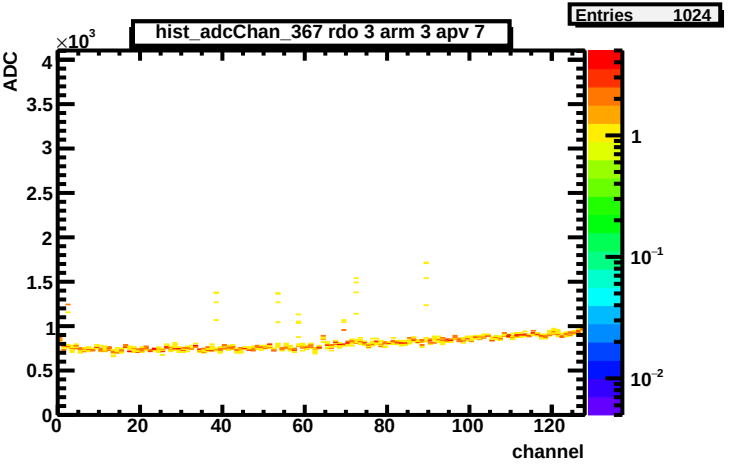

hist_adcChan_362 rdo 3 arm 3 apv 2
Entries 1024
ADC
×10^3
channel

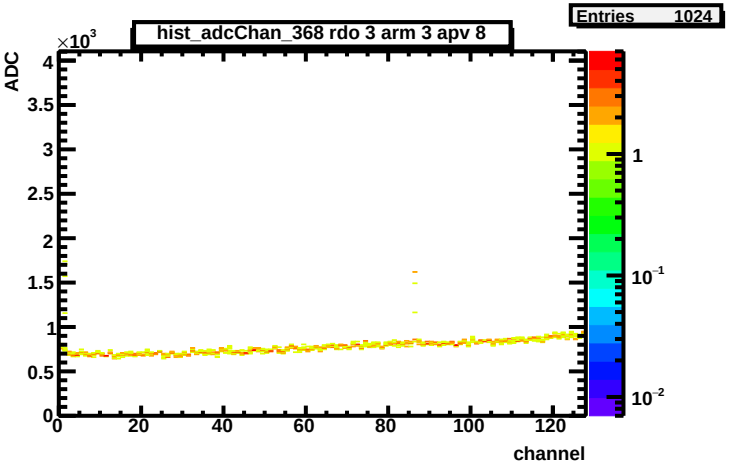

hist_adcChan_363 rdo 3 arm 3 apv 3
Entries 1024
ADC
×10^3
channel

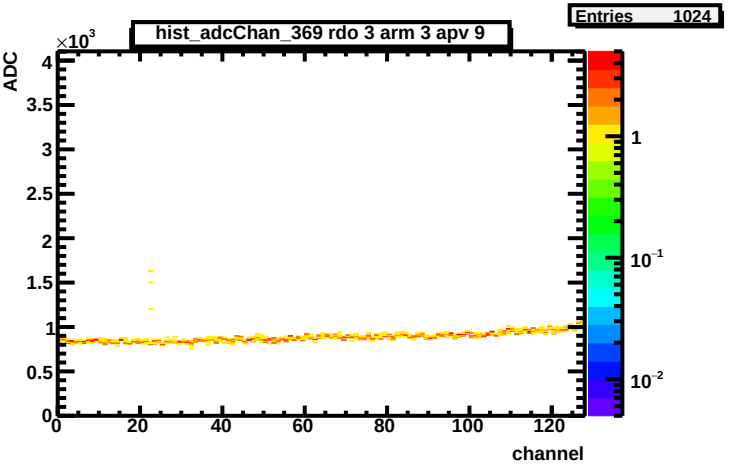

hist_adcChan_364 rdo 3 arm 3 apv 4
Entries 1024
ADC
×10^3
channel

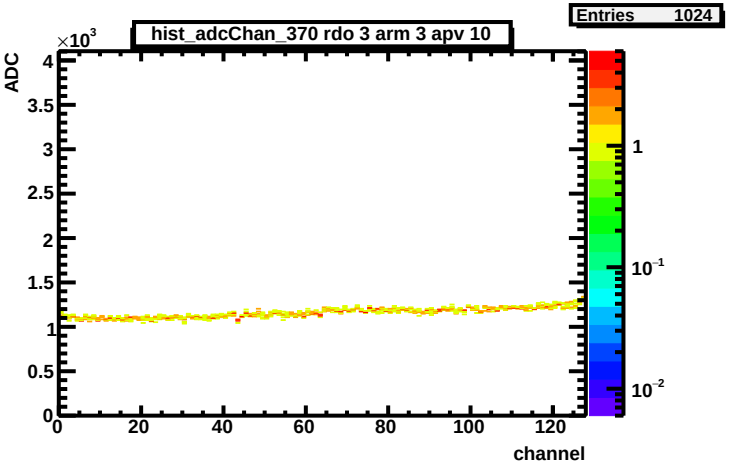

hist_adcChan_365 rdo 3 arm 3 apv 5
Entries 1024
ADC
×10^3
channel

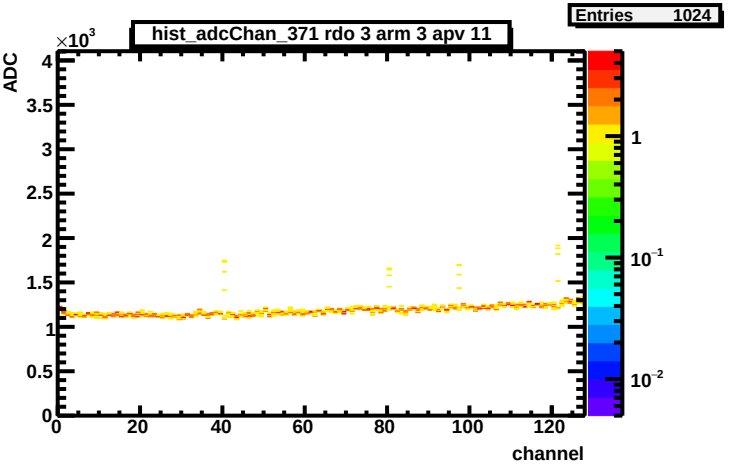

hist_adcChan_366 rdo 3 arm 3 apv 6
Entries 1024
ADC
×10^3
channel

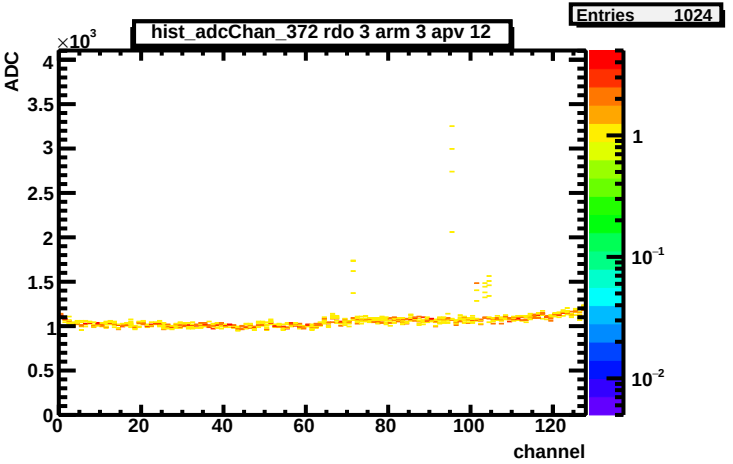

hist_adcChan_367 rdo 3 arm 3 apv 7
Entries 1024
ADC
×10^3
channel

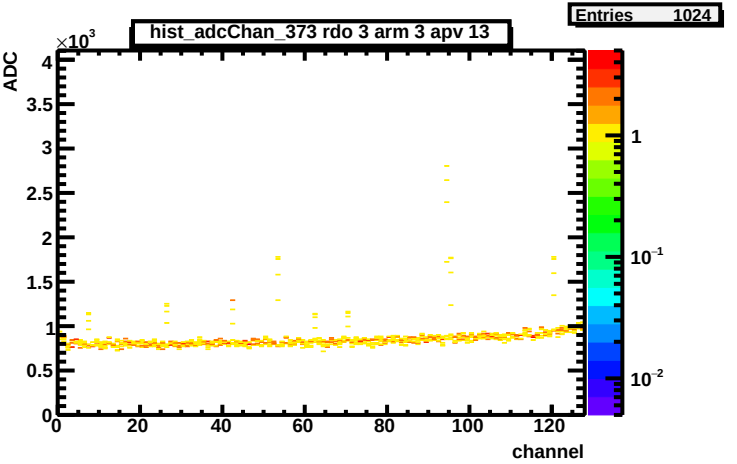

hist_adcChan_368 rdo 3 arm 3 apv 8
Entries 1024
ADC
×10^3
channel

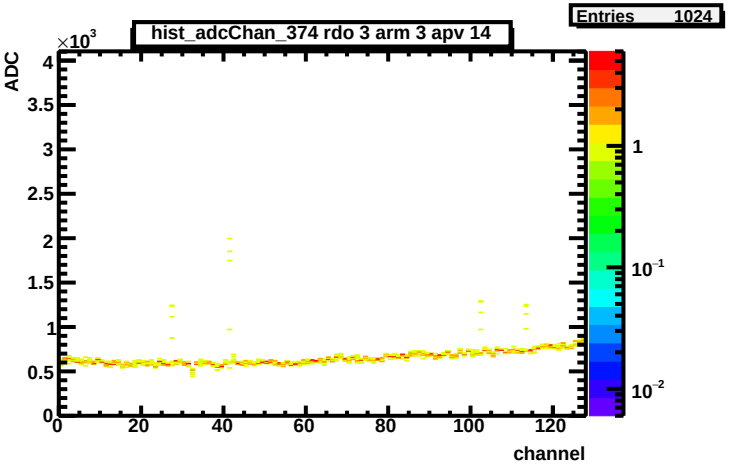

hist_adcChan_369 rdo 3 arm 3 apv 9
Entries 1024
ADC
×10^3
channel

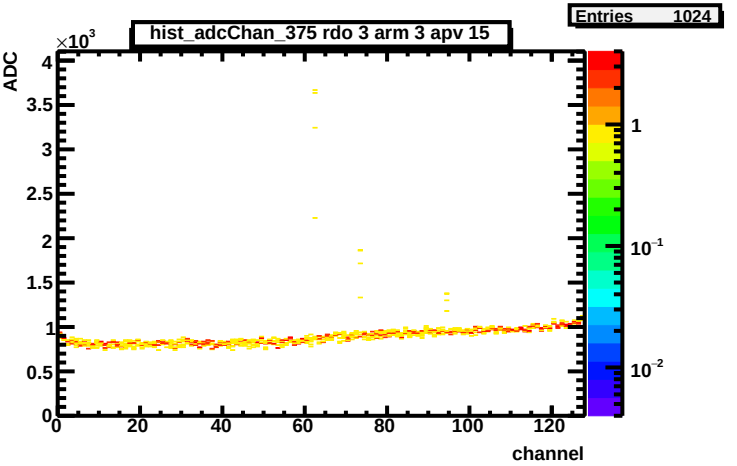

hist_adcChan_370 rdo 3 arm 3 apv 10
Entries 1024
ADC
×10^3
channel

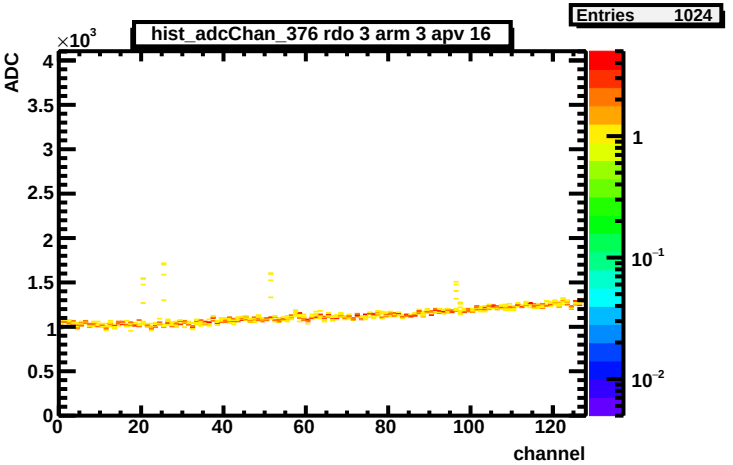

hist_adcChan_371 rdo 3 arm 3 apv 11
Entries 1024
ADC
×10^3
channel

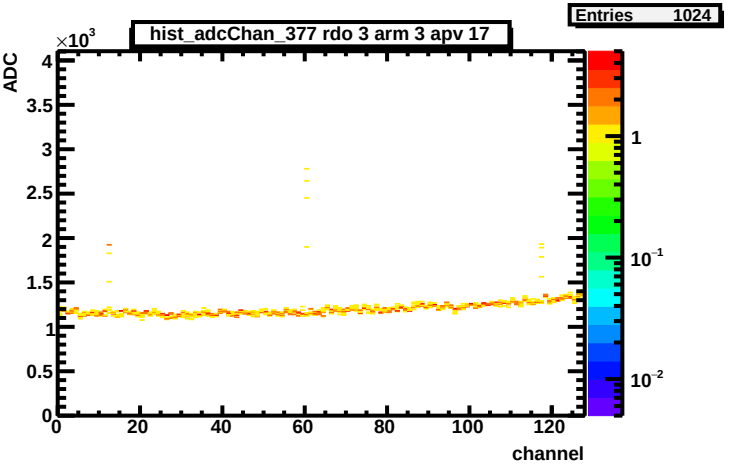

hist_adcChan_372 rdo 3 arm 3 apv 12
Entries 1024
ADC
×10^3
channel

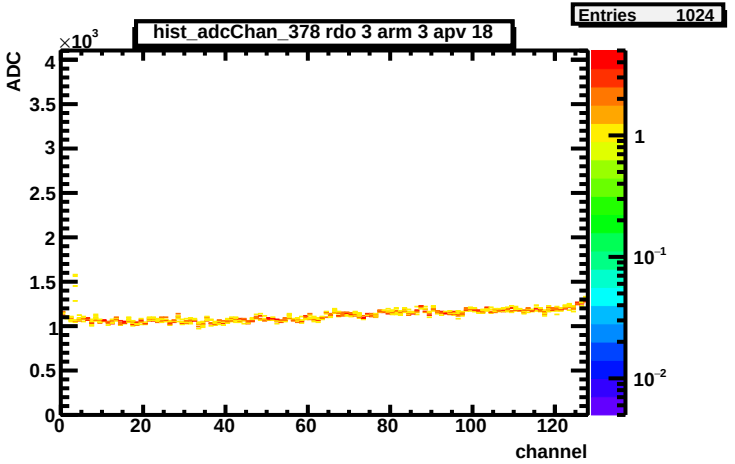

hist_adcChan_373 rdo 3 arm 3 apv 13
Entries 1024
ADC
×10^3
channel

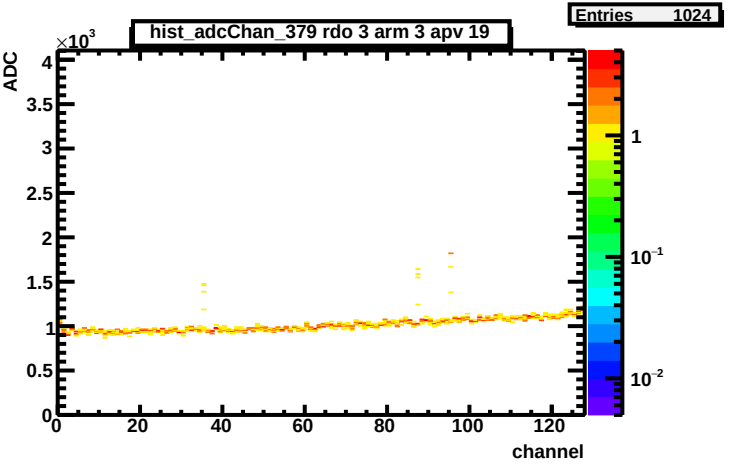

hist_adcChan_374 rdo 3 arm 3 apv 14
Entries 1024
ADC
×10^3
channel

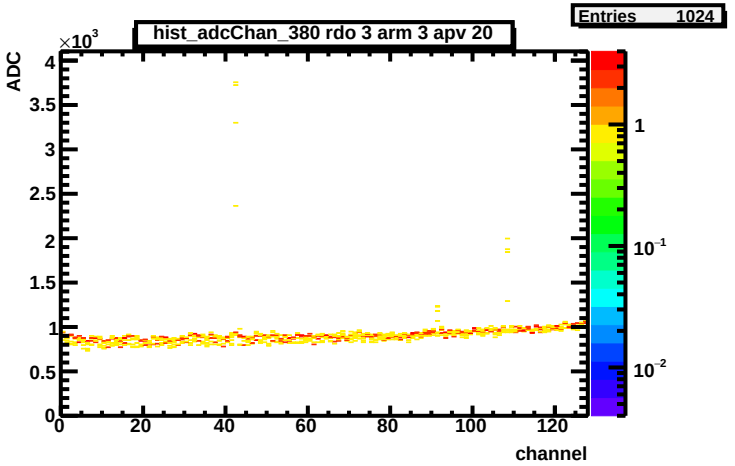

hist_adcChan_375 rdo 3 arm 3 apv 15
Entries 1024
ADC
×10^3
channel

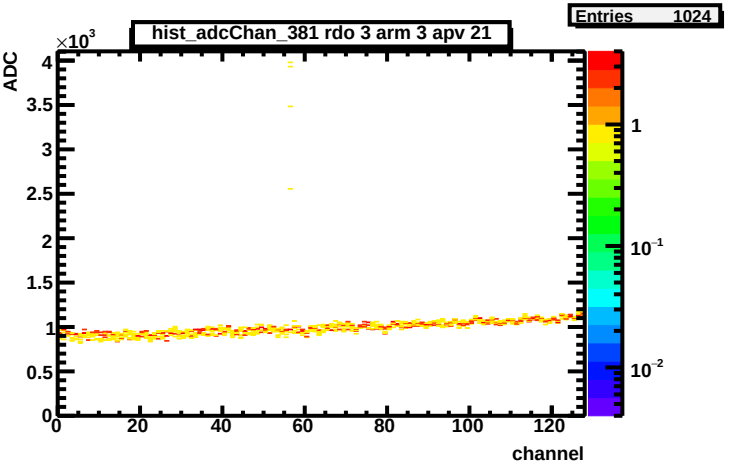

hist_adcChan_376 rdo 3 arm 3 apv 16
Entries 1024
ADC
×10^3
channel

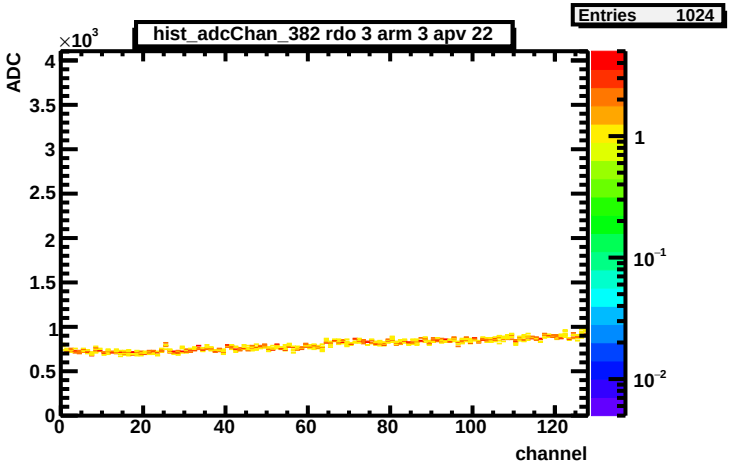

hist_adcChan_377 rdo 3 arm 3 apv 17
Entries 1024
ADC
×10^3
channel

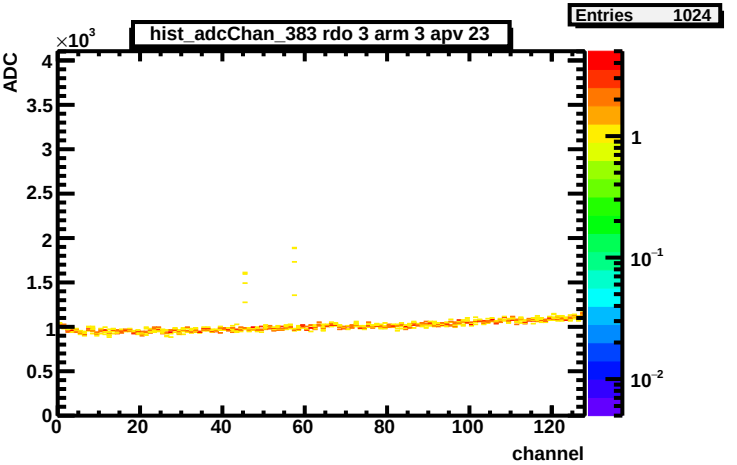

hist_adcChan_378 rdo 3 arm 3 apv 18
Entries 1024
ADC
channel

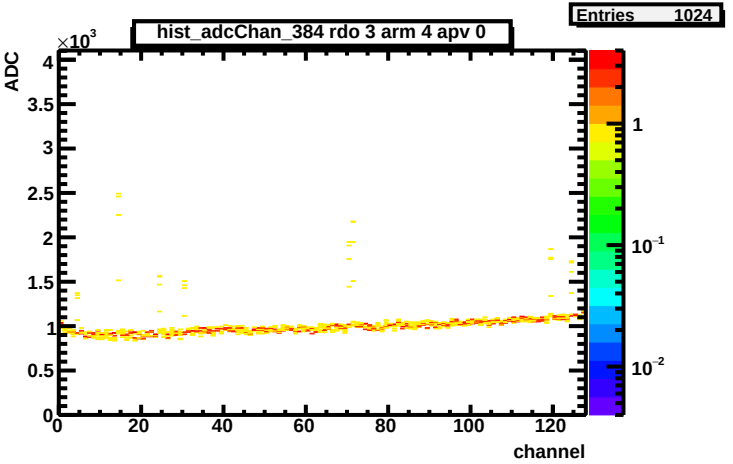

hist_adcChan_379 rdo 3 arm 3 apv 19
Entries 1024
ADC
channel

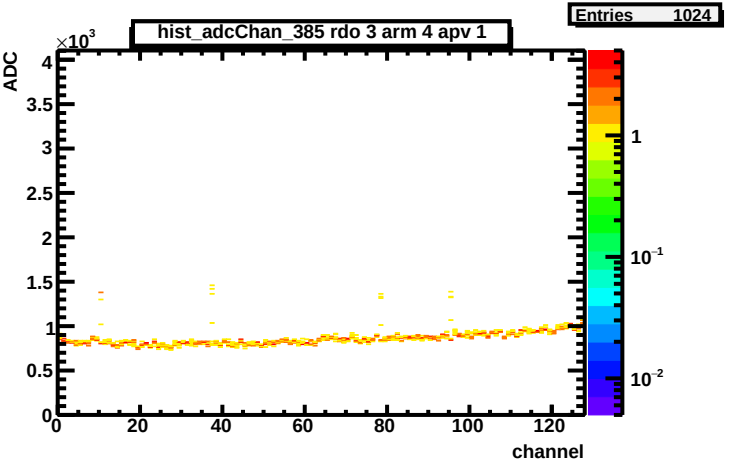

hist_adcChan_380 rdo 3 arm 3 apv 20
Entries 1024
ADC
channel

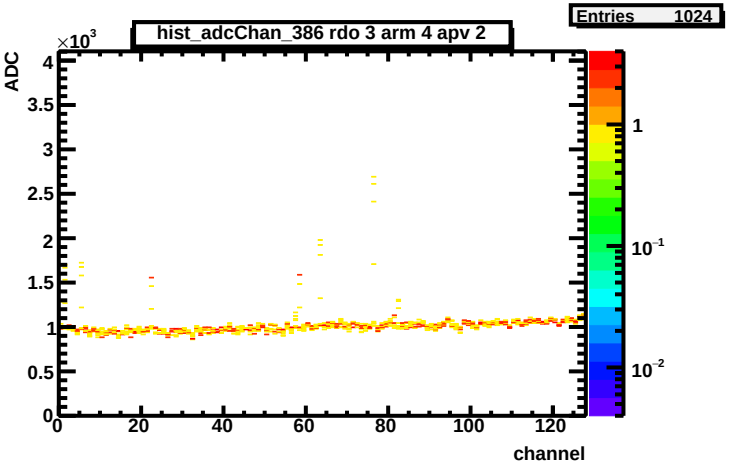

hist_adcChan_381 rdo 3 arm 3 apv 21
Entries 1024
ADC
channel

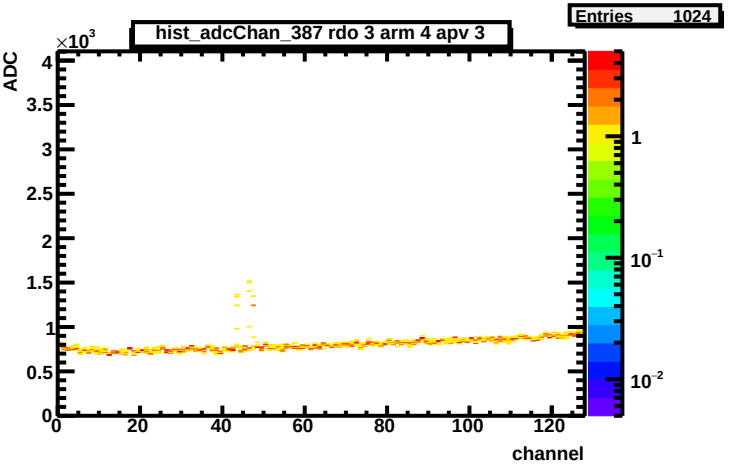

hist_adcChan_382 rdo 3 arm 3 apv 22
Entries 1024
ADC
channel

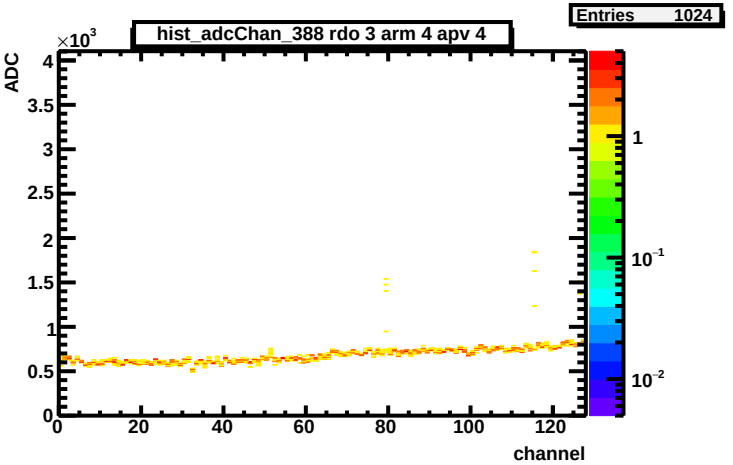

hist_adcChan_383 rdo 3 arm 3 apv 23
Entries 1024
ADC
channel

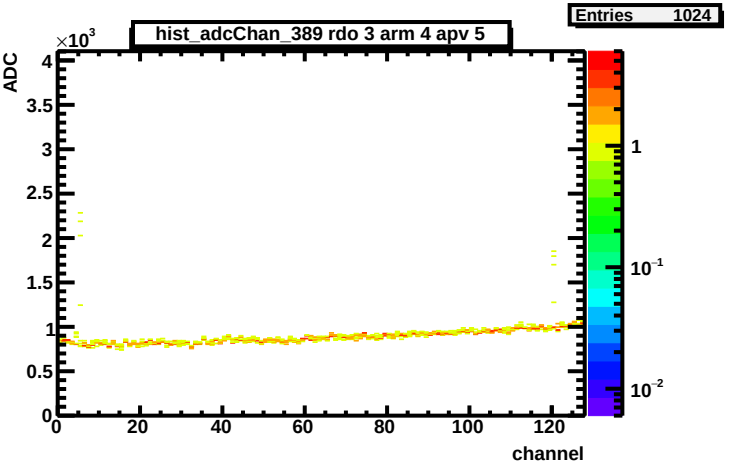

hist_adcChan_384 rdo 3 arm 4 apv 0
Entries 1024
ADC
×10^3
channel

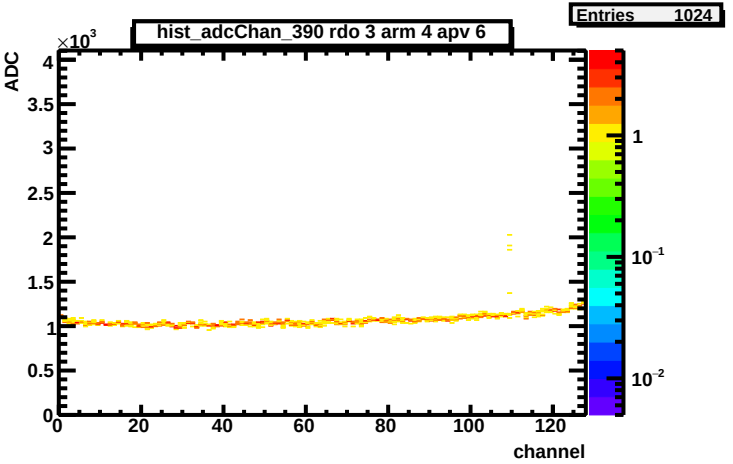

hist_adcChan_385 rdo 3 arm 4 apv 1
Entries 1024
ADC
×10^3
channel

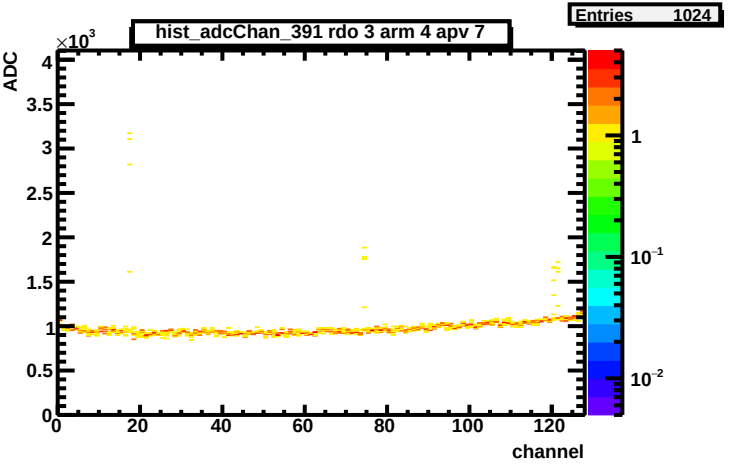

hist_adcChan_386 rdo 3 arm 4 apv 2
Entries 1024
ADC
×10^3
channel

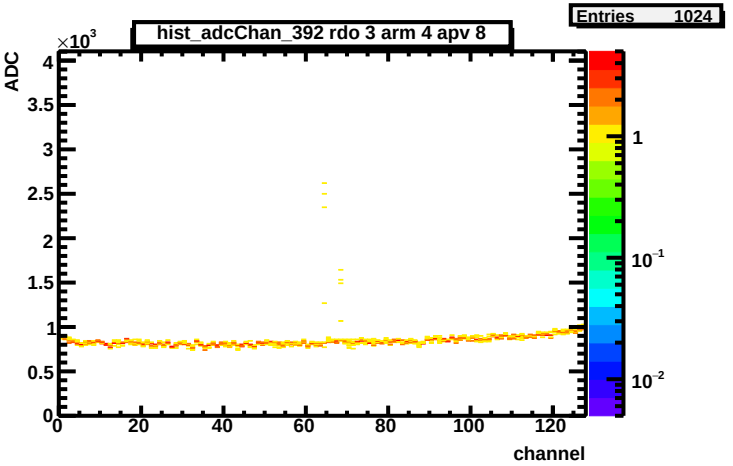

hist_adcChan_387 rdo 3 arm 4 apv 3
Entries 1024
ADC
×10^3
channel

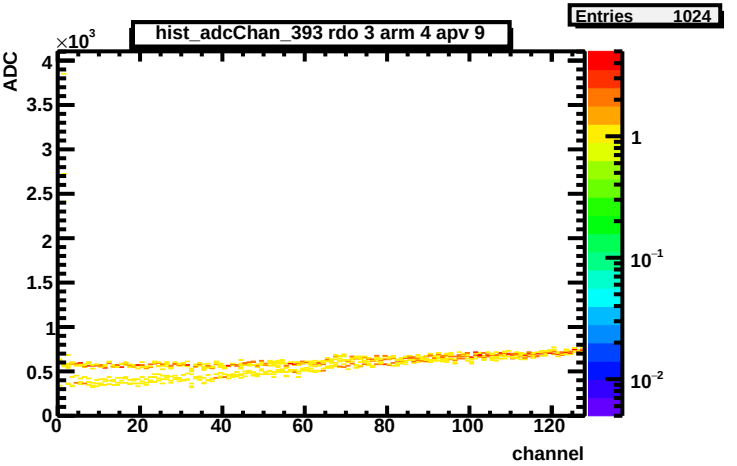

hist_adcChan_388 rdo 3 arm 4 apv 4
Entries 1024
ADC
×10^3
channel

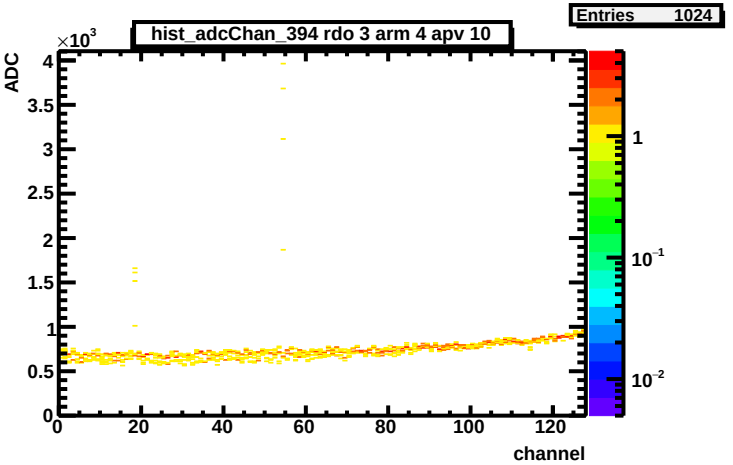

hist_adcChan_389 rdo 3 arm 4 apv 5
Entries 1024
ADC
×10^3
channel

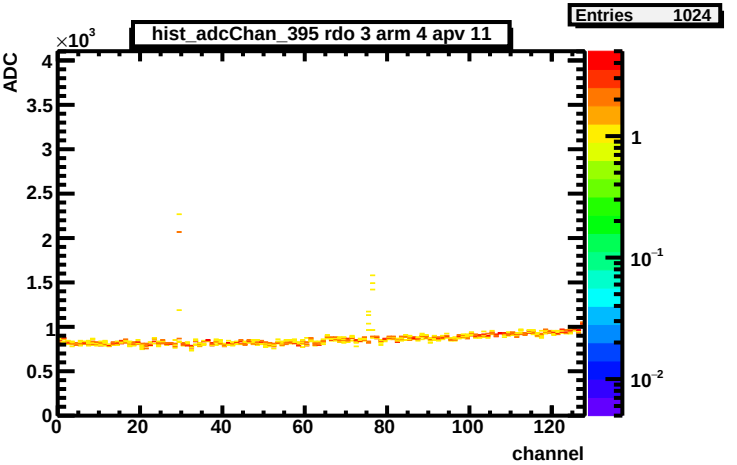

hist_adcChan_390 rdo 3 arm 4 apv 6
Entries 1024
ADC
channel

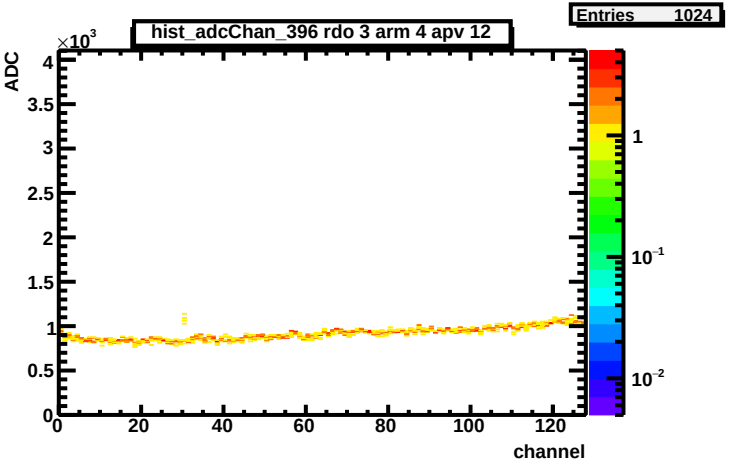

hist_adcChan_391 rdo 3 arm 4 apv 7
Entries 1024
ADC
channel

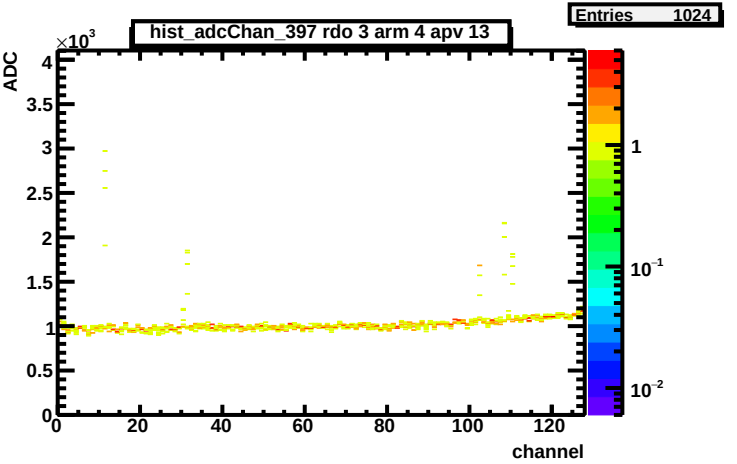

hist_adcChan_392 rdo 3 arm 4 apv 8
Entries 1024
ADC
channel

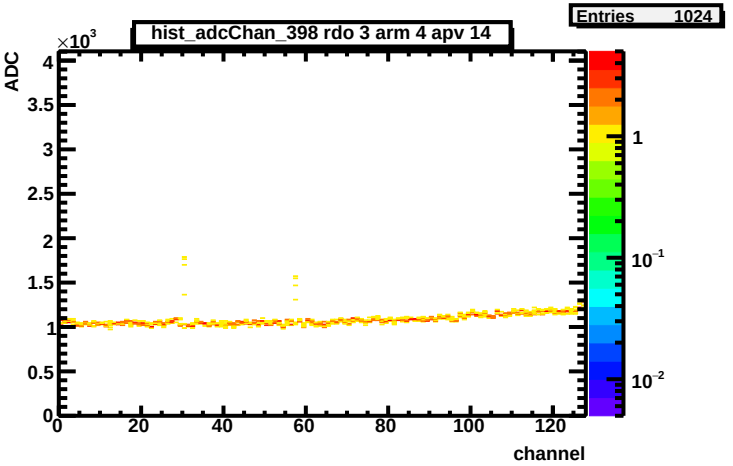

hist_adcChan_393 rdo 3 arm 4 apv 9
Entries 1024
ADC
channel

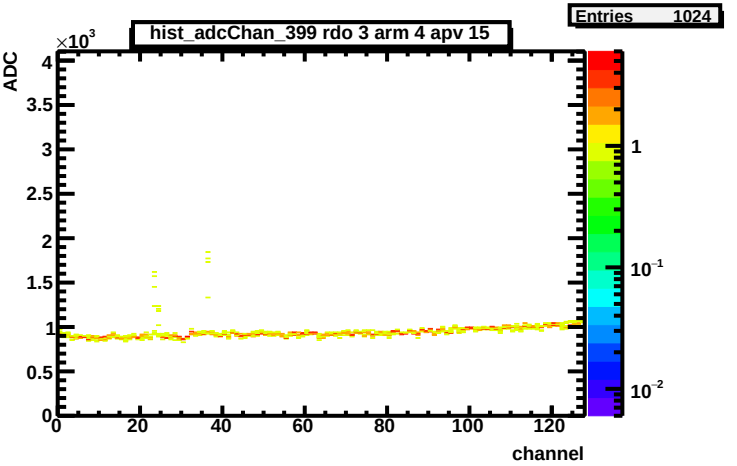

hist_adcChan_394 rdo 3 arm 4 apv 10
Entries 1024
ADC
channel

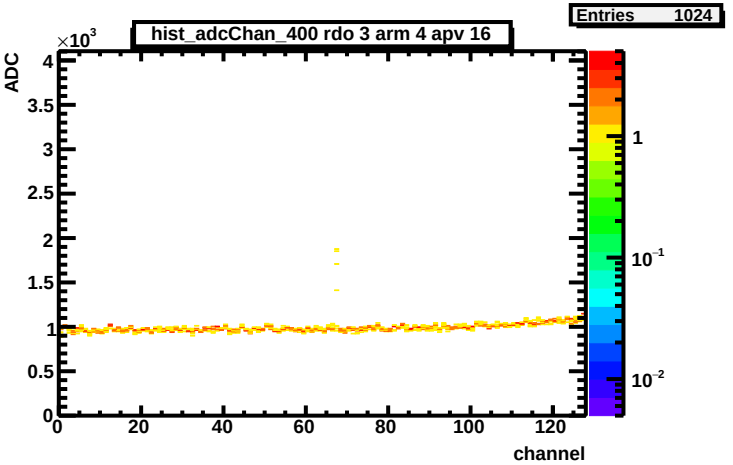

hist_adcChan_395 rdo 3 arm 4 apv 11
Entries 1024
ADC
channel

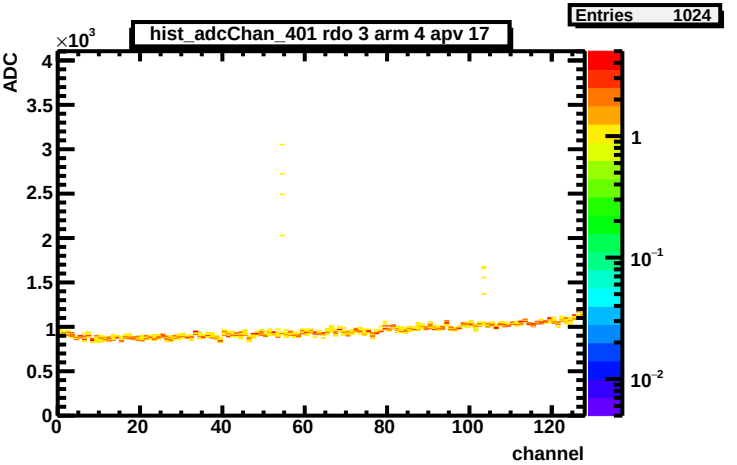

hist_adcChan_396 rdo 3 arm 4 apv 12
Entries 1024
ADC
channel

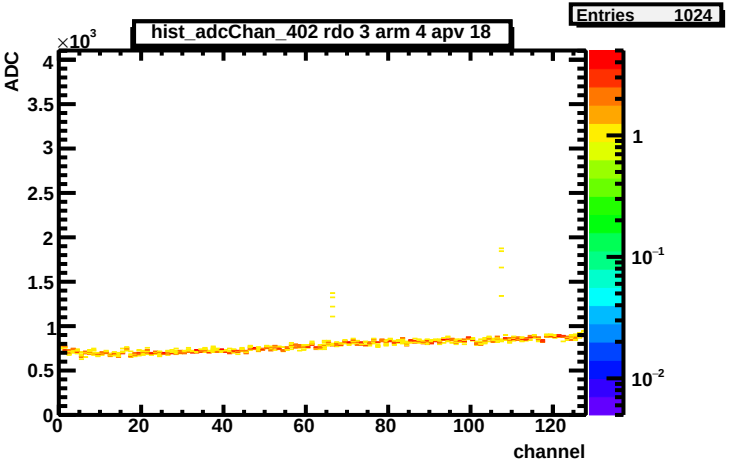

hist_adcChan_397 rdo 3 arm 4 apv 13
Entries 1024
ADC
channel

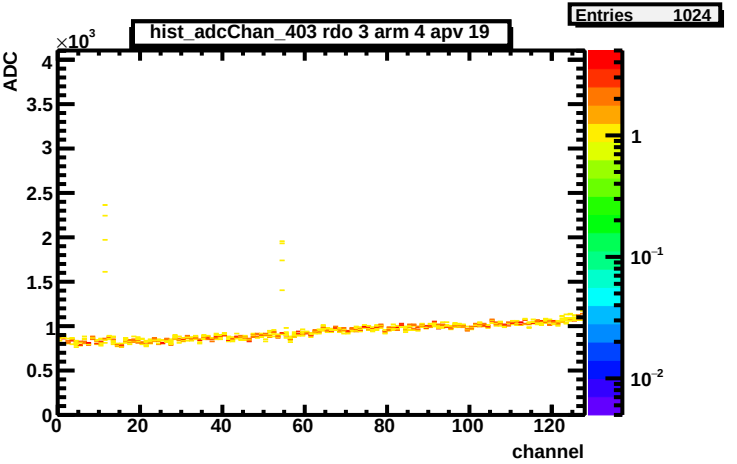

hist_adcChan_398 rdo 3 arm 4 apv 14
Entries 1024
ADC
channel

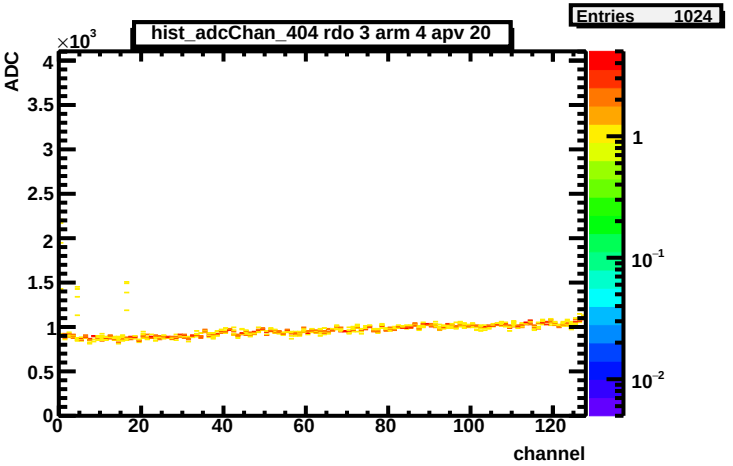

hist_adcChan_399 rdo 3 arm 4 apv 15
Entries 1024
ADC
channel

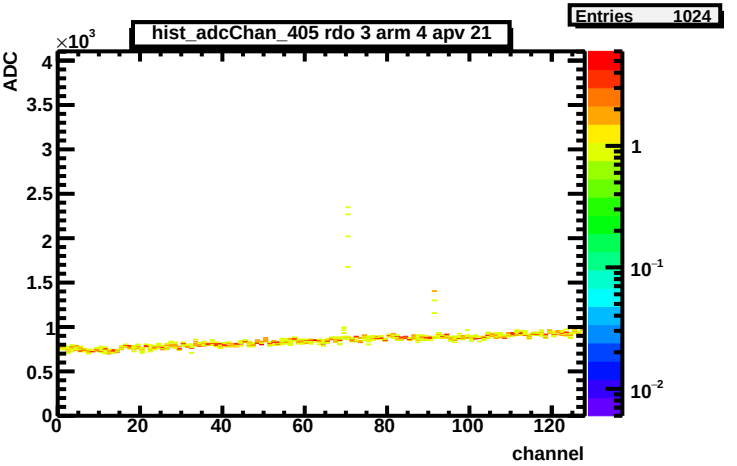

hist_adcChan_400 rdo 3 arm 4 apv 16
Entries 1024
ADC
channel

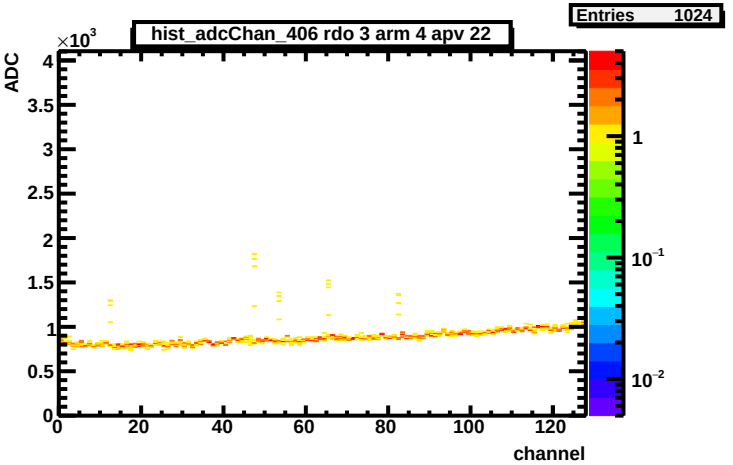

hist_adcChan_401 rdo 3 arm 4 apv 17
Entries 1024
ADC
channel

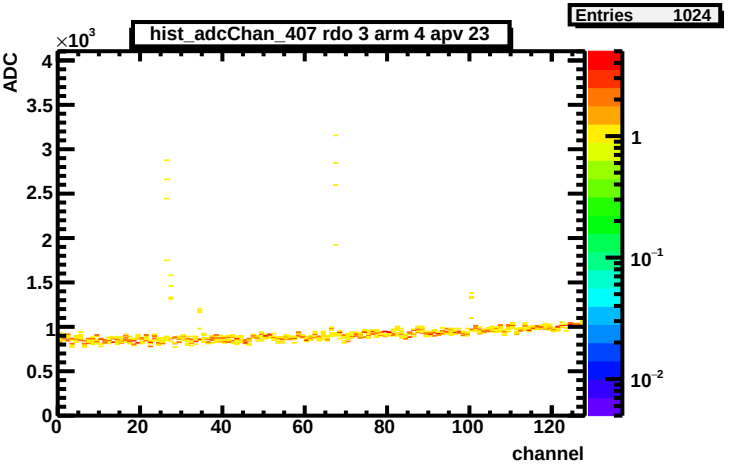

hist_adcChan_402 rdo 3 arm 4 apv 18
Entries 1024
ADC
×10³
channel

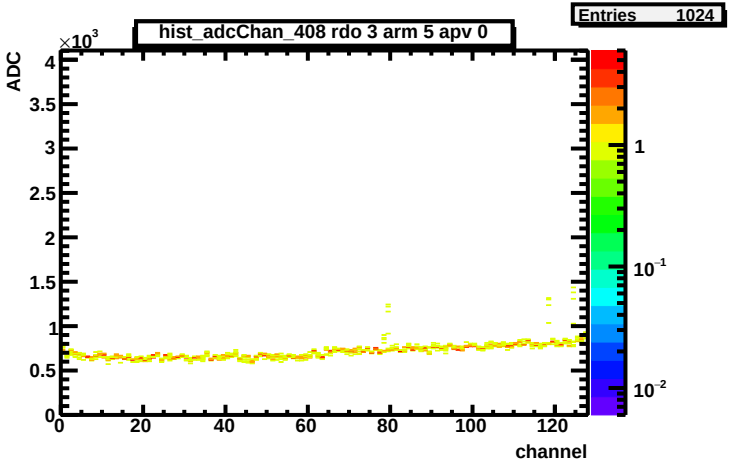

hist_adcChan_403 rdo 3 arm 4 apv 19
Entries 1024
ADC
×10³
channel

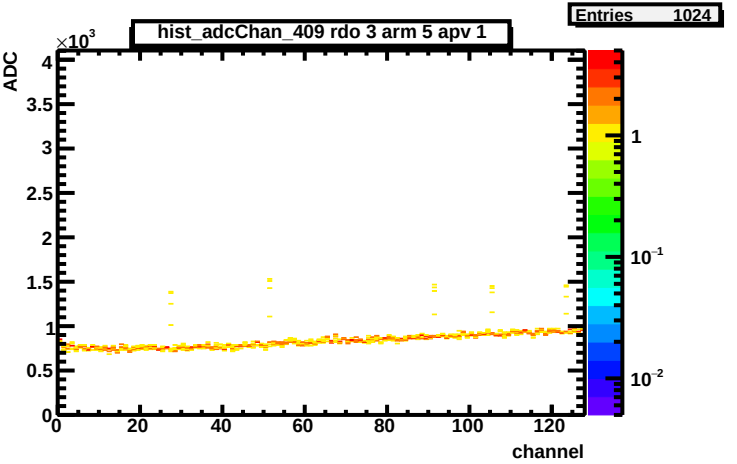

hist_adcChan_404 rdo 3 arm 4 apv 20
Entries 1024
ADC
×10³
channel

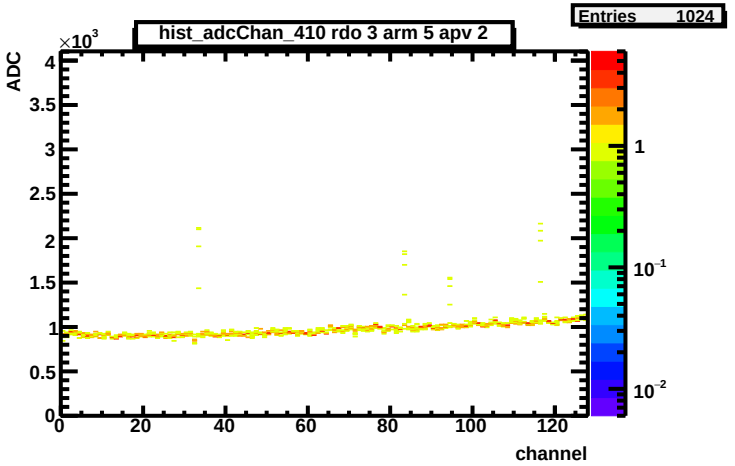

hist_adcChan_405 rdo 3 arm 4 apv 21
Entries 1024
ADC
×10³
channel

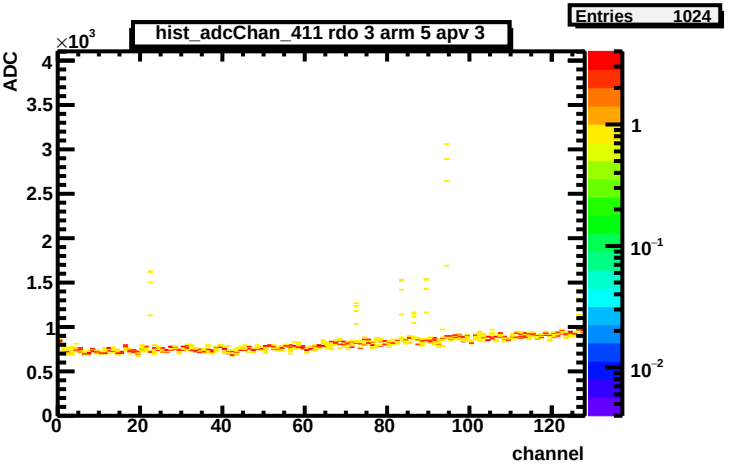

hist_adcChan_406 rdo 3 arm 4 apv 22
Entries 1024
ADC
×10³
channel

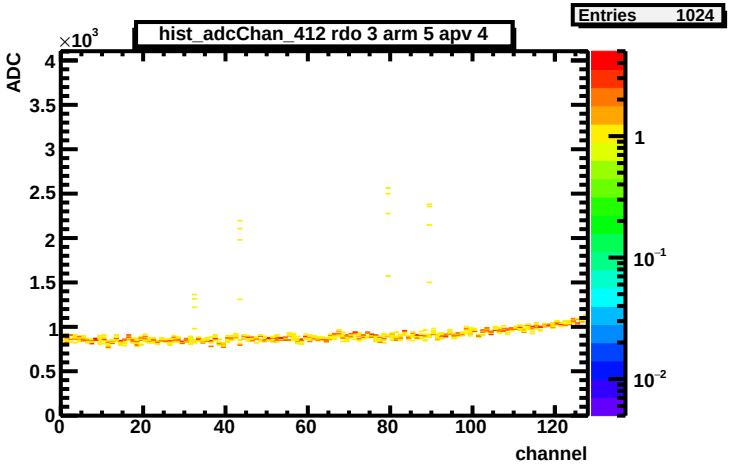

hist_adcChan_407 rdo 3 arm 4 apv 23
Entries 1024
ADC
×10³
channel

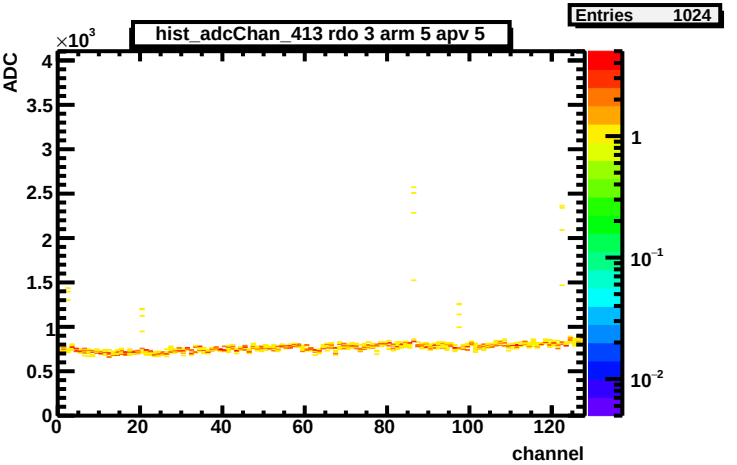

hist_adcChan_408 rdo 3 arm 5 apv 0
Entries 1024
ADC
×10^3
channel

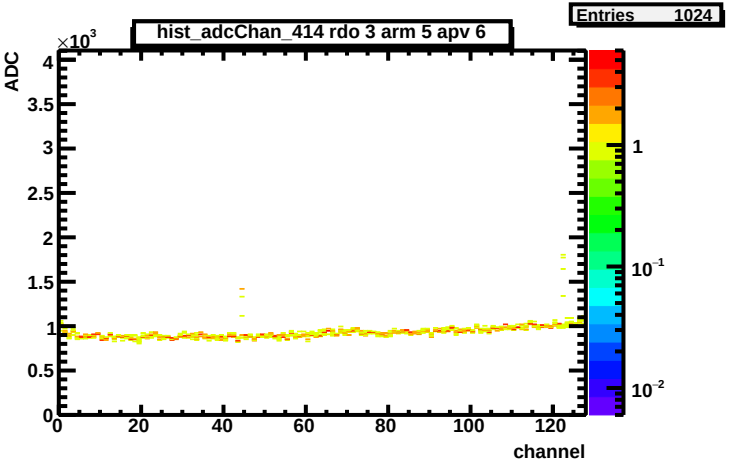

hist_adcChan_409 rdo 3 arm 5 apv 1
Entries 1024
ADC
×10^3
channel

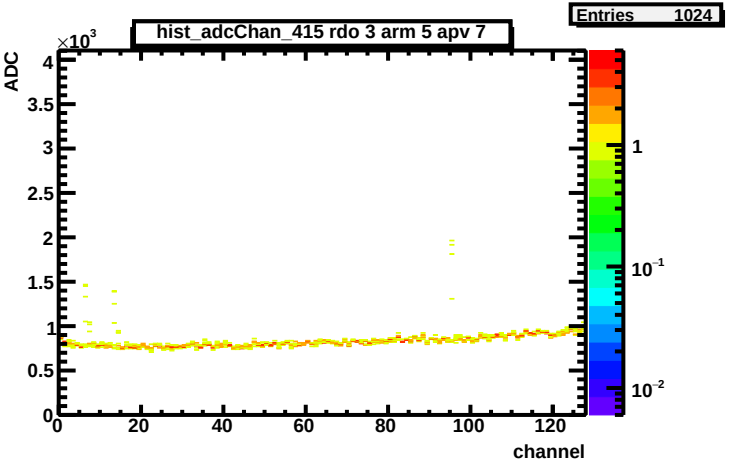

hist_adcChan_410 rdo 3 arm 5 apv 2
Entries 1024
ADC
×10^3
channel

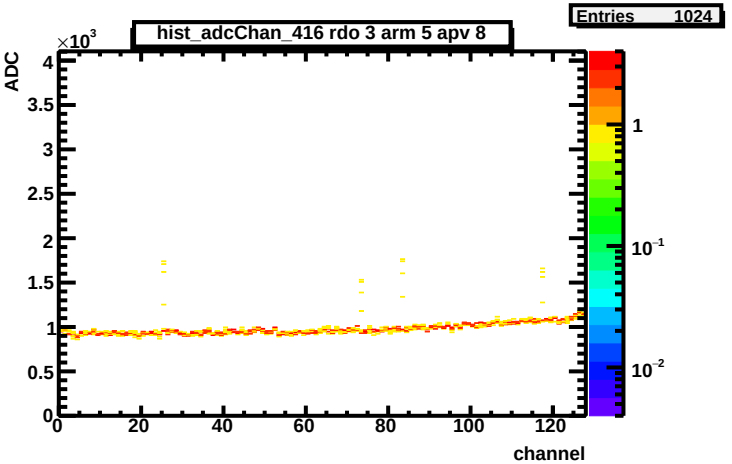

hist_adcChan_411 rdo 3 arm 5 apv 3
Entries 1024
ADC
×10^3
channel

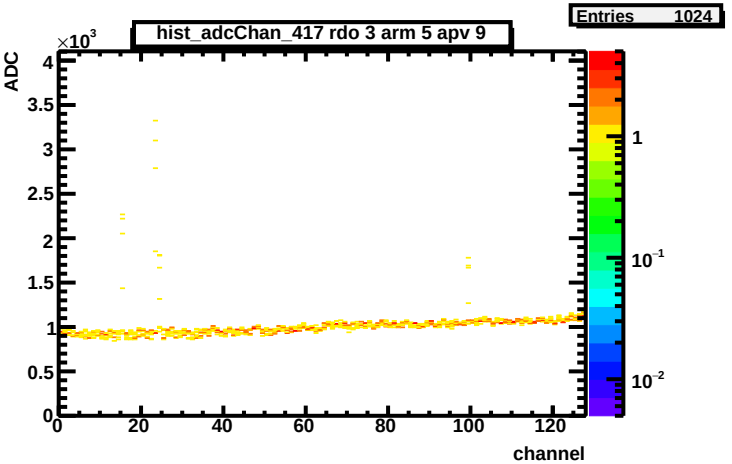

hist_adcChan_412 rdo 3 arm 5 apv 4
Entries 1024
ADC
×10^3
channel

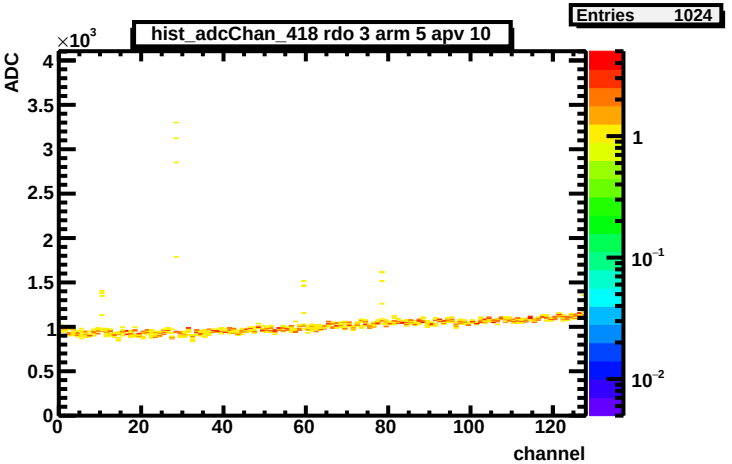

hist_adcChan_413 rdo 3 arm 5 apv 5
Entries 1024
ADC
×10^3
channel

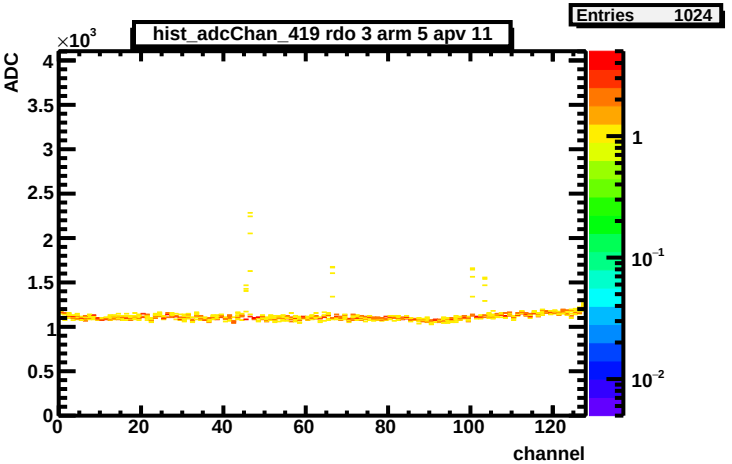

hist_adcChan_414 rdo 3 arm 5 apv 6
Entries 1024
ADC
channel

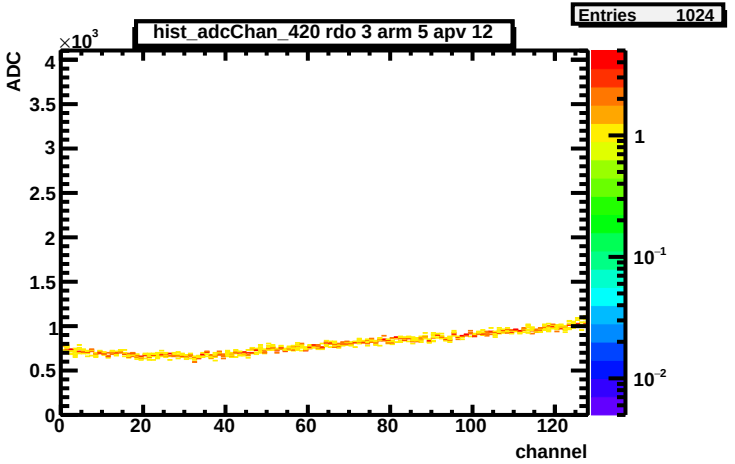

hist_adcChan_415 rdo 3 arm 5 apv 7
Entries 1024
ADC
channel

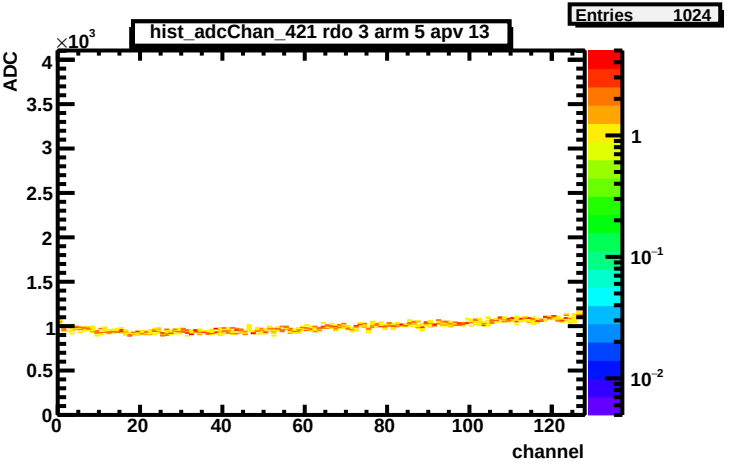

hist_adcChan_416 rdo 3 arm 5 apv 8
Entries 1024
ADC
channel

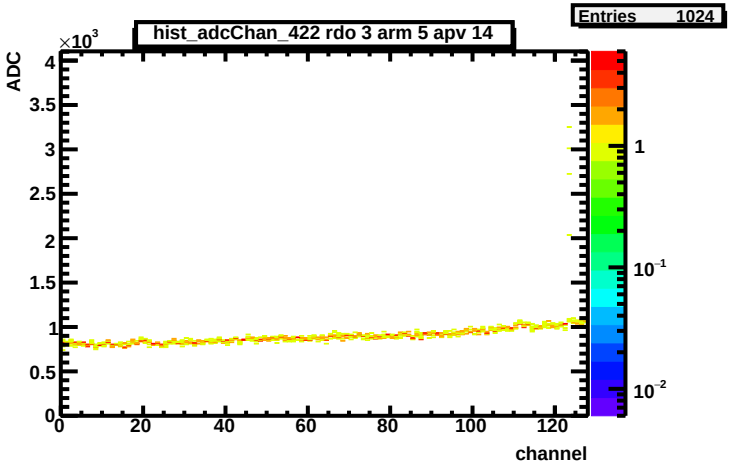

hist_adcChan_417 rdo 3 arm 5 apv 9
Entries 1024
ADC
channel

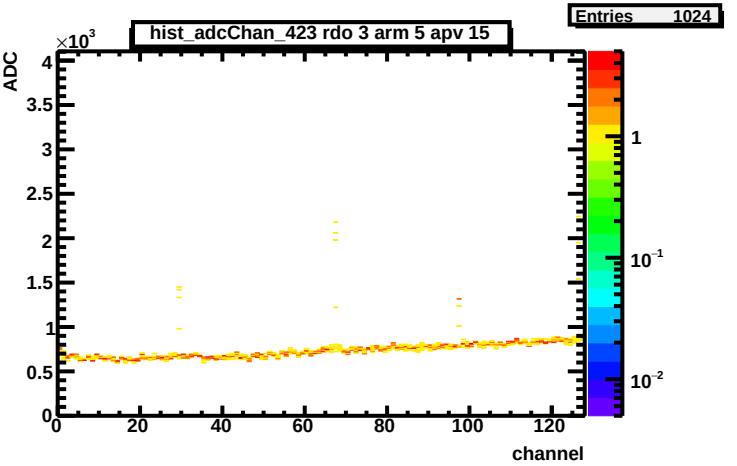

hist_adcChan_418 rdo 3 arm 5 apv 10
Entries 1024
ADC
channel

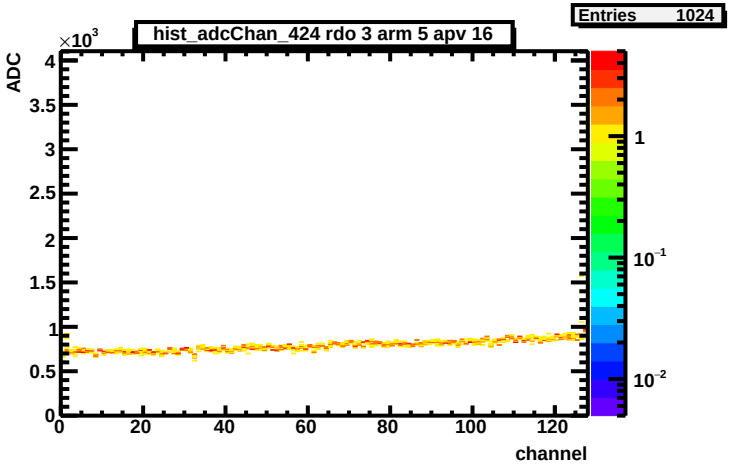

hist_adcChan_419 rdo 3 arm 5 apv 11
Entries 1024
ADC
channel

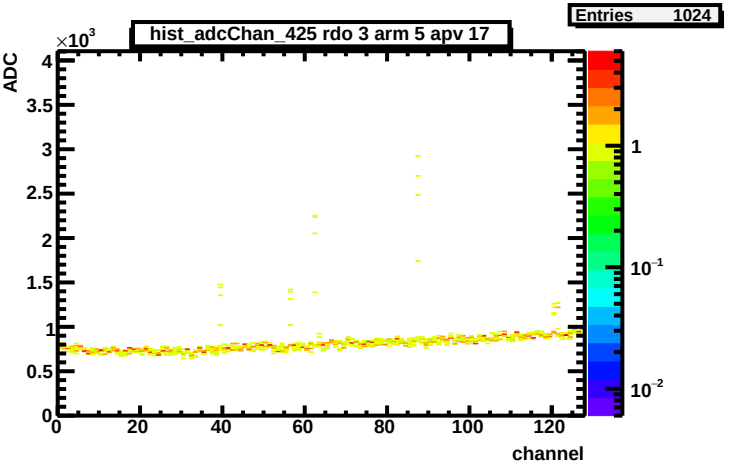

hist_adcChan_420 rdo 3 arm 5 apv 12
Entries 1024
ADC
channel

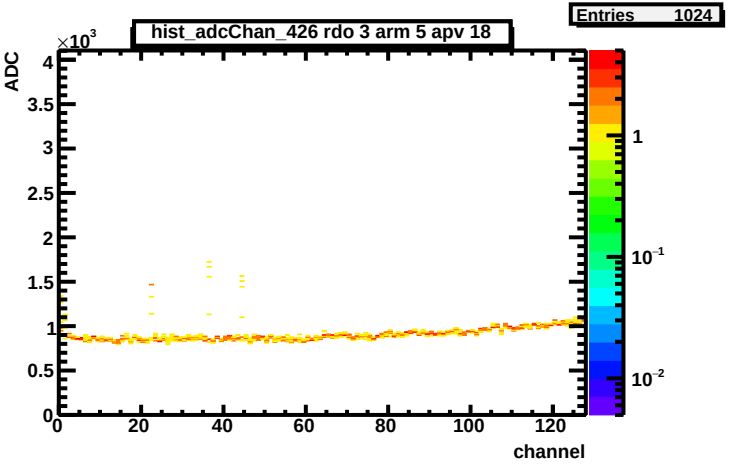

hist_adcChan_421 rdo 3 arm 5 apv 13
Entries 1024
ADC
channel

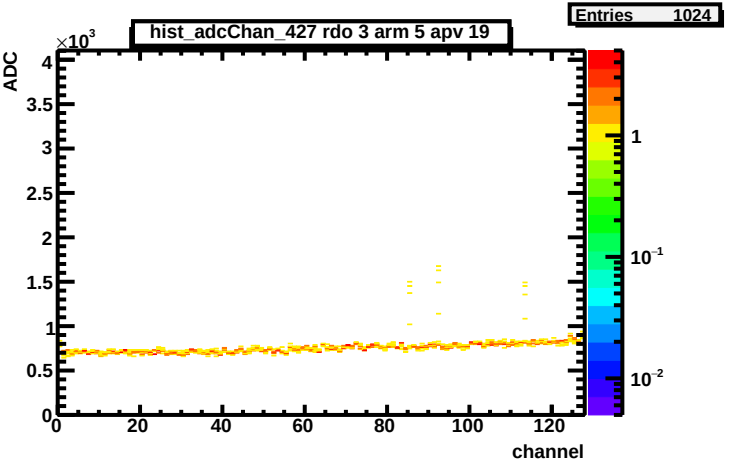

hist_adcChan_422 rdo 3 arm 5 apv 14
Entries 1024
ADC
channel

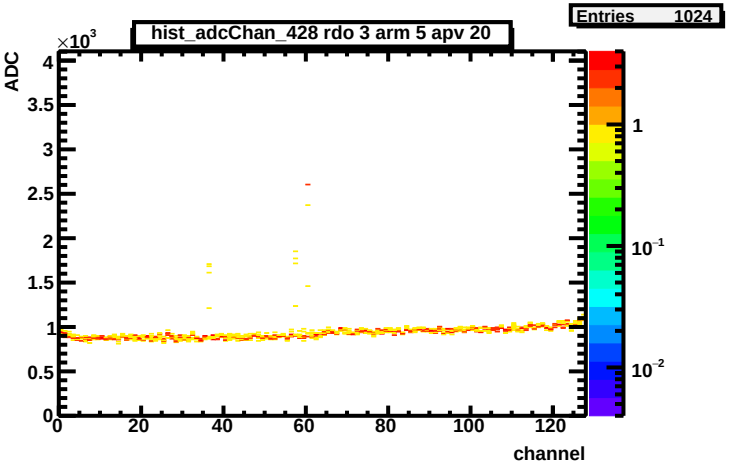

hist_adcChan_423 rdo 3 arm 5 apv 15
Entries 1024
ADC
channel

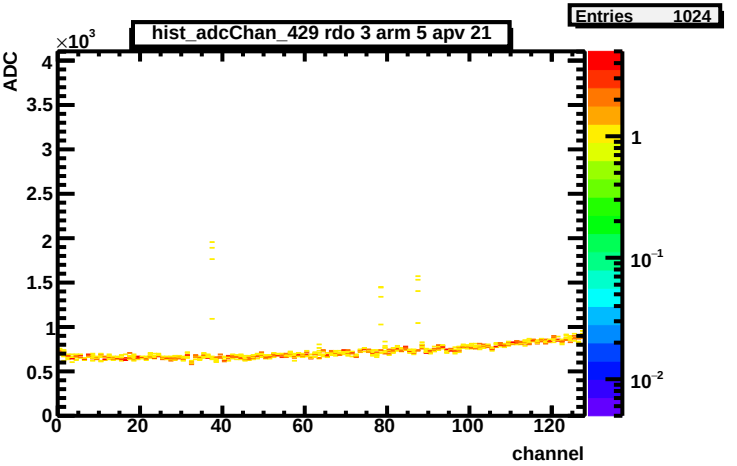

hist_adcChan_424 rdo 3 arm 5 apv 16
Entries 1024
ADC
channel

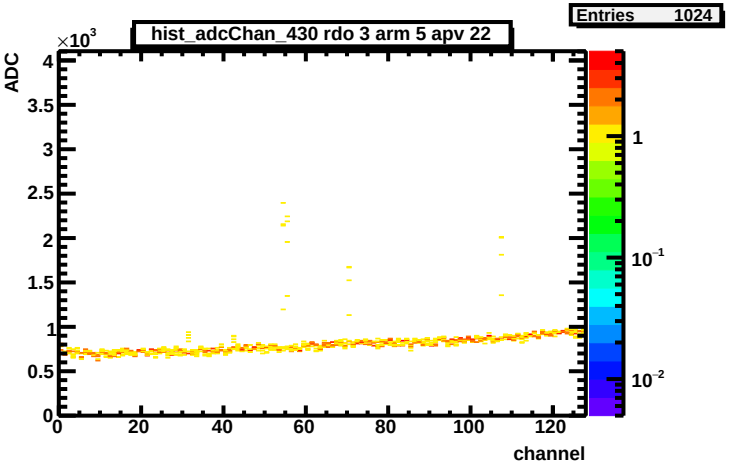

hist_adcChan_425 rdo 3 arm 5 apv 17
Entries 1024
ADC
channel

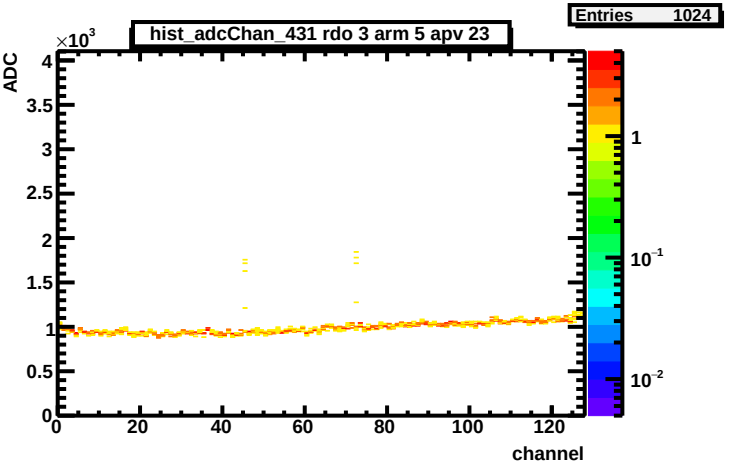

hist_adcChan_426 rdo 3 arm 5 apv 18
Entries 1024
ADC
×10^3
channel

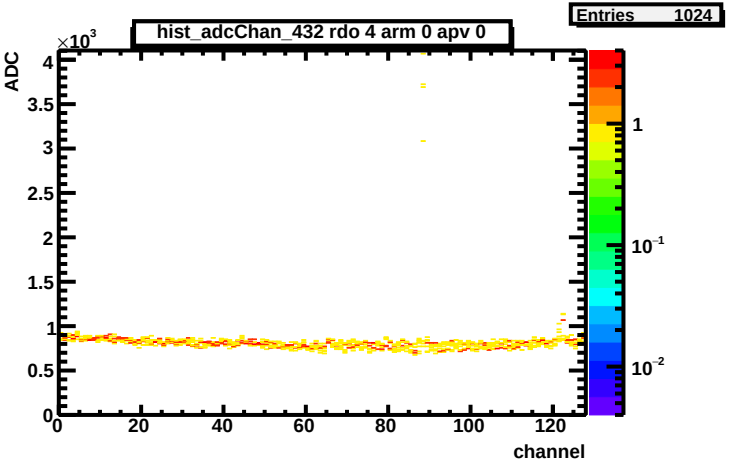

hist_adcChan_427 rdo 3 arm 5 apv 19
Entries 1024
ADC
×10^3
channel

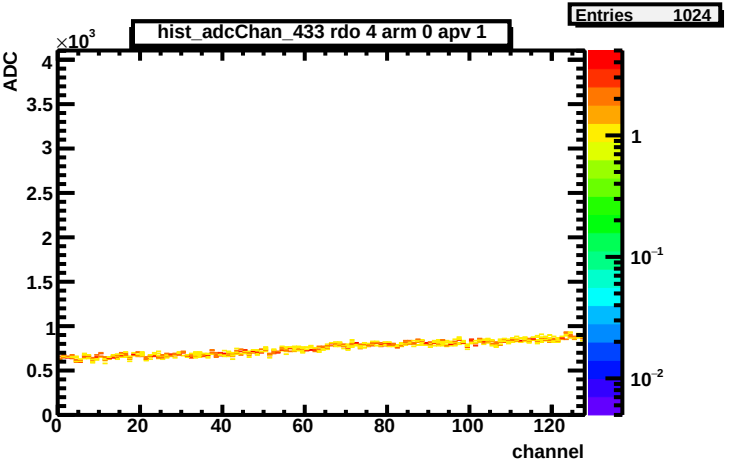

hist_adcChan_428 rdo 3 arm 5 apv 20
Entries 1024
ADC
×10^3
channel

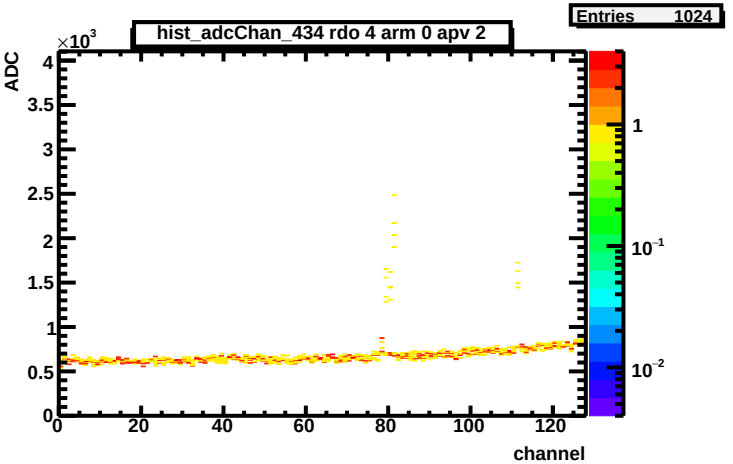

hist_adcChan_429 rdo 3 arm 5 apv 21
Entries 1024
ADC
×10^3
channel

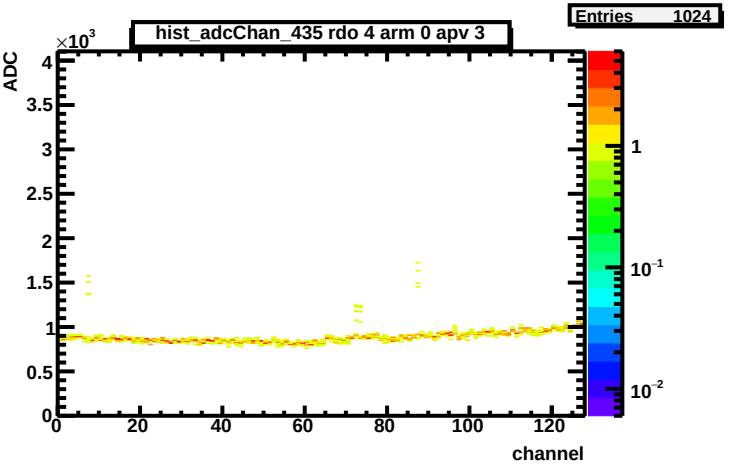

hist_adcChan_430 rdo 3 arm 5 apv 22
Entries 1024
ADC
×10^3
channel

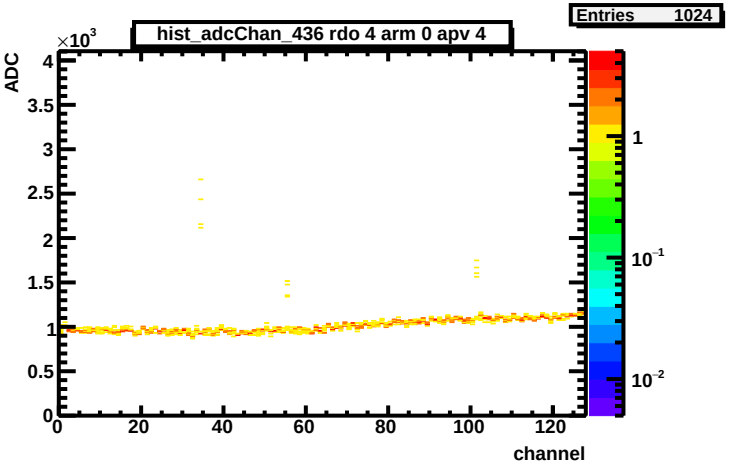

hist_adcChan_431 rdo 3 arm 5 apv 23
Entries 1024
ADC
×10^3
channel

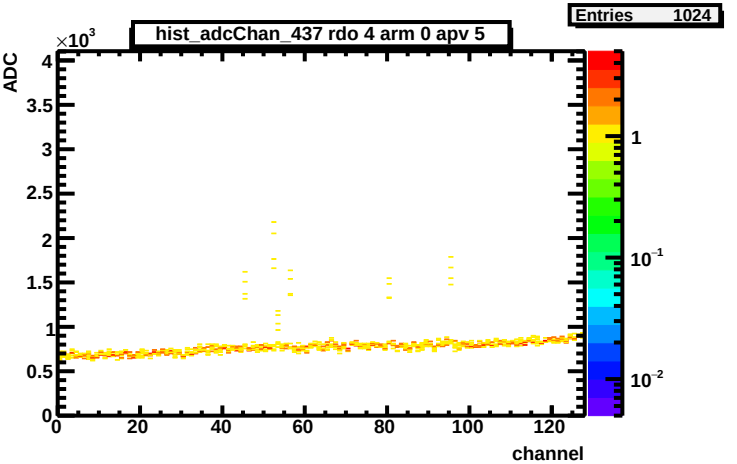

hist_adcChan_432 rdo 4 arm 0 apv 0
Entries 1024
ADC
channel

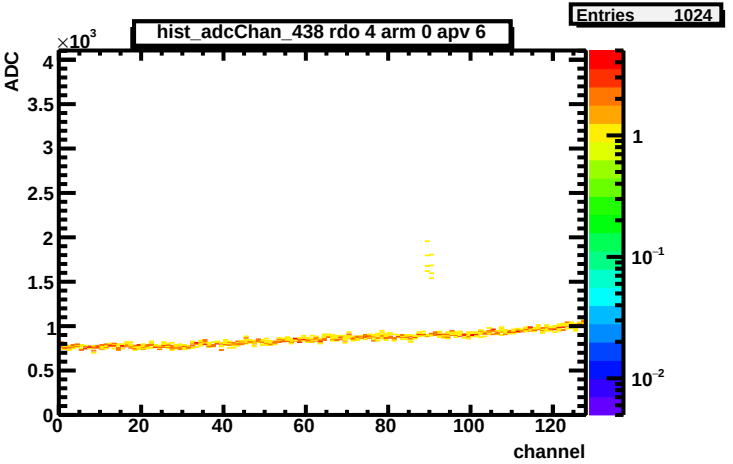

hist_adcChan_433 rdo 4 arm 0 apv 1
Entries 1024
ADC
channel

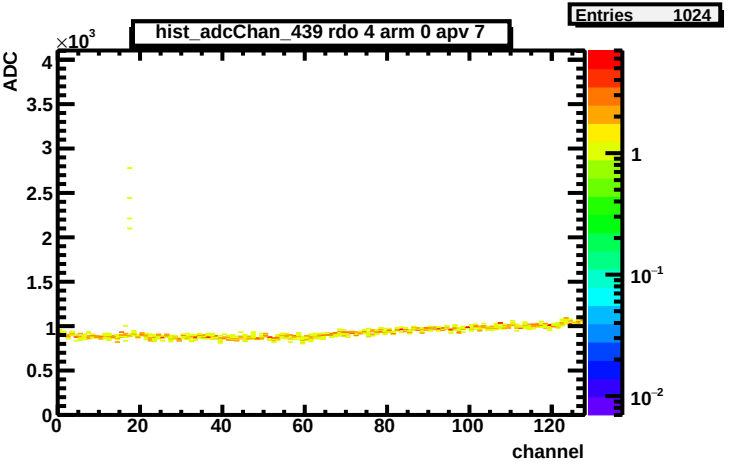

hist_adcChan_434 rdo 4 arm 0 apv 2
Entries 1024
ADC
channel

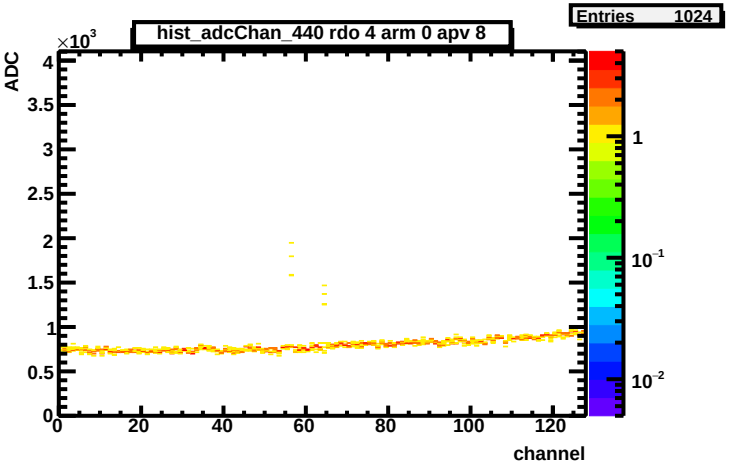

hist_adcChan_435 rdo 4 arm 0 apv 3
Entries 1024
ADC
channel

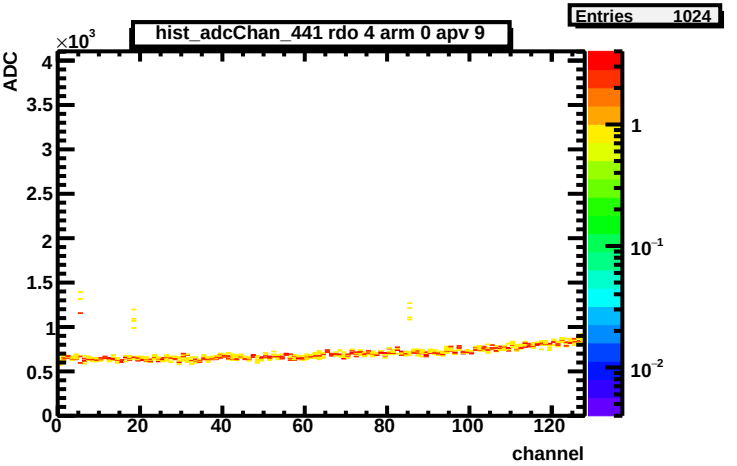

hist_adcChan_436 rdo 4 arm 0 apv 4
Entries 1024
ADC
channel

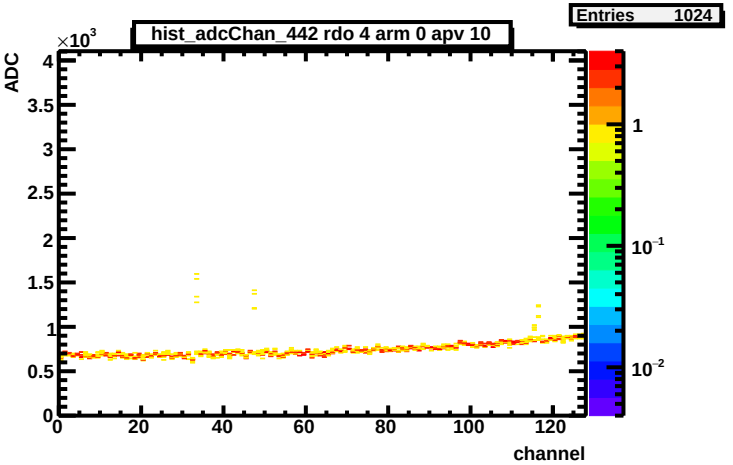

hist_adcChan_437 rdo 4 arm 0 apv 5
Entries 1024
ADC
channel

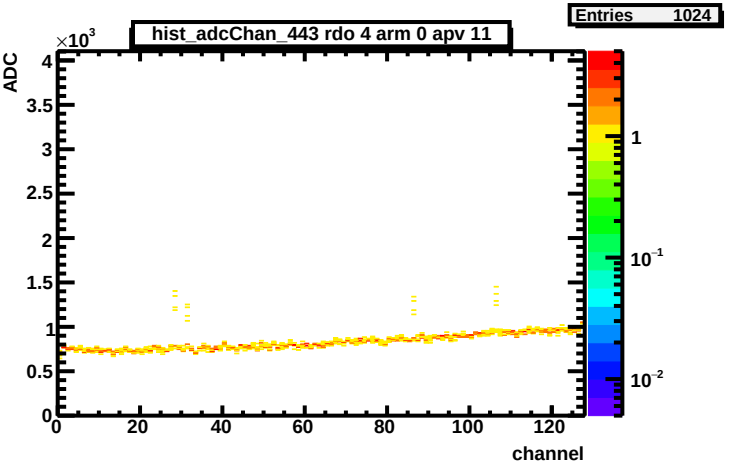

hist_adcChan_438 rdo 4 arm 0 apv 6
Entries 1024
ADC
channel

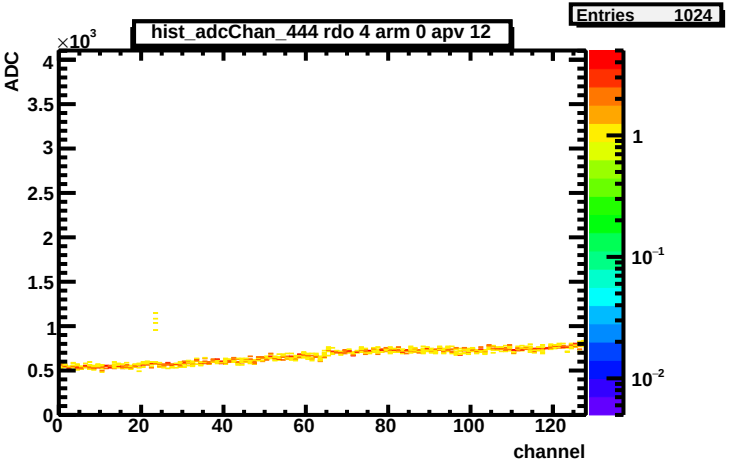

hist_adcChan_439 rdo 4 arm 0 apv 7
Entries 1024
ADC
channel

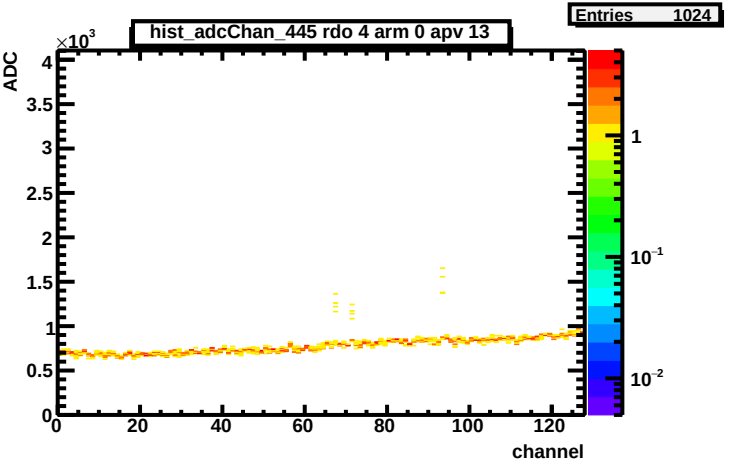

hist_adcChan_440 rdo 4 arm 0 apv 8
Entries 1024
ADC
channel

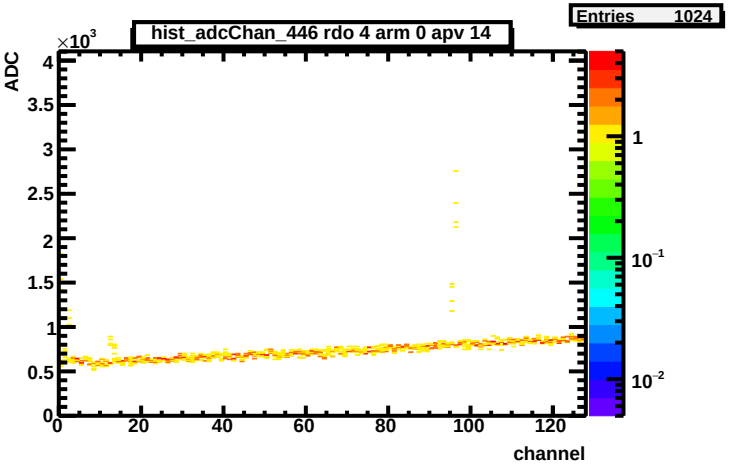

hist_adcChan_441 rdo 4 arm 0 apv 9
Entries 1024
ADC
channel

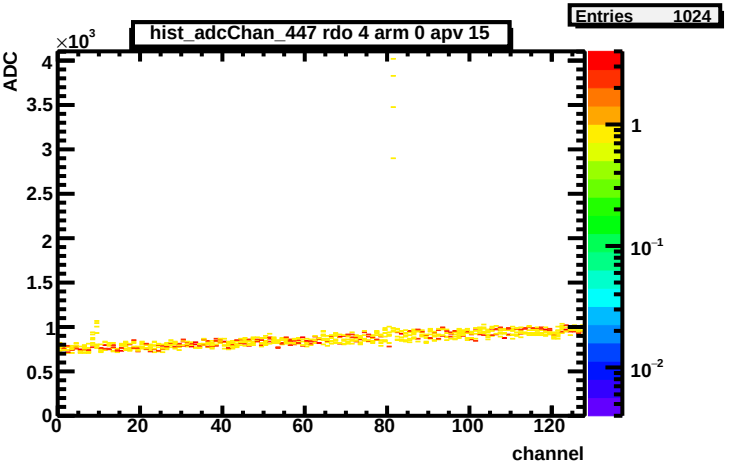

hist_adcChan_442 rdo 4 arm 0 apv 10
Entries 1024
ADC
channel

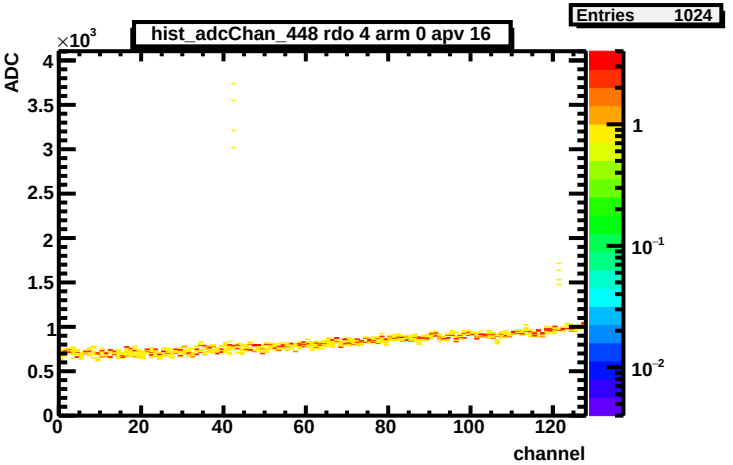

hist_adcChan_443 rdo 4 arm 0 apv 11
Entries 1024
ADC
channel

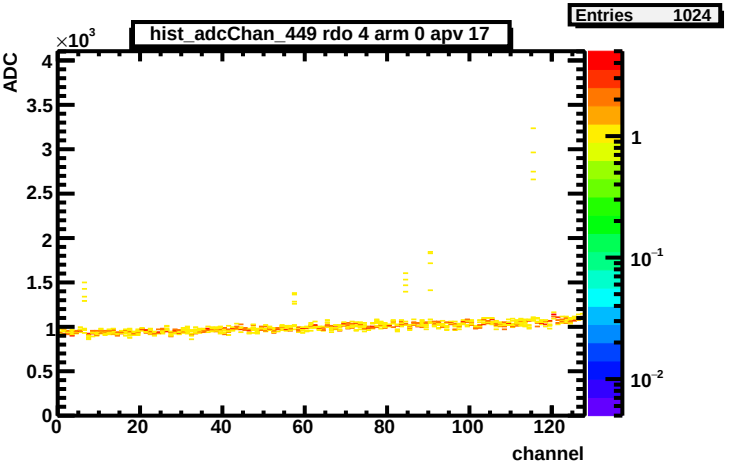

hist_adcChan_444 rdo 4 arm 0 apv 12
Entries 1024
ADC
channel

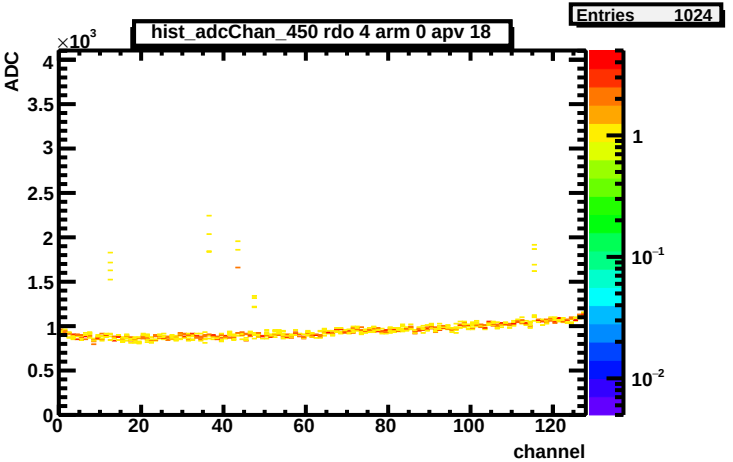

hist_adcChan_445 rdo 4 arm 0 apv 13
Entries 1024
ADC
channel

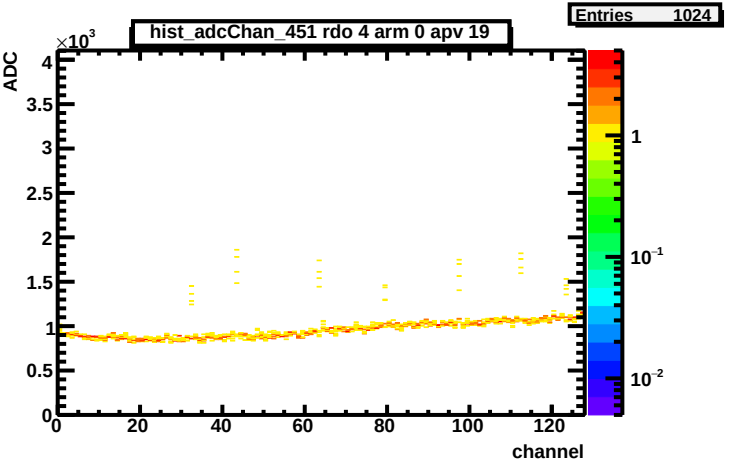

hist_adcChan_446 rdo 4 arm 0 apv 14
Entries 1024
ADC
channel

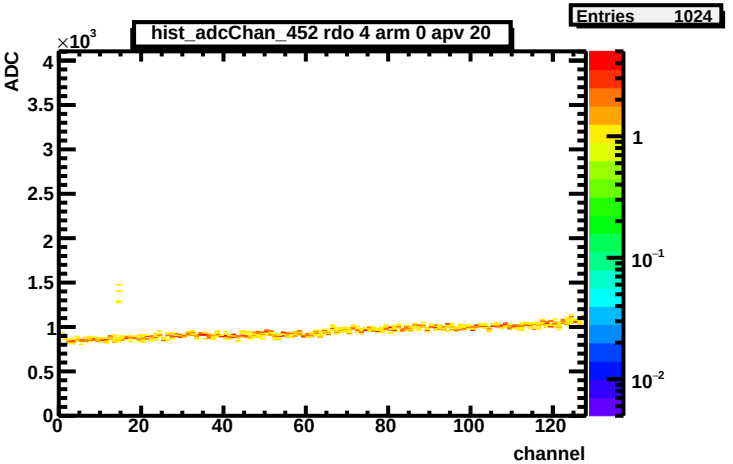

hist_adcChan_447 rdo 4 arm 0 apv 15
Entries 1024
ADC
channel

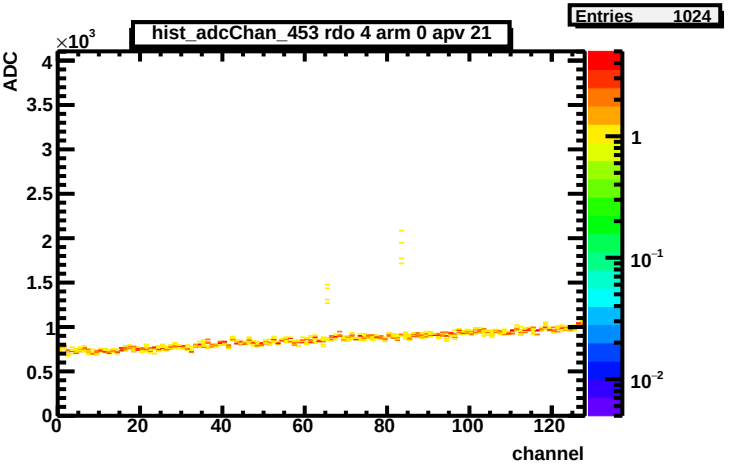

hist_adcChan_448 rdo 4 arm 0 apv 16
Entries 1024
ADC
channel

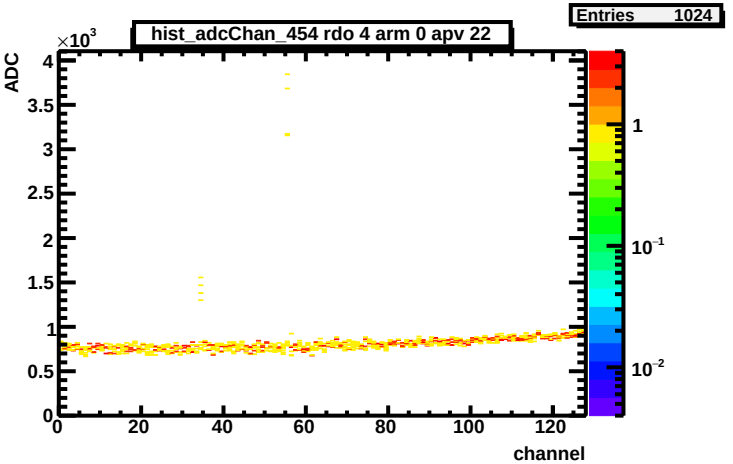

hist_adcChan_449 rdo 4 arm 0 apv 17
Entries 1024
ADC
channel

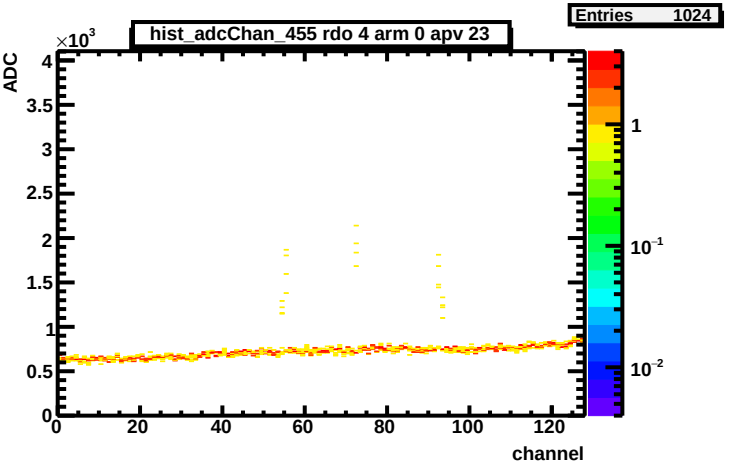

hist_adcChan_450 rdo 4 arm 0 apv 18
Entries 1024
ADC
channel

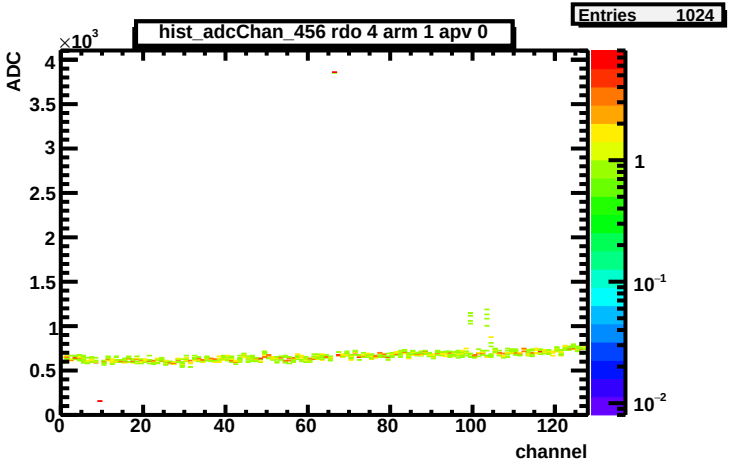

hist_adcChan_451 rdo 4 arm 0 apv 19
Entries 1024
ADC
channel

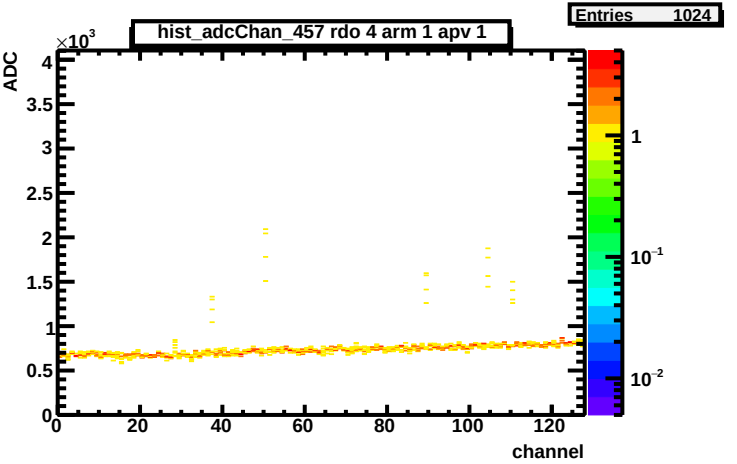

hist_adcChan_452 rdo 4 arm 0 apv 20
Entries 1024
ADC
channel

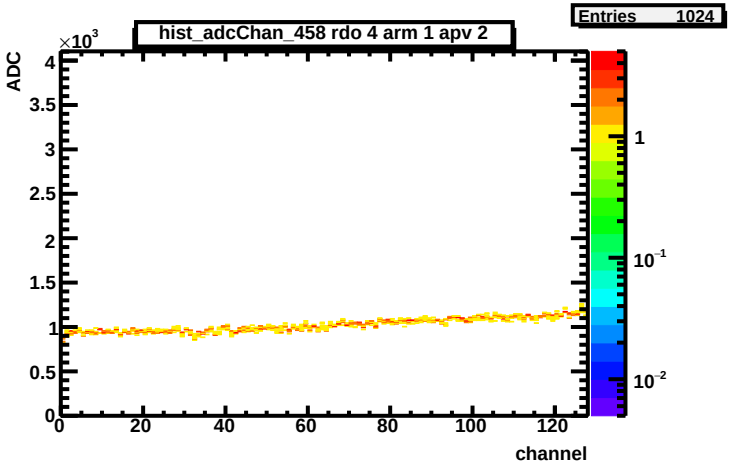

hist_adcChan_453 rdo 4 arm 0 apv 21
Entries 1024
ADC
channel

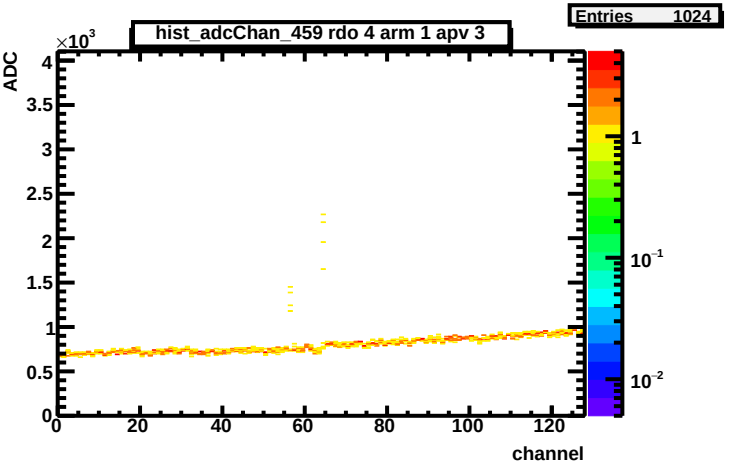

hist_adcChan_454 rdo 4 arm 0 apv 22
Entries 1024
ADC
channel

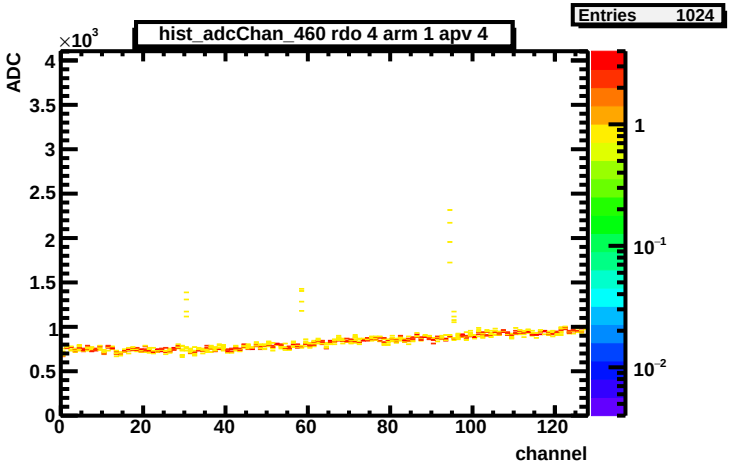

hist_adcChan_455 rdo 4 arm 0 apv 23
Entries 1024
ADC
channel

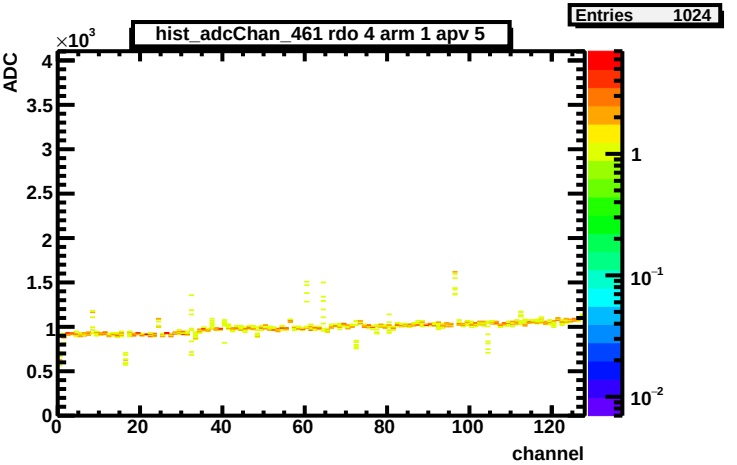

hist_adcChan_456 rdo 4 arm 1 apv 0
Entries 1024
ADC
channel

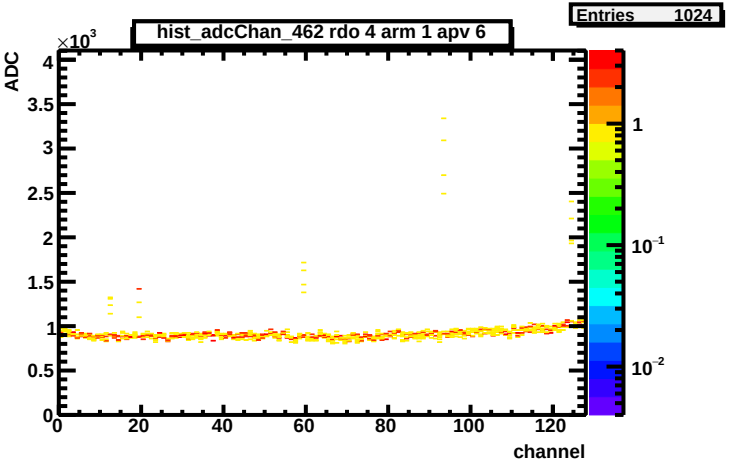

hist_adcChan_457 rdo 4 arm 1 apv 1
Entries 1024
ADC
channel

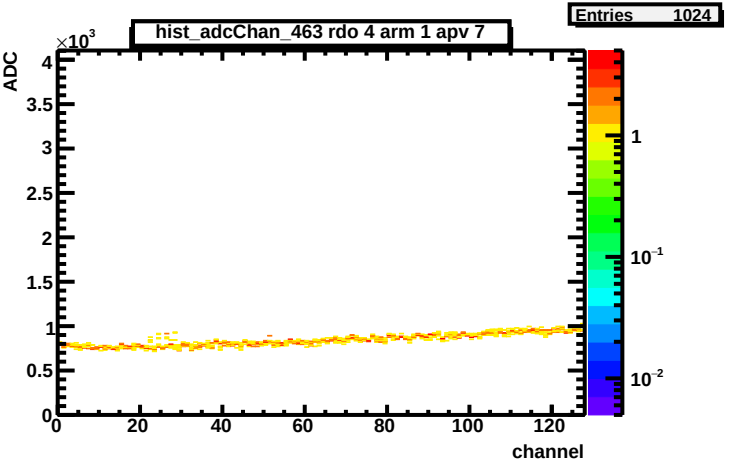

hist_adcChan_458 rdo 4 arm 1 apv 2
Entries 1024
ADC
channel

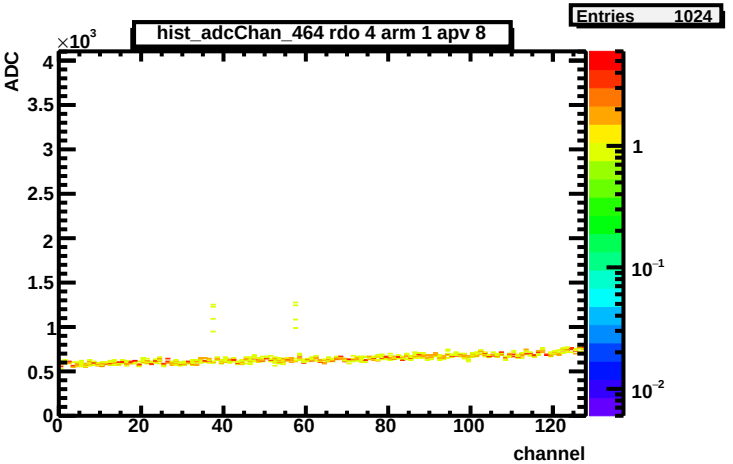

hist_adcChan_459 rdo 4 arm 1 apv 3
Entries 1024
ADC
channel

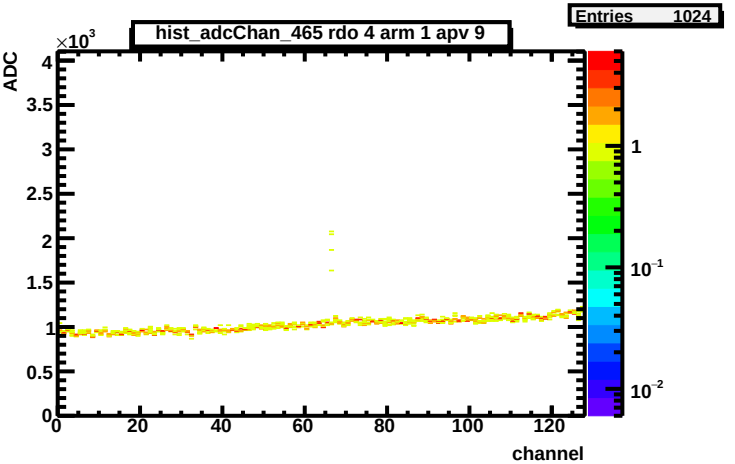

hist_adcChan_460 rdo 4 arm 1 apv 4
Entries 1024
ADC
channel

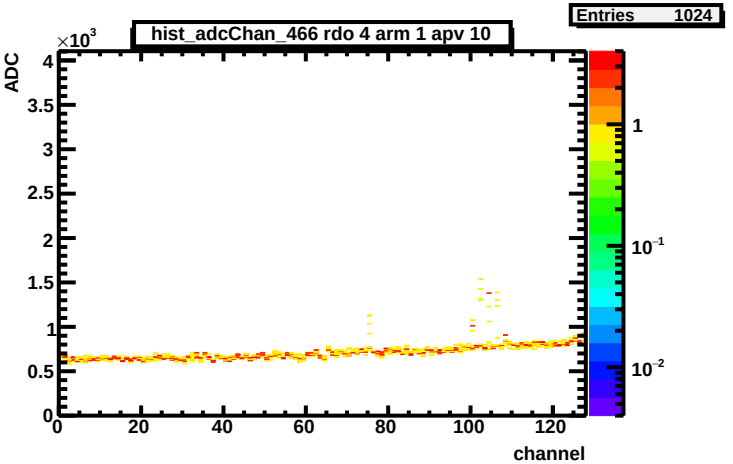

hist_adcChan_461 rdo 4 arm 1 apv 5
Entries 1024
ADC
channel

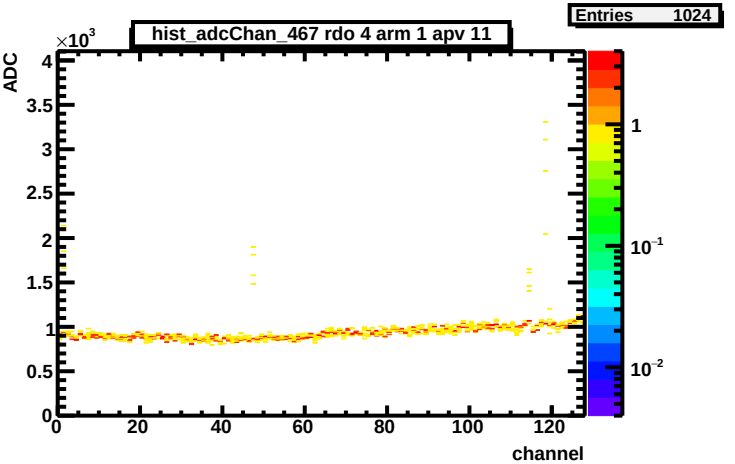

hist_adcChan_462 rdo 4 arm 1 apv 6
Entries 1024
ADC
channel

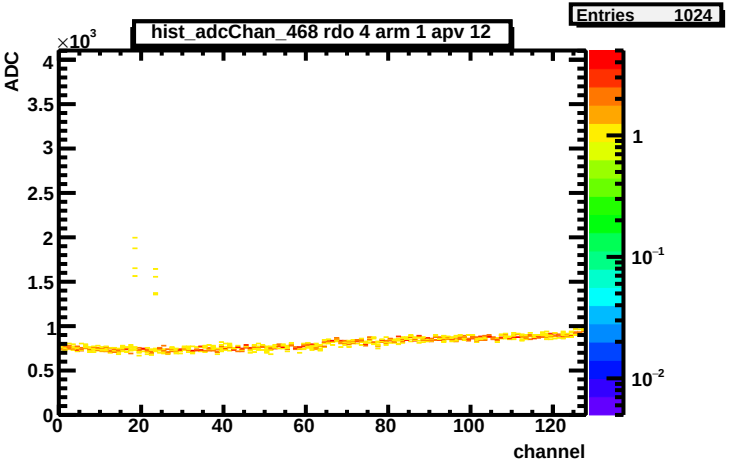

hist_adcChan_463 rdo 4 arm 1 apv 7
Entries 1024
ADC
channel

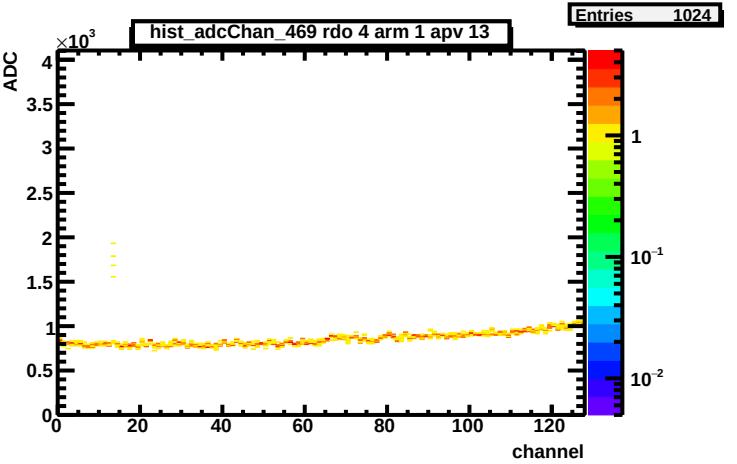

hist_adcChan_464 rdo 4 arm 1 apv 8
Entries 1024
ADC
channel

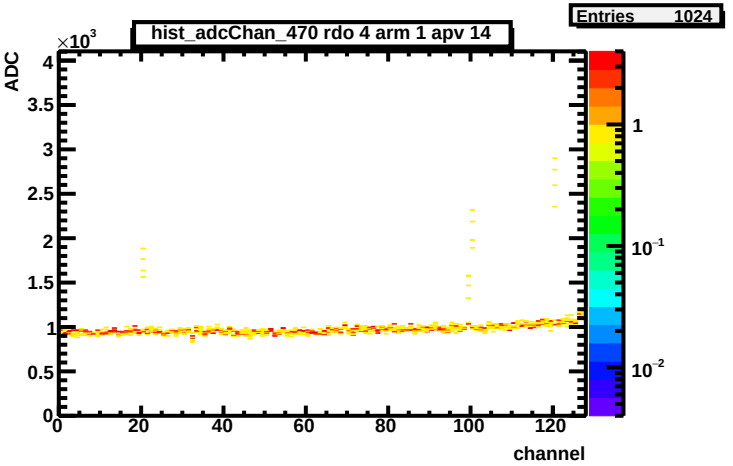

hist_adcChan_465 rdo 4 arm 1 apv 9
Entries 1024
ADC
channel

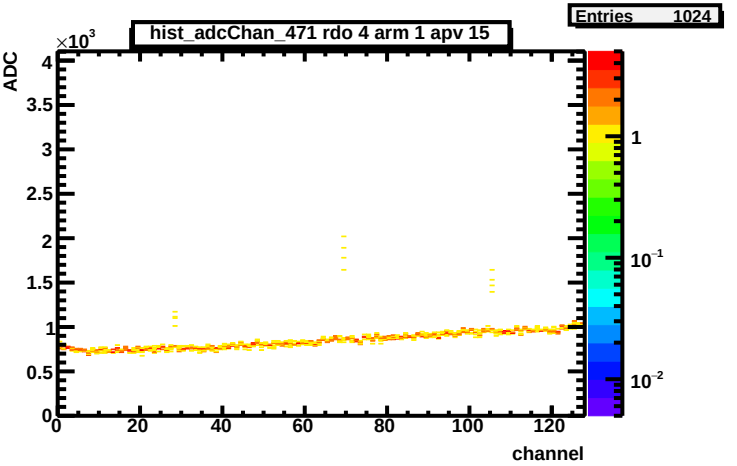

hist_adcChan_466 rdo 4 arm 1 apv 10
Entries 1024
ADC
channel

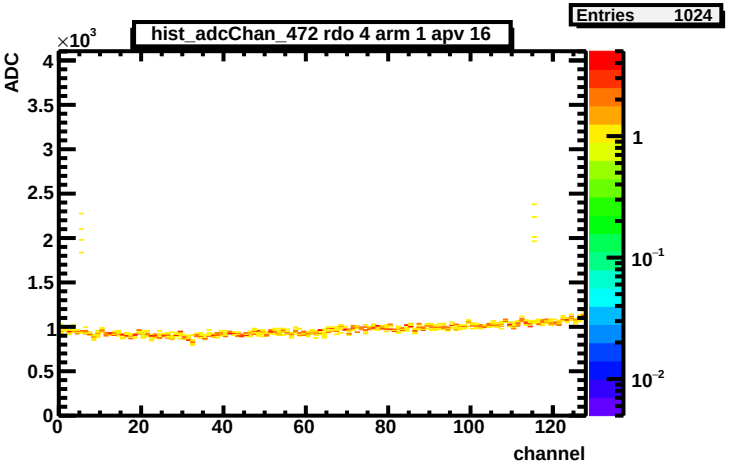

hist_adcChan_467 rdo 4 arm 1 apv 11
Entries 1024
ADC
channel

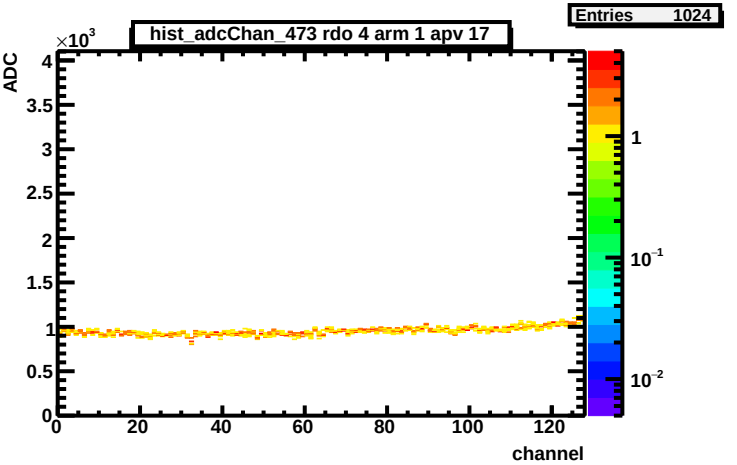

hist_adcChan_468 rdo 4 arm 1 apv 12
Entries 1024
ADC
×10³
channel

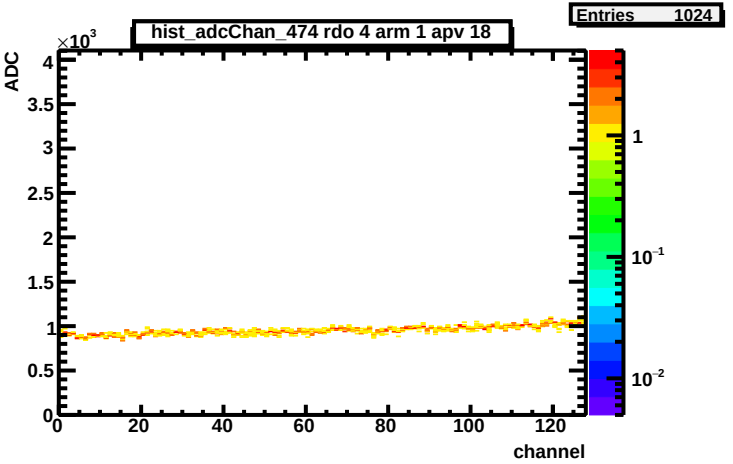

hist_adcChan_469 rdo 4 arm 1 apv 13
Entries 1024
ADC
×10³
channel

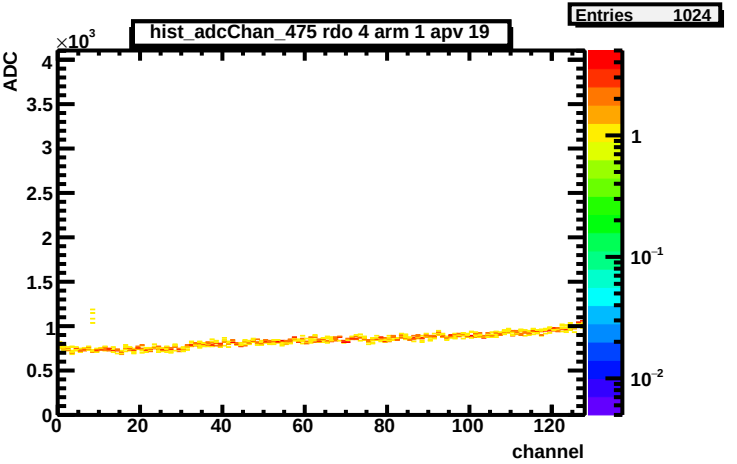

hist_adcChan_470 rdo 4 arm 1 apv 14
Entries 1024
ADC
×10³
channel

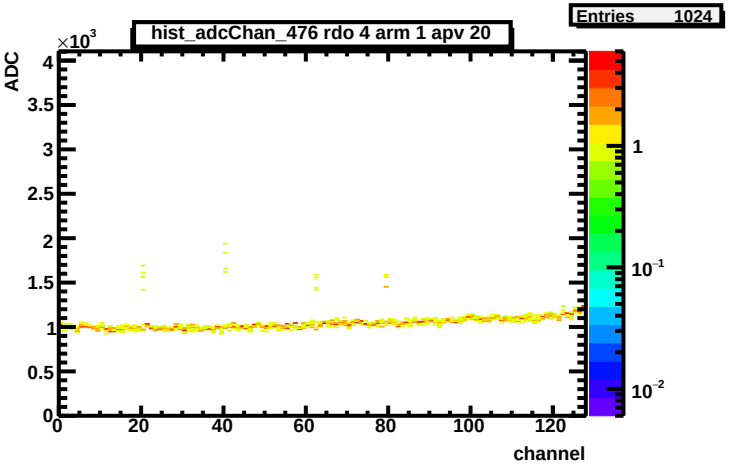

hist_adcChan_471 rdo 4 arm 1 apv 15
Entries 1024
ADC
×10³
channel

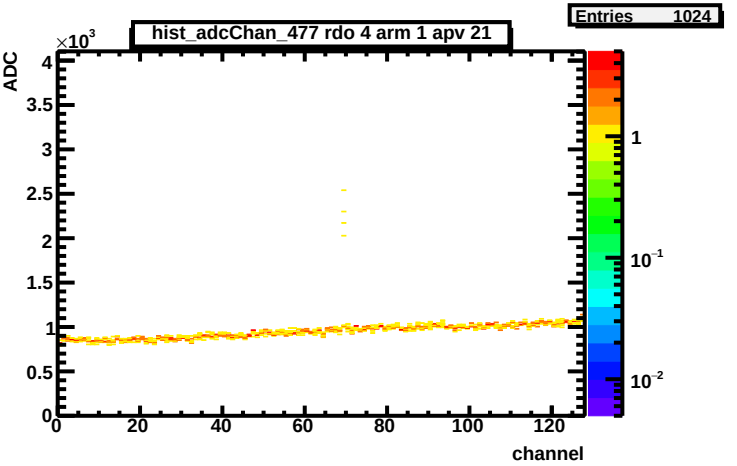

hist_adcChan_472 rdo 4 arm 1 apv 16
Entries 1024
ADC
×10³
channel

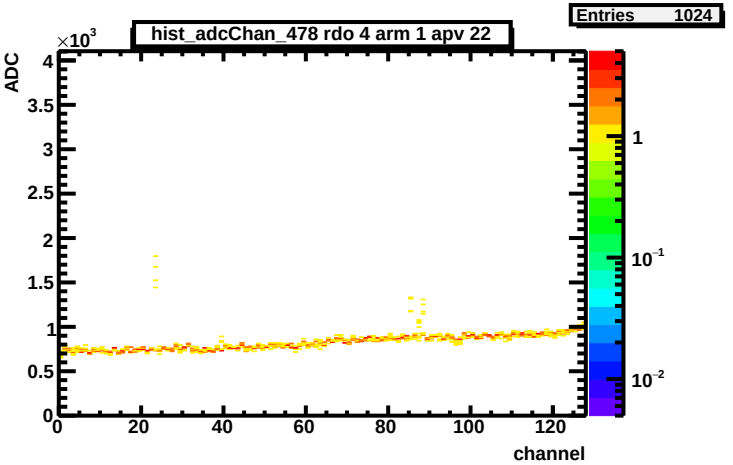

hist_adcChan_473 rdo 4 arm 1 apv 17
Entries 1024
ADC
×10³
channel

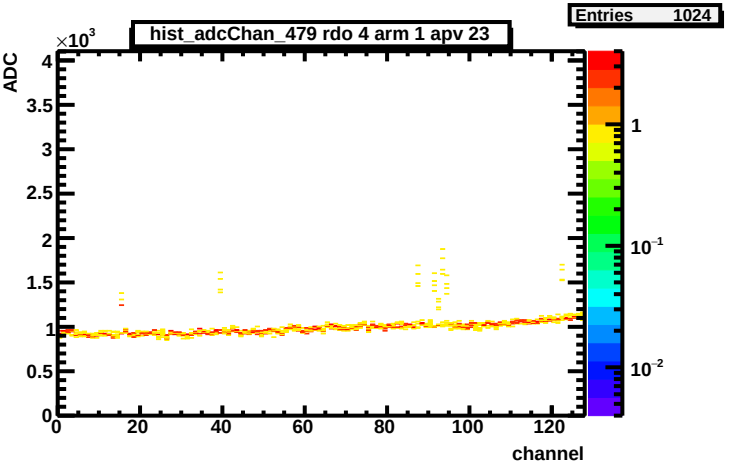

hist_adcChan_474 rdo 4 arm 1 apv 18
Entries 1024
ADC
×10^3
channel

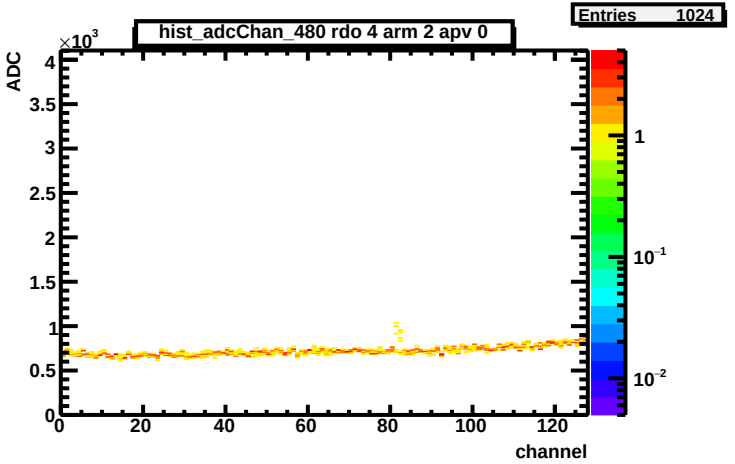

hist_adcChan_475 rdo 4 arm 1 apv 19
Entries 1024
ADC
×10^3
channel

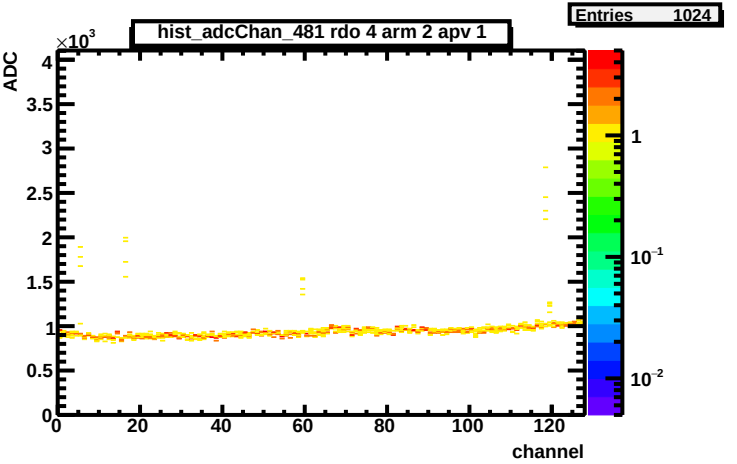

hist_adcChan_476 rdo 4 arm 1 apv 20
Entries 1024
ADC
×10^3
channel

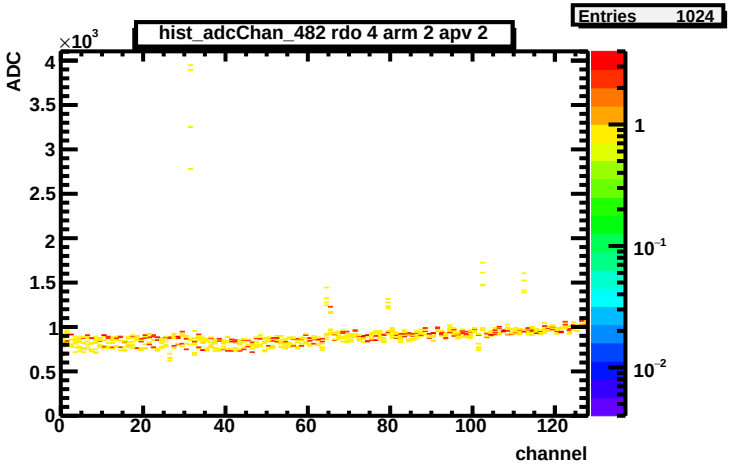

hist_adcChan_477 rdo 4 arm 1 apv 21
Entries 1024
ADC
×10^3
channel

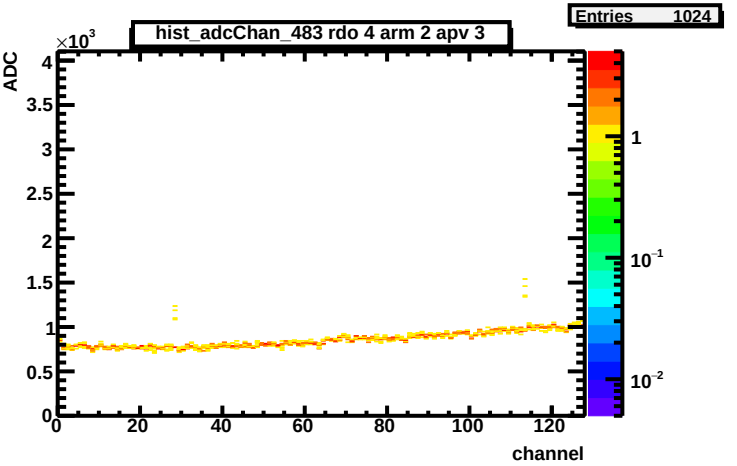

hist_adcChan_478 rdo 4 arm 1 apv 22
Entries 1024
ADC
×10^3
channel

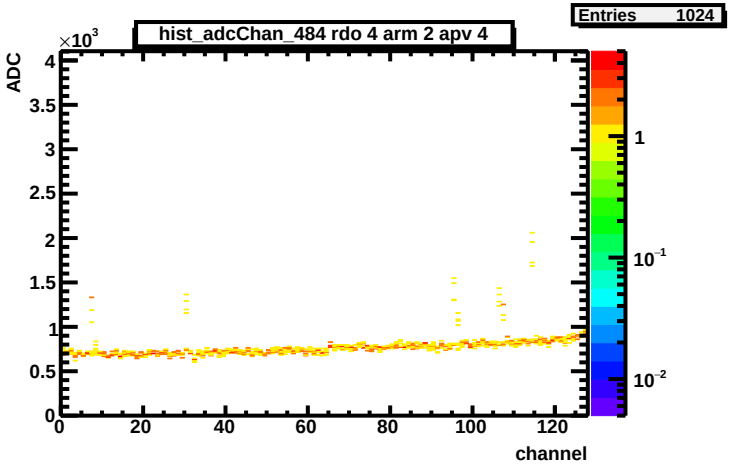

hist_adcChan_479 rdo 4 arm 1 apv 23
Entries 1024
ADC
×10^3
channel

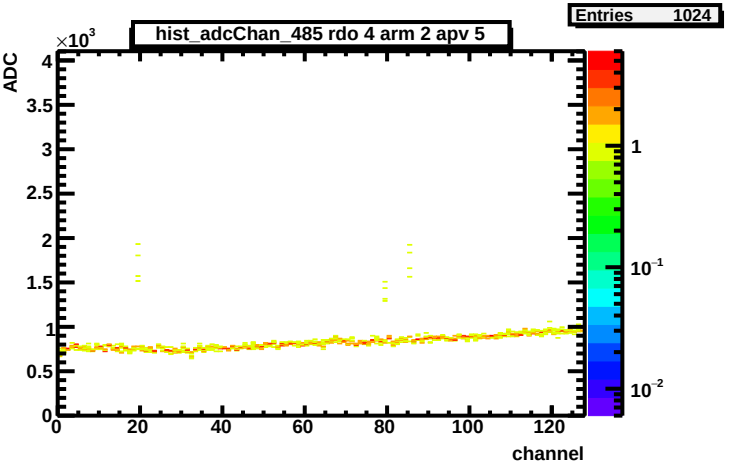

hist_adcChan_480 rdo 4 arm 2 apv 0
Entries 1024
ADC
channel

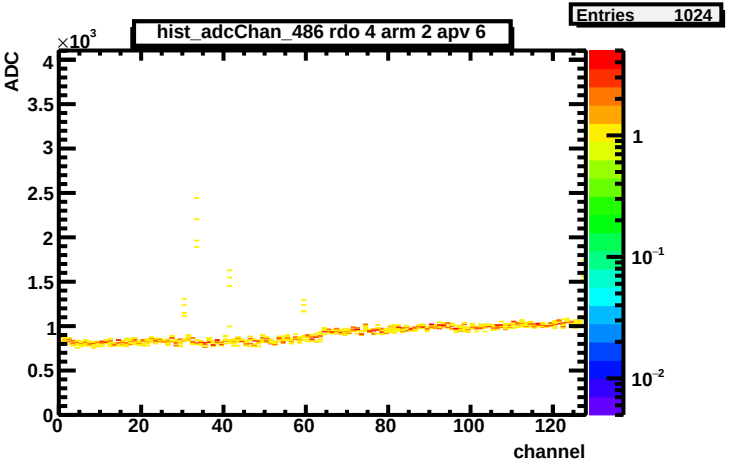

hist_adcChan_481 rdo 4 arm 2 apv 1
Entries 1024
ADC
channel

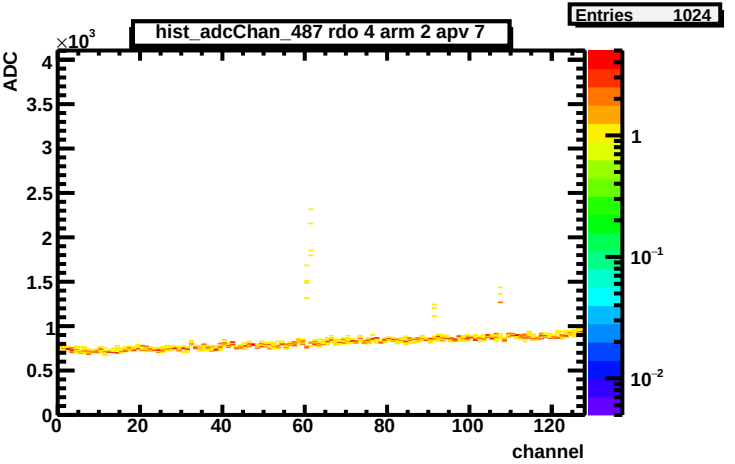

hist_adcChan_482 rdo 4 arm 2 apv 2
Entries 1024
ADC
channel

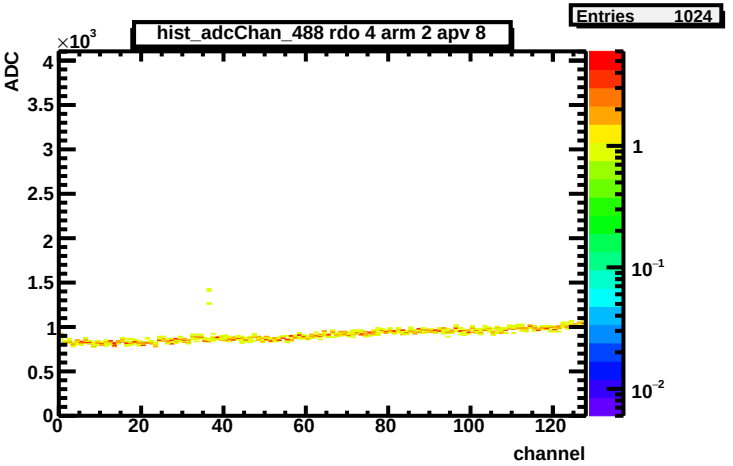

hist_adcChan_483 rdo 4 arm 2 apv 3
Entries 1024
ADC
channel

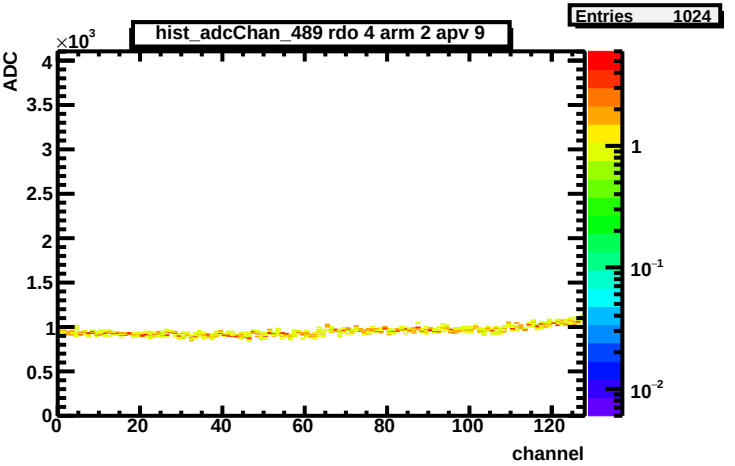

hist_adcChan_484 rdo 4 arm 2 apv 4
Entries 1024
ADC
channel

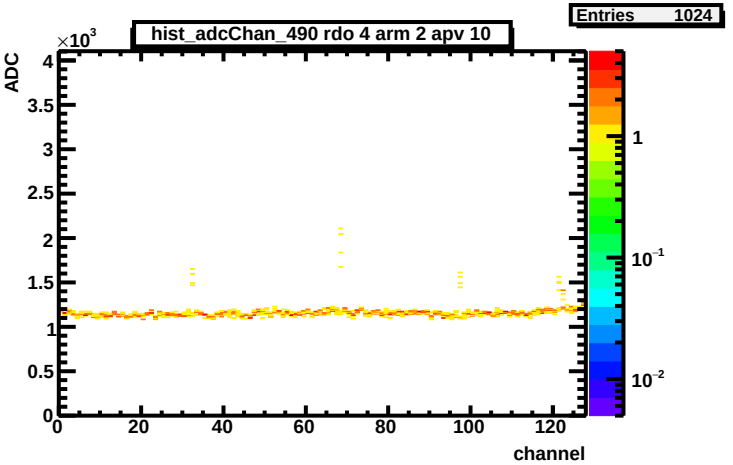

hist_adcChan_485 rdo 4 arm 2 apv 5
Entries 1024
ADC
channel

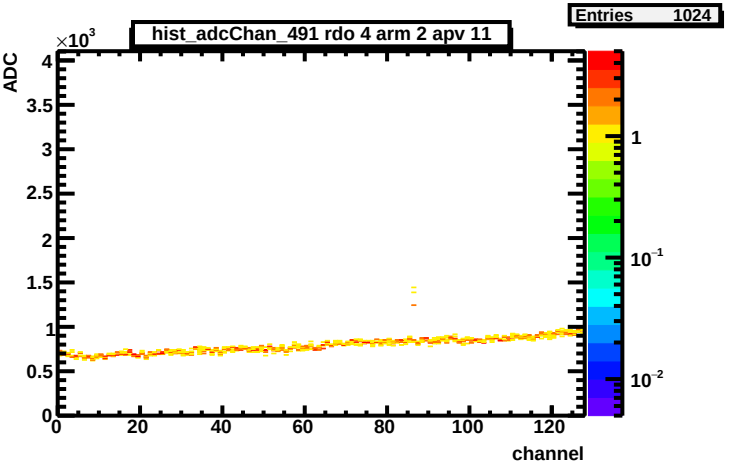

hist_adcChan_486 rdo 4 arm 2 apv 6
Entries 1024
ADC
channel

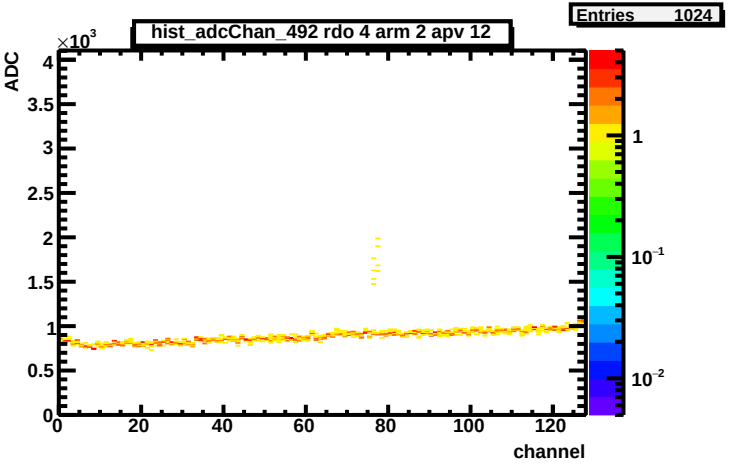

hist_adcChan_487 rdo 4 arm 2 apv 7
Entries 1024
ADC
channel

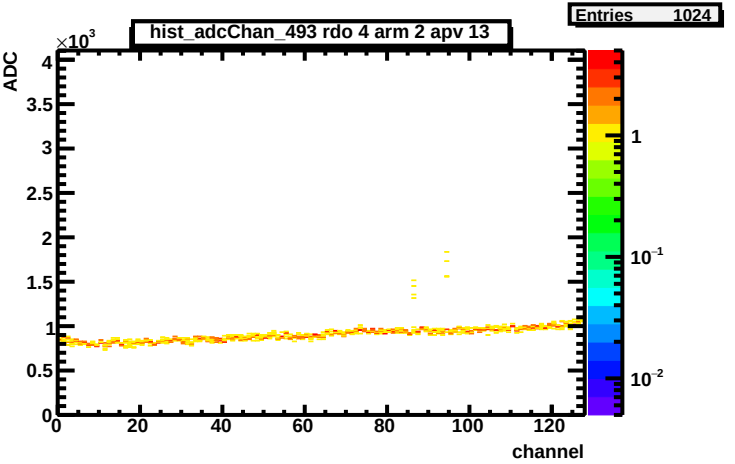

hist_adcChan_488 rdo 4 arm 2 apv 8
Entries 1024
ADC
channel

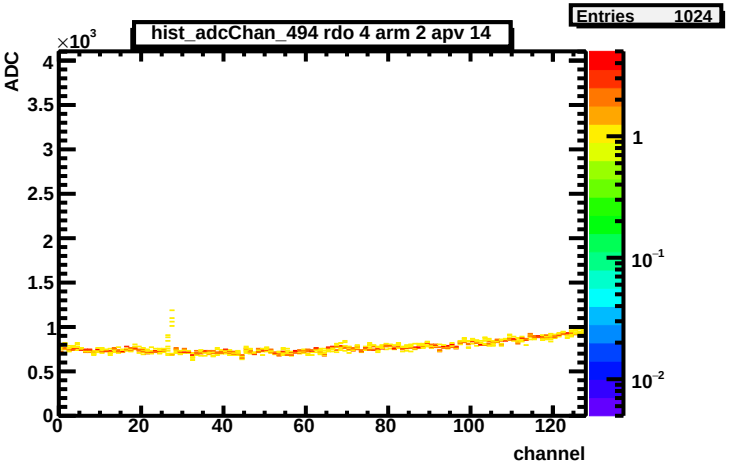

hist_adcChan_489 rdo 4 arm 2 apv 9
Entries 1024
ADC
channel

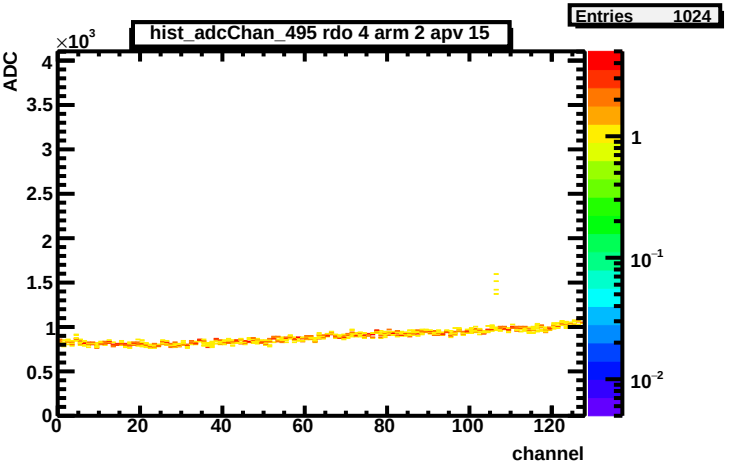

hist_adcChan_490 rdo 4 arm 2 apv 10
Entries 1024
ADC
channel

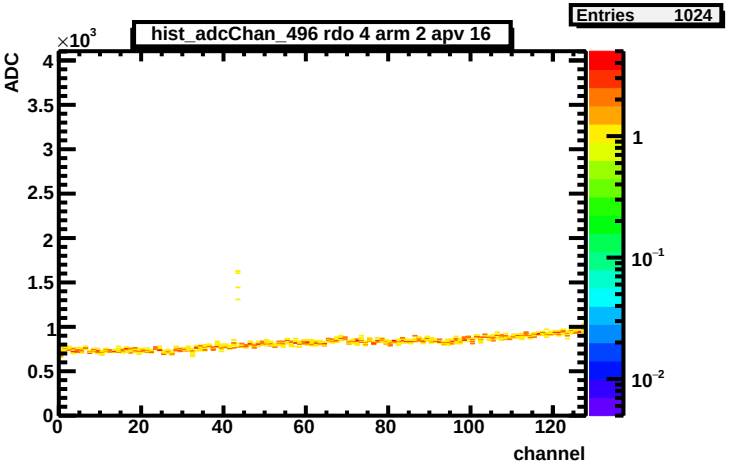

hist_adcChan_491 rdo 4 arm 2 apv 11
Entries 1024
ADC
channel


hist_adcChan_492 rdo 4 arm 2 apv 12
Entries 1024
ADC
channel


hist_adcChan_493 rdo 4 arm 2 apv 13
Entries 1024
ADC
channel


hist_adcChan_494 rdo 4 arm 2 apv 14
Entries 1024
ADC
channel


hist_adcChan_495 rdo 4 arm 2 apv 15
Entries 1024
ADC
channel


hist_adcChan_496 rdo 4 arm 2 apv 16
Entries 1024
ADC
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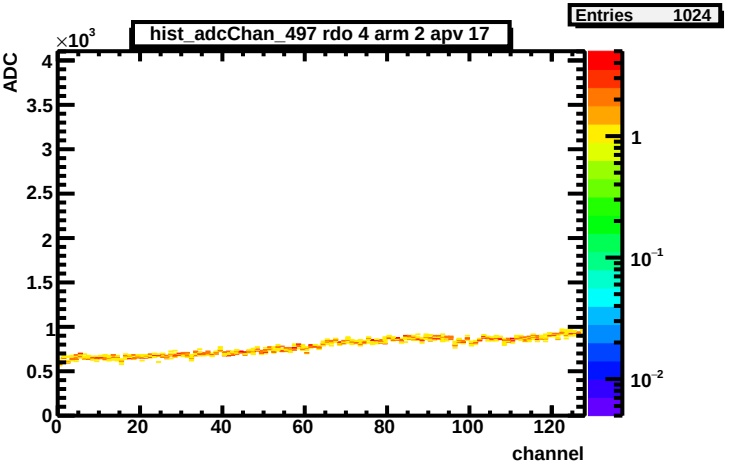
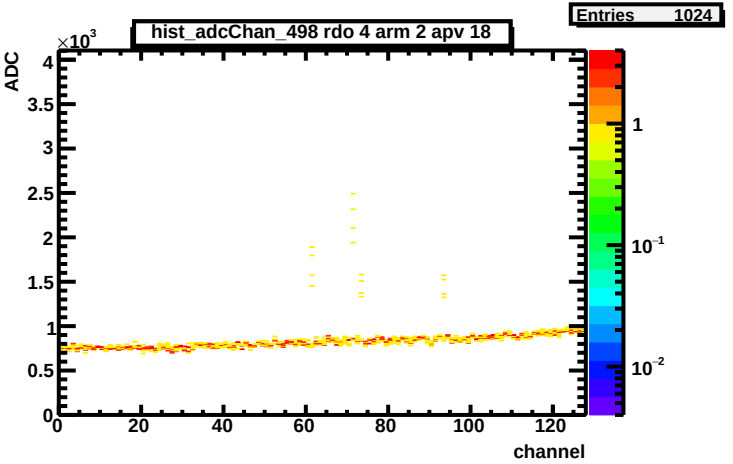
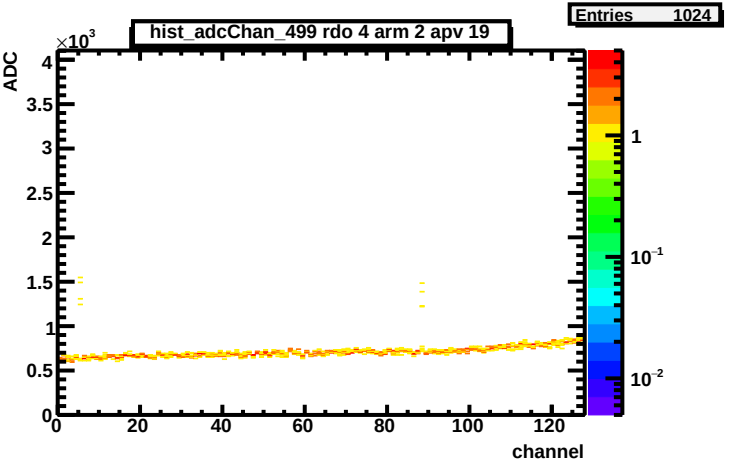
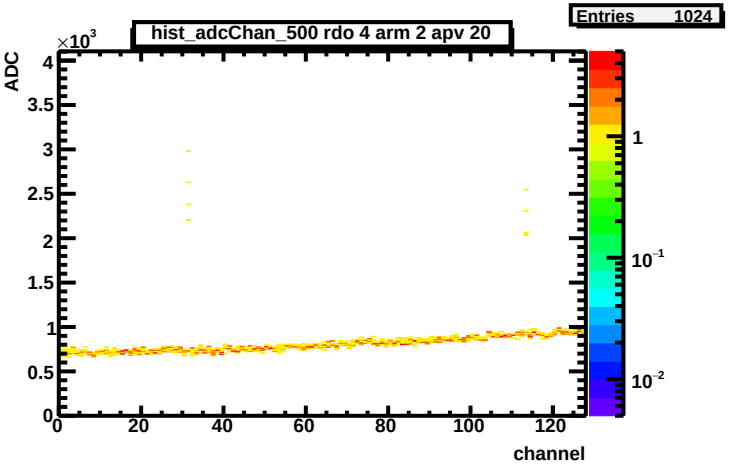
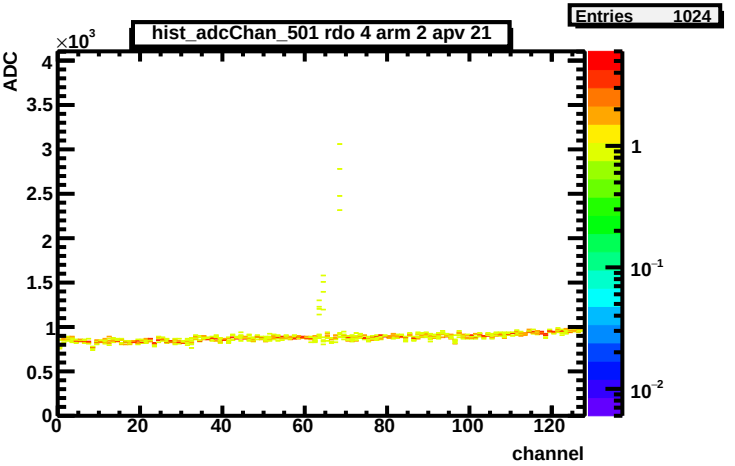
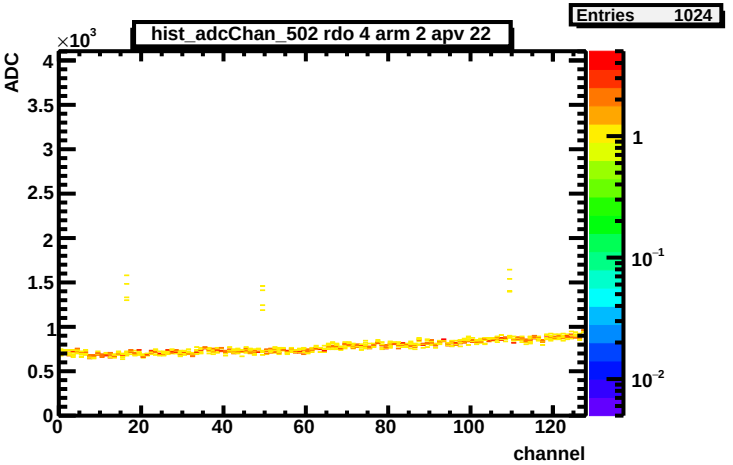

hist_adcChan_497 rdo 4 arm 2 apv 17
Entries 1024
ADC
channel

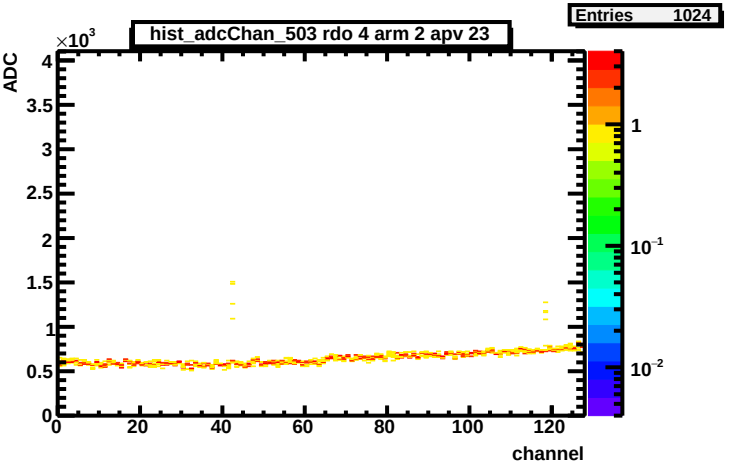

hist_adcChan_498 rdo 4 arm 2 apv 18
Entries 1024
ADC
channel

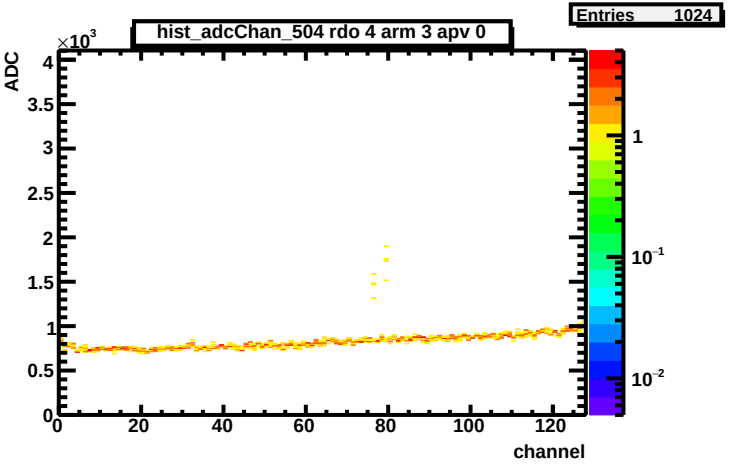

hist_adcChan_499 rdo 4 arm 2 apv 19
Entries 1024
ADC
channel

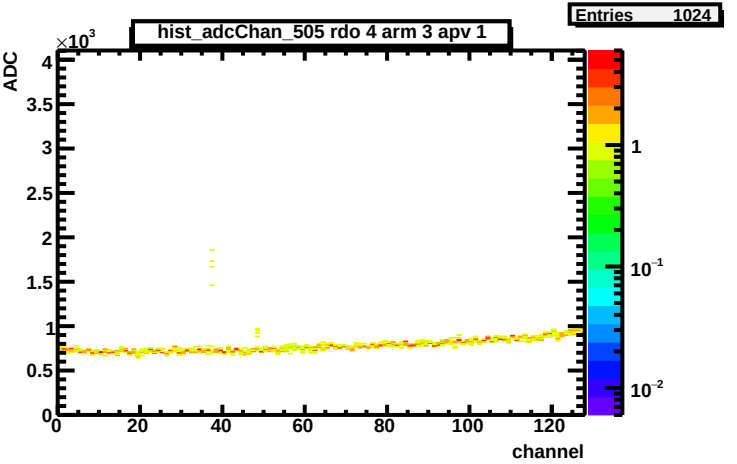

hist_adcChan_500 rdo 4 arm 2 apv 20
Entries 1024
ADC
channel

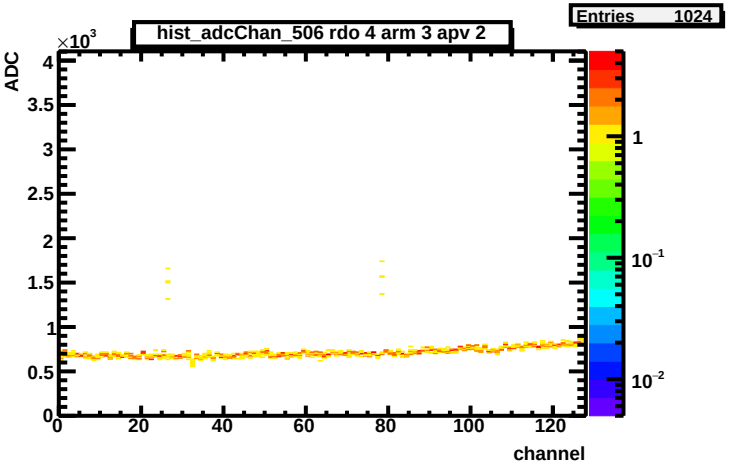

hist_adcChan_501 rdo 4 arm 2 apv 21
Entries 1024
ADC
channel

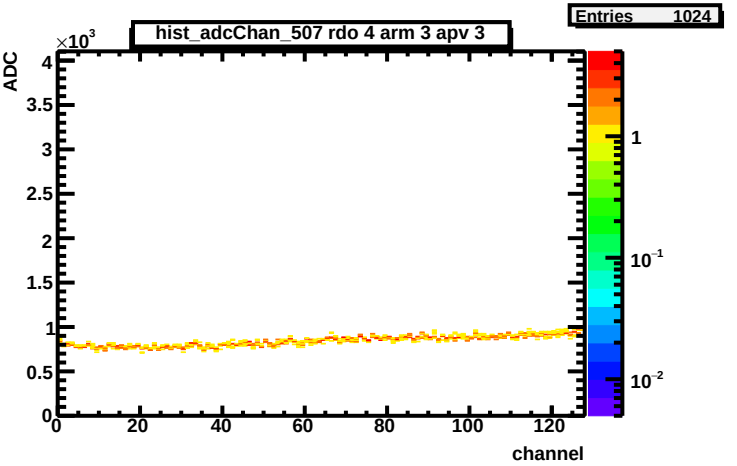

hist_adcChan_502 rdo 4 arm 2 apv 22
Entries 1024
ADC
channel

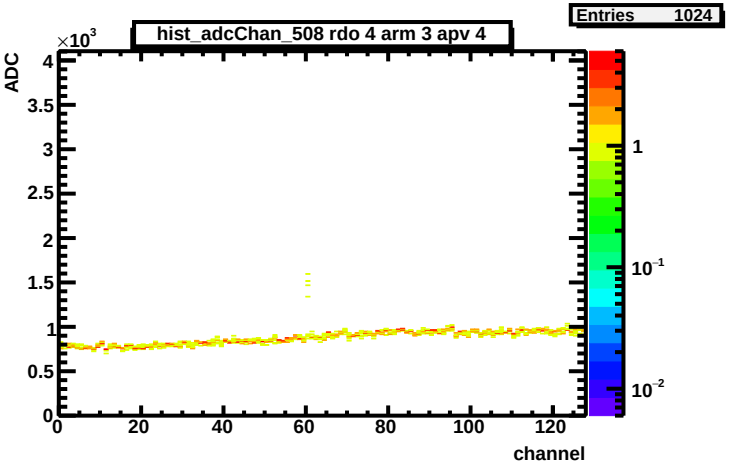

hist_adcChan_503 rdo 4 arm 2 apv 23
Entries 1024
ADC
channel

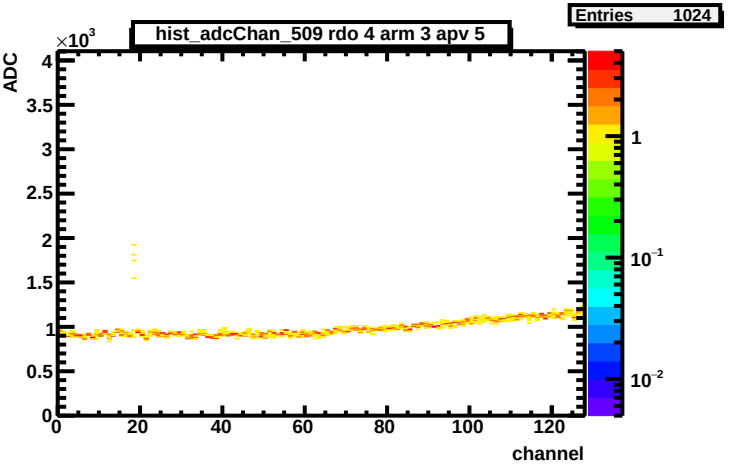

hist_adcChan_504 rdo 4 arm 3 apv 0
Entries 1024
ADC
×10^3
channel

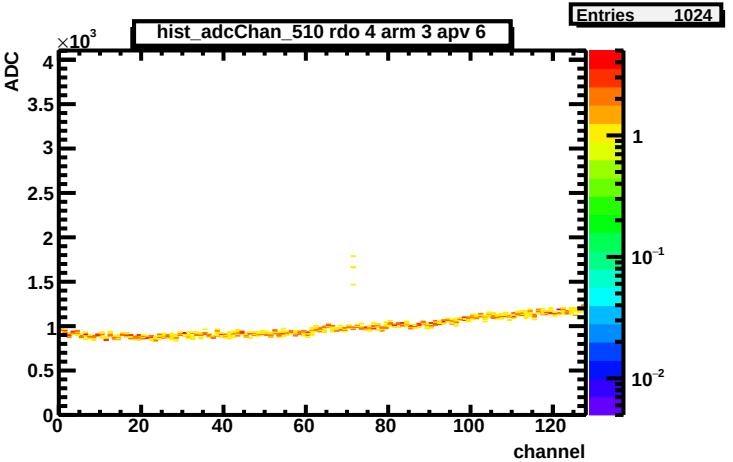

hist_adcChan_505 rdo 4 arm 3 apv 1
Entries 1024
ADC
×10^3
channel

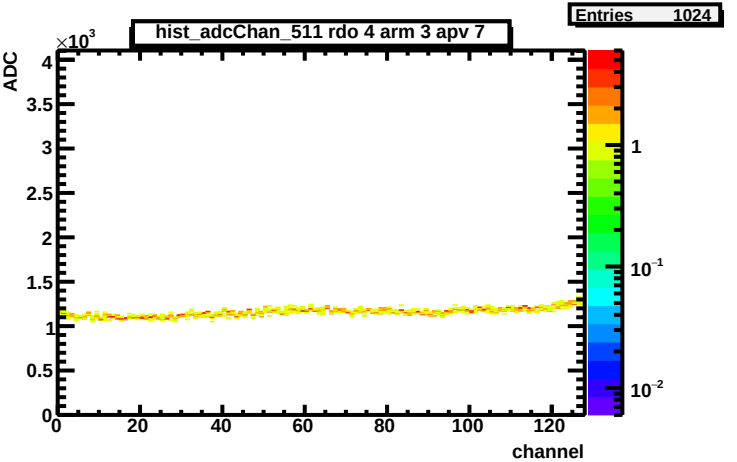

hist_adcChan_506 rdo 4 arm 3 apv 2
Entries 1024
ADC
×10^3
channel

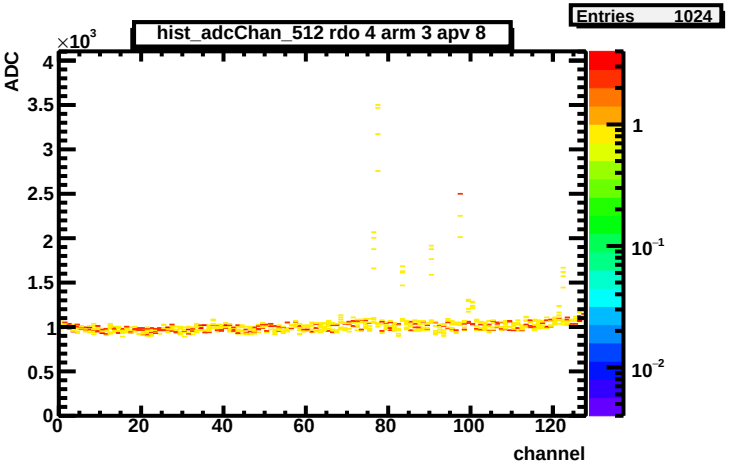

hist_adcChan_507 rdo 4 arm 3 apv 3
Entries 1024
ADC
×10^3
channel

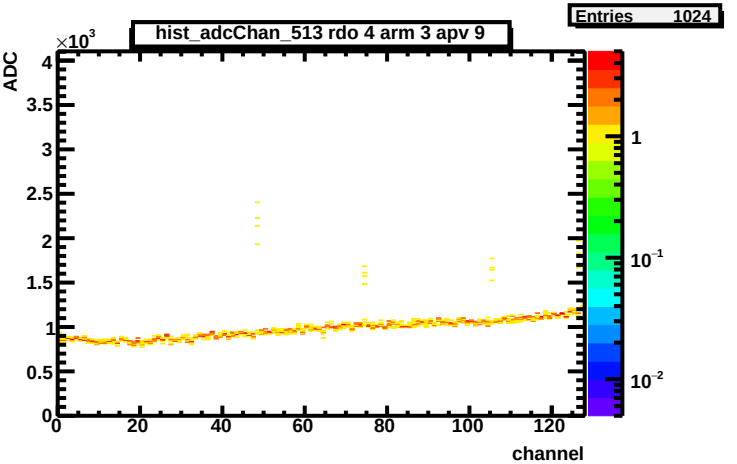

hist_adcChan_508 rdo 4 arm 3 apv 4
Entries 1024
ADC
×10^3
channel

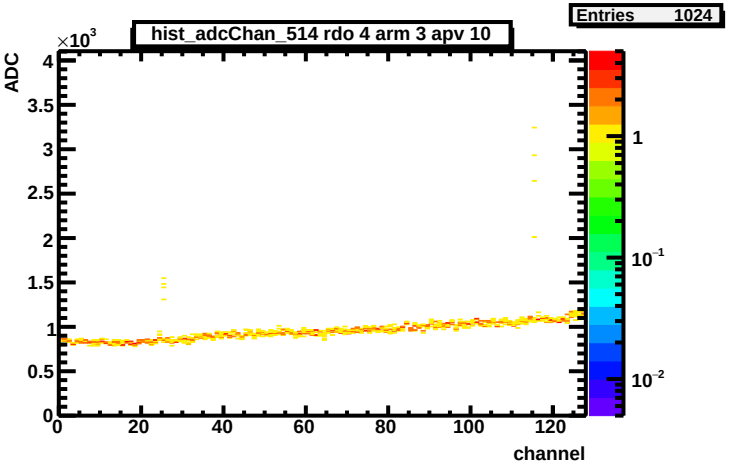

hist_adcChan_509 rdo 4 arm 3 apv 5
Entries 1024
ADC
×10^3
channel

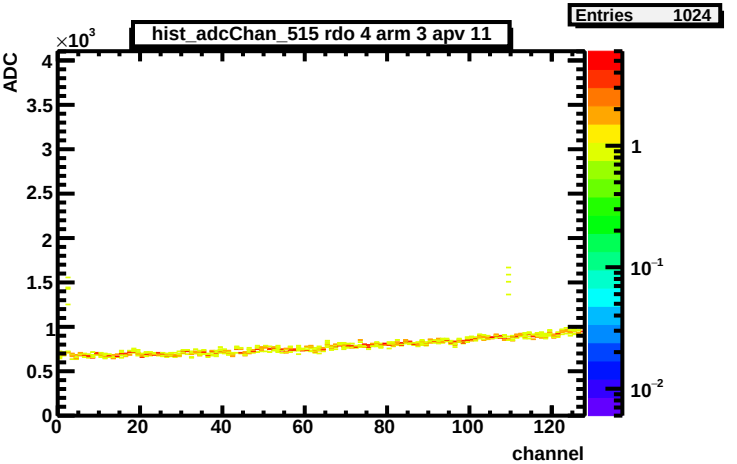

hist_adcChan_510 rdo 4 arm 3 apv 6
Entries 1024
ADC
×10^3
channel

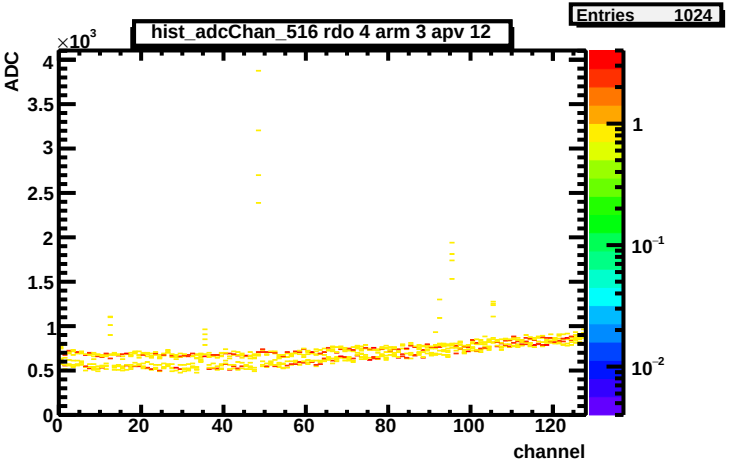

hist_adcChan_511 rdo 4 arm 3 apv 7
Entries 1024
ADC
×10^3
channel

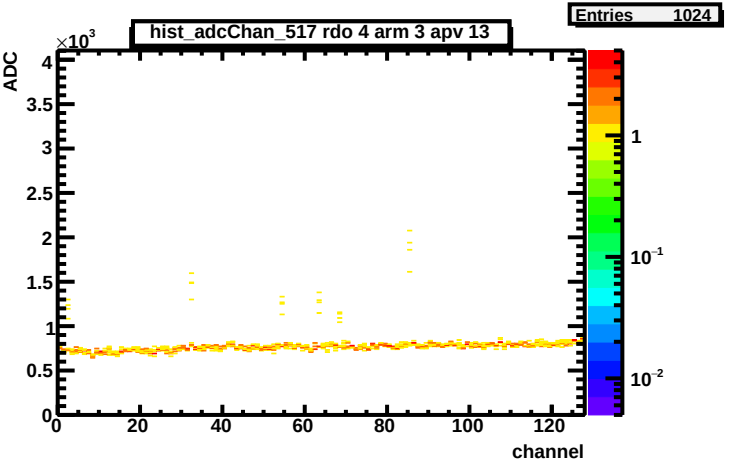

hist_adcChan_512 rdo 4 arm 3 apv 8
Entries 1024
ADC
×10^3
channel

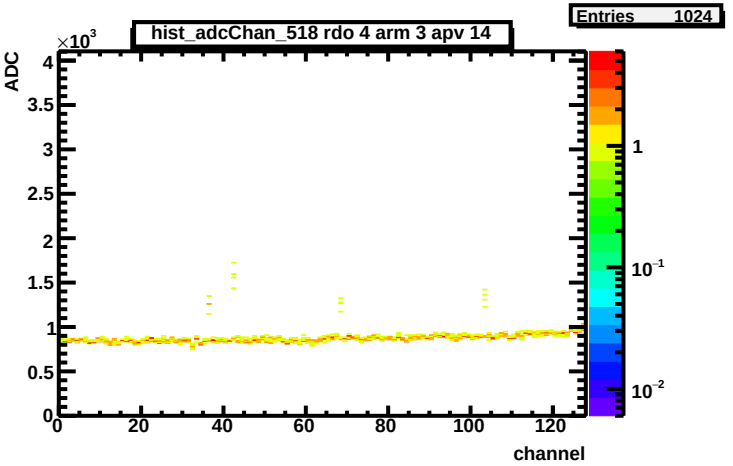

hist_adcChan_513 rdo 4 arm 3 apv 9
Entries 1024
ADC
×10^3
channel

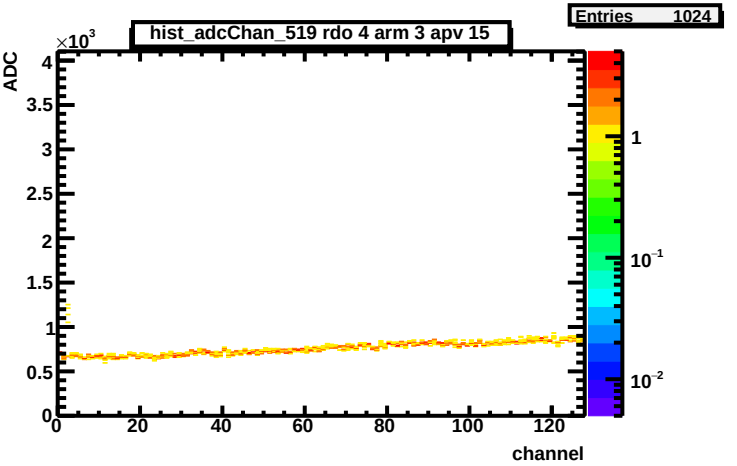

hist_adcChan_514 rdo 4 arm 3 apv 10
Entries 1024
ADC
×10^3
channel

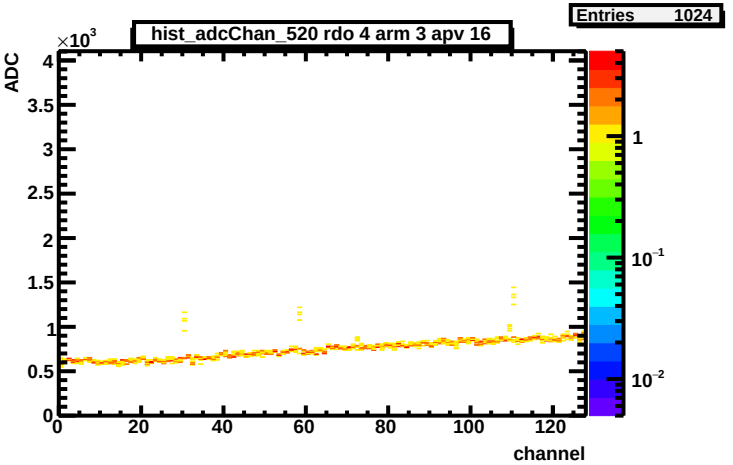

hist_adcChan_515 rdo 4 arm 3 apv 11
Entries 1024
ADC
×10^3
channel

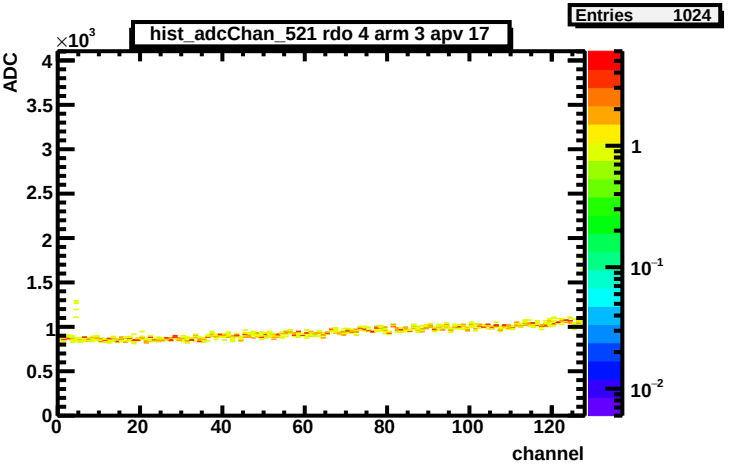

hist_adcChan_516 rdo 4 arm 3 apv 12
Entries 1024
ADC
×10³
channel

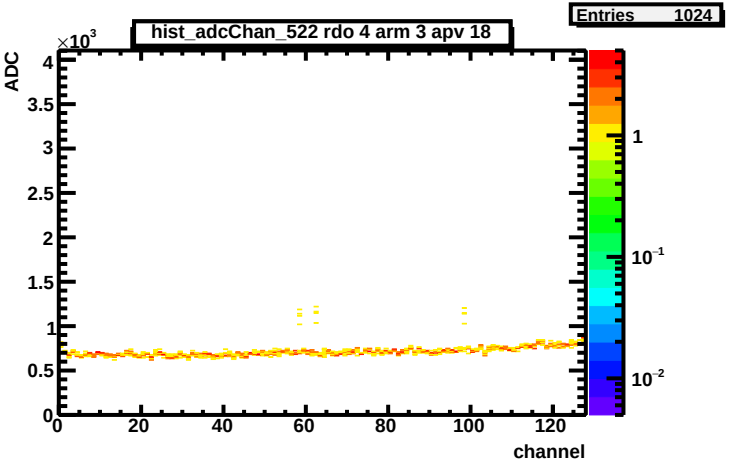

hist_adcChan_517 rdo 4 arm 3 apv 13
Entries 1024
ADC
×10³
channel

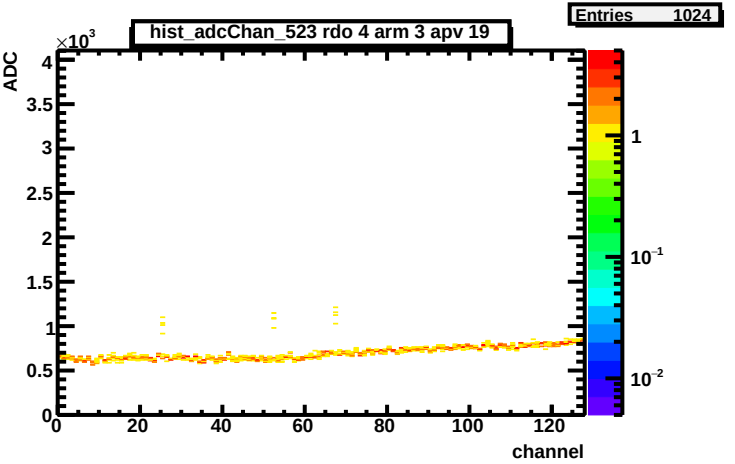

hist_adcChan_518 rdo 4 arm 3 apv 14
Entries 1024
ADC
×10³
channel

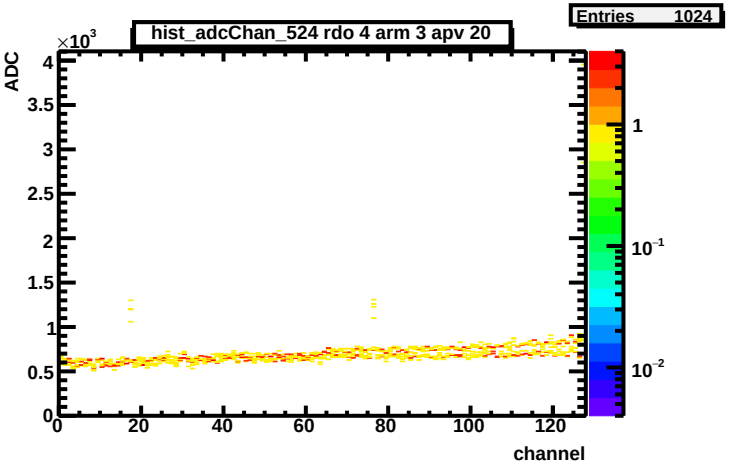

hist_adcChan_519 rdo 4 arm 3 apv 15
Entries 1024
ADC
×10³
channel

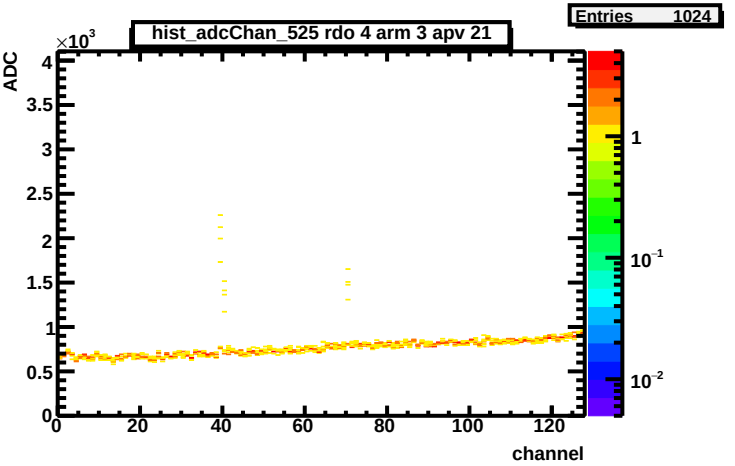

hist_adcChan_520 rdo 4 arm 3 apv 16
Entries 1024
ADC
×10³
channel

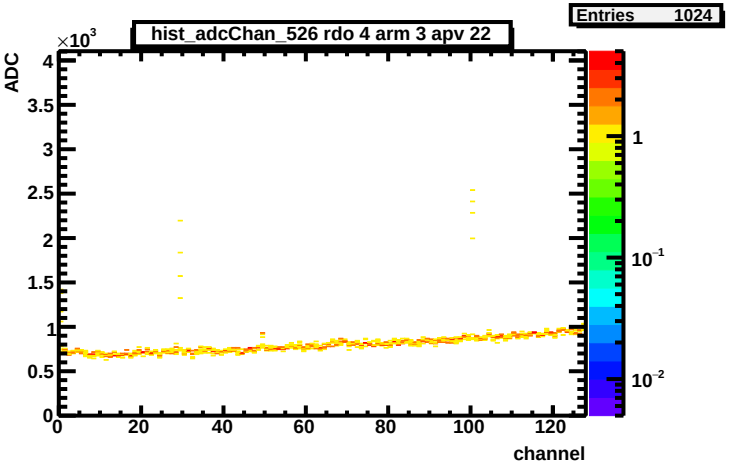

hist_adcChan_521 rdo 4 arm 3 apv 17
Entries 1024
ADC
×10³
channel

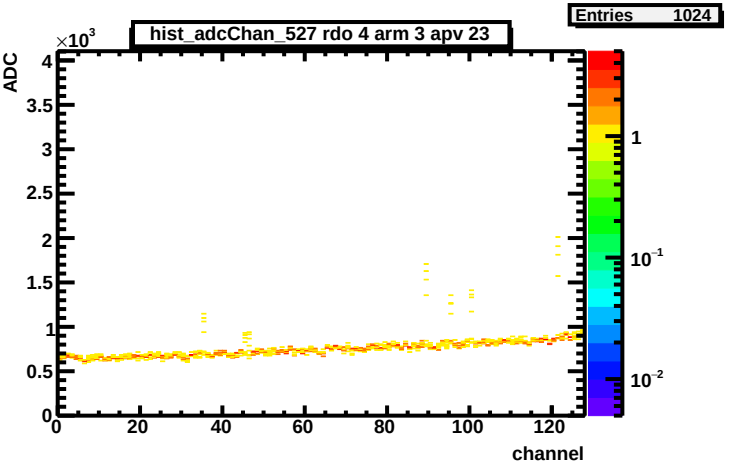

hist_adcChan_522 rdo 4 arm 3 apv 18
Entries 1024
ADC
channel

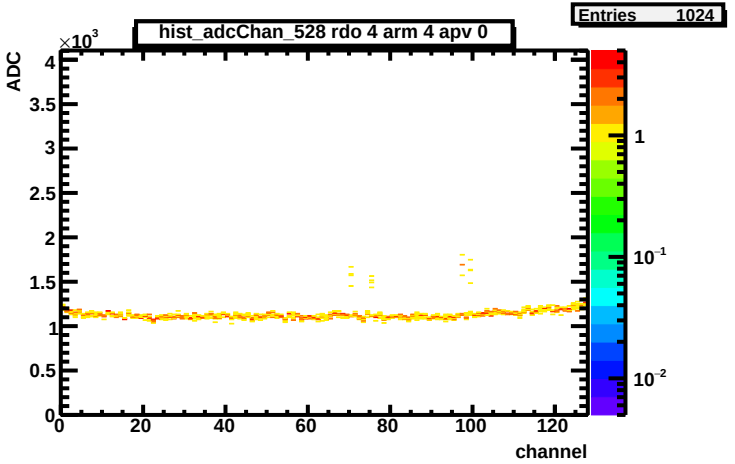

hist_adcChan_523 rdo 4 arm 3 apv 19
Entries 1024
ADC
channel

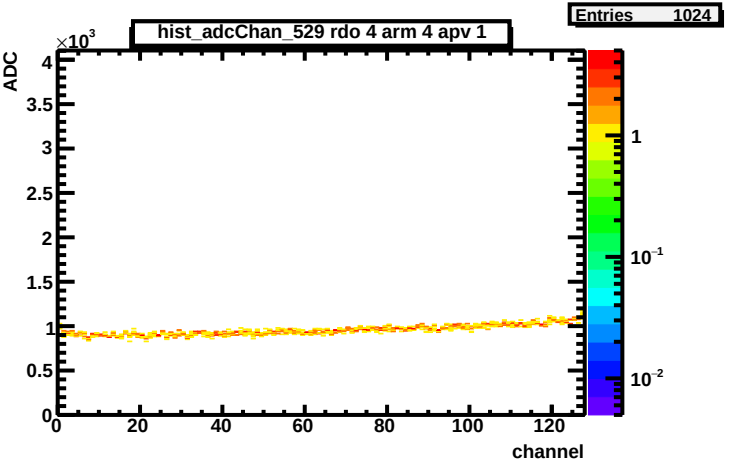

hist_adcChan_524 rdo 4 arm 3 apv 20
Entries 1024
ADC
channel

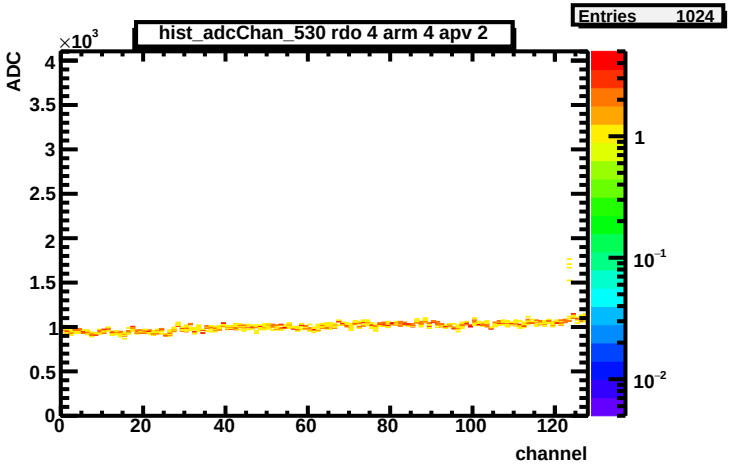

hist_adcChan_525 rdo 4 arm 3 apv 21
Entries 1024
ADC
channel

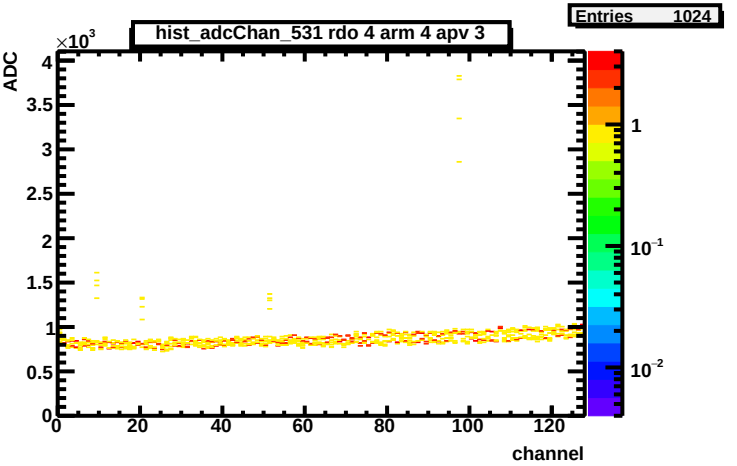

hist_adcChan_526 rdo 4 arm 3 apv 22
Entries 1024
ADC
channel

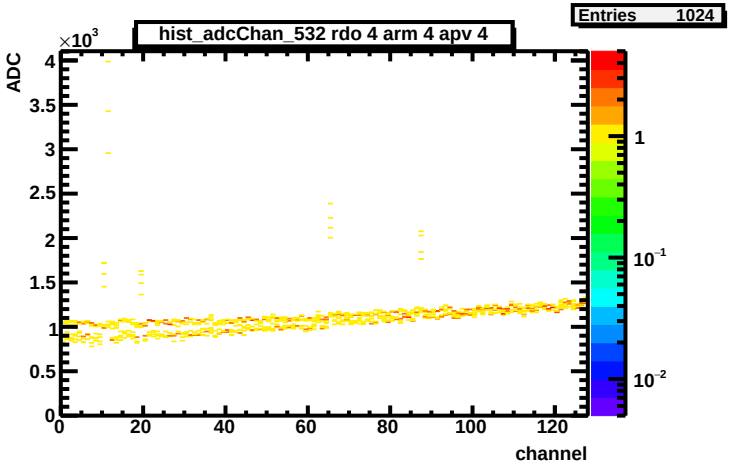

hist_adcChan_527 rdo 4 arm 3 apv 23
Entries 1024
ADC
channel

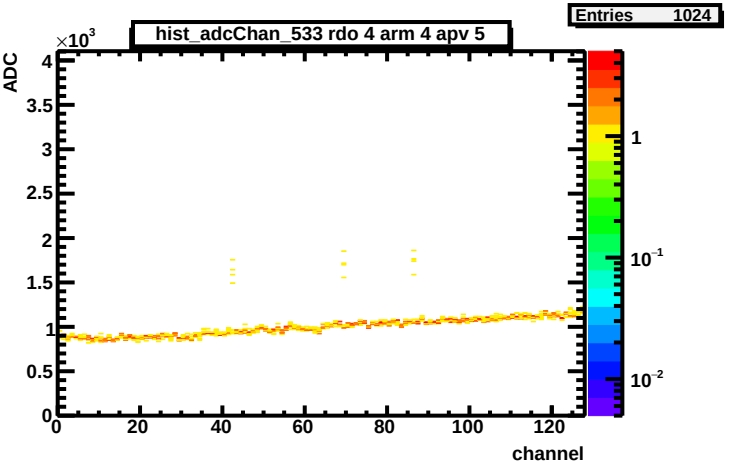

hist_adcChan_528 rdo 4 arm 4 apv 0
Entries 1024
ADC
channel

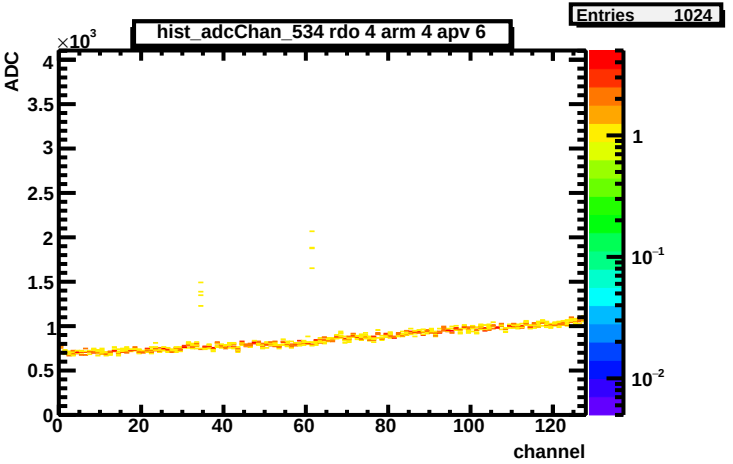

hist_adcChan_529 rdo 4 arm 4 apv 1
Entries 1024
ADC
channel

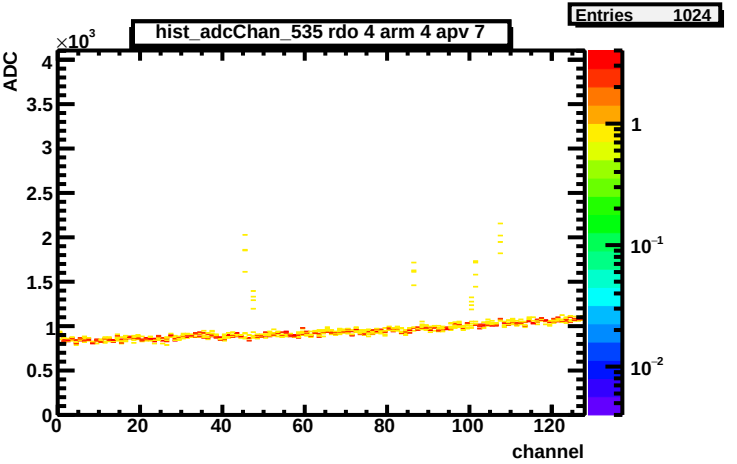

hist_adcChan_530 rdo 4 arm 4 apv 2
Entries 1024
ADC
channel

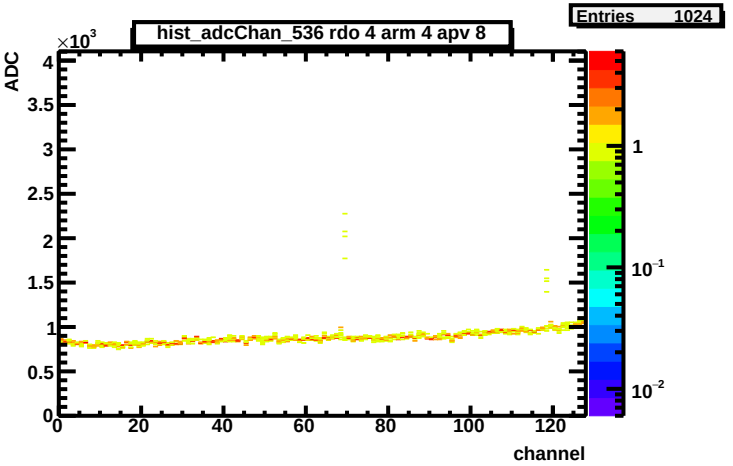

hist_adcChan_531 rdo 4 arm 4 apv 3
Entries 1024
ADC
channel

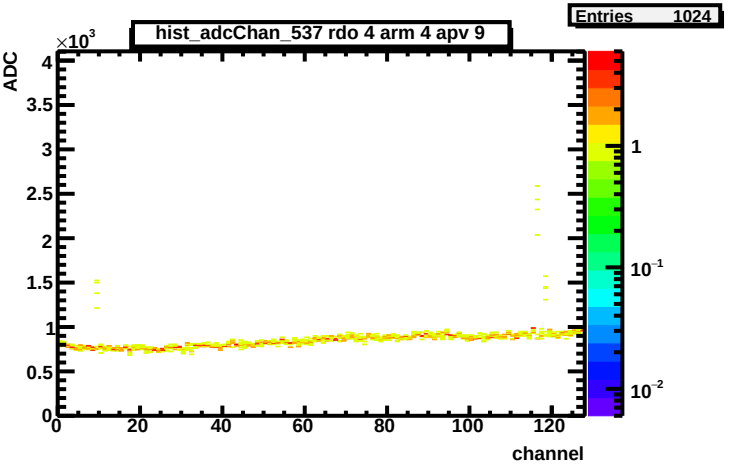

hist_adcChan_532 rdo 4 arm 4 apv 4
Entries 1024
ADC
channel

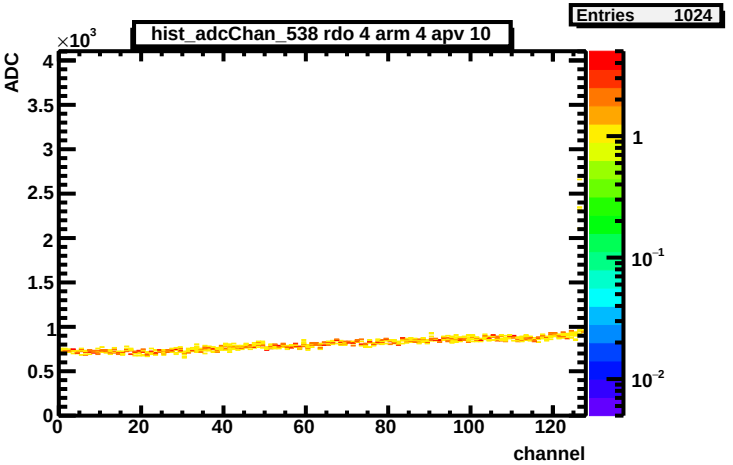

hist_adcChan_533 rdo 4 arm 4 apv 5
Entries 1024
ADC
channel

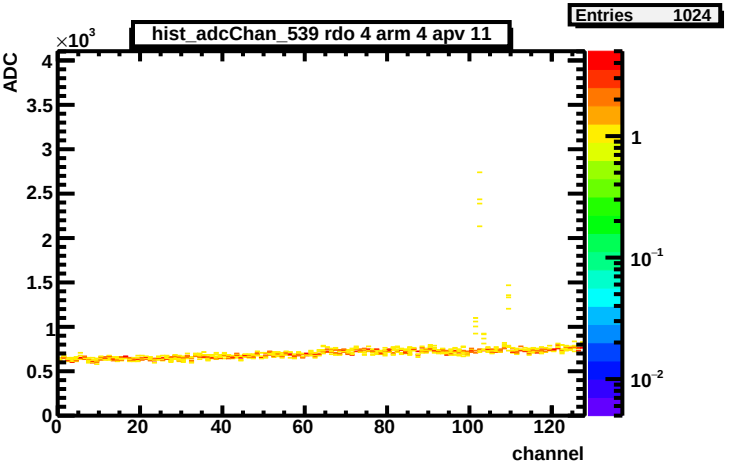

hist_adcChan_534 rdo 4 arm 4 apv 6
Entries 1024
ADC
×10^3
channel

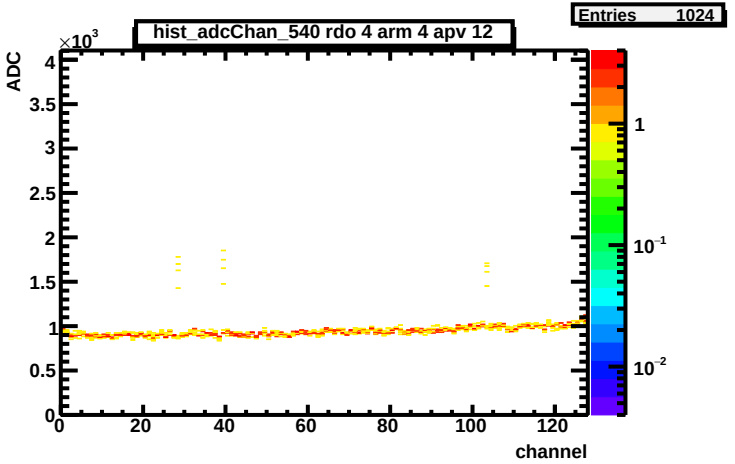

hist_adcChan_535 rdo 4 arm 4 apv 7
Entries 1024
ADC
×10^3
channel

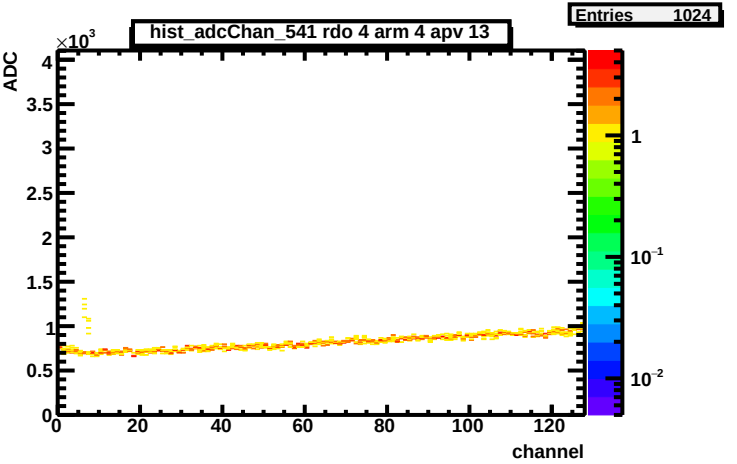

hist_adcChan_536 rdo 4 arm 4 apv 8
Entries 1024
ADC
×10^3
channel

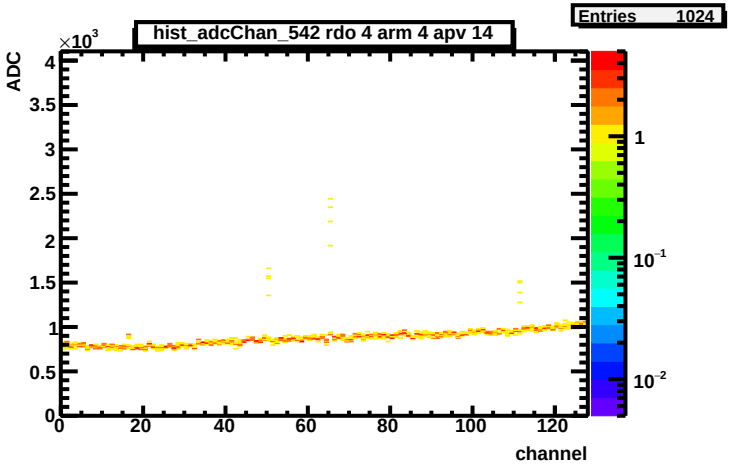

hist_adcChan_537 rdo 4 arm 4 apv 9
Entries 1024
ADC
×10^3
channel

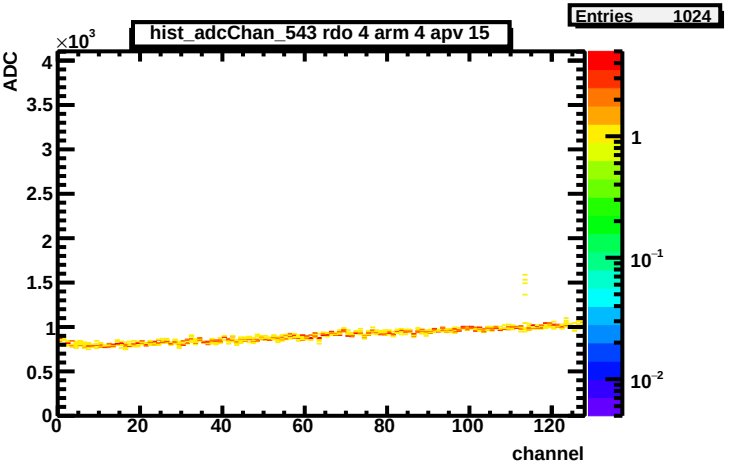

hist_adcChan_538 rdo 4 arm 4 apv 10
Entries 1024
ADC
×10^3
channel

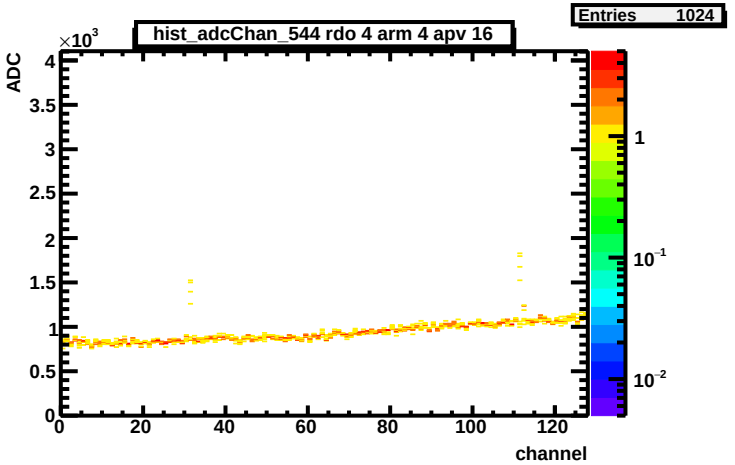

hist_adcChan_539 rdo 4 arm 4 apv 11
Entries 1024
ADC
×10^3
channel

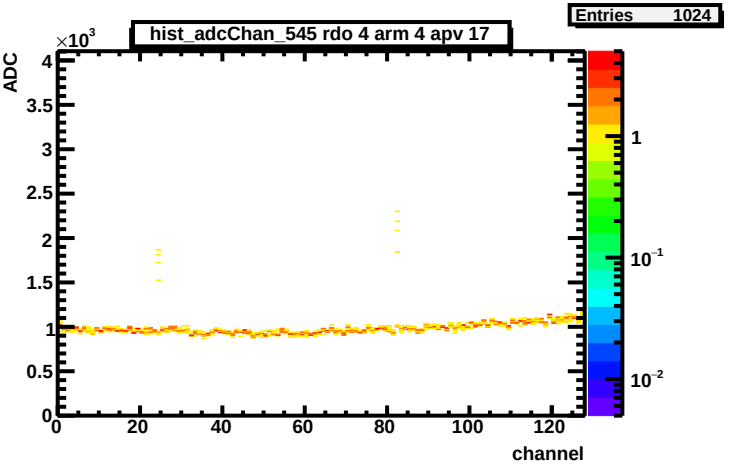

hist_adcChan_540 rdo 4 arm 4 apv 12
Entries 1024
ADC
×10³
channel

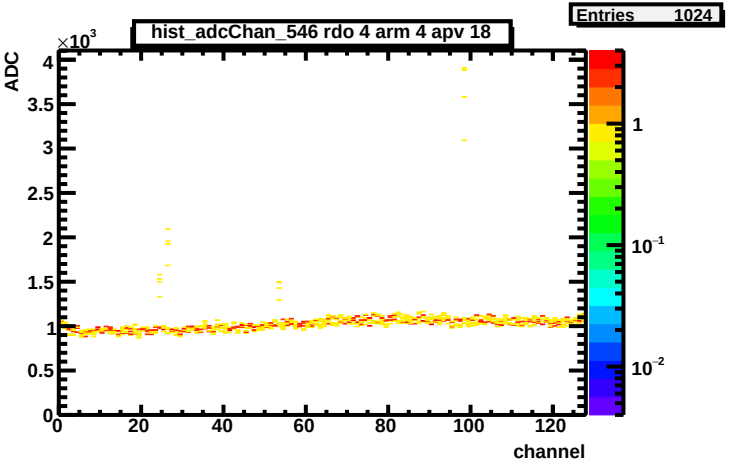

hist_adcChan_541 rdo 4 arm 4 apv 13
Entries 1024
ADC
×10³
channel

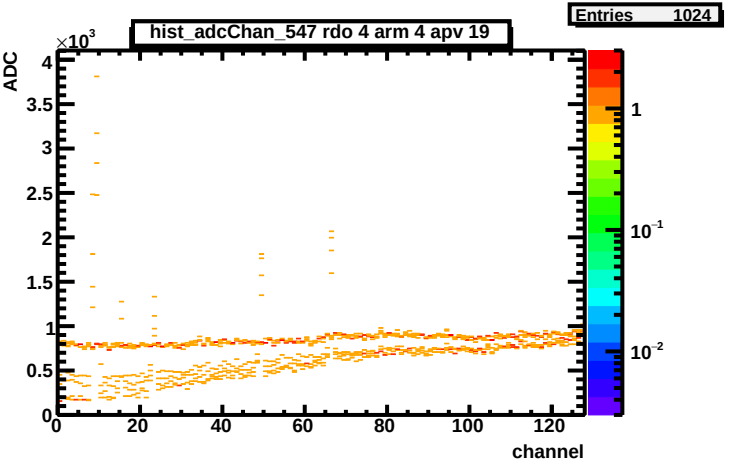

hist_adcChan_542 rdo 4 arm 4 apv 14
Entries 1024
ADC
×10³
channel

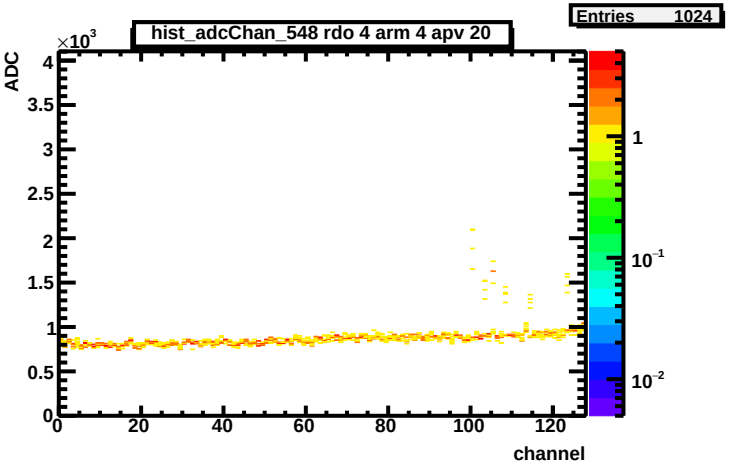

hist_adcChan_543 rdo 4 arm 4 apv 15
Entries 1024
ADC
×10³
channel

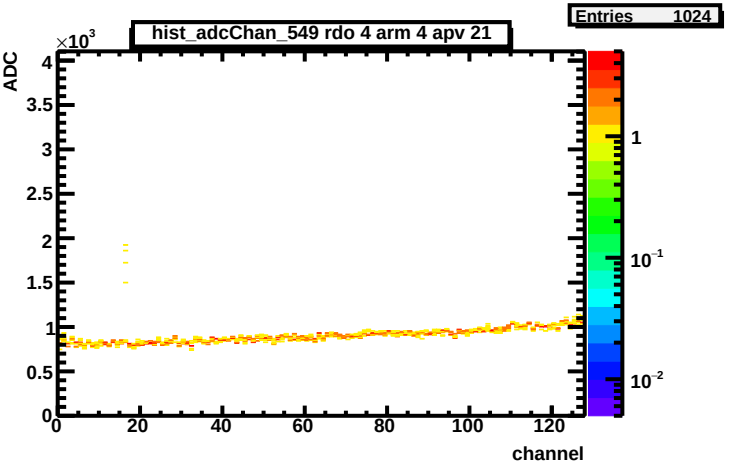

hist_adcChan_544 rdo 4 arm 4 apv 16
Entries 1024
ADC
×10³
channel

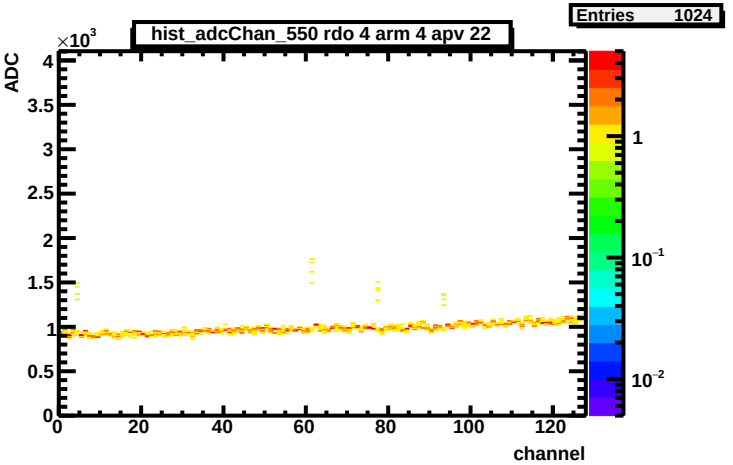

hist_adcChan_545 rdo 4 arm 4 apv 17
Entries 1024
ADC
×10³
channel

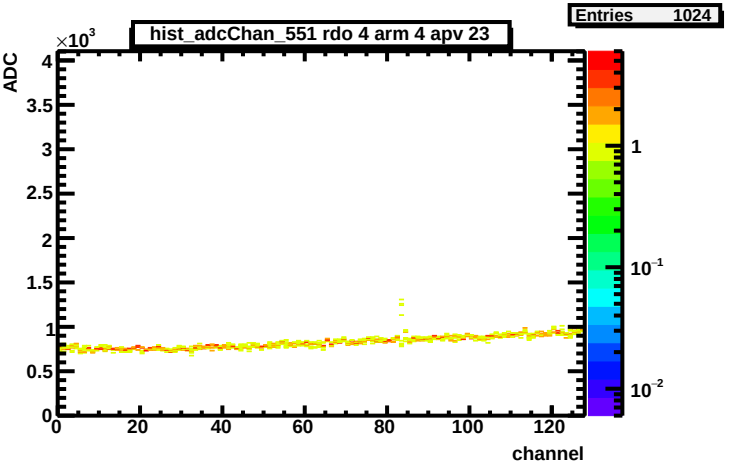

hist_adcChan_546 rdo 4 arm 4 apv 18
Entries 1024
ADC
channel

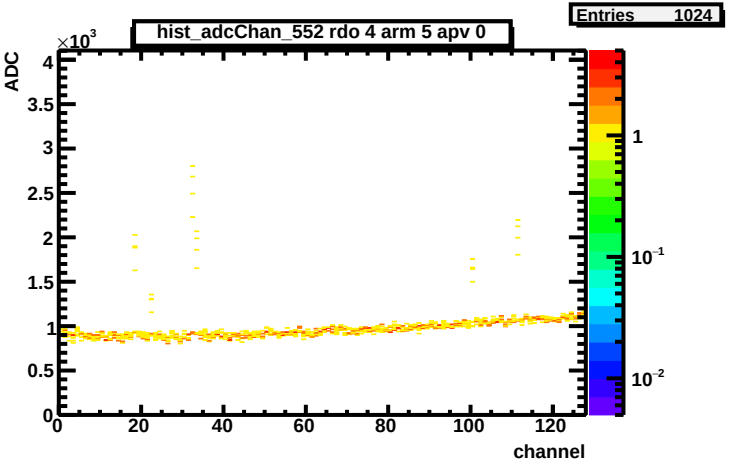

hist_adcChan_547 rdo 4 arm 4 apv 19
Entries 1024
ADC
channel

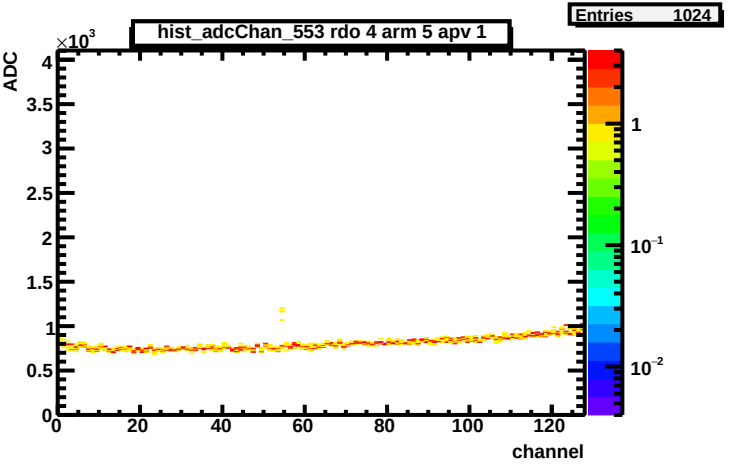

hist_adcChan_548 rdo 4 arm 4 apv 20
Entries 1024
ADC
channel

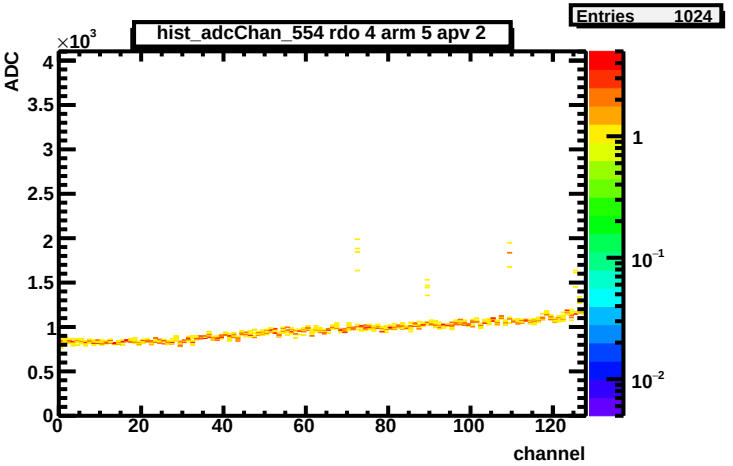

hist_adcChan_549 rdo 4 arm 4 apv 21
Entries 1024
ADC
channel

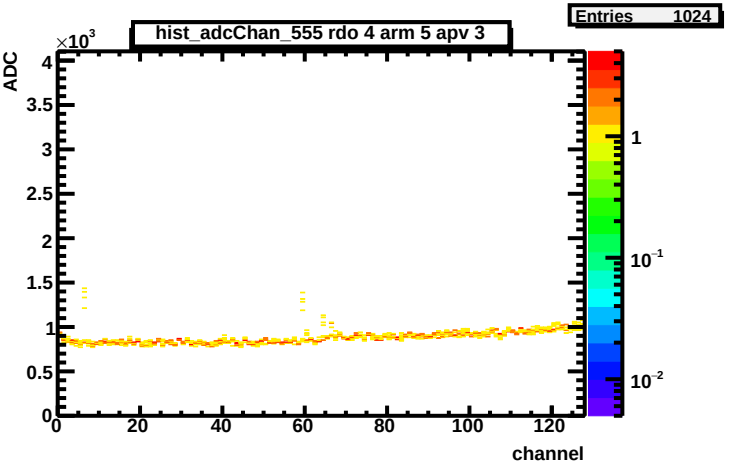

hist_adcChan_550 rdo 4 arm 4 apv 22
Entries 1024
ADC
channel

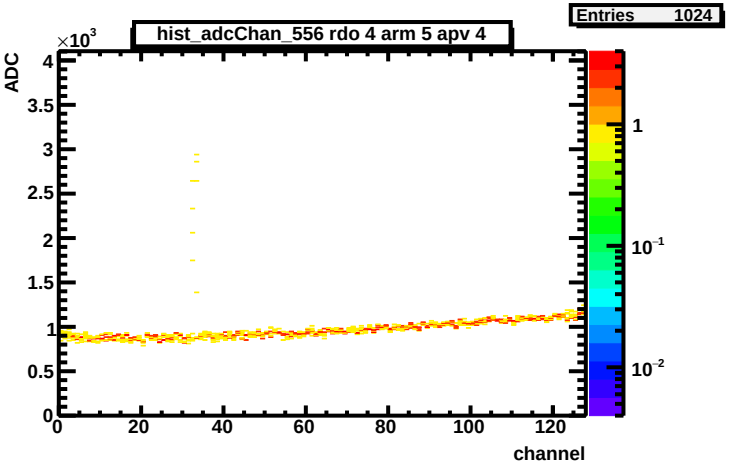

hist_adcChan_551 rdo 4 arm 4 apv 23
Entries 1024
ADC
channel

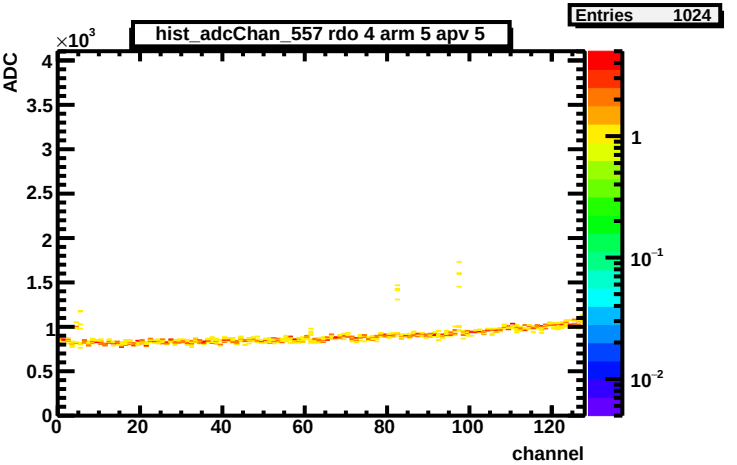

hist_adcChan_552 rdo 4 arm 5 apv 0
Entries 1024
ADC
×10^3
channel

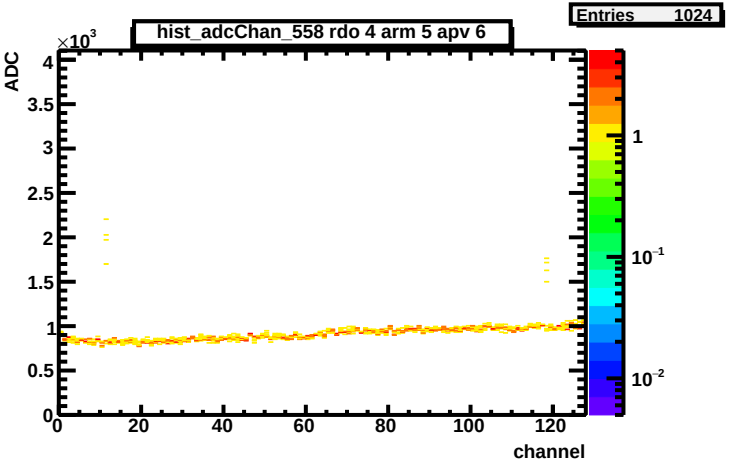

hist_adcChan_553 rdo 4 arm 5 apv 1
Entries 1024
ADC
×10^3
channel

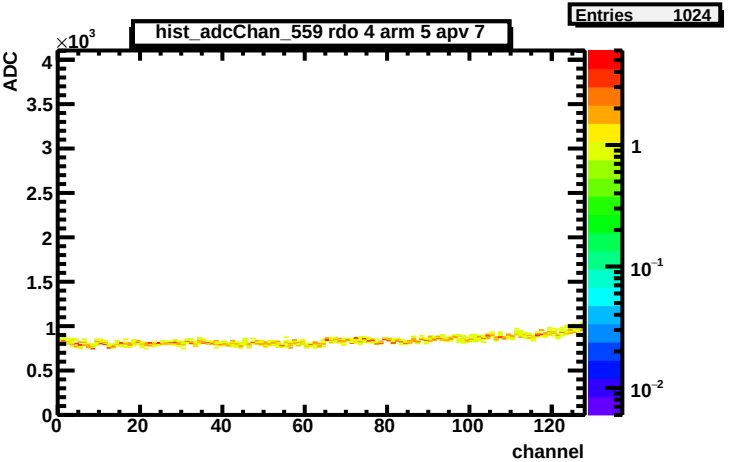

hist_adcChan_554 rdo 4 arm 5 apv 2
Entries 1024
ADC
×10^3
channel

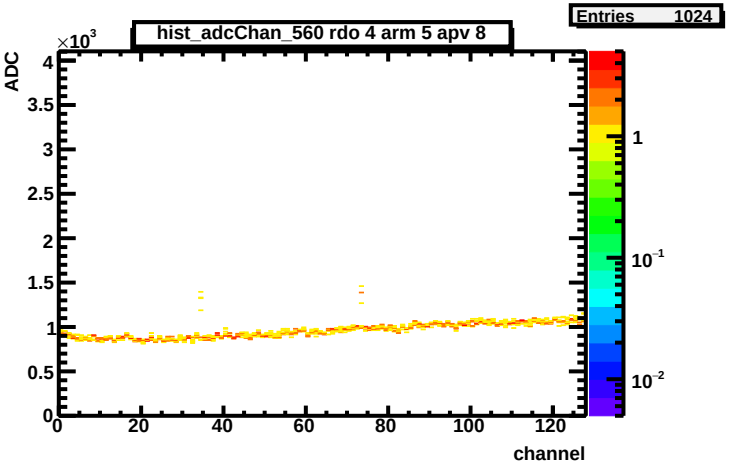

hist_adcChan_555 rdo 4 arm 5 apv 3
Entries 1024
ADC
×10^3
channel

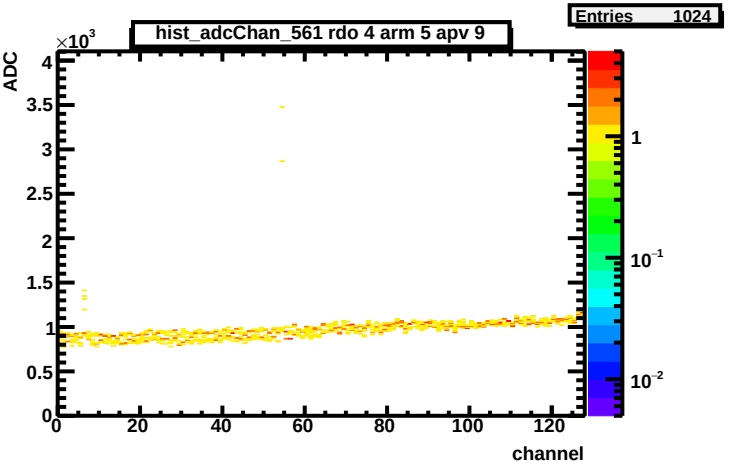

hist_adcChan_556 rdo 4 arm 5 apv 4
Entries 1024
ADC
×10^3
channel

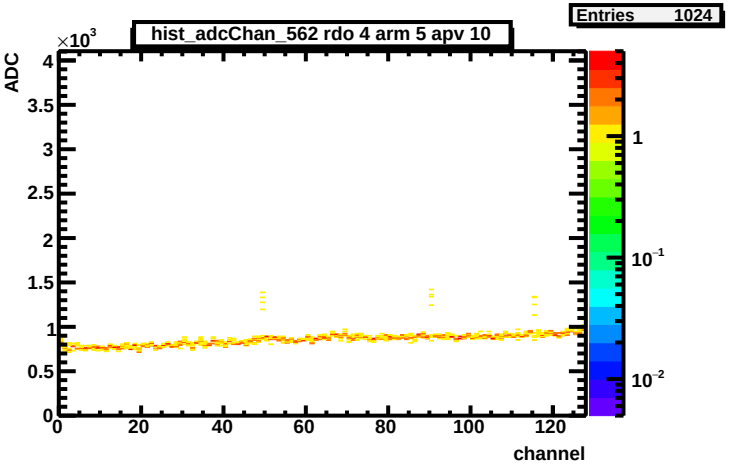

hist_adcChan_557 rdo 4 arm 5 apv 5
Entries 1024
ADC
×10^3
channel

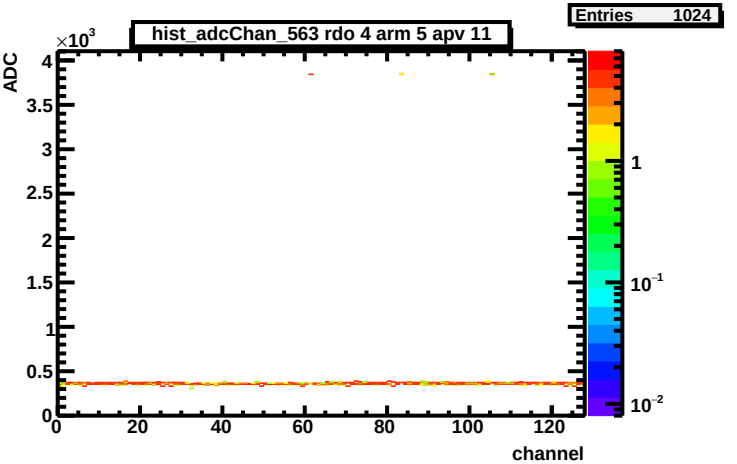

hist_adcChan_558 rdo 4 arm 5 apv 6
Entries 1024
ADC
×10^3
channel

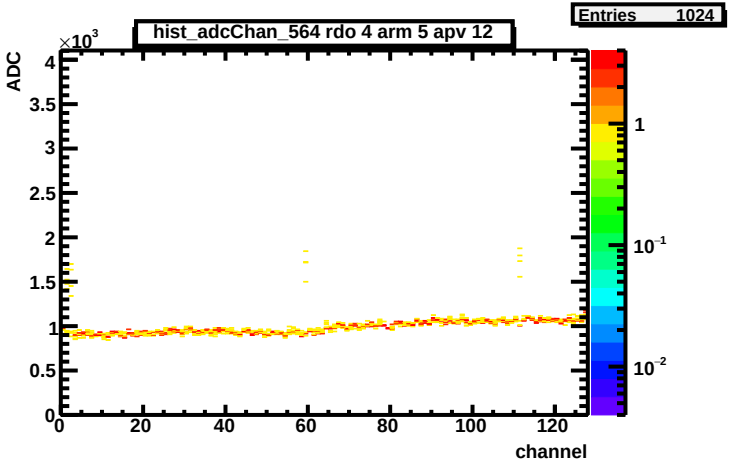

hist_adcChan_559 rdo 4 arm 5 apv 7
Entries 1024
ADC
×10^3
channel

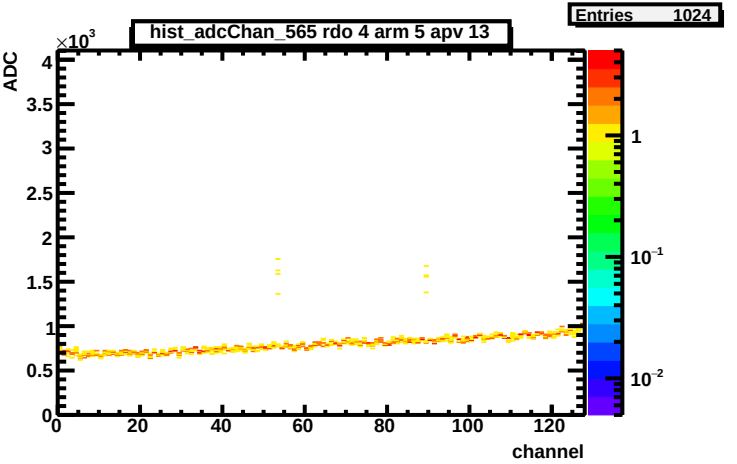

hist_adcChan_560 rdo 4 arm 5 apv 8
Entries 1024
ADC
×10^3
channel

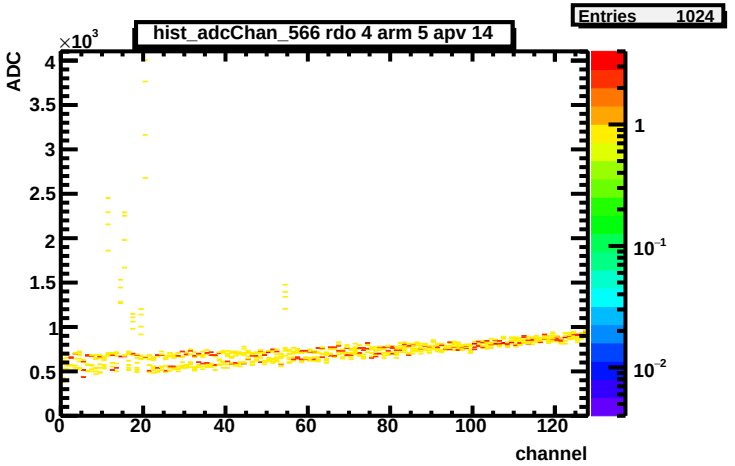

hist_adcChan_561 rdo 4 arm 5 apv 9
Entries 1024
ADC
×10^3
channel

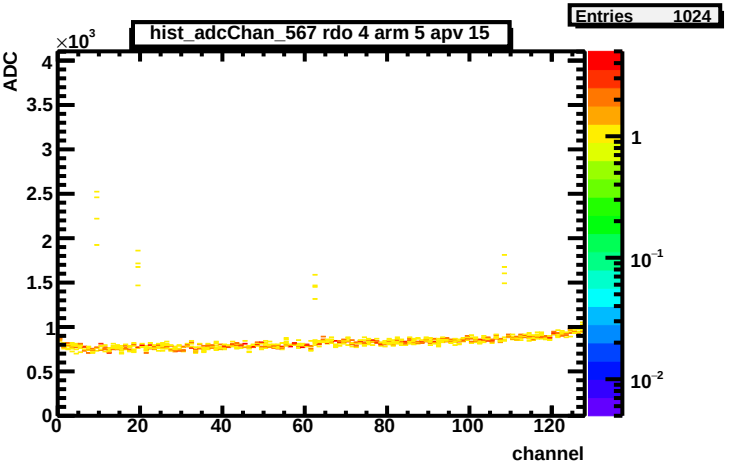

hist_adcChan_562 rdo 4 arm 5 apv 10
Entries 1024
ADC
×10^3
channel

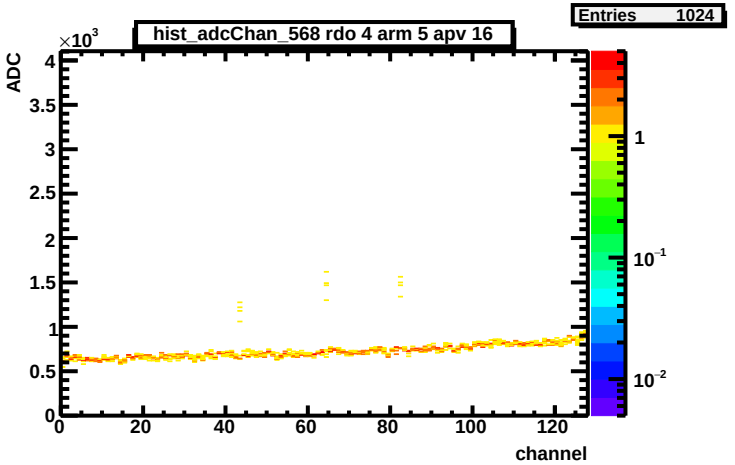

hist_adcChan_563 rdo 4 arm 5 apv 11
Entries 1024
ADC
×10^3
channel

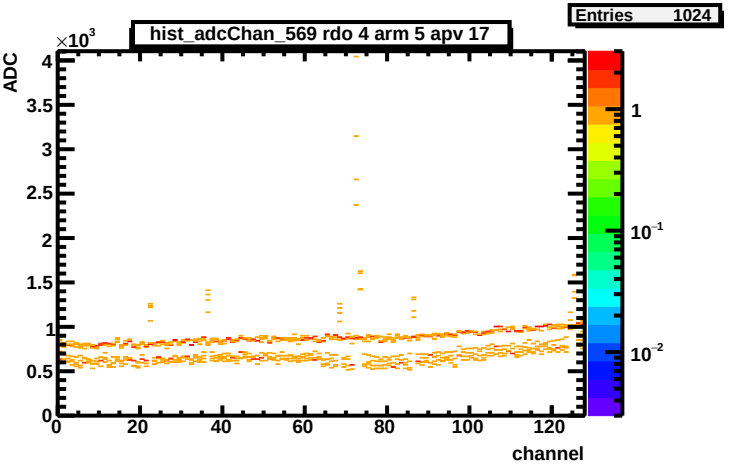

hist_adcChan_564 rdo 4 arm 5 apv 12
Entries 1024
ADC
×10³
channel

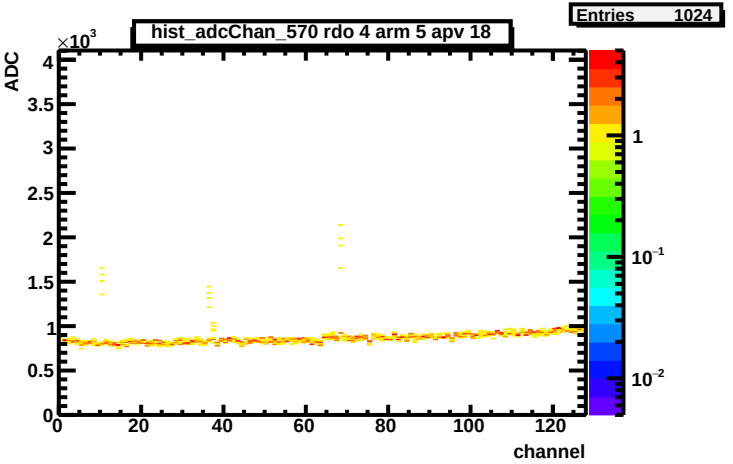

hist_adcChan_565 rdo 4 arm 5 apv 13
Entries 1024
ADC
×10³
channel

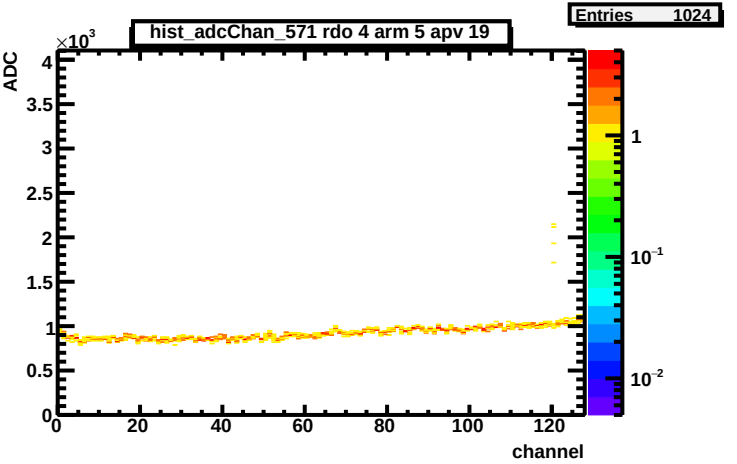

hist_adcChan_566 rdo 4 arm 5 apv 14
Entries 1024
ADC
×10³
channel

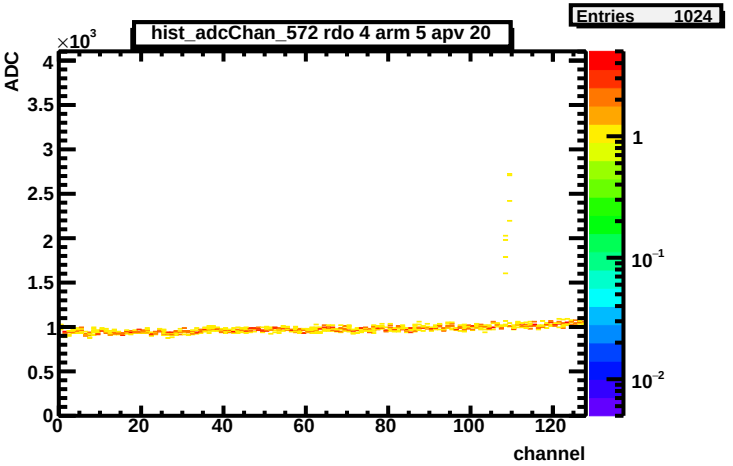

hist_adcChan_567 rdo 4 arm 5 apv 15
Entries 1024
ADC
×10³
channel

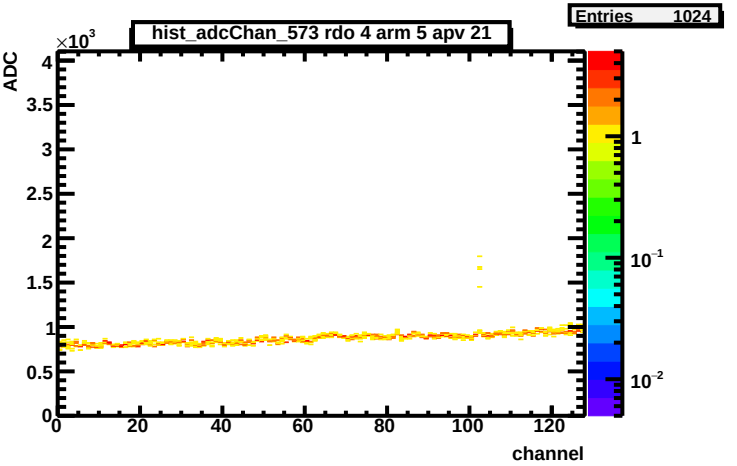

hist_adcChan_568 rdo 4 arm 5 apv 16
Entries 1024
ADC
×10³
channel

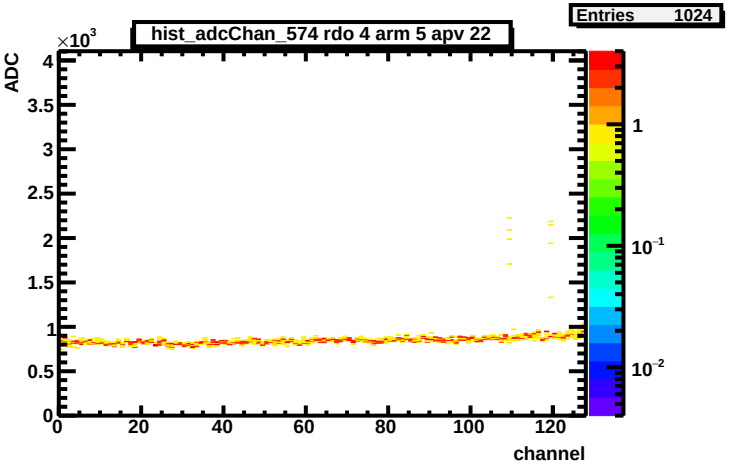

hist_adcChan_569 rdo 4 arm 5 apv 17
Entries 1024
ADC
×10³
channel

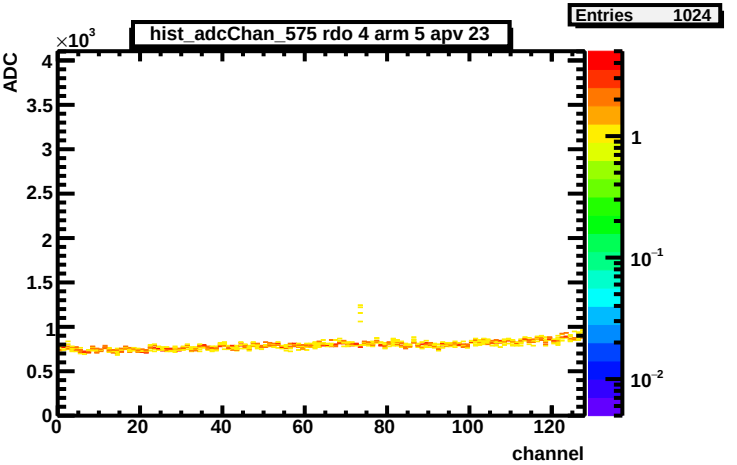

hist_adcChan_570 rdo 4 arm 5 apv 18
Entries 1024
ADC
channel

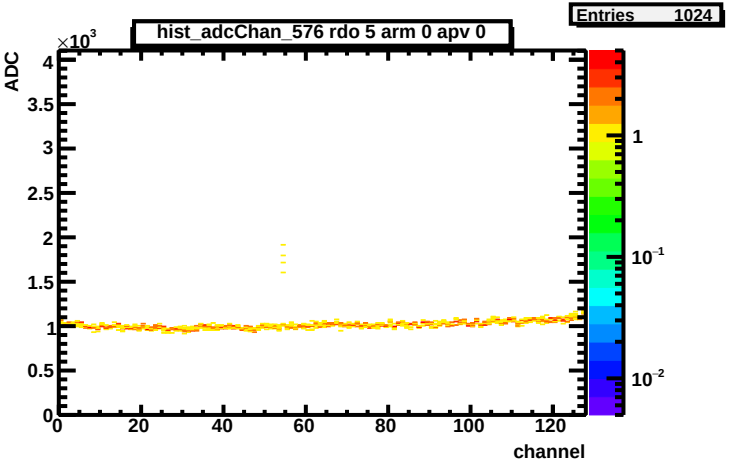

hist_adcChan_571 rdo 4 arm 5 apv 19
Entries 1024
ADC
channel

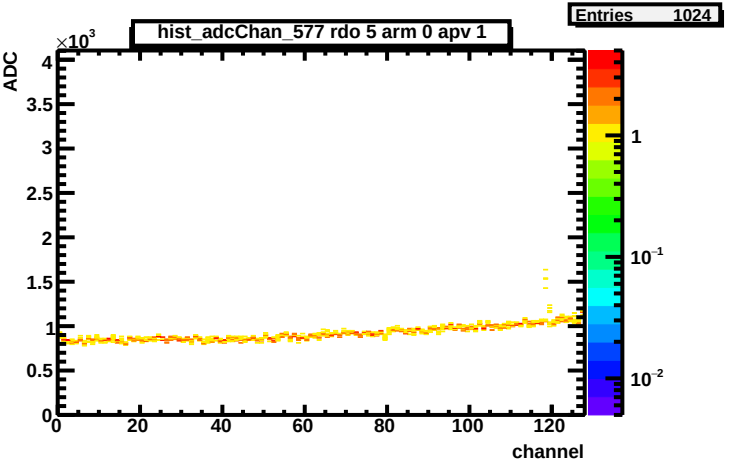

hist_adcChan_572 rdo 4 arm 5 apv 20
Entries 1024
ADC
channel

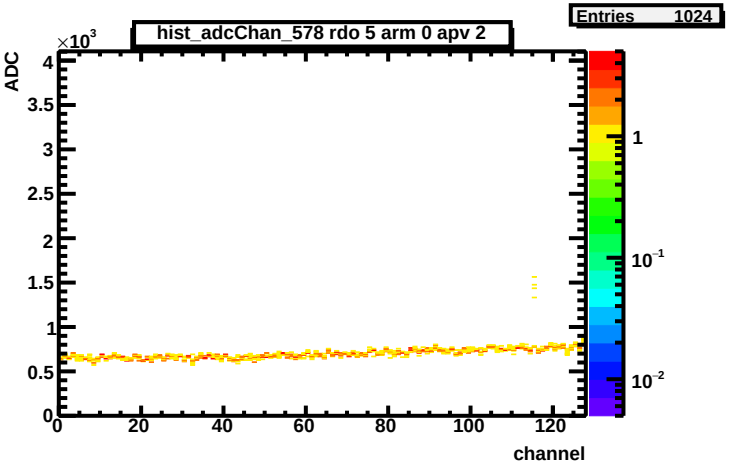

hist_adcChan_573 rdo 4 arm 5 apv 21
Entries 1024
ADC
channel

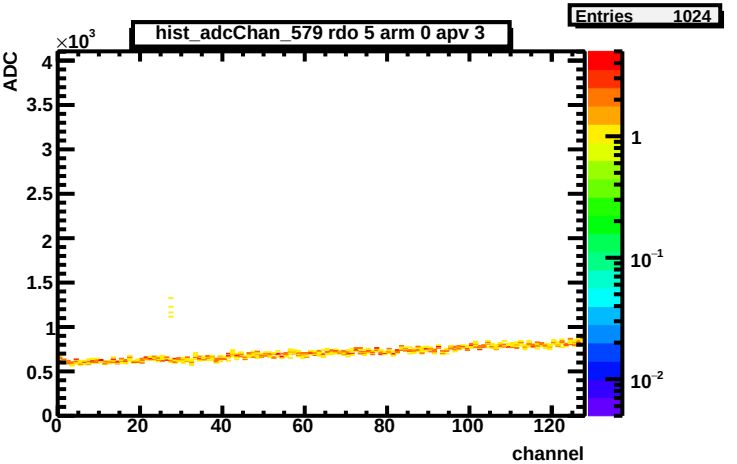

hist_adcChan_574 rdo 4 arm 5 apv 22
Entries 1024
ADC
channel

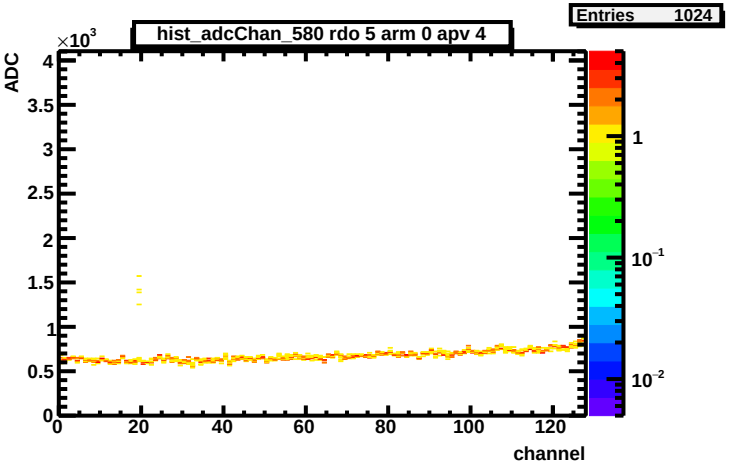

hist_adcChan_575 rdo 4 arm 5 apv 23
Entries 1024
ADC
channel

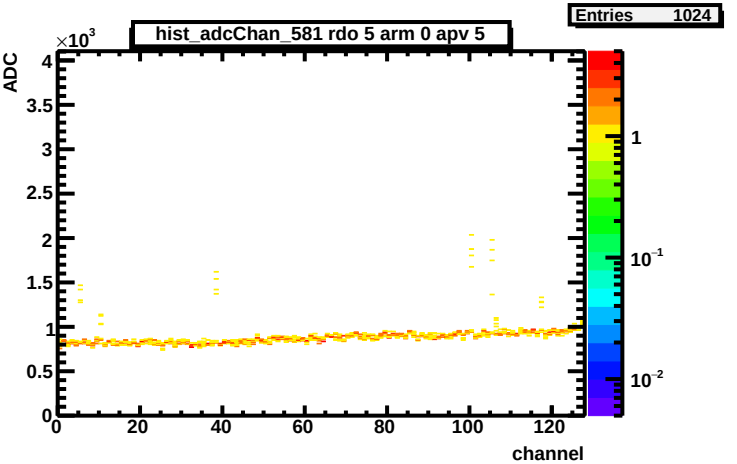

hist_adcChan_576 rdo 5 arm 0 apv 0
Entries 1024
ADC
channel

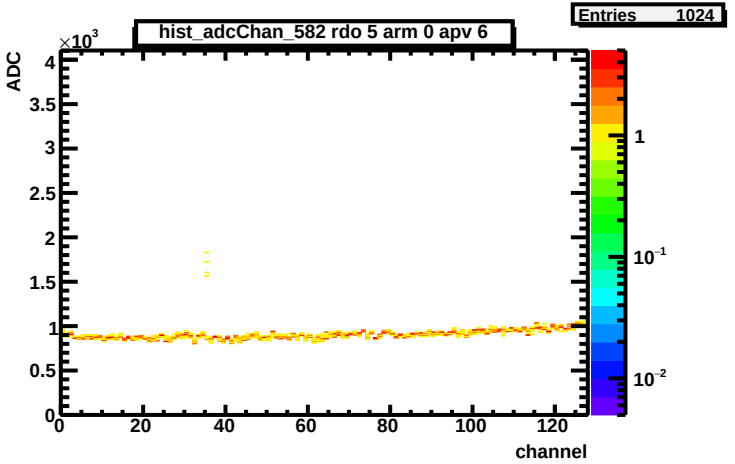

hist_adcChan_577 rdo 5 arm 0 apv 1
Entries 1024
ADC
channel

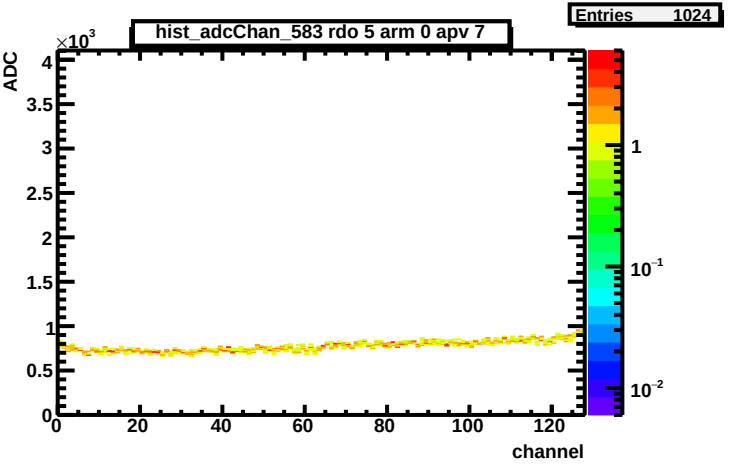

hist_adcChan_578 rdo 5 arm 0 apv 2
Entries 1024
ADC
channel

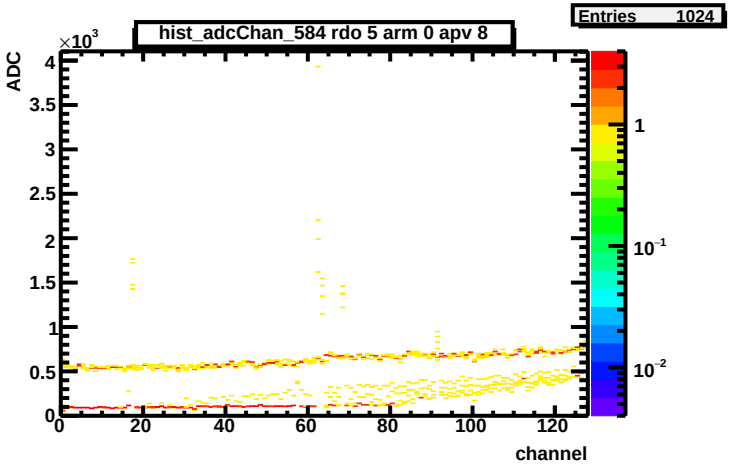

hist_adcChan_579 rdo 5 arm 0 apv 3
Entries 1024
ADC
channel

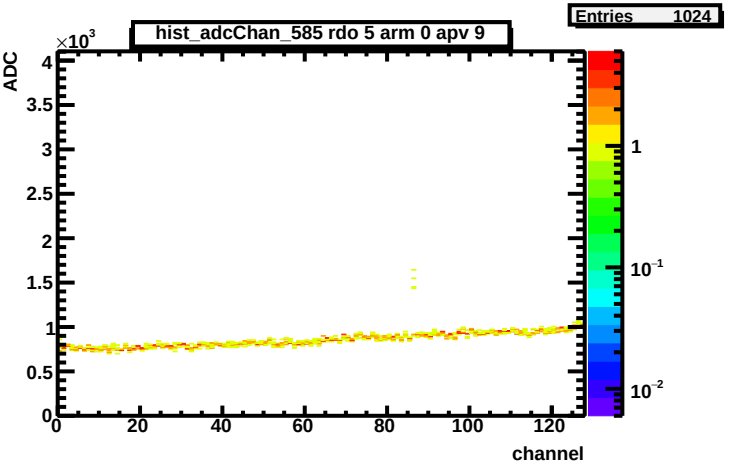

hist_adcChan_580 rdo 5 arm 0 apv 4
Entries 1024
ADC
channel

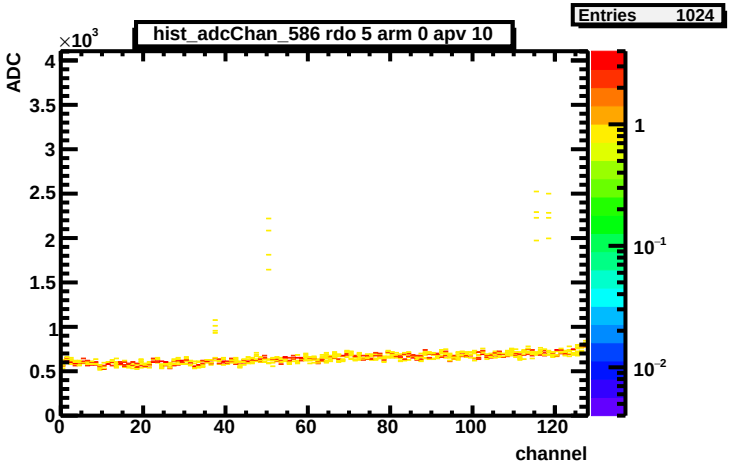

hist_adcChan_581 rdo 5 arm 0 apv 5
Entries 1024
ADC
channel

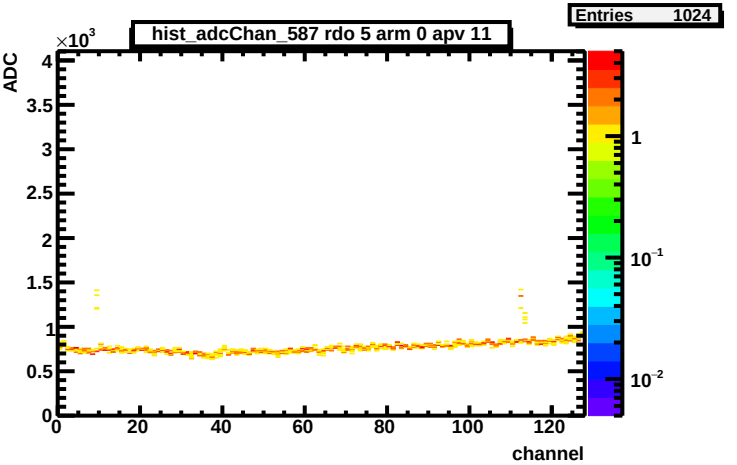

hist_adcChan_582 rdo 5 arm 0 apv 6
Entries 1024
ADC
channel

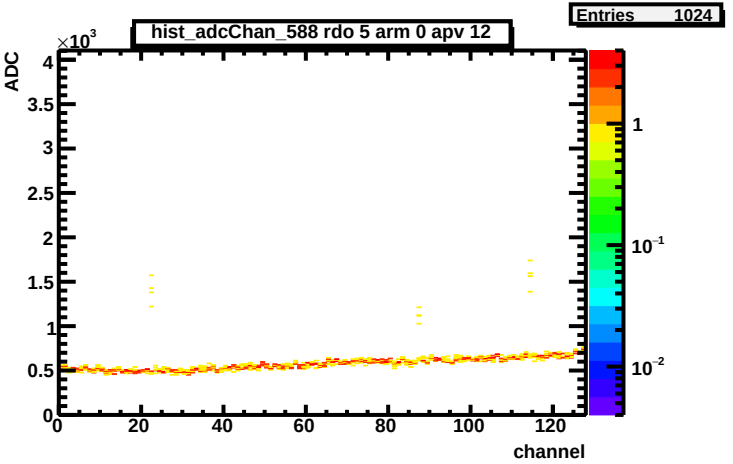

hist_adcChan_583 rdo 5 arm 0 apv 7
Entries 1024
ADC
channel

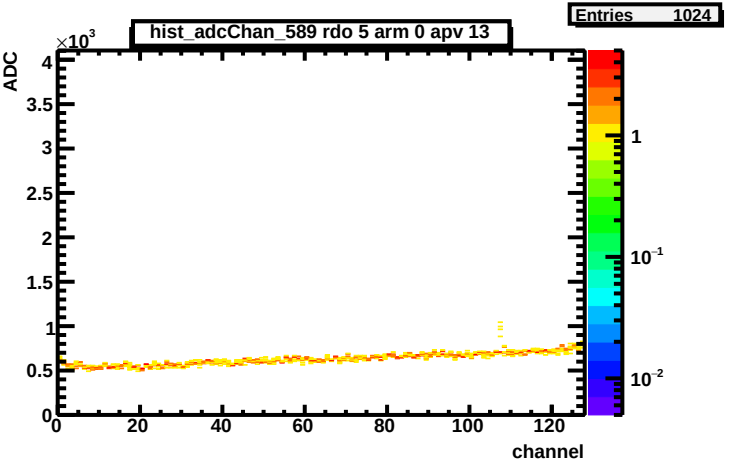

hist_adcChan_584 rdo 5 arm 0 apv 8
Entries 1024
ADC
channel

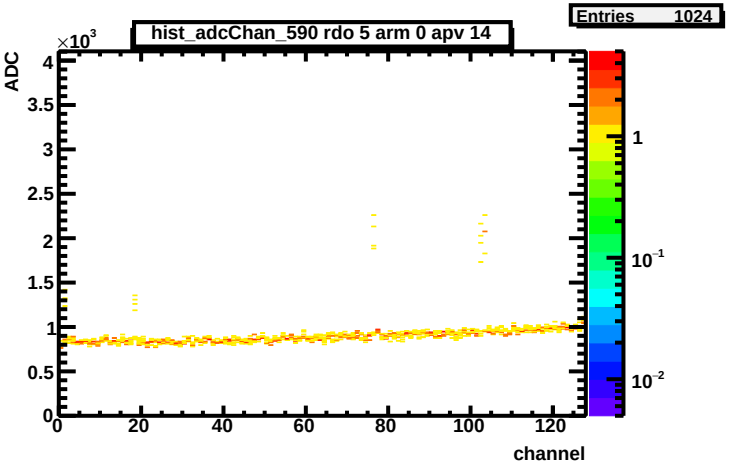

hist_adcChan_585 rdo 5 arm 0 apv 9
Entries 1024
ADC
channel

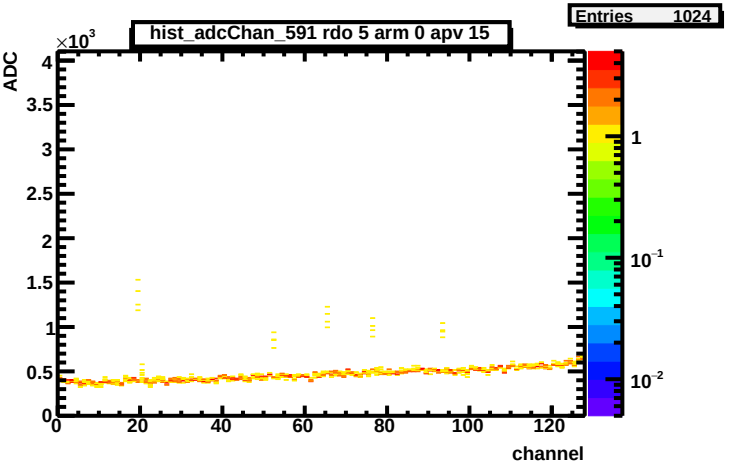

hist_adcChan_586 rdo 5 arm 0 apv 10
Entries 1024
ADC
channel

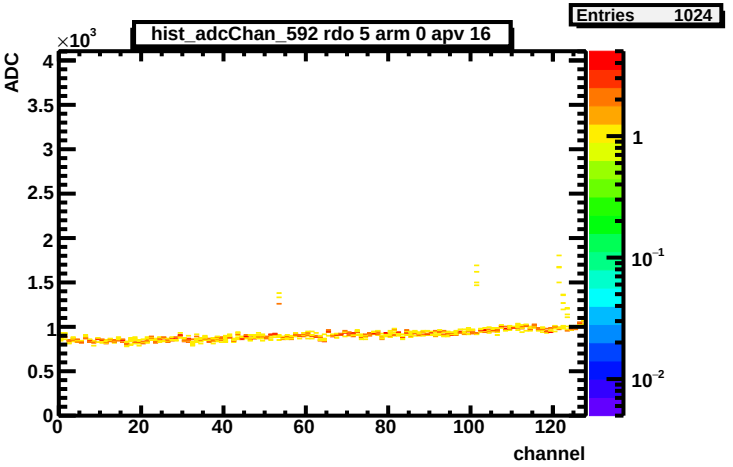

hist_adcChan_587 rdo 5 arm 0 apv 11
Entries 1024
ADC
channel

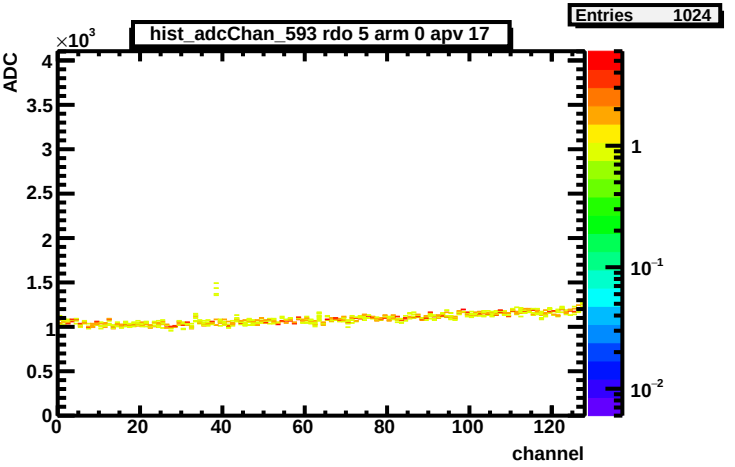

hist_adcChan_588 rdo 5 arm 0 apv 12
Entries 1024
ADC
channel

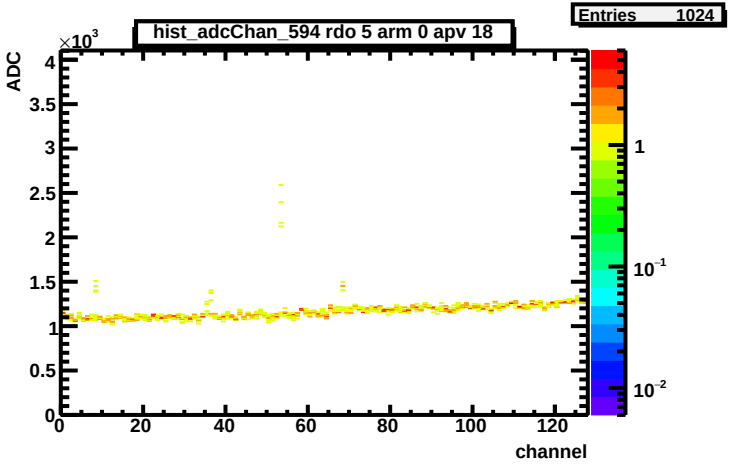

hist_adcChan_589 rdo 5 arm 0 apv 13
Entries 1024
ADC
channel

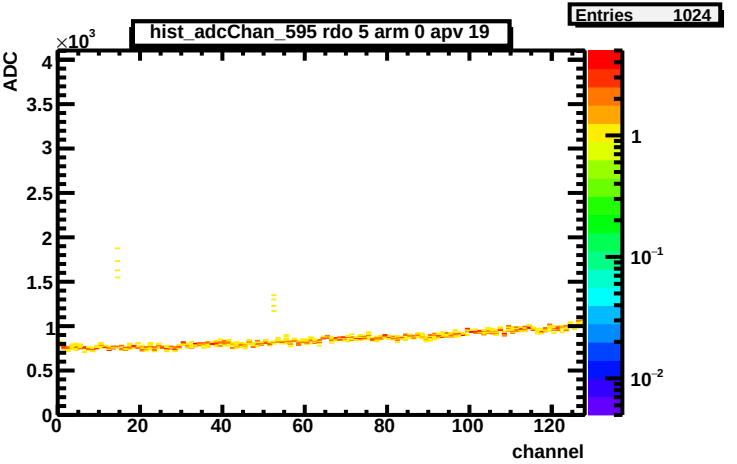

hist_adcChan_590 rdo 5 arm 0 apv 14
Entries 1024
ADC
channel

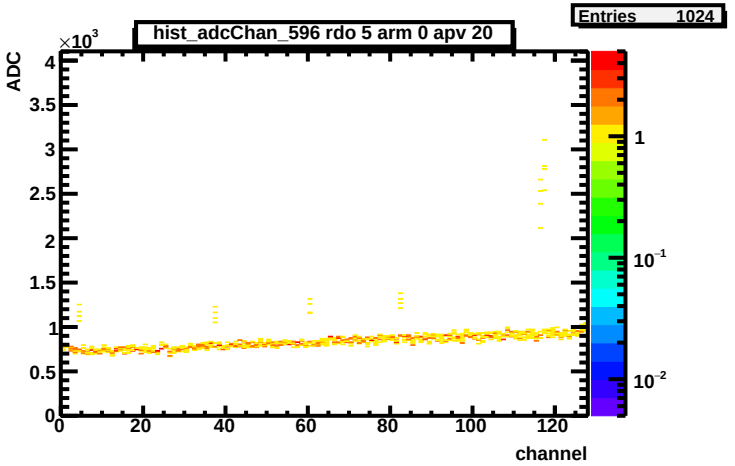

hist_adcChan_591 rdo 5 arm 0 apv 15
Entries 1024
ADC
channel

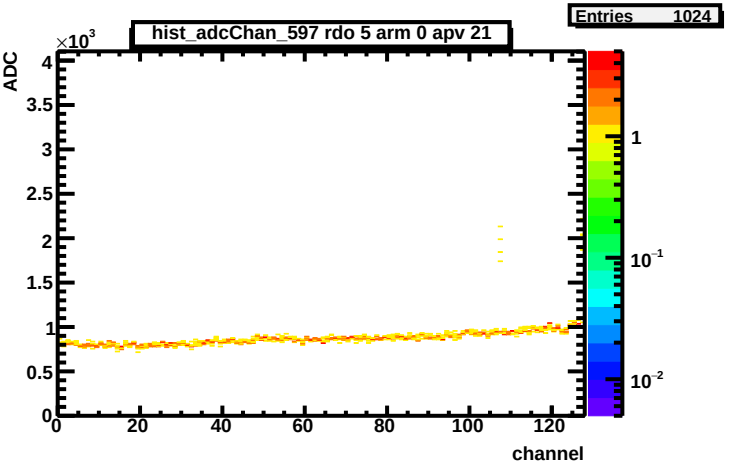

hist_adcChan_592 rdo 5 arm 0 apv 16
Entries 1024
ADC
channel

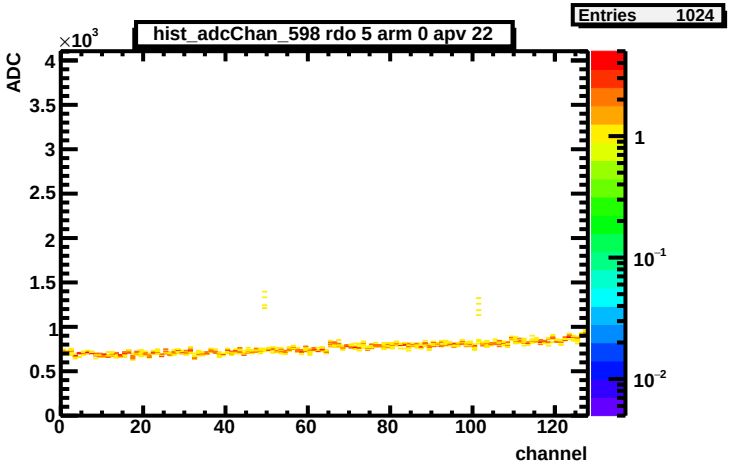

hist_adcChan_593 rdo 5 arm 0 apv 17
Entries 1024
ADC
channel

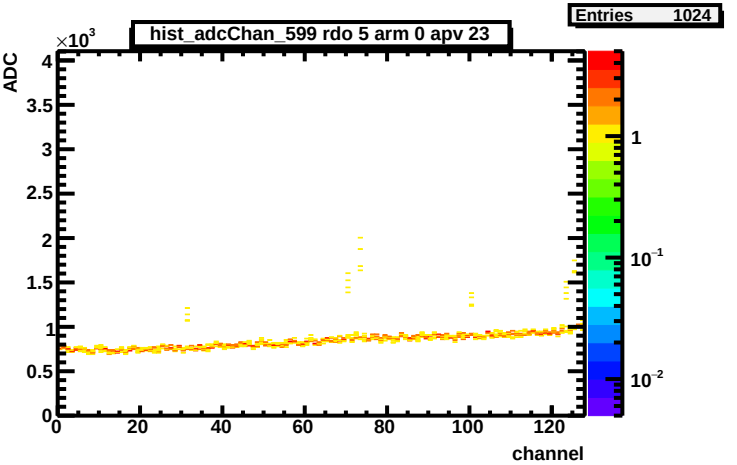

hist_adcChan_594 rdo 5 arm 0 apv 18
Entries 1024
ADC
channel

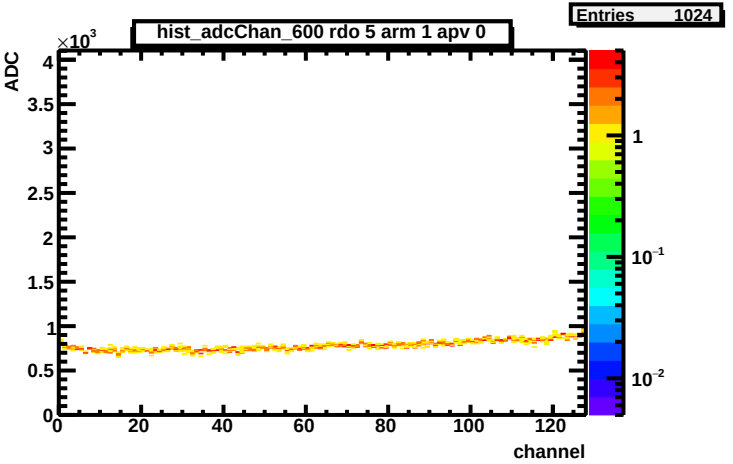

hist_adcChan_595 rdo 5 arm 0 apv 19
Entries 1024
ADC
channel

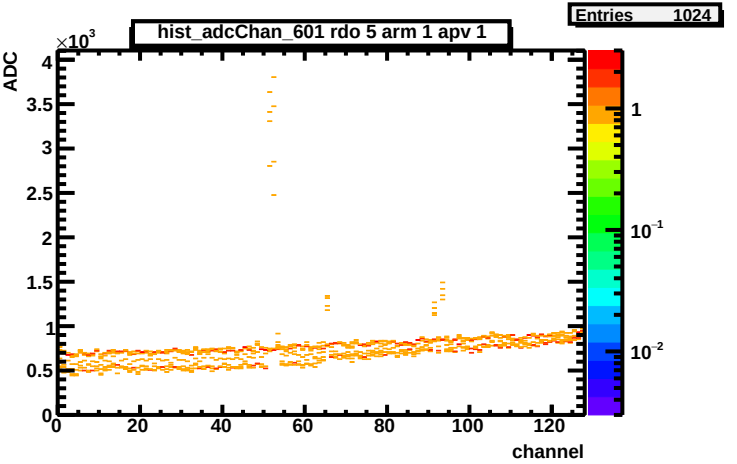

hist_adcChan_596 rdo 5 arm 0 apv 20
Entries 1024
ADC
channel

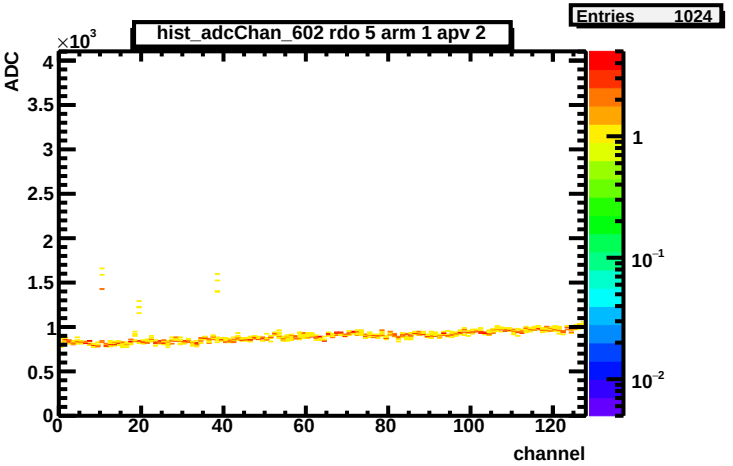

hist_adcChan_597 rdo 5 arm 0 apv 21
Entries 1024
ADC
channel

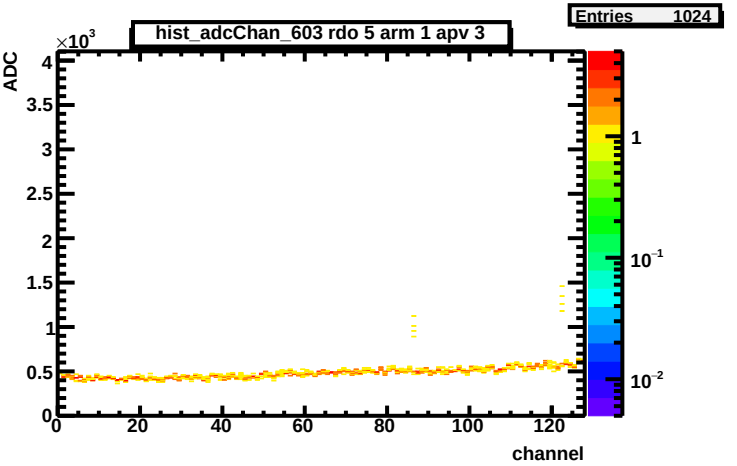

hist_adcChan_598 rdo 5 arm 0 apv 22
Entries 1024
ADC
channel

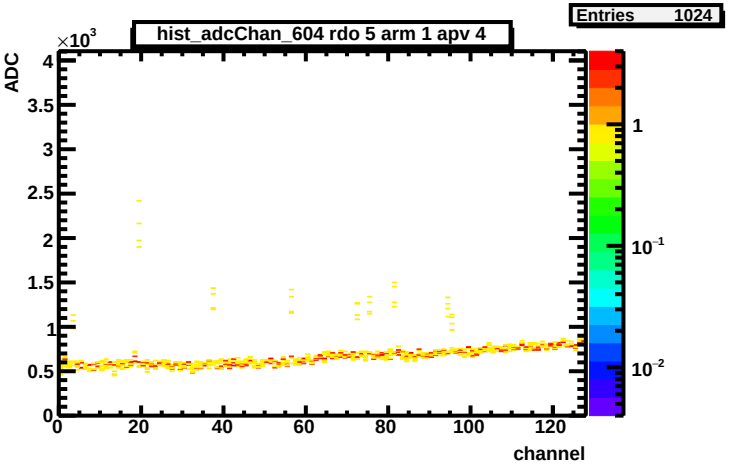

hist_adcChan_599 rdo 5 arm 0 apv 23
Entries 1024
ADC
channel

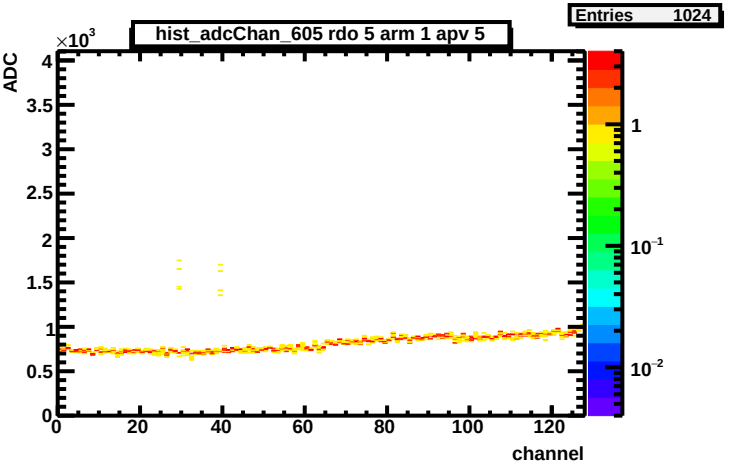

hist_adcChan_600 rdo 5 arm 1 apv 0
Entries 1024
ADC
×10^3
channel

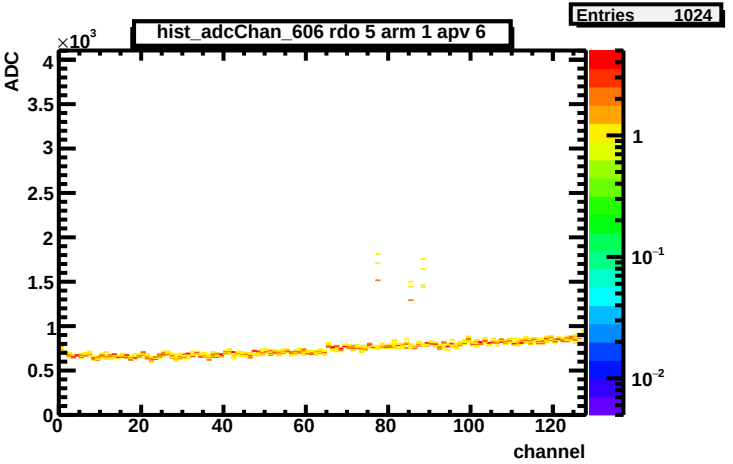

hist_adcChan_601 rdo 5 arm 1 apv 1
Entries 1024
ADC
×10^3
channel

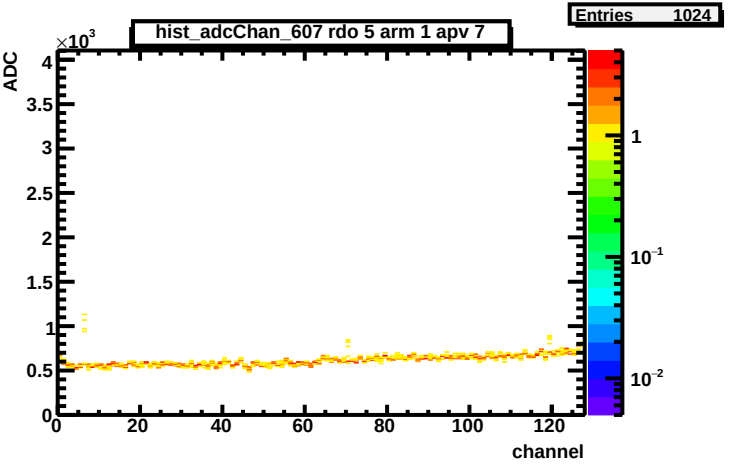

hist_adcChan_602 rdo 5 arm 1 apv 2
Entries 1024
ADC
×10^3
channel

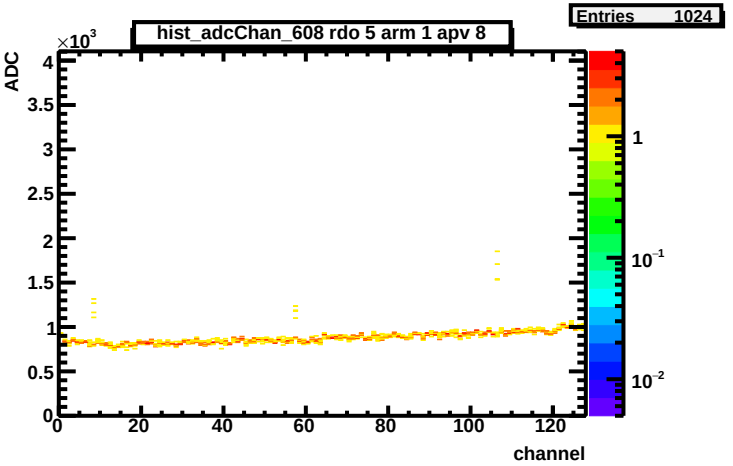

hist_adcChan_603 rdo 5 arm 1 apv 3
Entries 1024
ADC
×10^3
channel

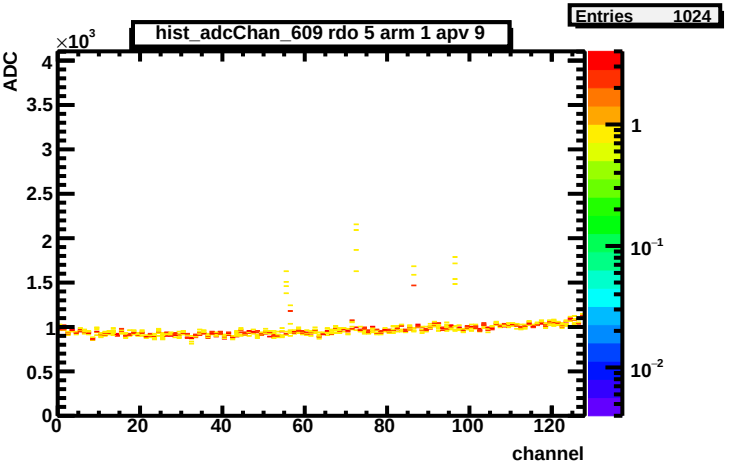

hist_adcChan_604 rdo 5 arm 1 apv 4
Entries 1024
ADC
×10^3
channel

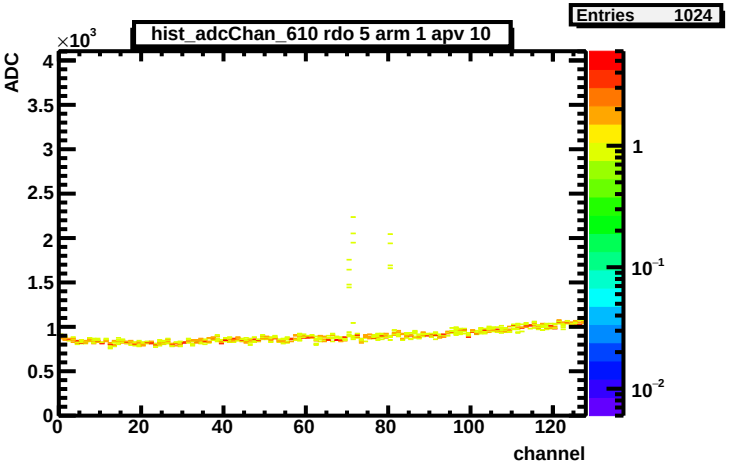

hist_adcChan_605 rdo 5 arm 1 apv 5
Entries 1024
ADC
×10^3
channel

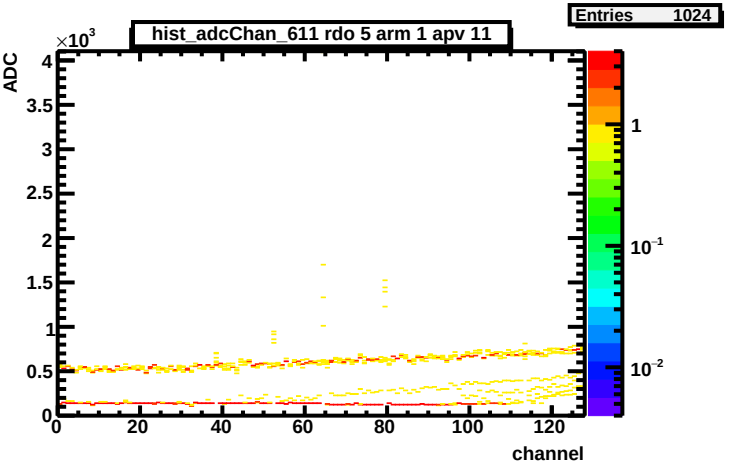

hist_adcChan_606 rdo 5 arm 1 apv 6
Entries 1024
ADC
channel

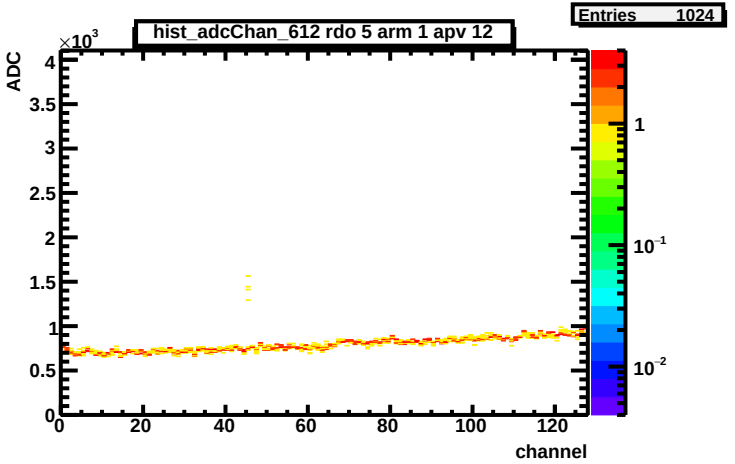

hist_adcChan_607 rdo 5 arm 1 apv 7
Entries 1024
ADC
channel

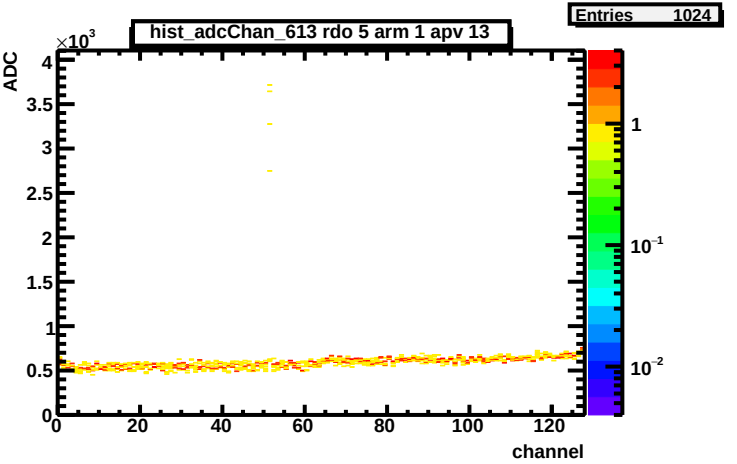

hist_adcChan_608 rdo 5 arm 1 apv 8
Entries 1024
ADC
channel

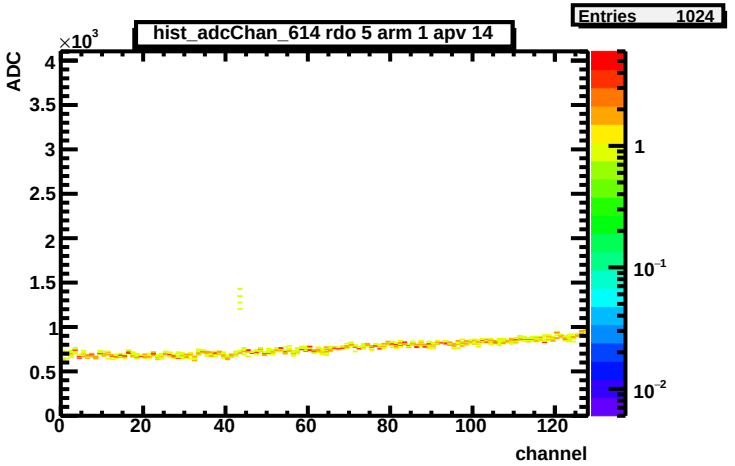

hist_adcChan_609 rdo 5 arm 1 apv 9
Entries 1024
ADC
channel

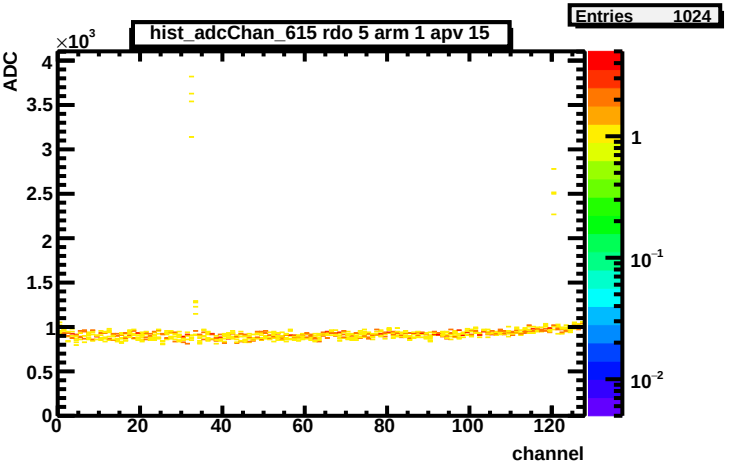

hist_adcChan_610 rdo 5 arm 1 apv 10
Entries 1024
ADC
channel

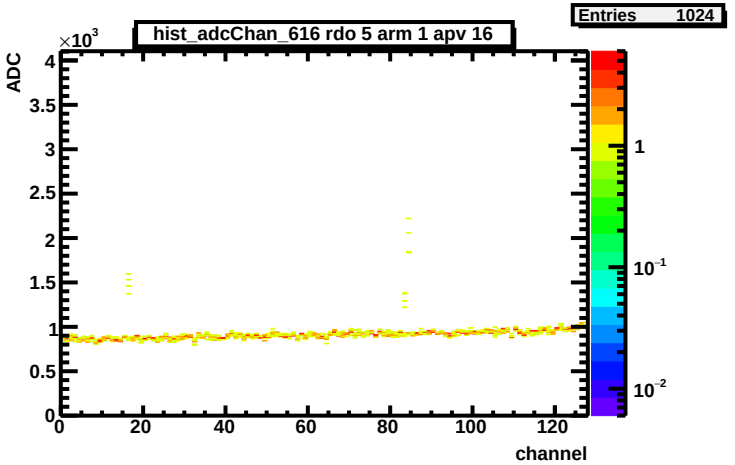

hist_adcChan_611 rdo 5 arm 1 apv 11
Entries 1024
ADC
channel

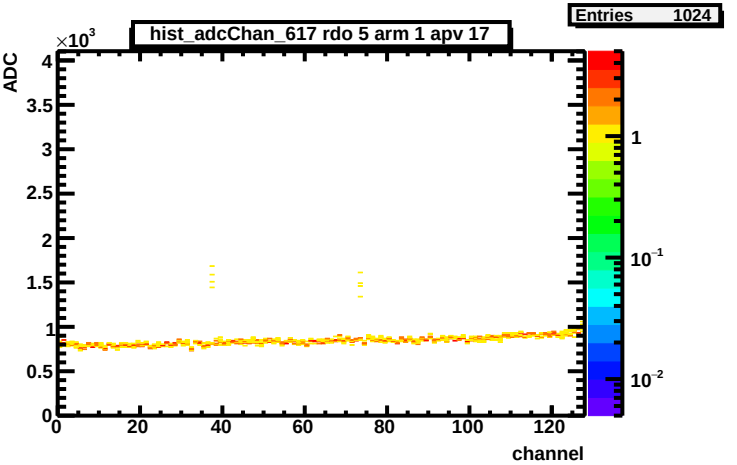

hist_adcChan_612 rdo 5 arm 1 apv 12
Entries 1024
ADC
×10^3
channel

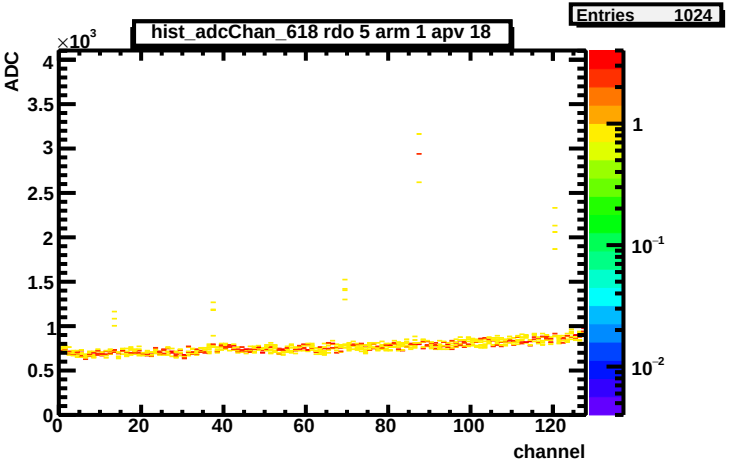

hist_adcChan_613 rdo 5 arm 1 apv 13
Entries 1024
ADC
×10^3
channel

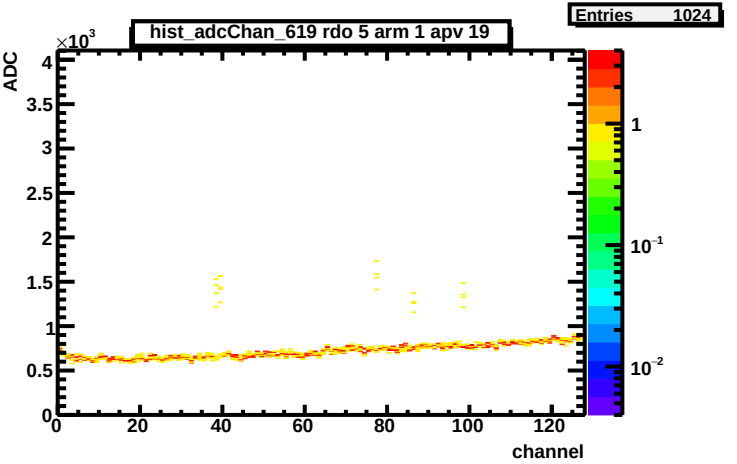

hist_adcChan_614 rdo 5 arm 1 apv 14
Entries 1024
ADC
×10^3
channel

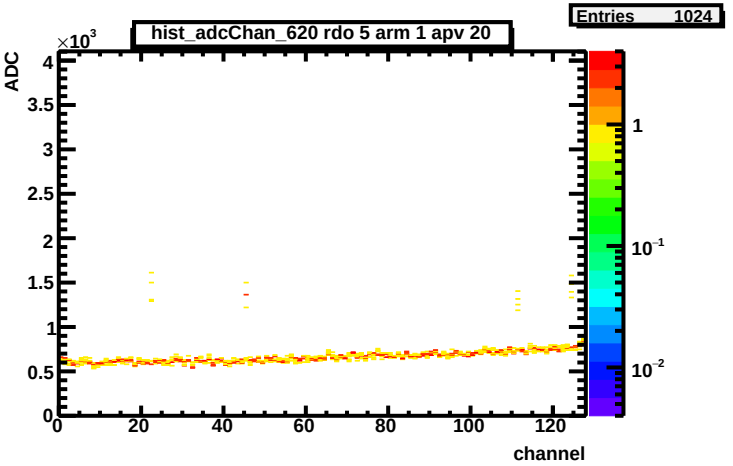

hist_adcChan_615 rdo 5 arm 1 apv 15
Entries 1024
ADC
×10^3
channel

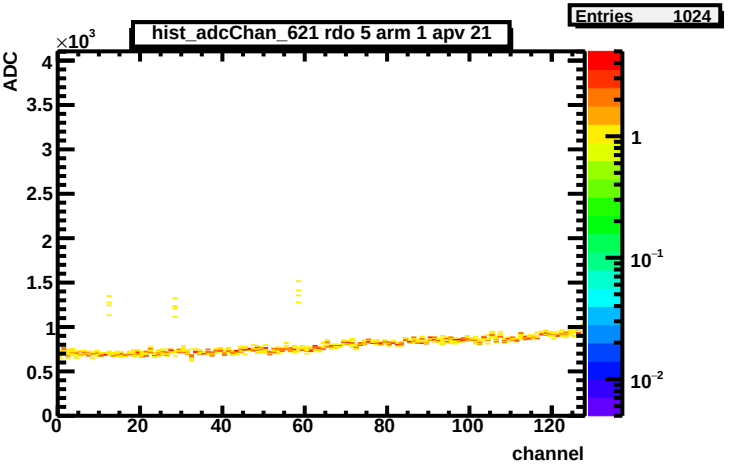

hist_adcChan_616 rdo 5 arm 1 apv 16
Entries 1024
ADC
×10^3
channel

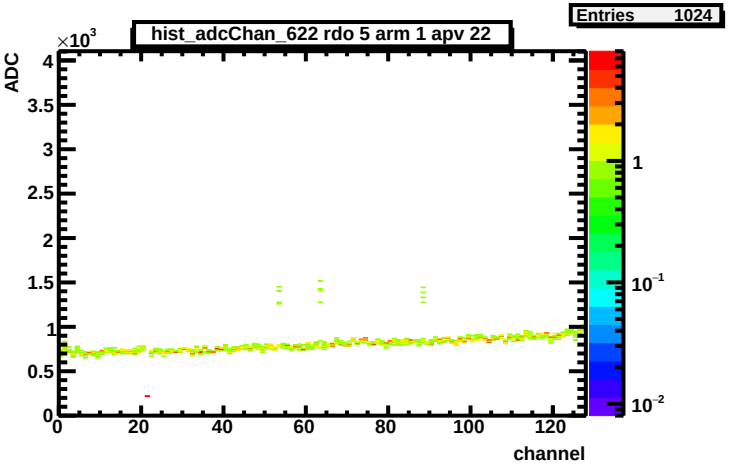

hist_adcChan_617 rdo 5 arm 1 apv 17
Entries 1024
ADC
×10^3
channel

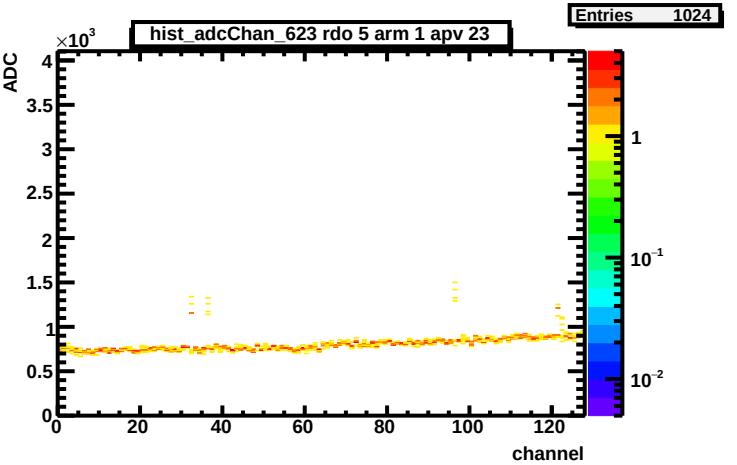

hist_adcChan_618 rdo 5 arm 1 apv 18
Entries 1024
ADC
×10^3
channel

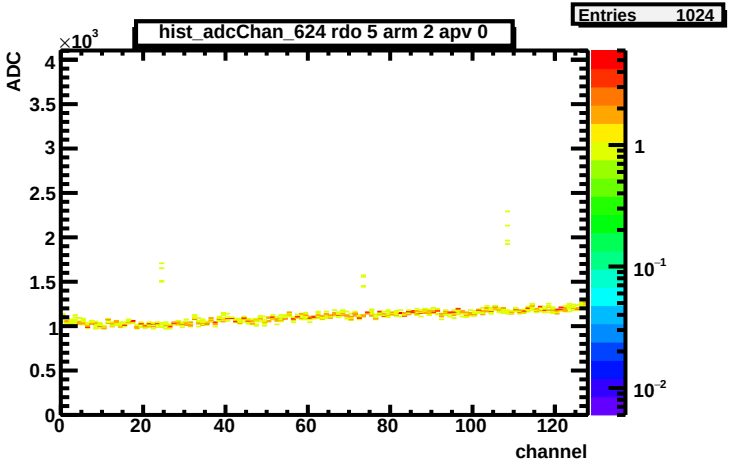

hist_adcChan_619 rdo 5 arm 1 apv 19
Entries 1024
ADC
×10^3
channel

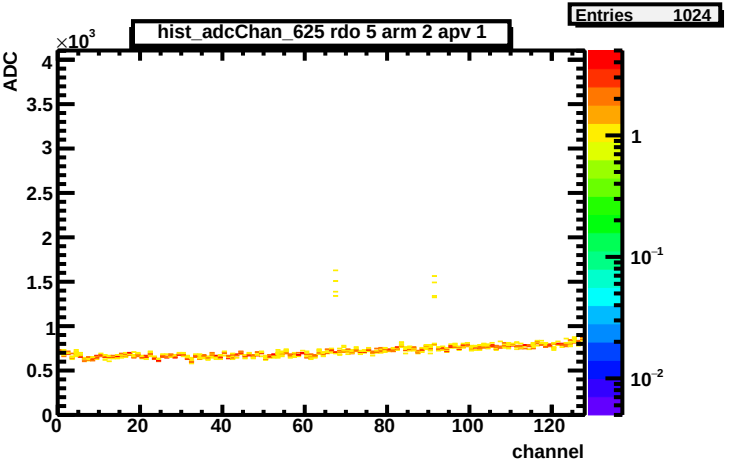

hist_adcChan_620 rdo 5 arm 1 apv 20
Entries 1024
ADC
×10^3
channel

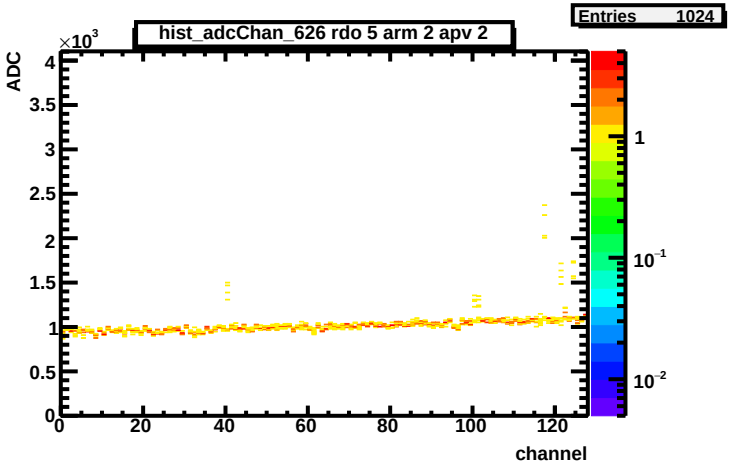

hist_adcChan_621 rdo 5 arm 1 apv 21
Entries 1024
ADC
×10^3
channel

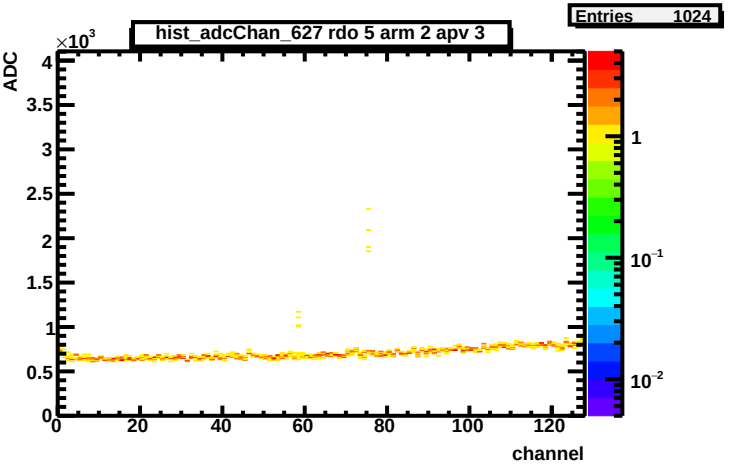

hist_adcChan_622 rdo 5 arm 1 apv 22
Entries 1024
ADC
×10^3
channel

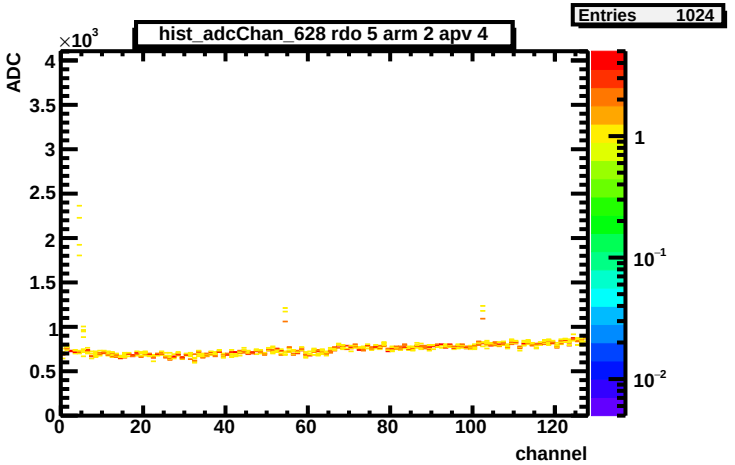

hist_adcChan_623 rdo 5 arm 1 apv 23
Entries 1024
ADC
×10^3
channel

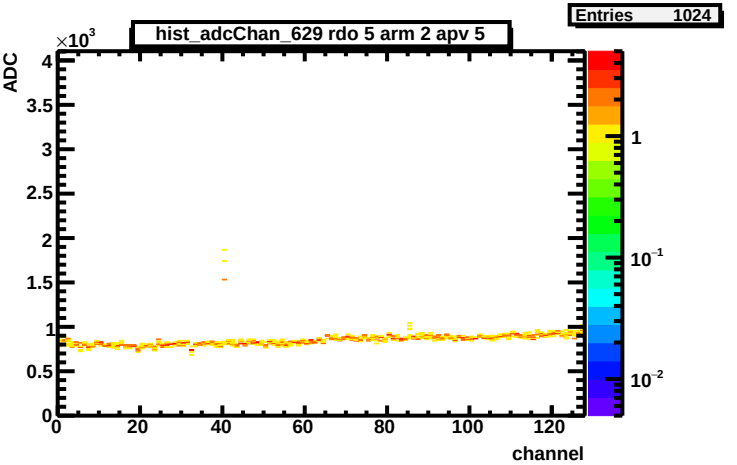

hist_adcChan_624 rdo 5 arm 2 apv 0
Entries 1024
ADC
×10^3
channel

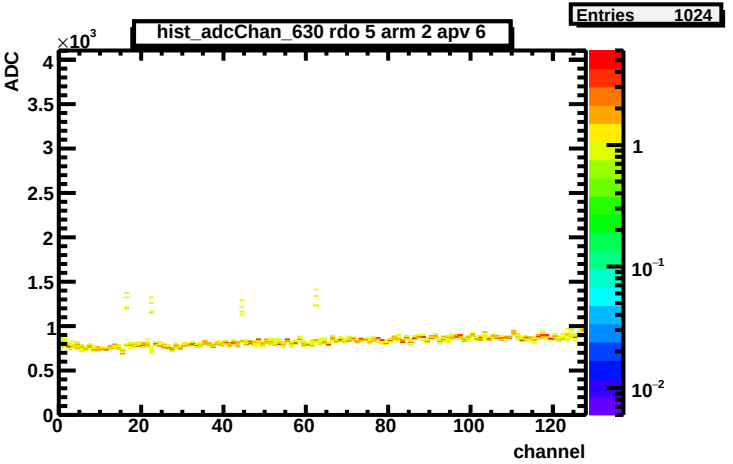

hist_adcChan_625 rdo 5 arm 2 apv 1
Entries 1024
ADC
×10^3
channel

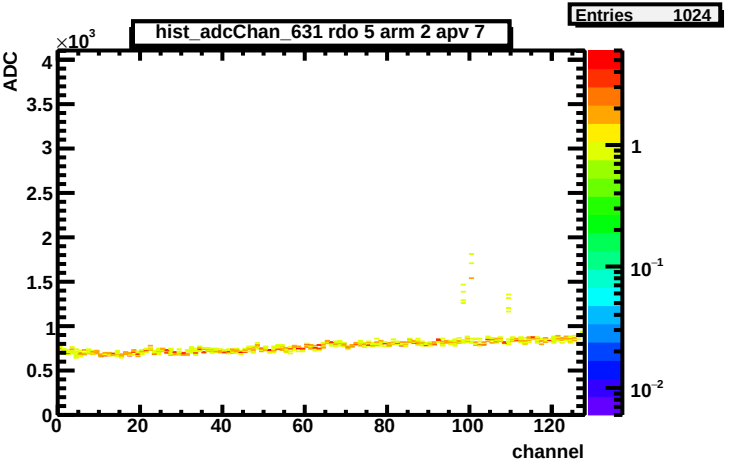

hist_adcChan_626 rdo 5 arm 2 apv 2
Entries 1024
ADC
×10^3
channel

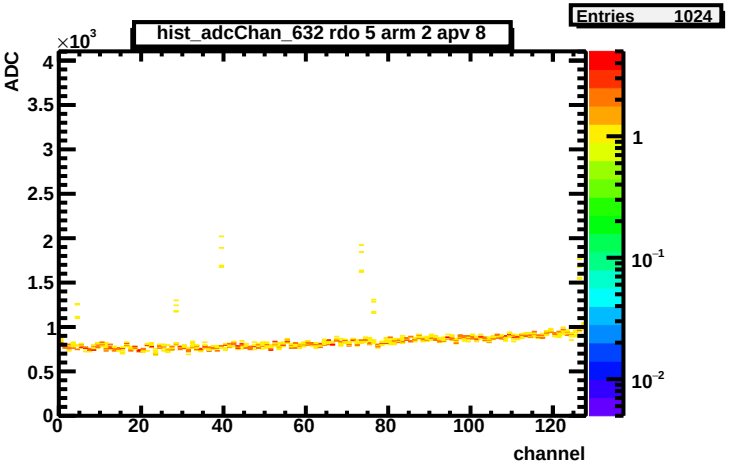

hist_adcChan_627 rdo 5 arm 2 apv 3
Entries 1024
ADC
×10^3
channel

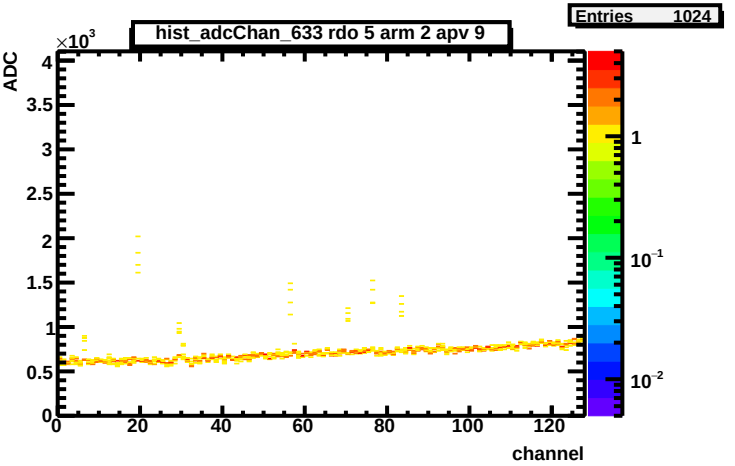

hist_adcChan_628 rdo 5 arm 2 apv 4
Entries 1024
ADC
×10^3
channel

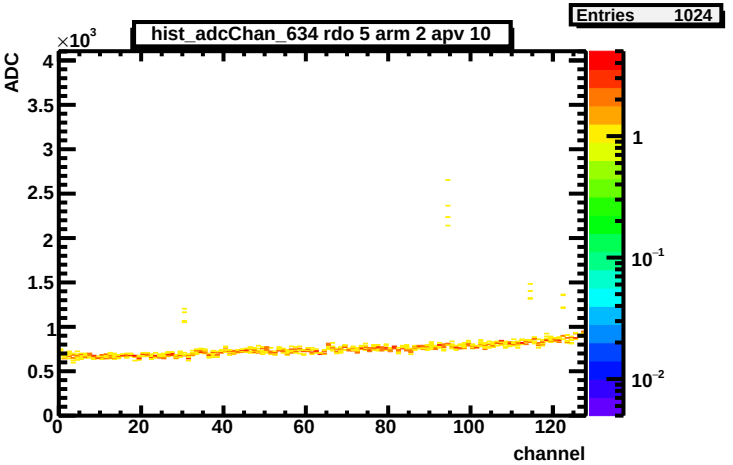

hist_adcChan_629 rdo 5 arm 2 apv 5
Entries 1024
ADC
×10^3
channel

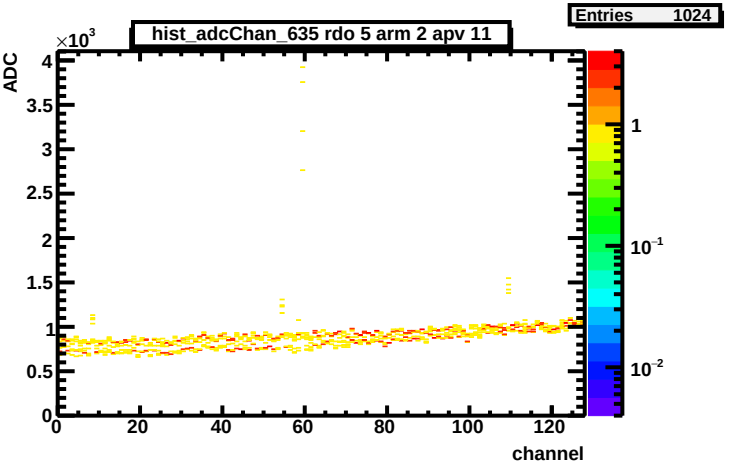

hist_adcChan_630 rdo 5 arm 2 apv 6
Entries 1024
ADC
channel

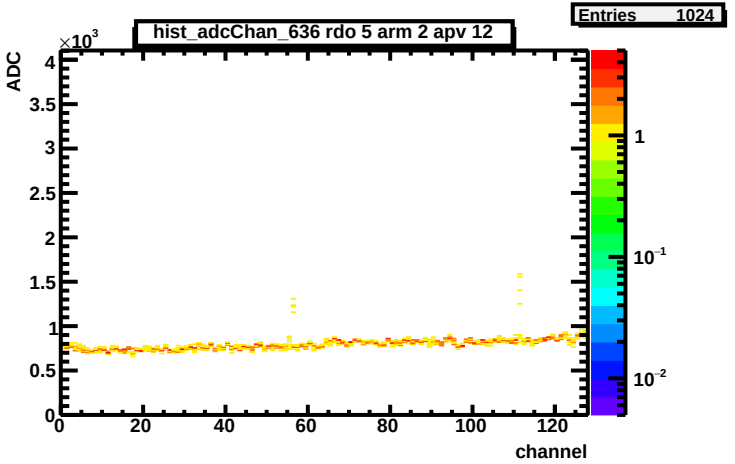

hist_adcChan_631 rdo 5 arm 2 apv 7
Entries 1024
ADC
channel

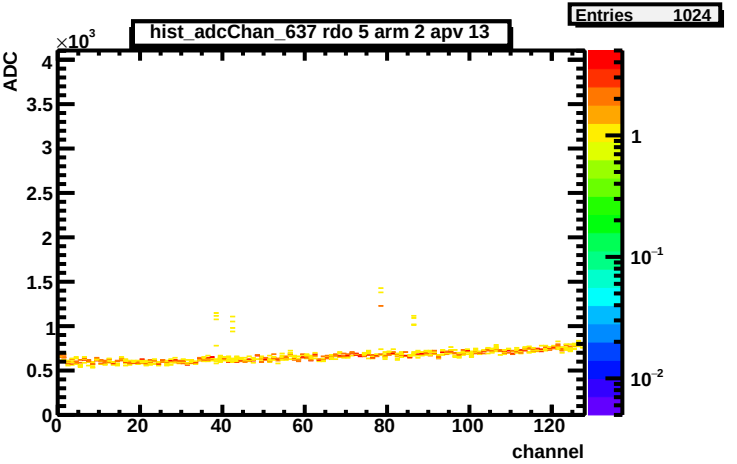

hist_adcChan_632 rdo 5 arm 2 apv 8
Entries 1024
ADC
channel

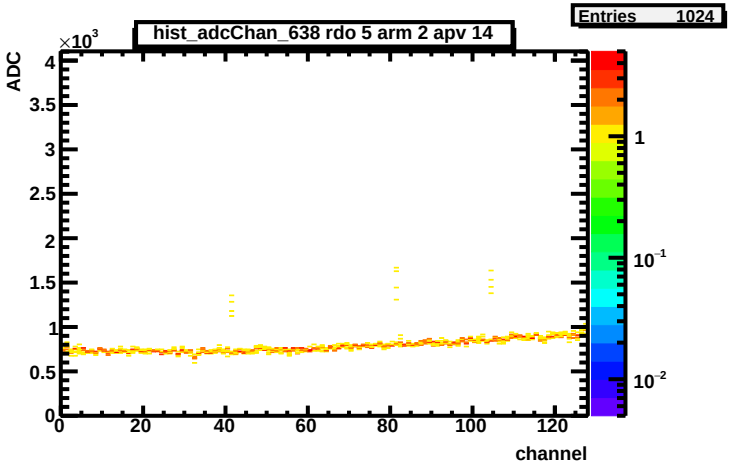

hist_adcChan_633 rdo 5 arm 2 apv 9
Entries 1024
ADC
channel

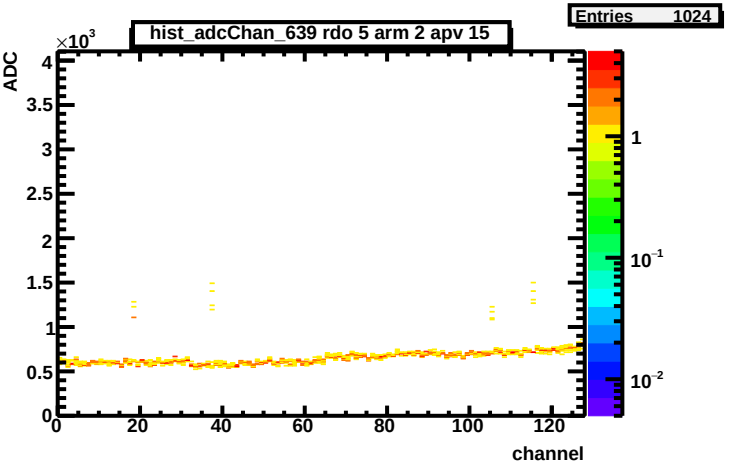

hist_adcChan_634 rdo 5 arm 2 apv 10
Entries 1024
ADC
channel

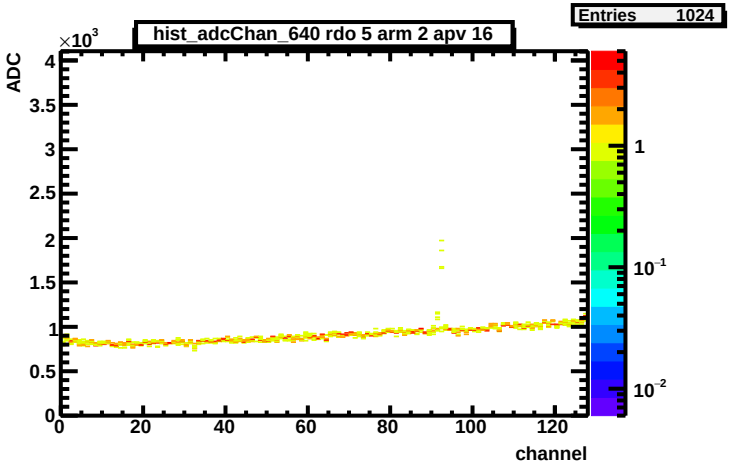

hist_adcChan_635 rdo 5 arm 2 apv 11
Entries 1024
ADC
channel

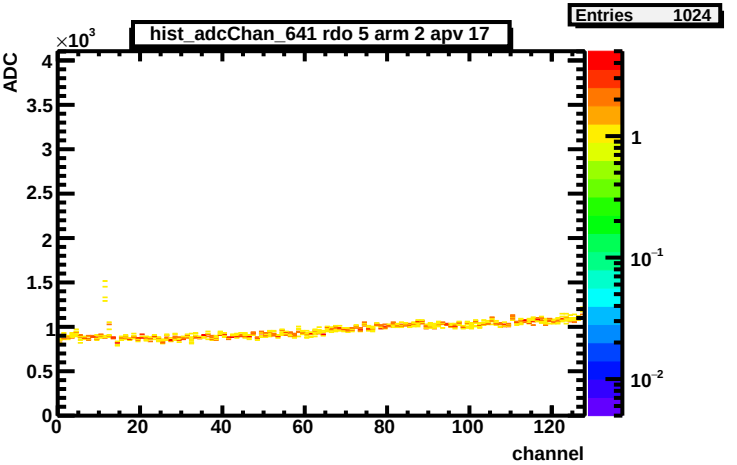

hist_adcChan_636 rdo 5 arm 2 apv 12
Entries 1024
ADC
channel

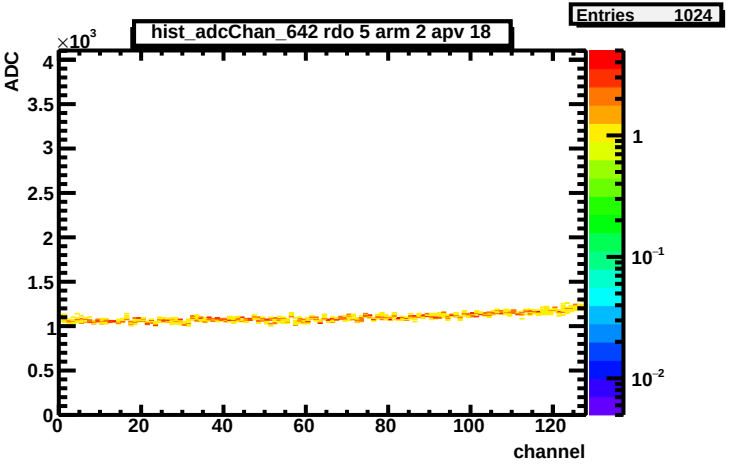

hist_adcChan_637 rdo 5 arm 2 apv 13
Entries 1024
ADC
channel

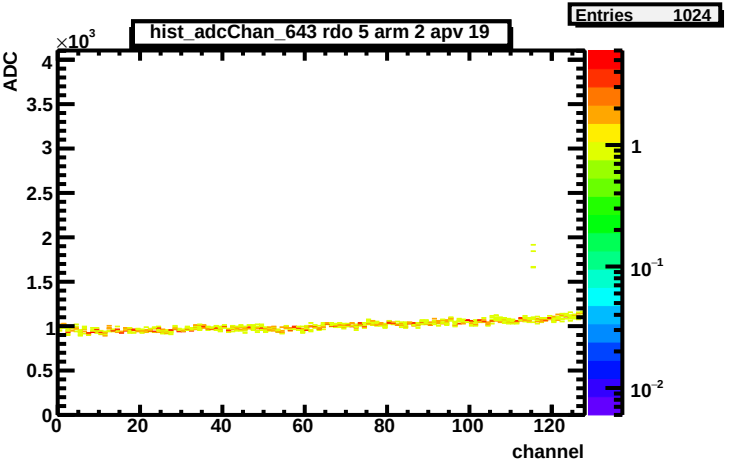

hist_adcChan_638 rdo 5 arm 2 apv 14
Entries 1024
ADC
channel

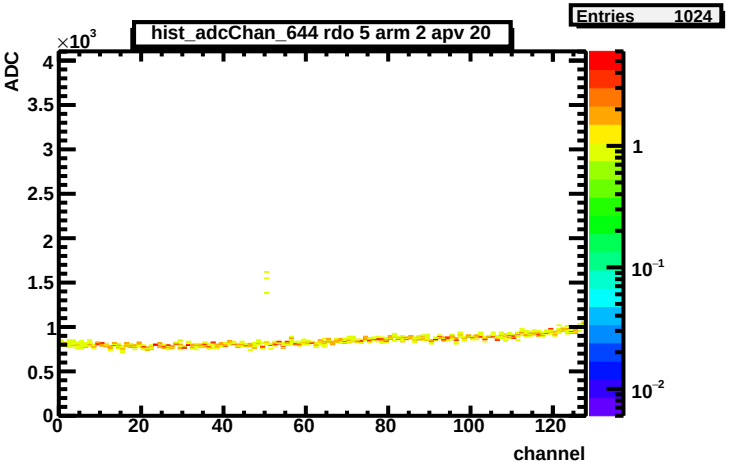

hist_adcChan_639 rdo 5 arm 2 apv 15
Entries 1024
ADC
channel

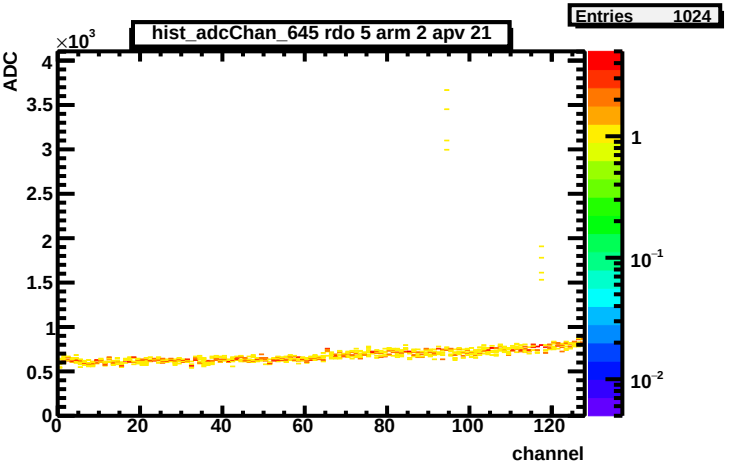

hist_adcChan_640 rdo 5 arm 2 apv 16
Entries 1024
ADC
channel

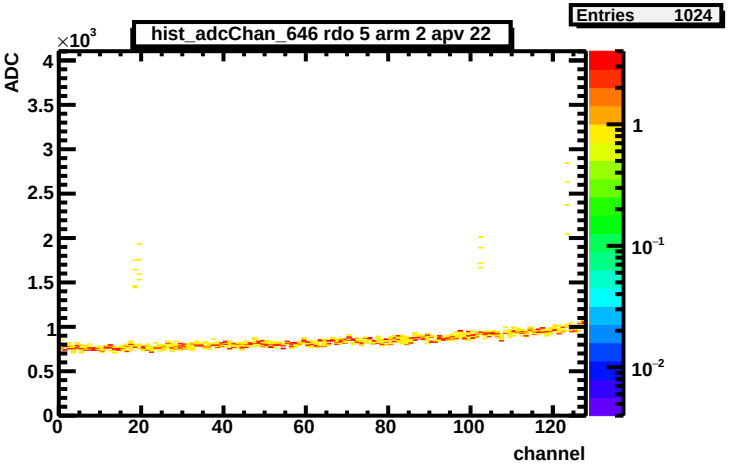

hist_adcChan_641 rdo 5 arm 2 apv 17
Entries 1024
ADC
channel

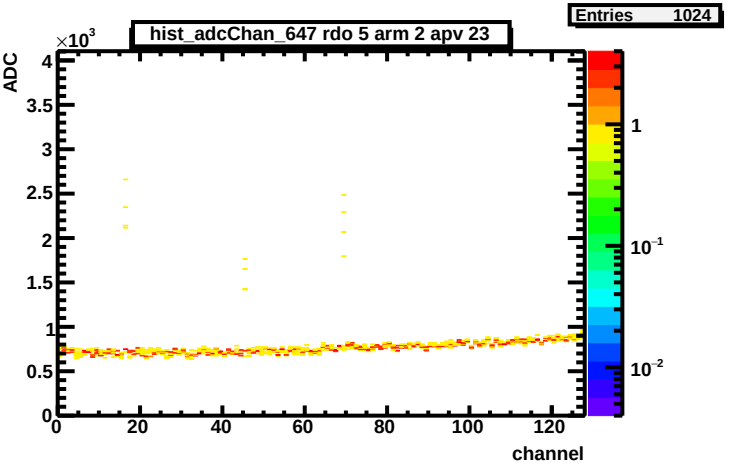

hist_adcChan_642 rdo 5 arm 2 apv 18
Entries 1024
ADC
channel

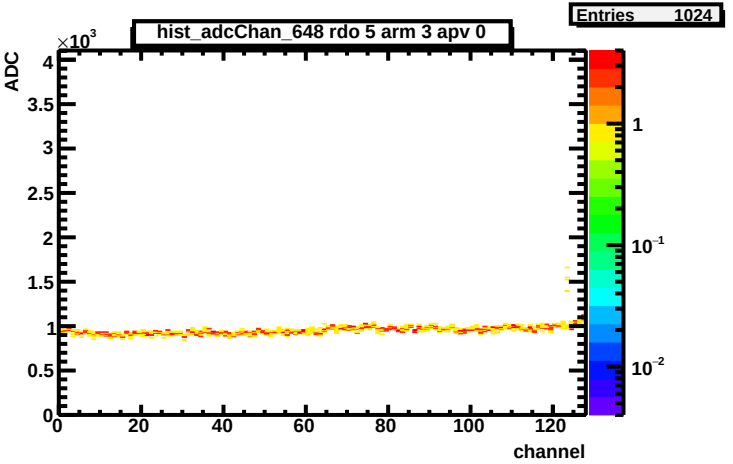

hist_adcChan_643 rdo 5 arm 2 apv 19
Entries 1024
ADC
channel

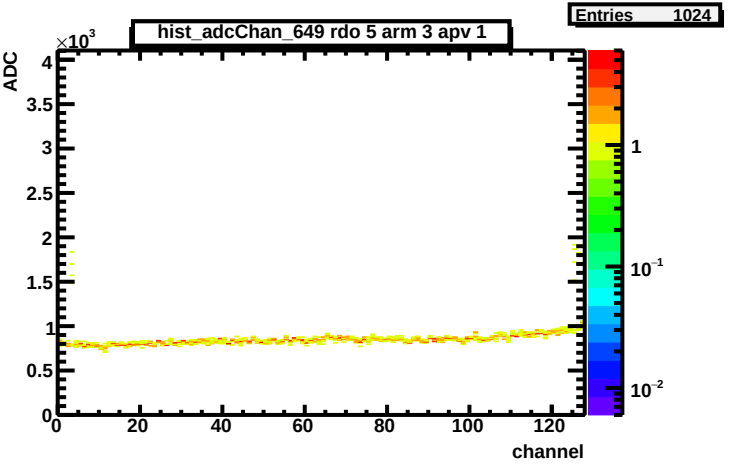

hist_adcChan_644 rdo 5 arm 2 apv 20
Entries 1024
ADC
channel

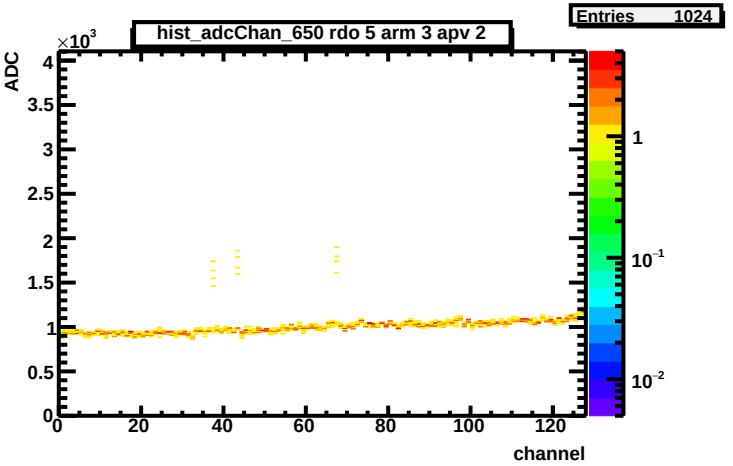

hist_adcChan_645 rdo 5 arm 2 apv 21
Entries 1024
ADC
channel

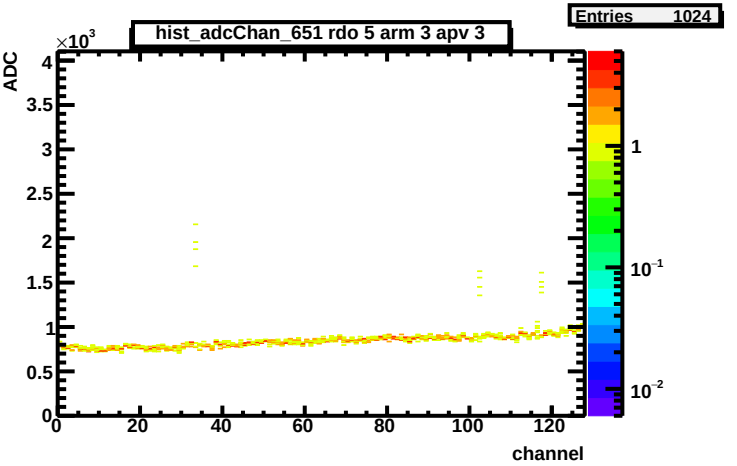

hist_adcChan_646 rdo 5 arm 2 apv 22
Entries 1024
ADC
channel

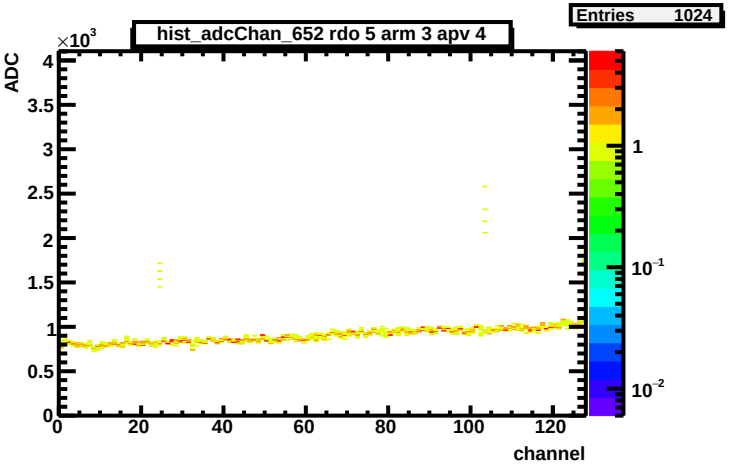

hist_adcChan_647 rdo 5 arm 2 apv 23
Entries 1024
ADC
channel

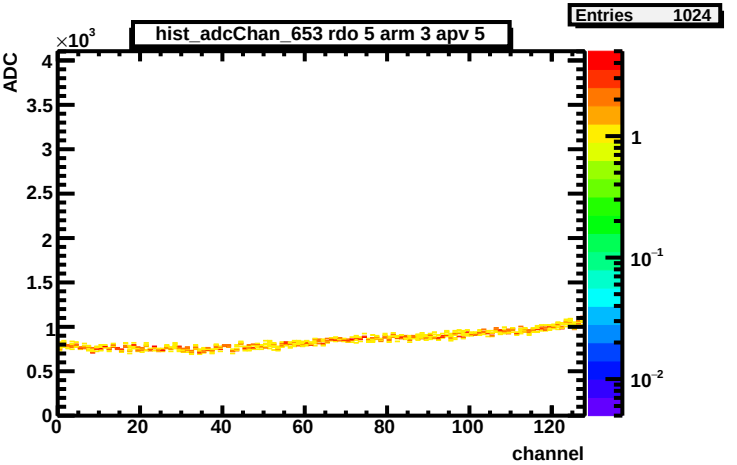

hist_adcChan_648 rdo 5 arm 3 apv 0
Entries 1024
ADC
×10^3
channel

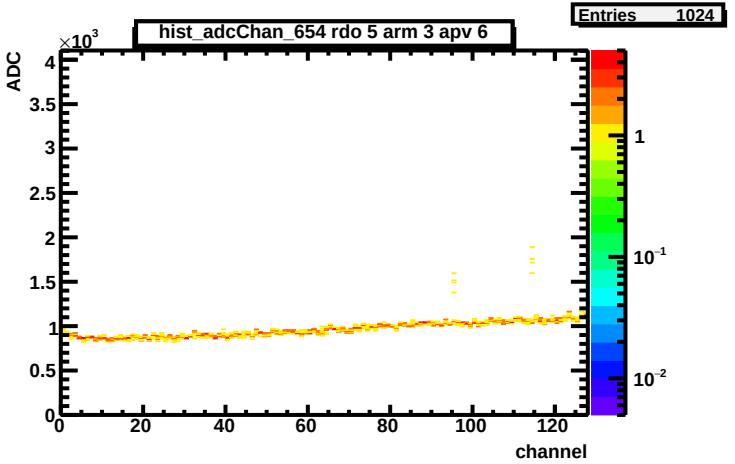

hist_adcChan_649 rdo 5 arm 3 apv 1
Entries 1024
ADC
×10^3
channel

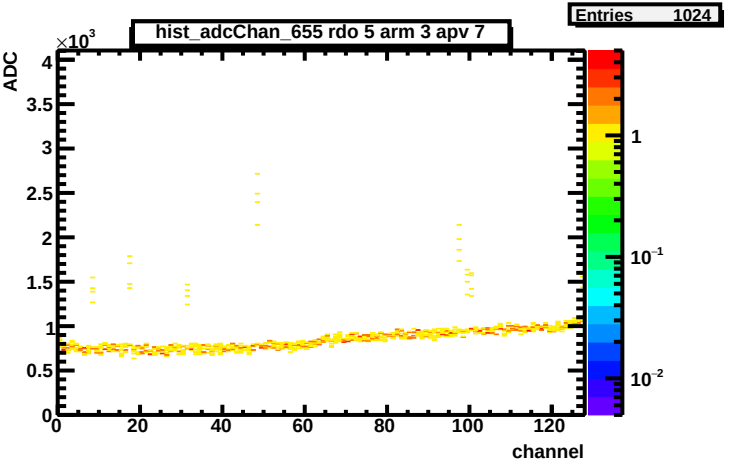

hist_adcChan_650 rdo 5 arm 3 apv 2
Entries 1024
ADC
×10^3
channel

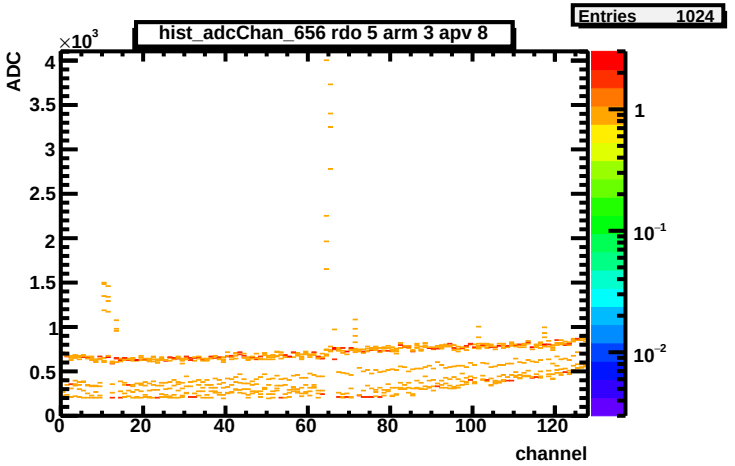

hist_adcChan_651 rdo 5 arm 3 apv 3
Entries 1024
ADC
×10^3
channel

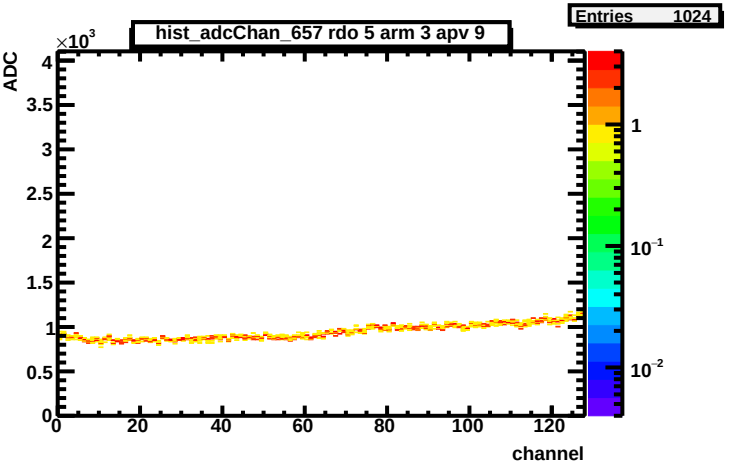

hist_adcChan_652 rdo 5 arm 3 apv 4
Entries 1024
ADC
×10^3
channel

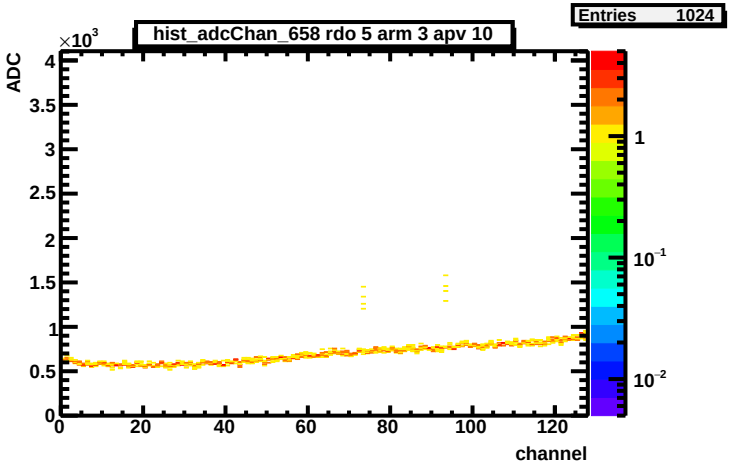

hist_adcChan_653 rdo 5 arm 3 apv 5
Entries 1024
ADC
×10^3
channel

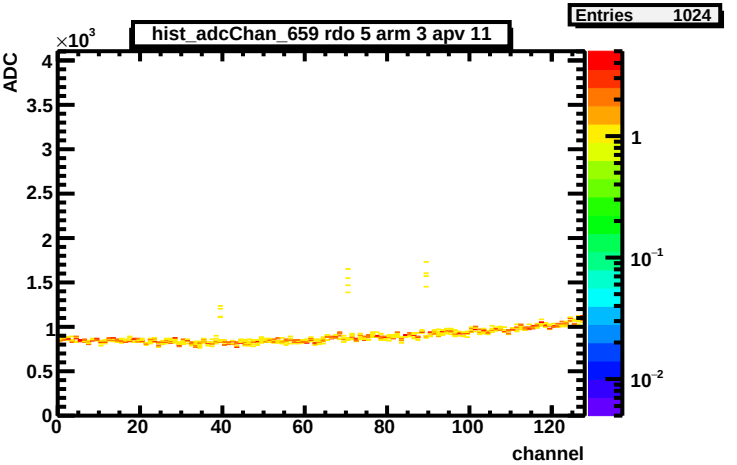

hist_adcChan_654 rdo 5 arm 3 apv 6
Entries 1024
ADC
×10^3
channel

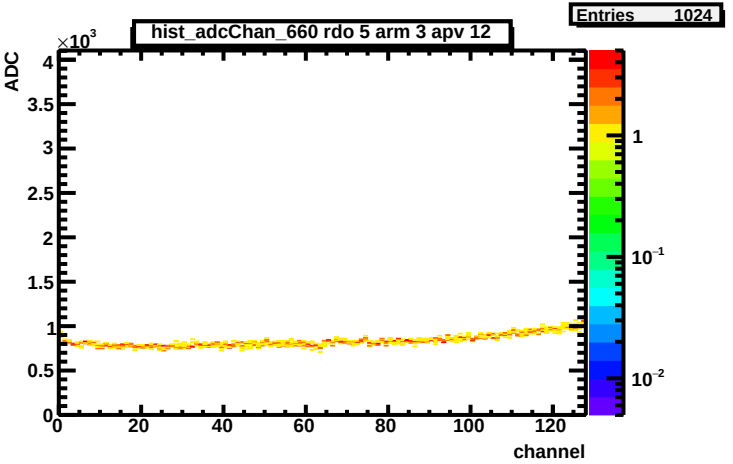

hist_adcChan_655 rdo 5 arm 3 apv 7
Entries 1024
ADC
×10^3
channel

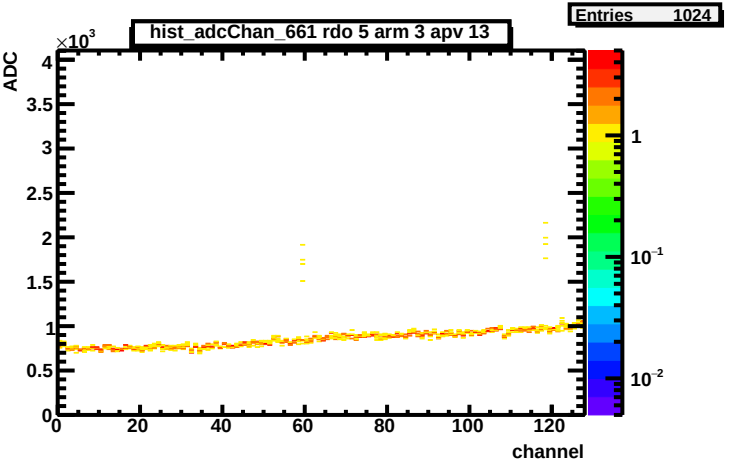

hist_adcChan_656 rdo 5 arm 3 apv 8
Entries 1024
ADC
×10^3
channel

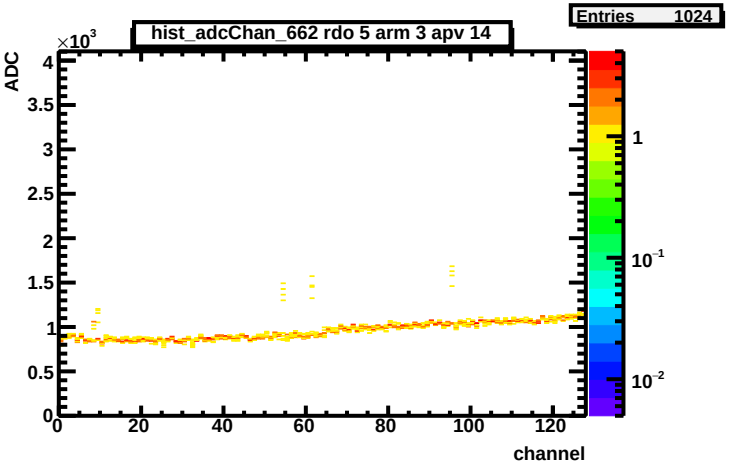

hist_adcChan_657 rdo 5 arm 3 apv 9
Entries 1024
ADC
×10^3
channel

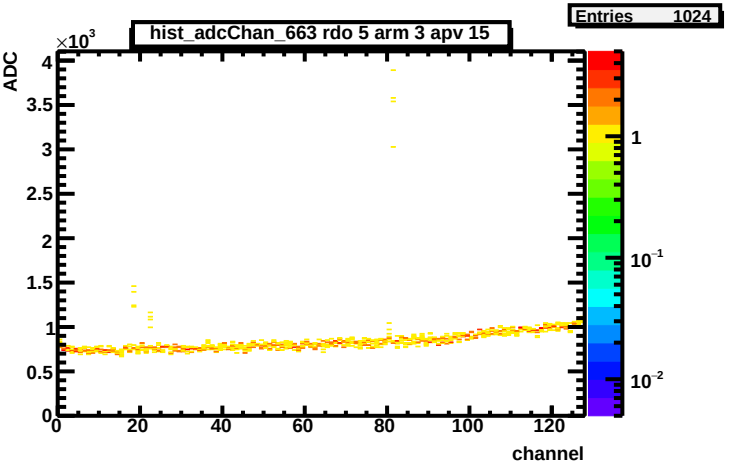

hist_adcChan_658 rdo 5 arm 3 apv 10
Entries 1024
ADC
×10^3
channel

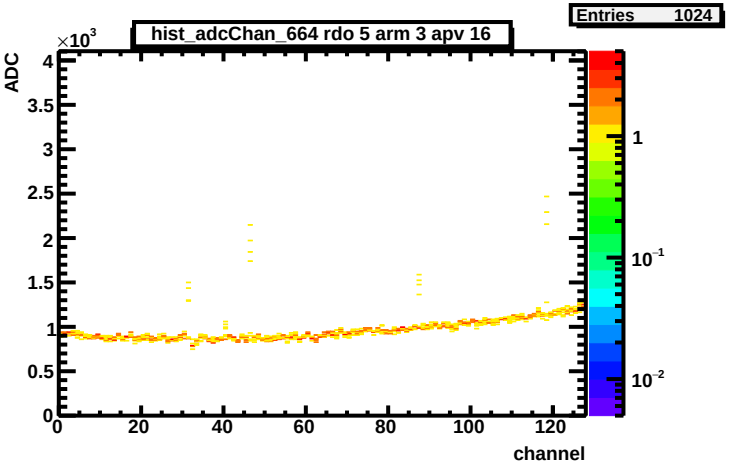

hist_adcChan_659 rdo 5 arm 3 apv 11
Entries 1024
ADC
×10^3
channel

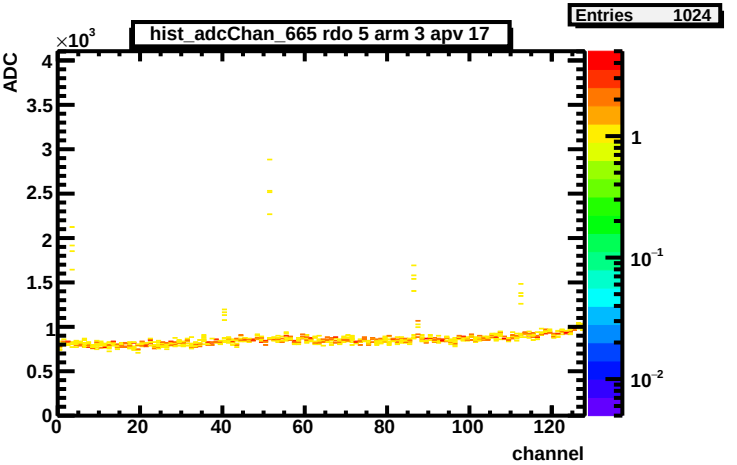

hist_adcChan_660 rdo 5 arm 3 apv 12
Entries 1024
ADC
×10^3
channel

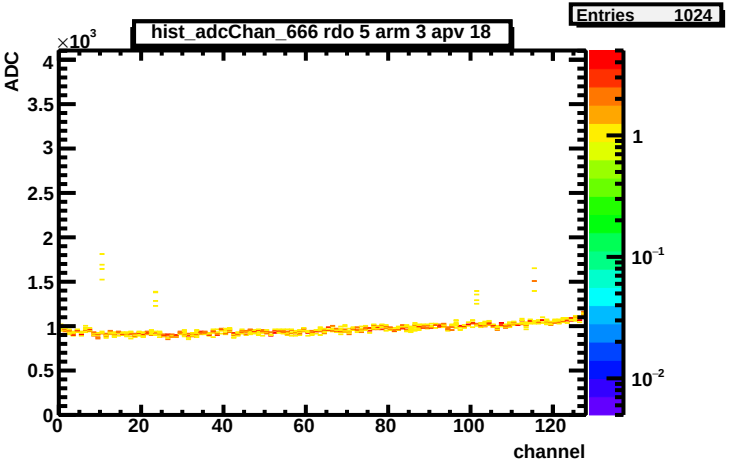

hist_adcChan_661 rdo 5 arm 3 apv 13
Entries 1024
ADC
×10^3
channel

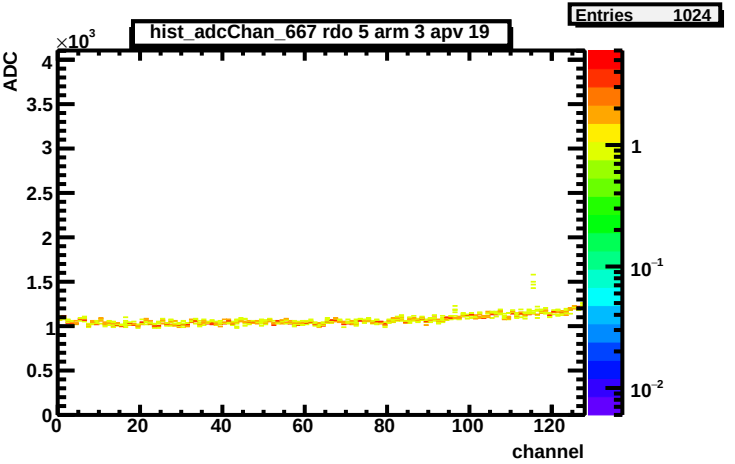

hist_adcChan_662 rdo 5 arm 3 apv 14
Entries 1024
ADC
×10^3
channel

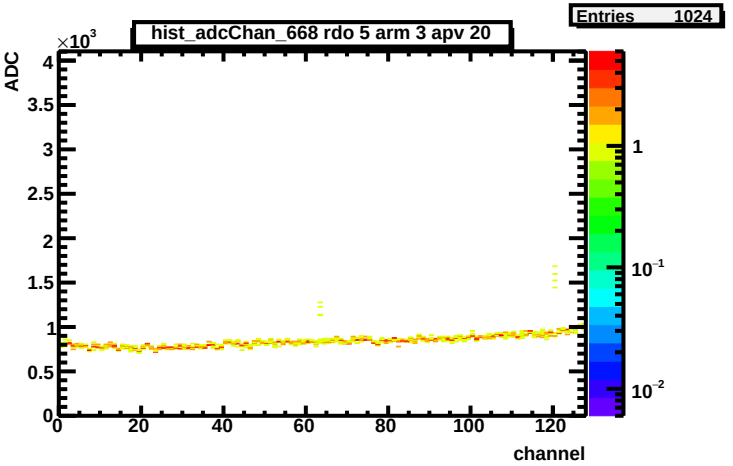

hist_adcChan_663 rdo 5 arm 3 apv 15
Entries 1024
ADC
×10^3
channel

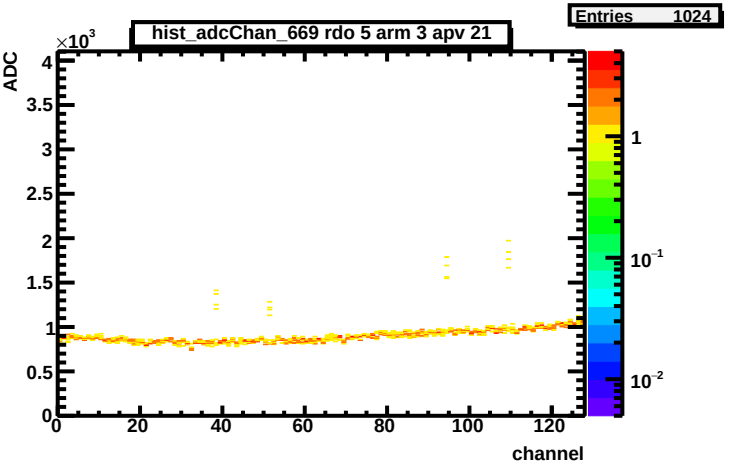

hist_adcChan_664 rdo 5 arm 3 apv 16
Entries 1024
ADC
×10^3
channel

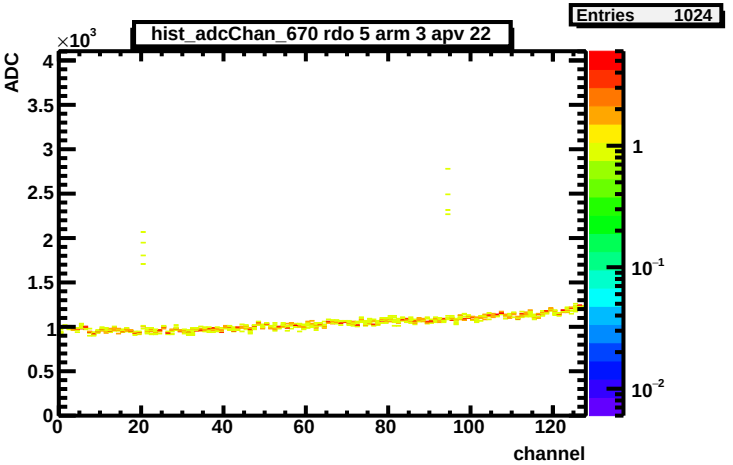

hist_adcChan_665 rdo 5 arm 3 apv 17
Entries 1024
ADC
×10^3
channel

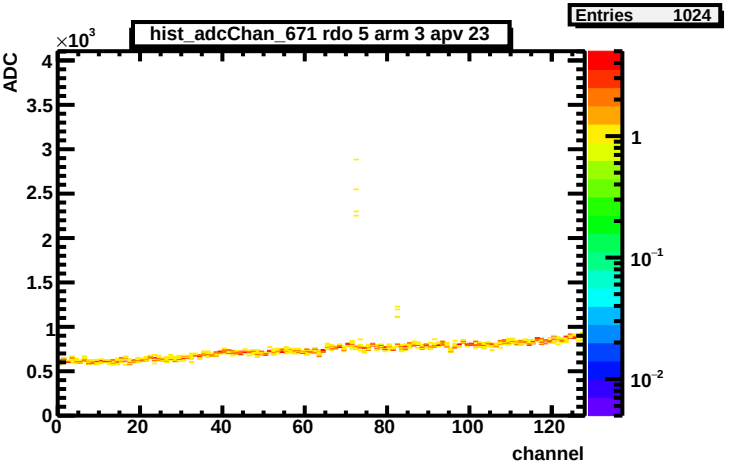

hist_adcChan_666 rdo 5 arm 3 apv 18
Entries 1024
ADC
channel

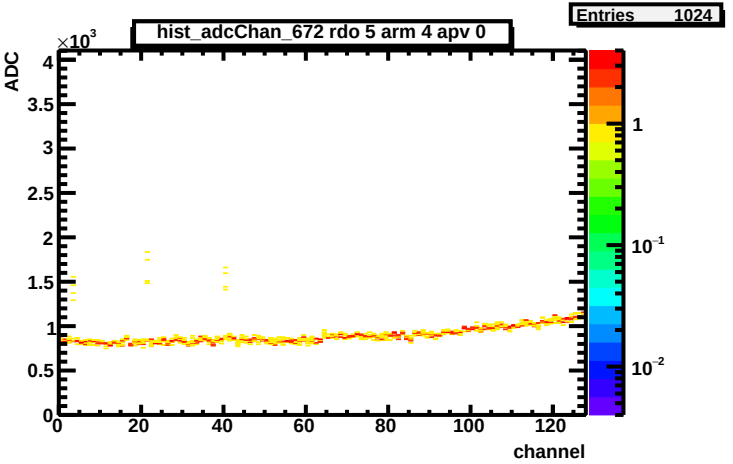

hist_adcChan_667 rdo 5 arm 3 apv 19
Entries 1024
ADC
channel

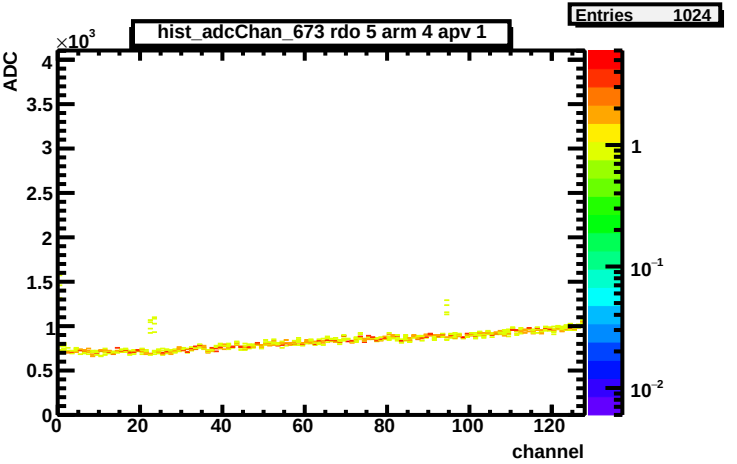

hist_adcChan_668 rdo 5 arm 3 apv 20
Entries 1024
ADC
channel

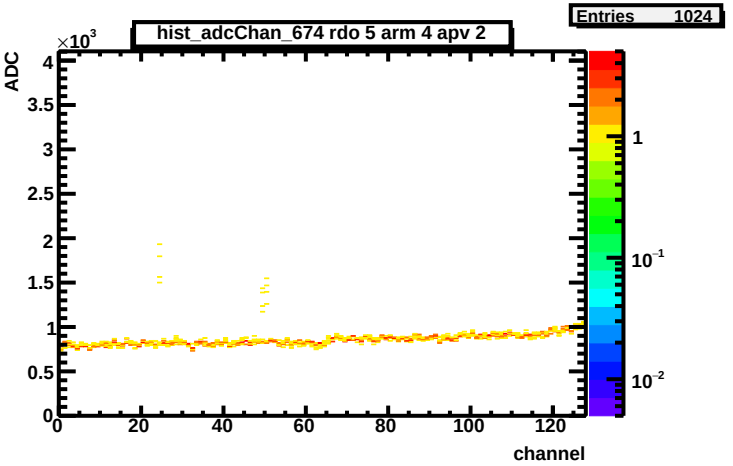

hist_adcChan_669 rdo 5 arm 3 apv 21
Entries 1024
ADC
channel

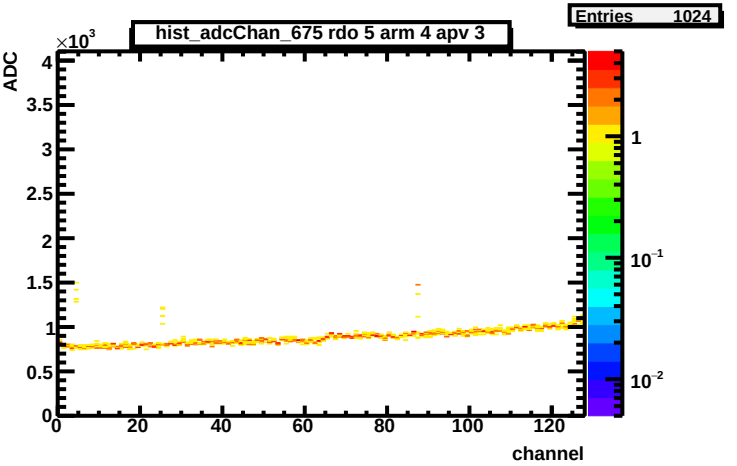

hist_adcChan_670 rdo 5 arm 3 apv 22
Entries 1024
ADC
channel

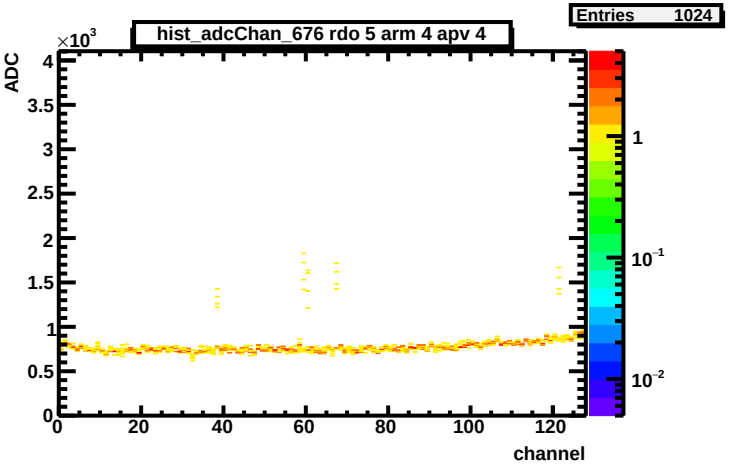

hist_adcChan_671 rdo 5 arm 3 apv 23
Entries 1024
ADC
channel

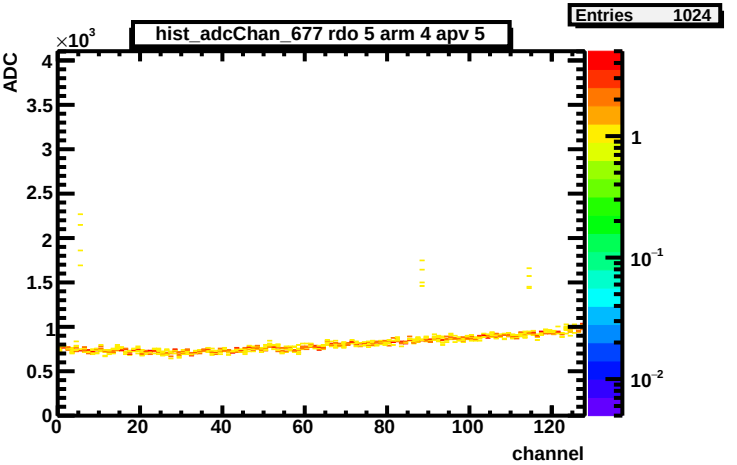

hist_adcChan_672 rdo 5 arm 4 apv 0
Entries 1024
ADC
×10^3
channel

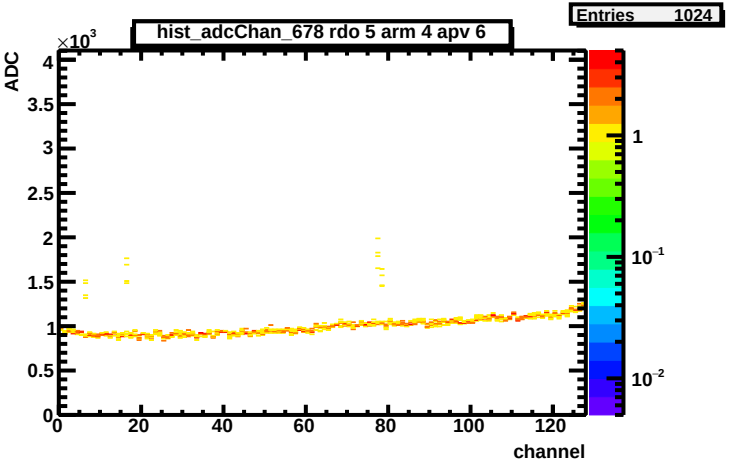

hist_adcChan_673 rdo 5 arm 4 apv 1
Entries 1024
ADC
×10^3
channel

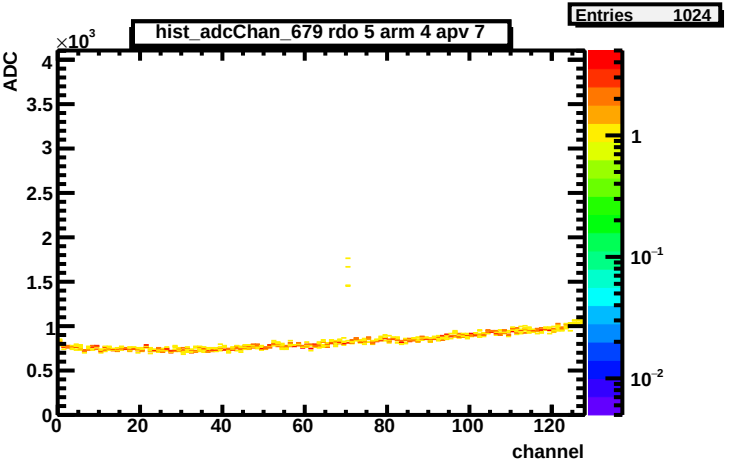

hist_adcChan_674 rdo 5 arm 4 apv 2
Entries 1024
ADC
×10^3
channel

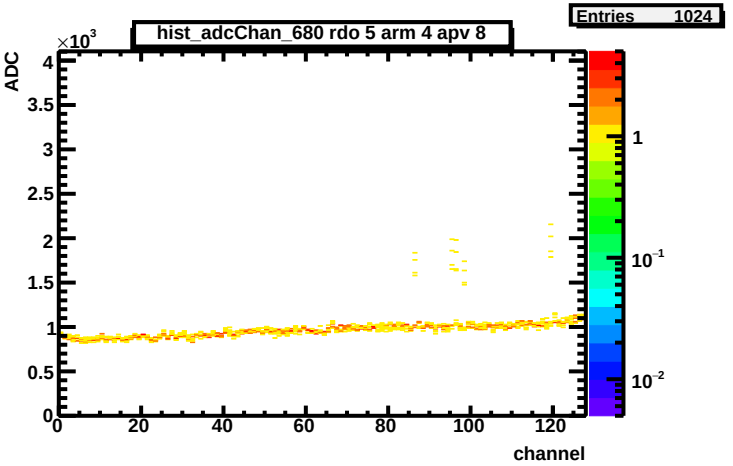

hist_adcChan_675 rdo 5 arm 4 apv 3
Entries 1024
ADC
×10^3
channel

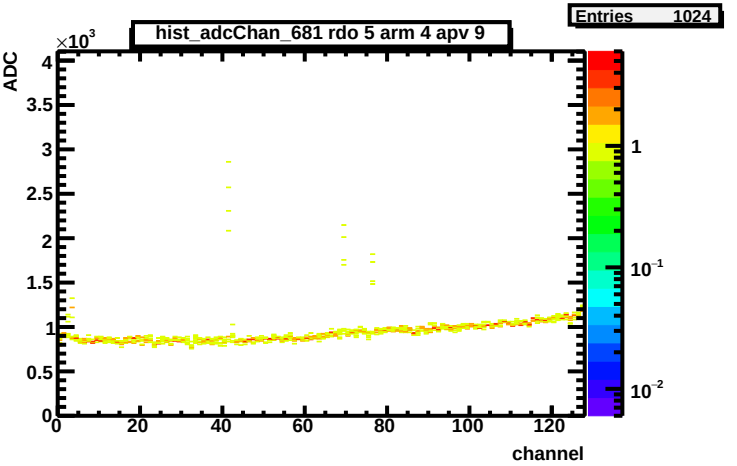

hist_adcChan_676 rdo 5 arm 4 apv 4
Entries 1024
ADC
×10^3
channel

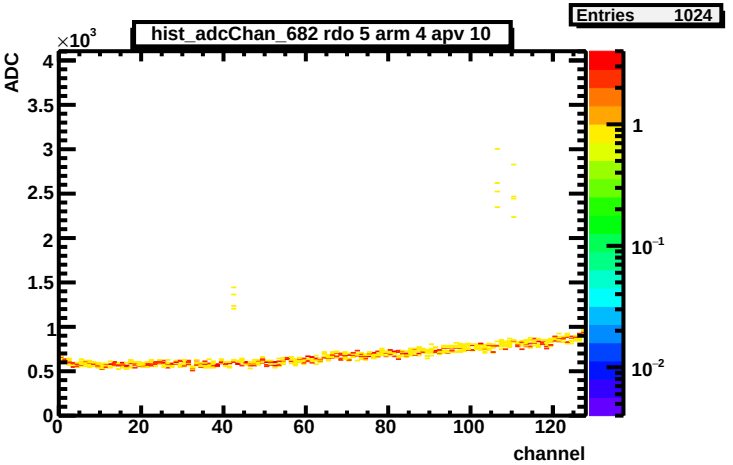

hist_adcChan_677 rdo 5 arm 4 apv 5
Entries 1024
ADC
×10^3
channel

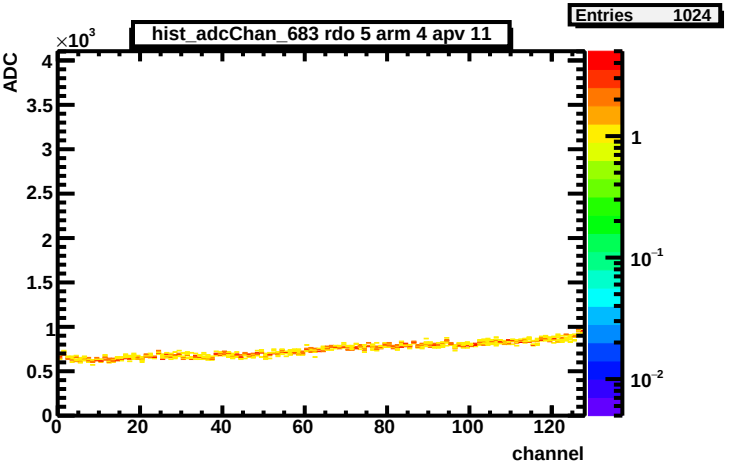

hist_adcChan_678 rdo 5 arm 4 apv 6
Entries 1024
ADC
×10^3
channel

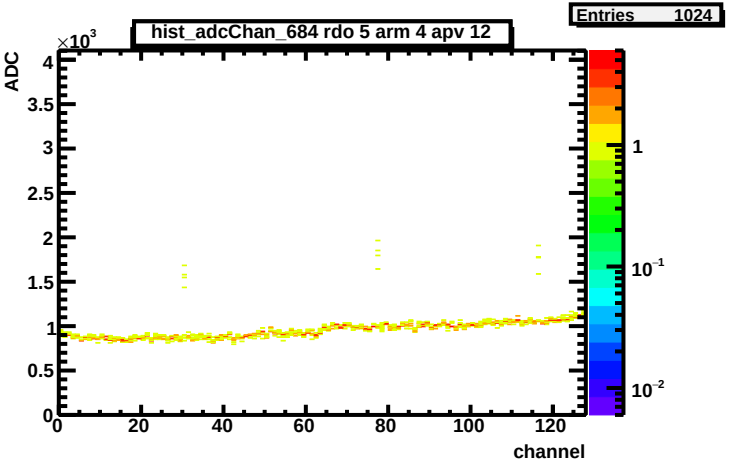

hist_adcChan_679 rdo 5 arm 4 apv 7
Entries 1024
ADC
×10^3
channel

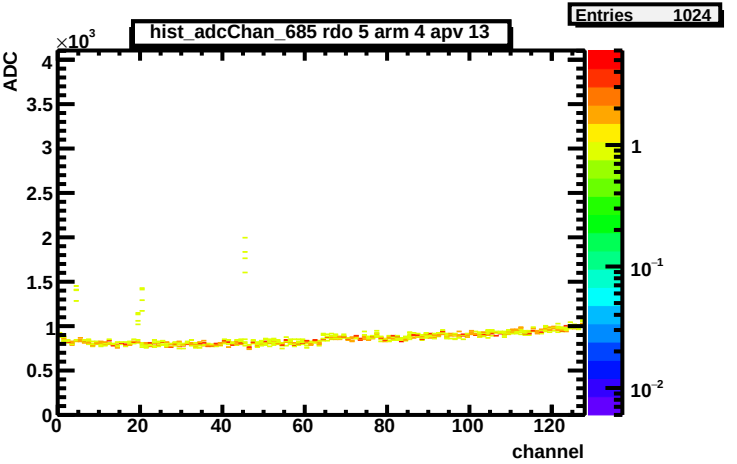

hist_adcChan_680 rdo 5 arm 4 apv 8
Entries 1024
ADC
×10^3
channel

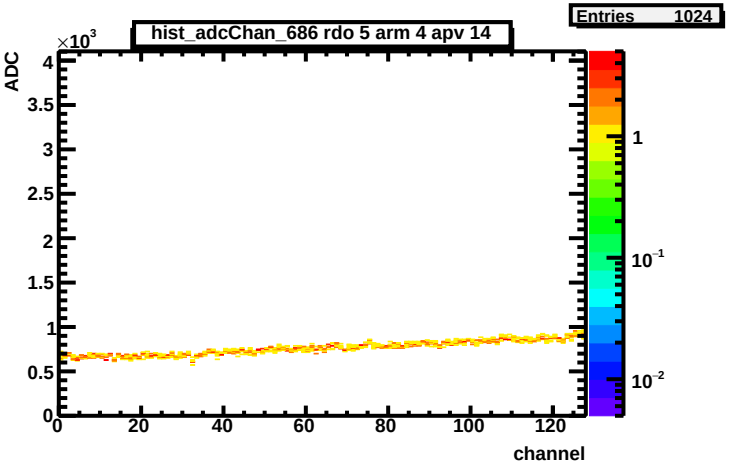

hist_adcChan_681 rdo 5 arm 4 apv 9
Entries 1024
ADC
×10^3
channel

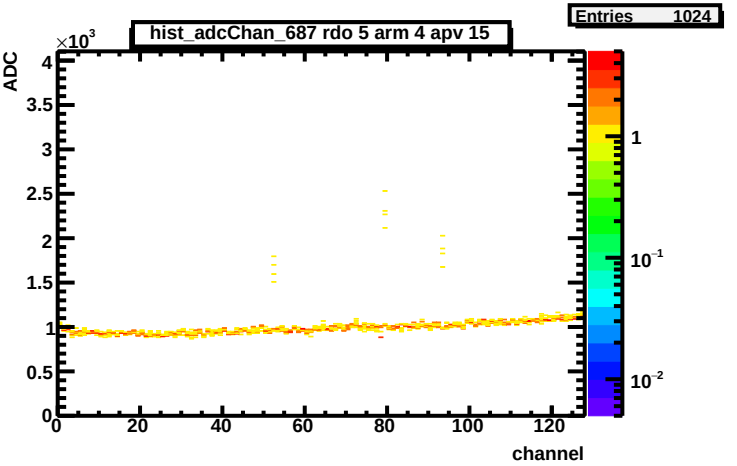

hist_adcChan_682 rdo 5 arm 4 apv 10
Entries 1024
ADC
×10^3
channel

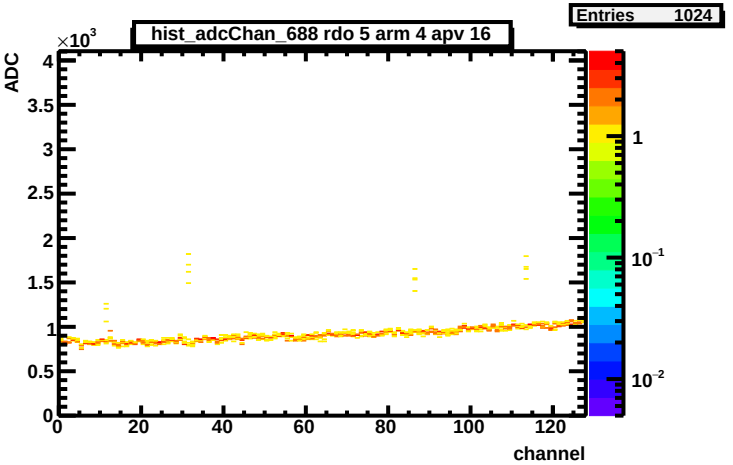

hist_adcChan_683 rdo 5 arm 4 apv 11
Entries 1024
ADC
×10^3
channel

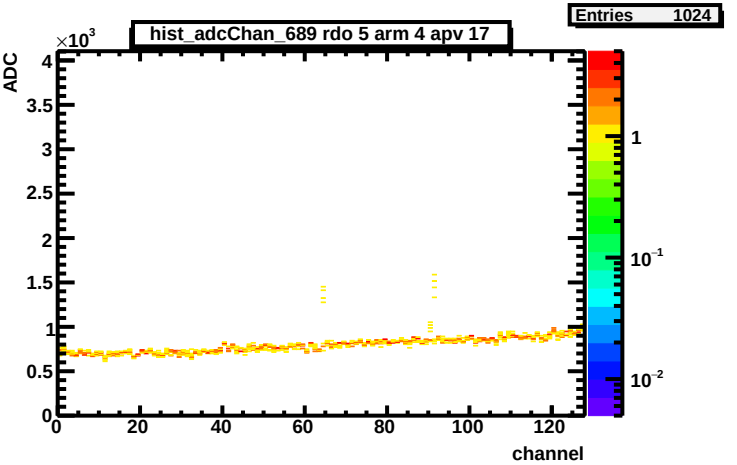

hist_adcChan_684 rdo 5 arm 4 apv 12
Entries 1024
ADC
×10^3
channel

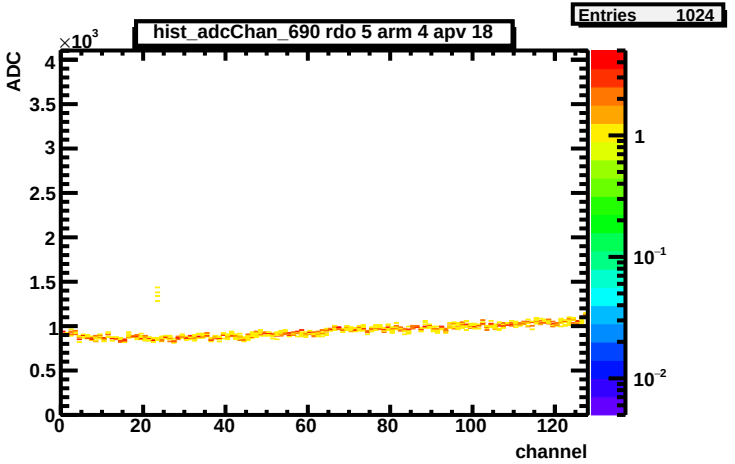

hist_adcChan_685 rdo 5 arm 4 apv 13
Entries 1024
ADC
×10^3
channel

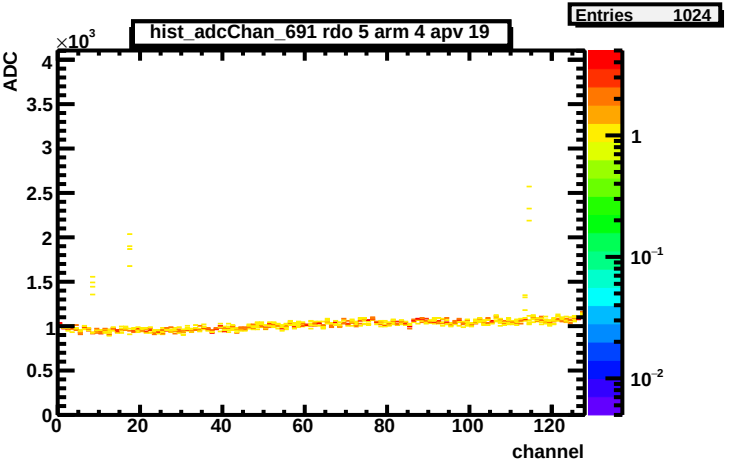

hist_adcChan_686 rdo 5 arm 4 apv 14
Entries 1024
ADC
×10^3
channel

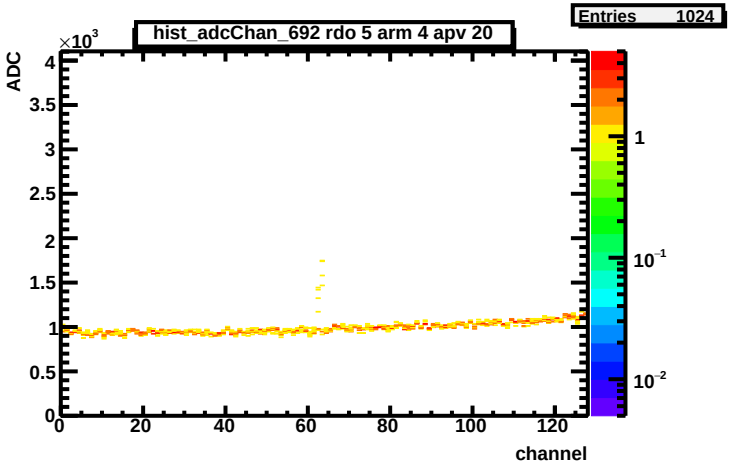

hist_adcChan_687 rdo 5 arm 4 apv 15
Entries 1024
ADC
×10^3
channel

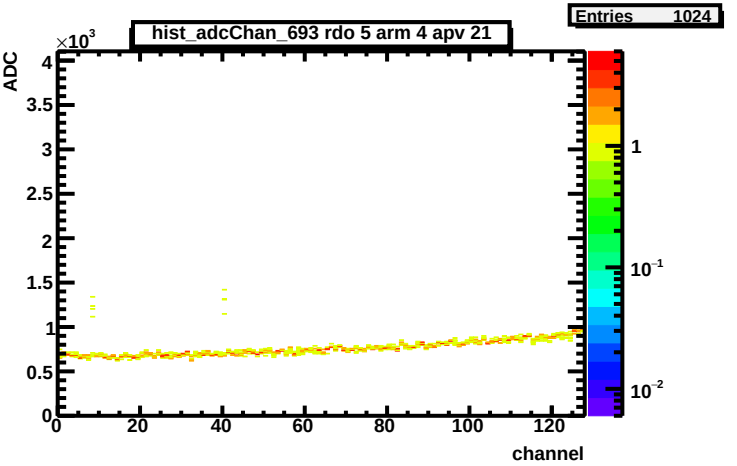

hist_adcChan_688 rdo 5 arm 4 apv 16
Entries 1024
ADC
×10^3
channel

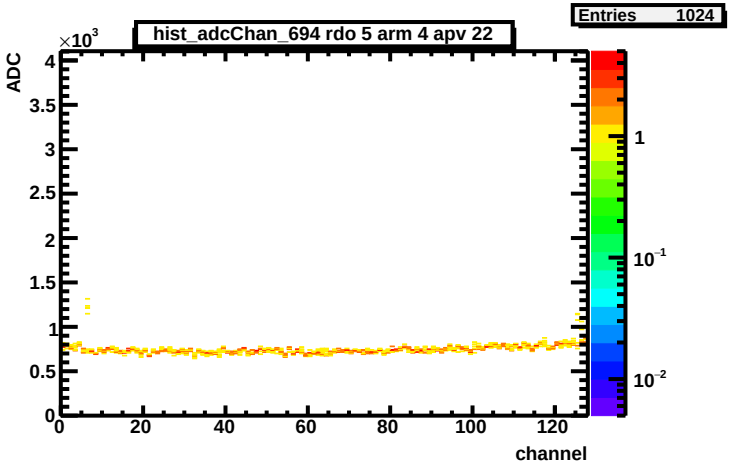

hist_adcChan_689 rdo 5 arm 4 apv 17
Entries 1024
ADC
×10^3
channel

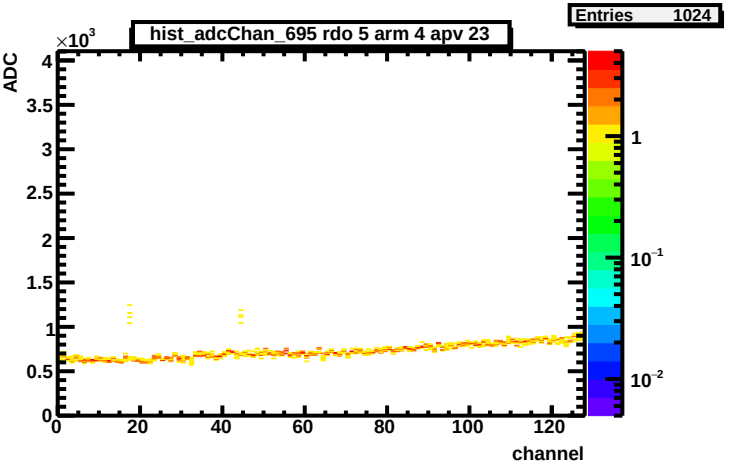

hist_adcChan_690 rdo 5 arm 4 apv 18
Entries 1024
ADC
channel

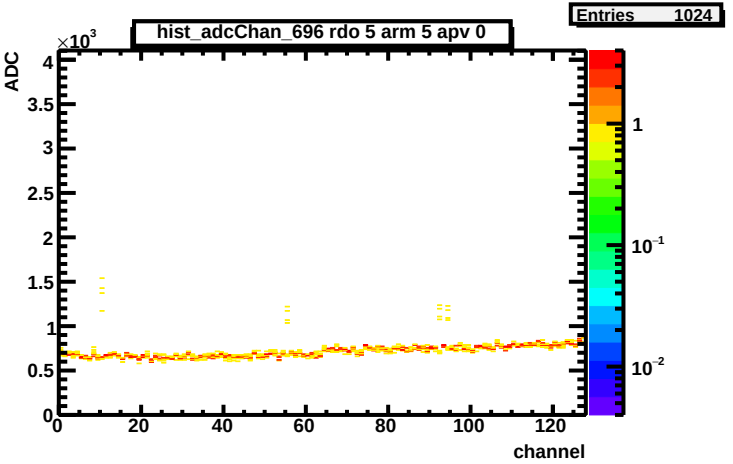

hist_adcChan_691 rdo 5 arm 4 apv 19
Entries 1024
ADC
channel

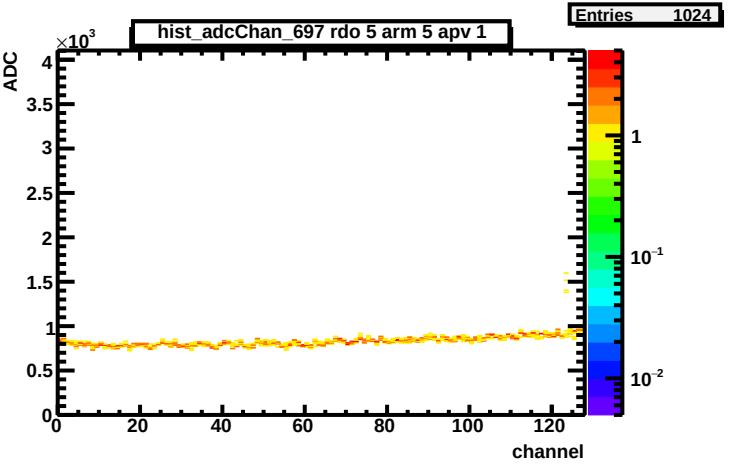

hist_adcChan_692 rdo 5 arm 4 apv 20
Entries 1024
ADC
channel

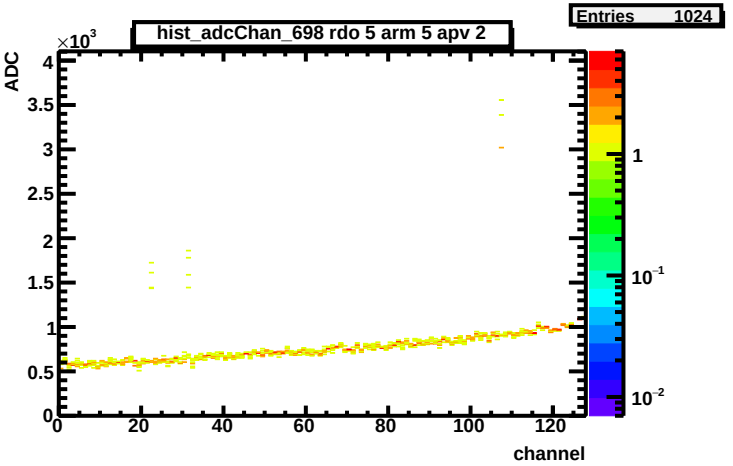

hist_adcChan_693 rdo 5 arm 4 apv 21
Entries 1024
ADC
channel

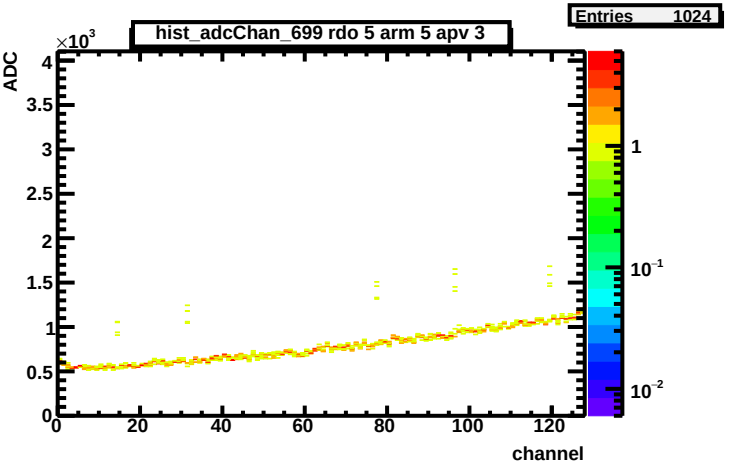

hist_adcChan_694 rdo 5 arm 4 apv 22
Entries 1024
ADC
channel

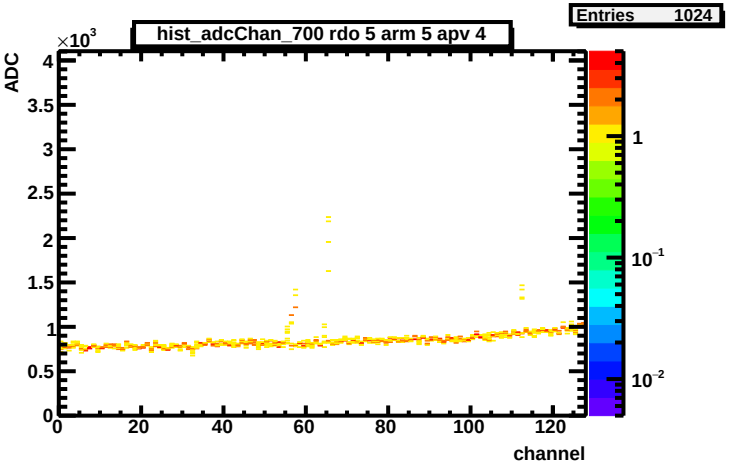

hist_adcChan_695 rdo 5 arm 4 apv 23
Entries 1024
ADC
channel

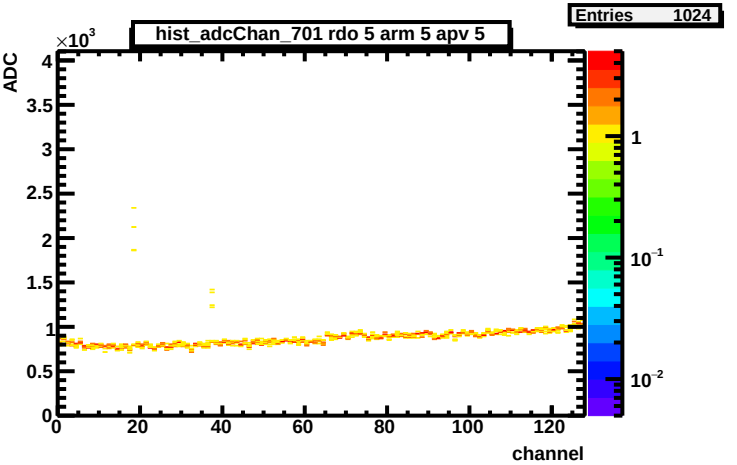

hist_adcChan_696 rdo 5 arm 5 apv 0
Entries 1024
ADC
channel

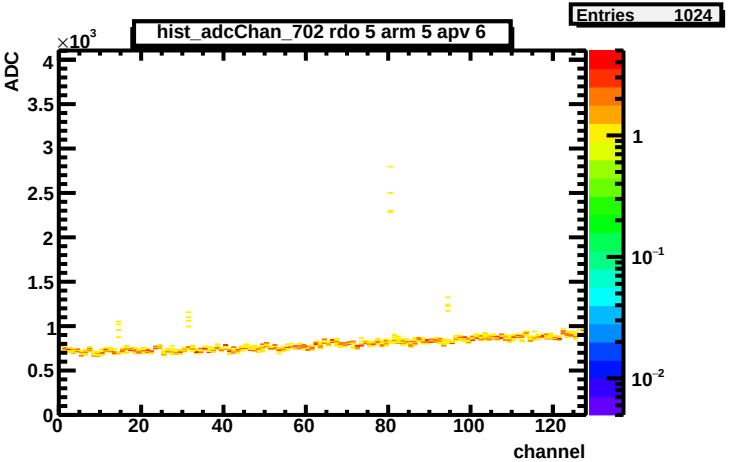

hist_adcChan_697 rdo 5 arm 5 apv 1
Entries 1024
ADC
channel

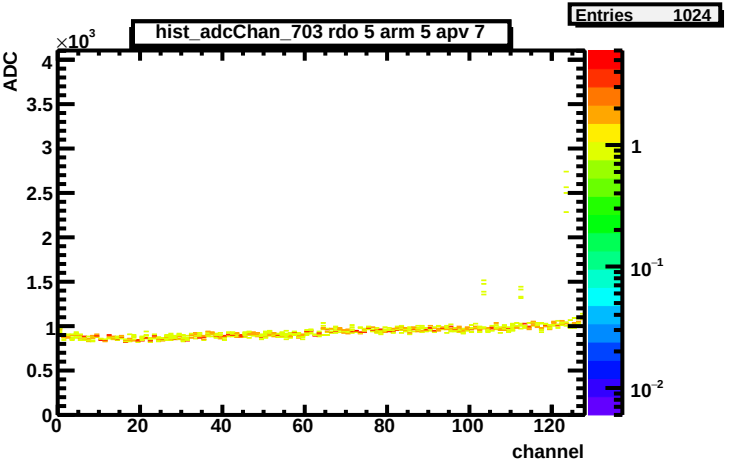

hist_adcChan_698 rdo 5 arm 5 apv 2
Entries 1024
ADC
channel

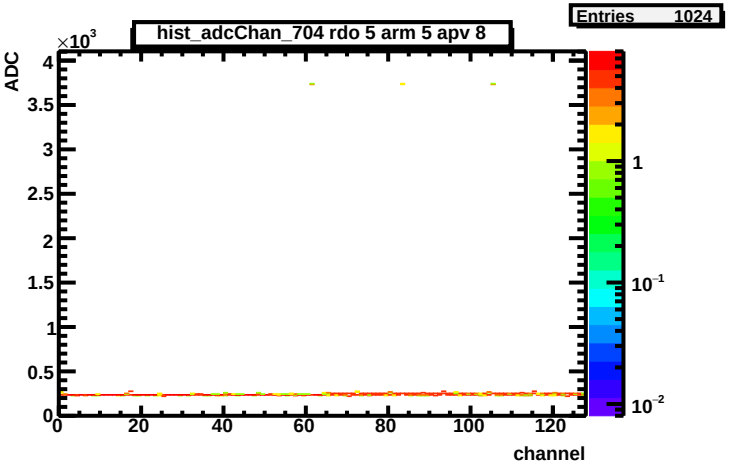

hist_adcChan_699 rdo 5 arm 5 apv 3
Entries 1024
ADC
channel

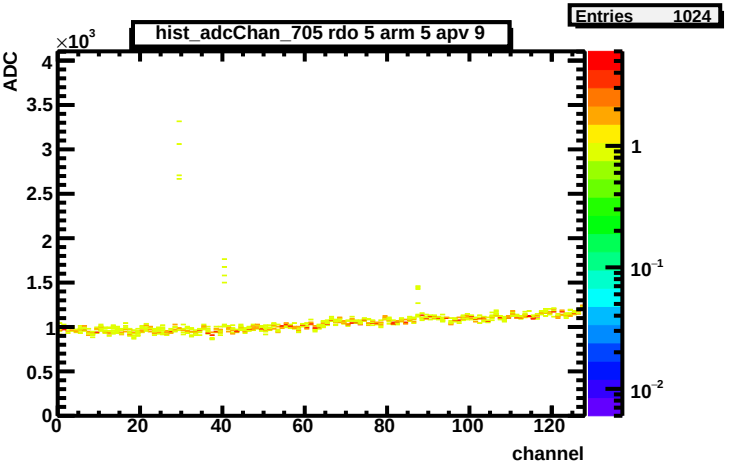

hist_adcChan_700 rdo 5 arm 5 apv 4
Entries 1024
ADC
channel

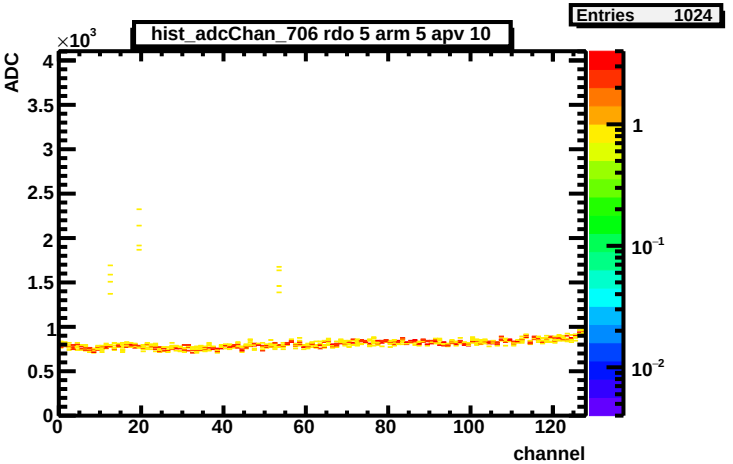

hist_adcChan_701 rdo 5 arm 5 apv 5
Entries 1024
ADC
channel

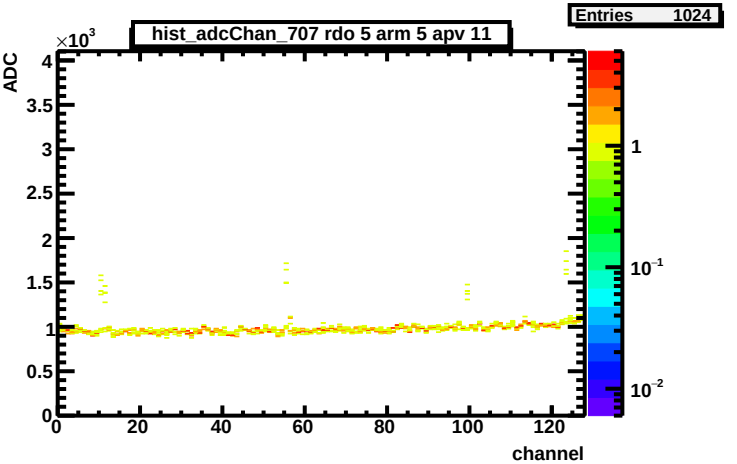

hist_adcChan_702 rdo 5 arm 5 apv 6
Entries 1024
ADC
channel

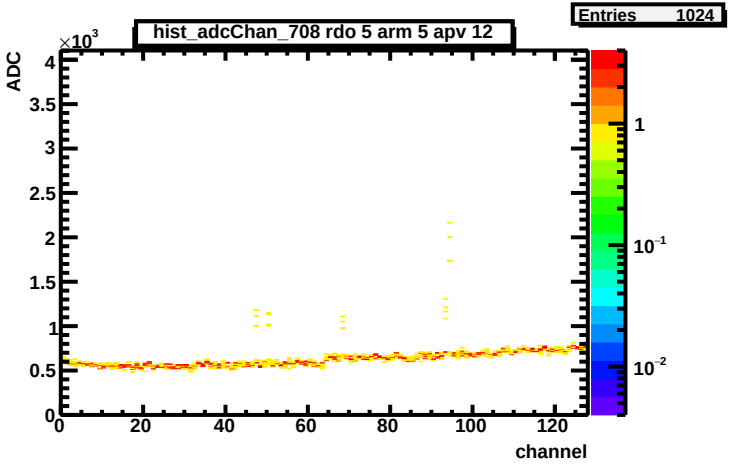

hist_adcChan_703 rdo 5 arm 5 apv 7
Entries 1024
ADC
channel

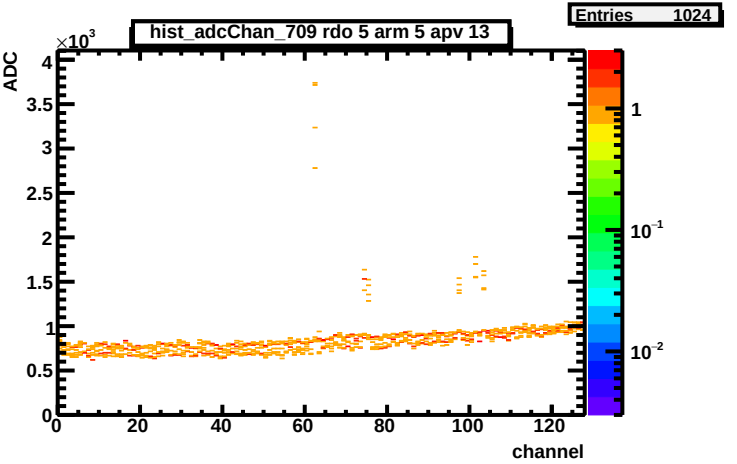

hist_adcChan_704 rdo 5 arm 5 apv 8
Entries 1024
ADC
channel

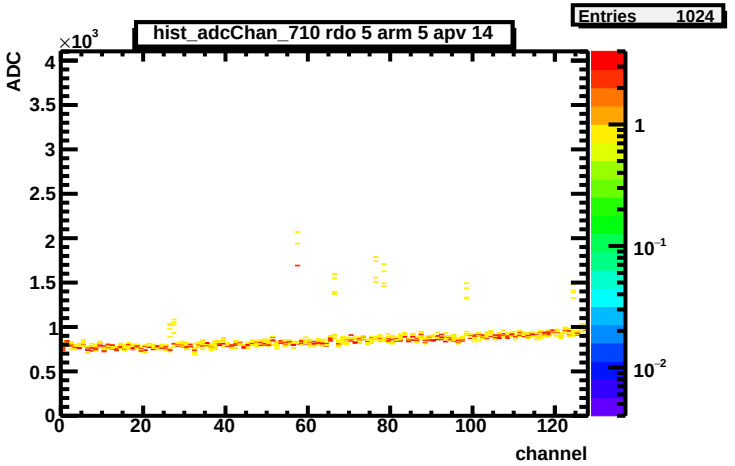

hist_adcChan_705 rdo 5 arm 5 apv 9
Entries 1024
ADC
channel

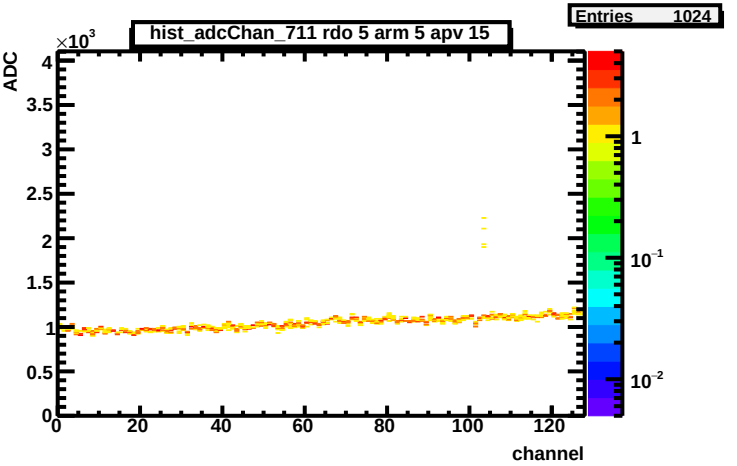

hist_adcChan_706 rdo 5 arm 5 apv 10
Entries 1024
ADC
channel

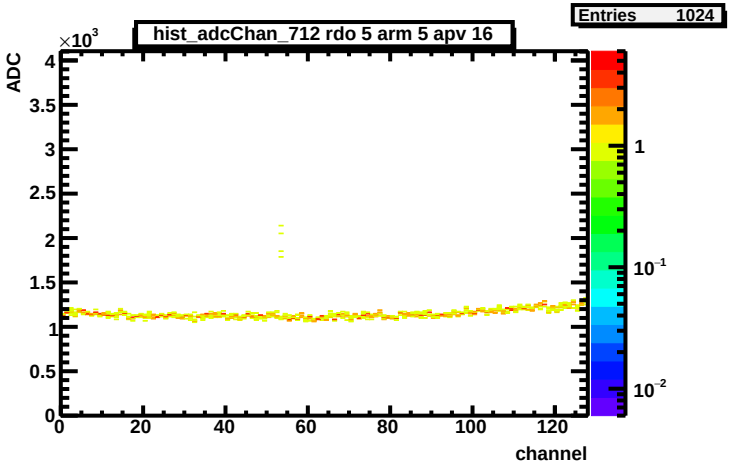

hist_adcChan_707 rdo 5 arm 5 apv 11
Entries 1024
ADC
channel

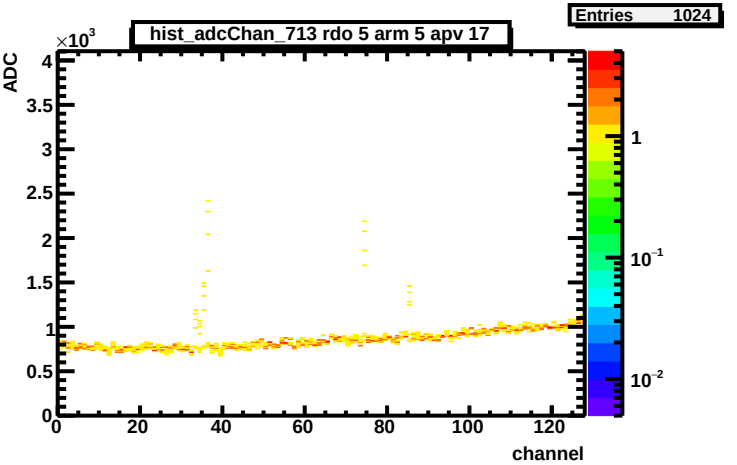

hist_adcChan_708 rdo 5 arm 5 apv 12
Entries 1024
ADC
×10^3
channel

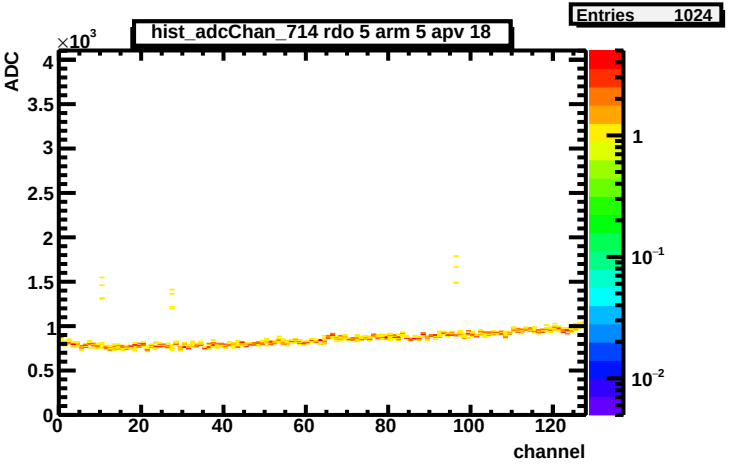

hist_adcChan_709 rdo 5 arm 5 apv 13
Entries 1024
ADC
×10^3
channel

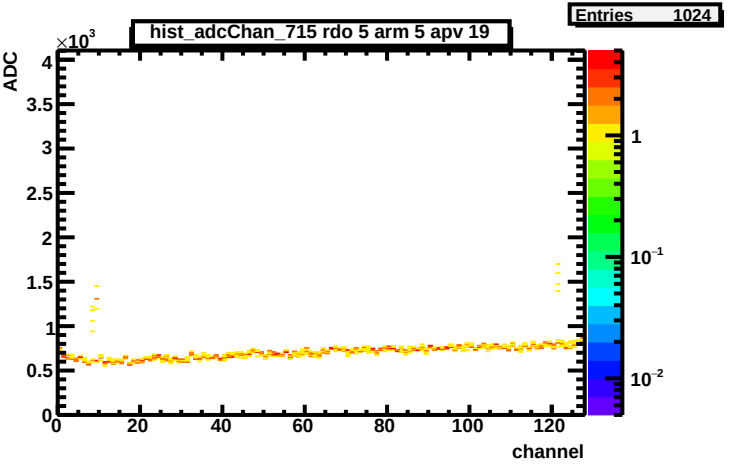

hist_adcChan_710 rdo 5 arm 5 apv 14
Entries 1024
ADC
×10^3
channel

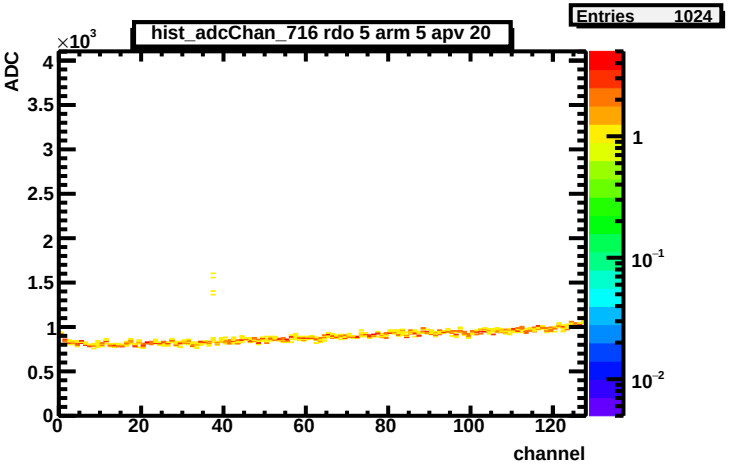

hist_adcChan_711 rdo 5 arm 5 apv 15
Entries 1024
ADC
×10^3
channel

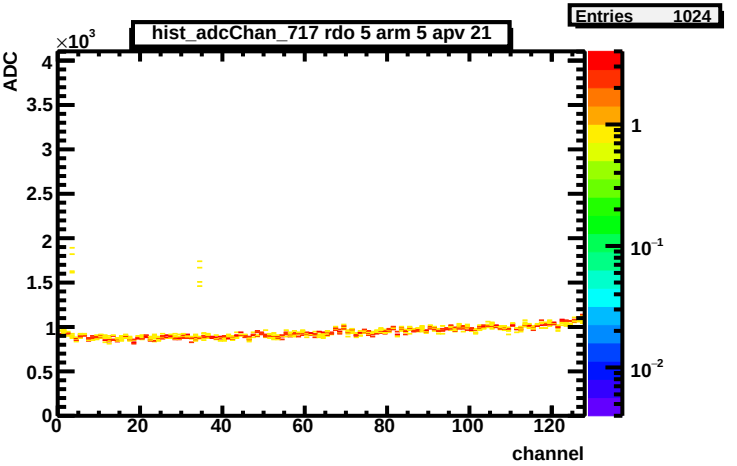

hist_adcChan_712 rdo 5 arm 5 apv 16
Entries 1024
ADC
×10^3
channel

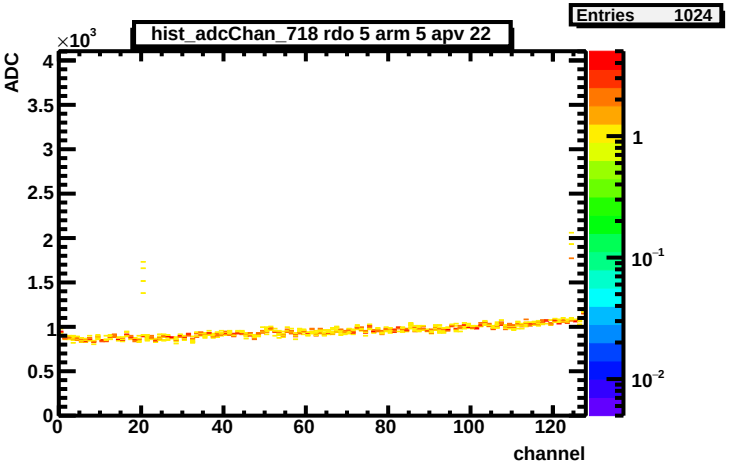

hist_adcChan_713 rdo 5 arm 5 apv 17
Entries 1024
ADC
×10^3
channel

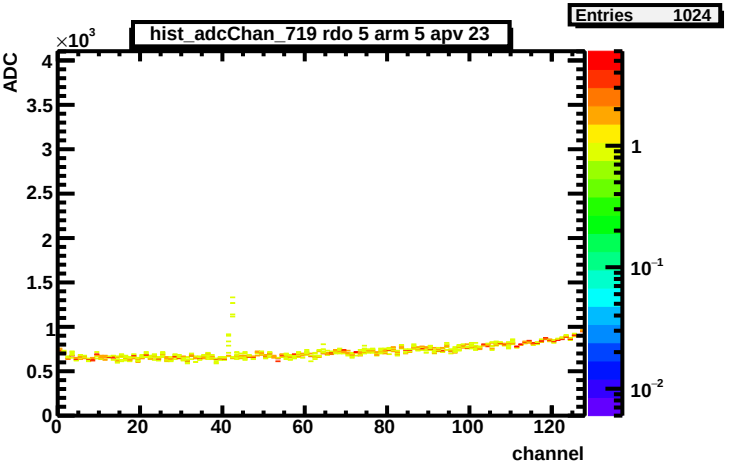

hist_adcChan_714 rdo 5 arm 5 apv 18
Entries 1024
ADC
×10^3
channel

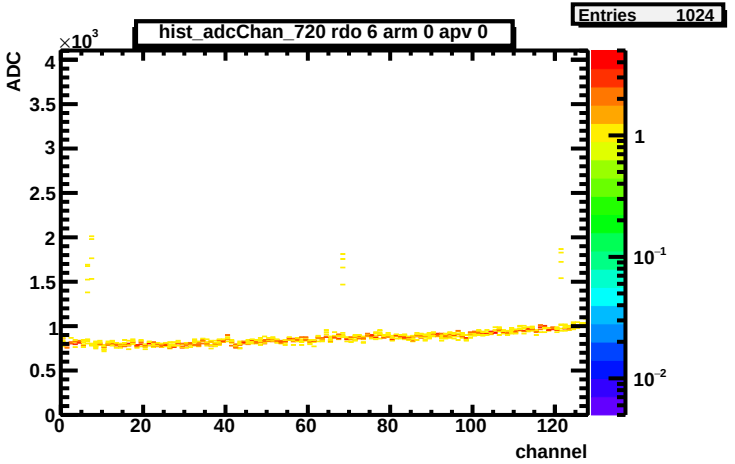

hist_adcChan_715 rdo 5 arm 5 apv 19
Entries 1024
ADC
×10^3
channel

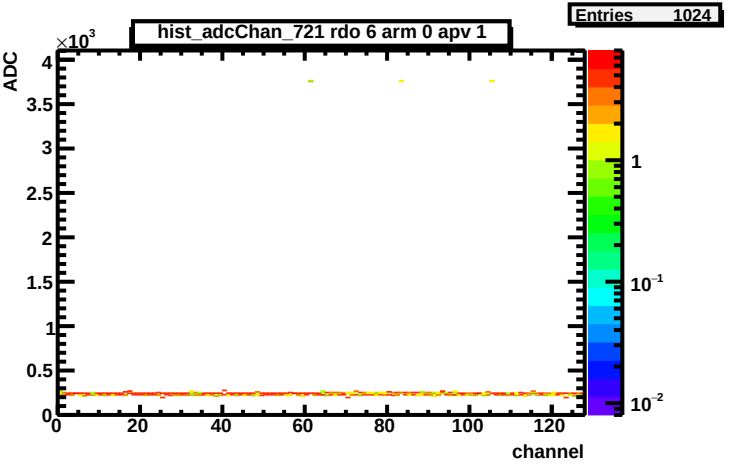

hist_adcChan_716 rdo 5 arm 5 apv 20
Entries 1024
ADC
×10^3
channel

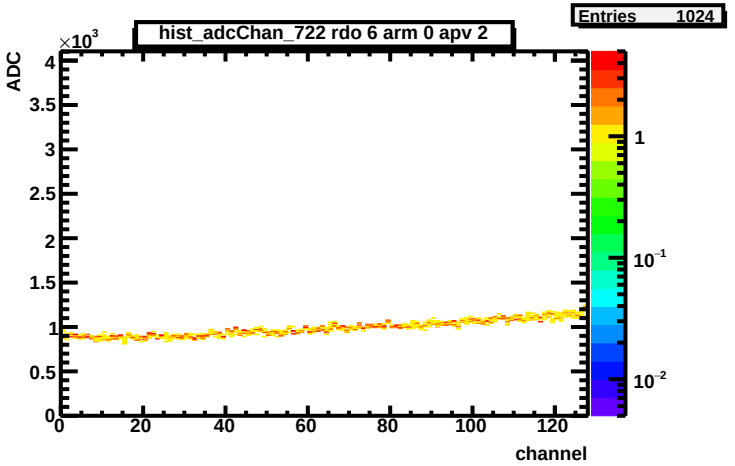

hist_adcChan_717 rdo 5 arm 5 apv 21
Entries 1024
ADC
×10^3
channel

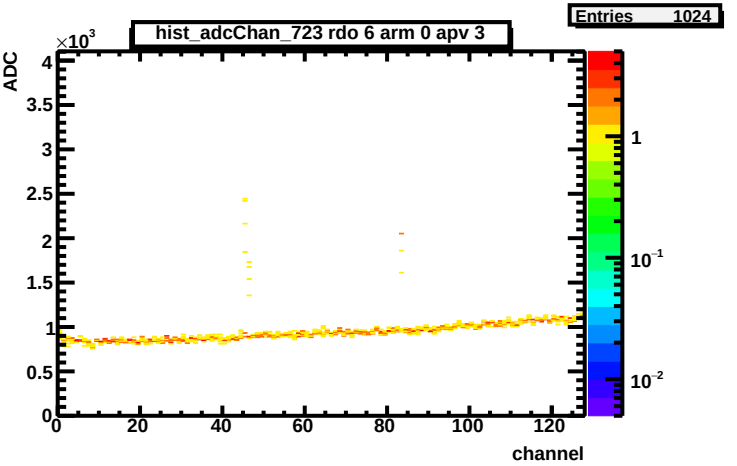

hist_adcChan_718 rdo 5 arm 5 apv 22
Entries 1024
ADC
×10^3
channel

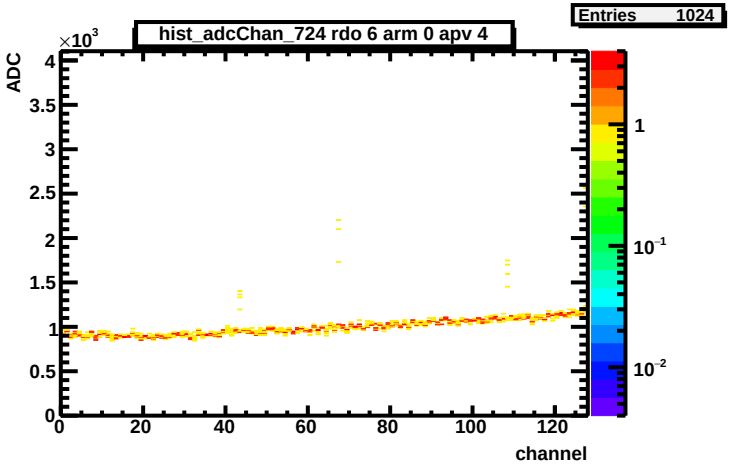

hist_adcChan_719 rdo 5 arm 5 apv 23
Entries 1024
ADC
×10^3
channel

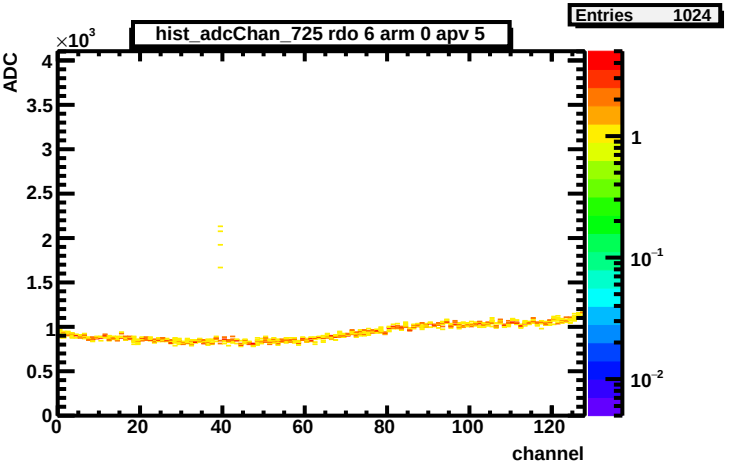

hist_adcChan_720 rdo 6 arm 0 apv 0
Entries 1024
ADC
×10^3
channel

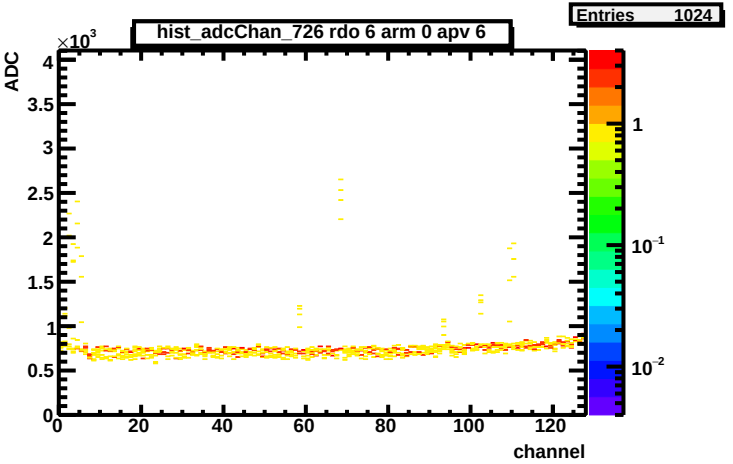

hist_adcChan_721 rdo 6 arm 0 apv 1
Entries 1024
ADC
×10^3
channel

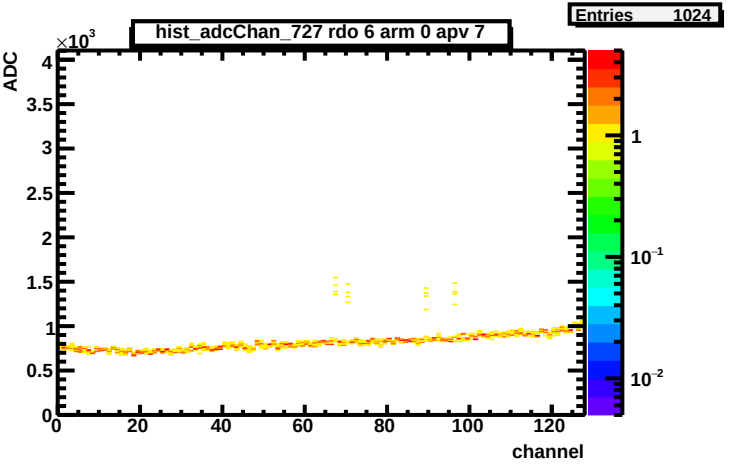

hist_adcChan_722 rdo 6 arm 0 apv 2
Entries 1024
ADC
×10^3
channel

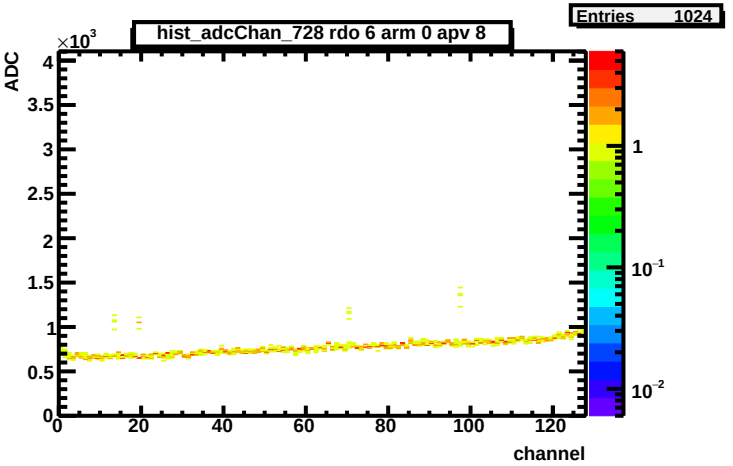

hist_adcChan_723 rdo 6 arm 0 apv 3
Entries 1024
ADC
×10^3
channel

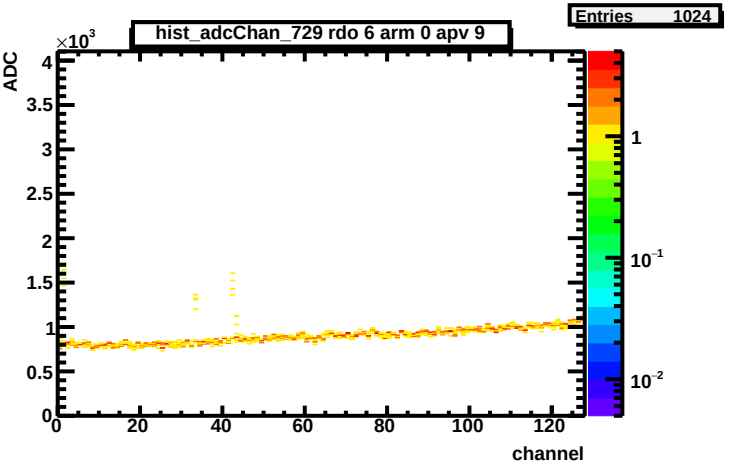

hist_adcChan_724 rdo 6 arm 0 apv 4
Entries 1024
ADC
×10^3
channel

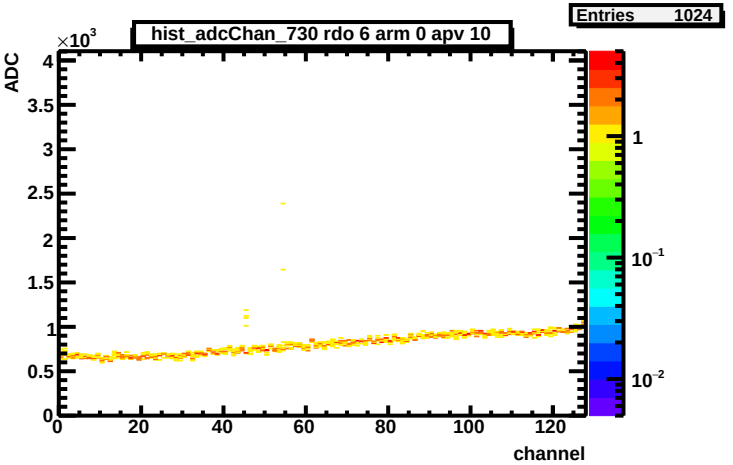

hist_adcChan_725 rdo 6 arm 0 apv 5
Entries 1024
ADC
×10^3
channel

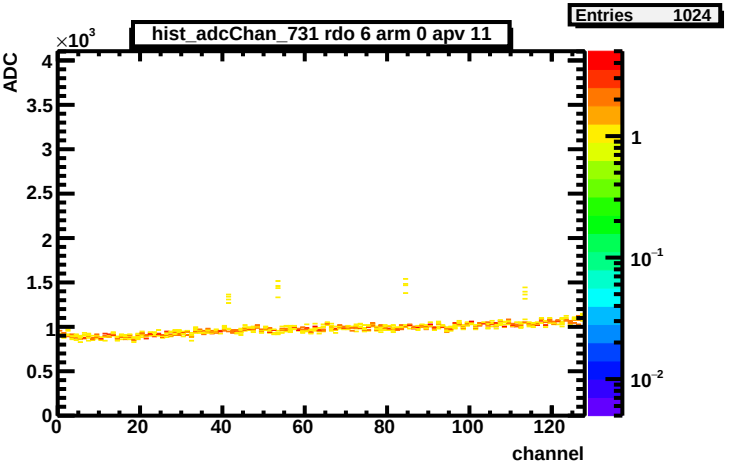

hist_adcChan_726 rdo 6 arm 0 apv 6
Entries 1024
ADC
channel

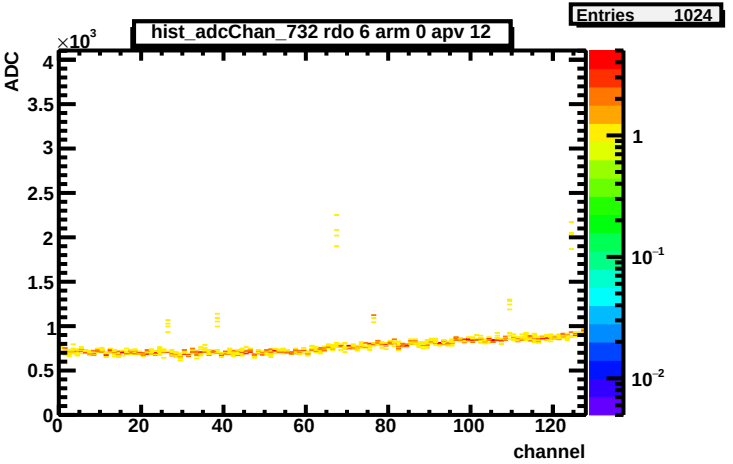

hist_adcChan_727 rdo 6 arm 0 apv 7
Entries 1024
ADC
channel

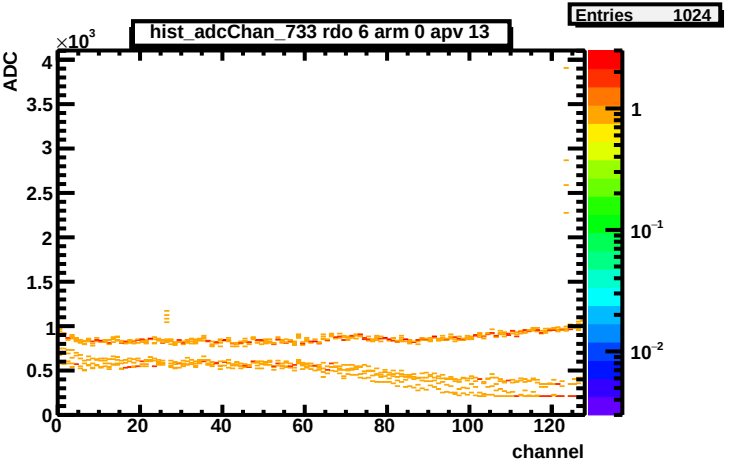

hist_adcChan_728 rdo 6 arm 0 apv 8
Entries 1024
ADC
channel

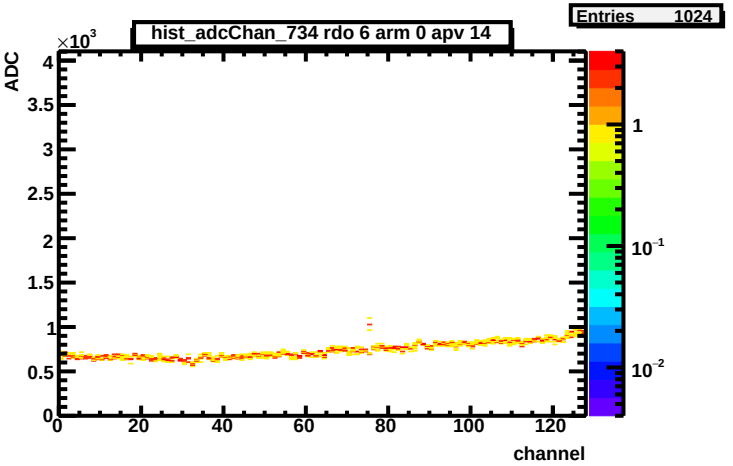

hist_adcChan_729 rdo 6 arm 0 apv 9
Entries 1024
ADC
channel

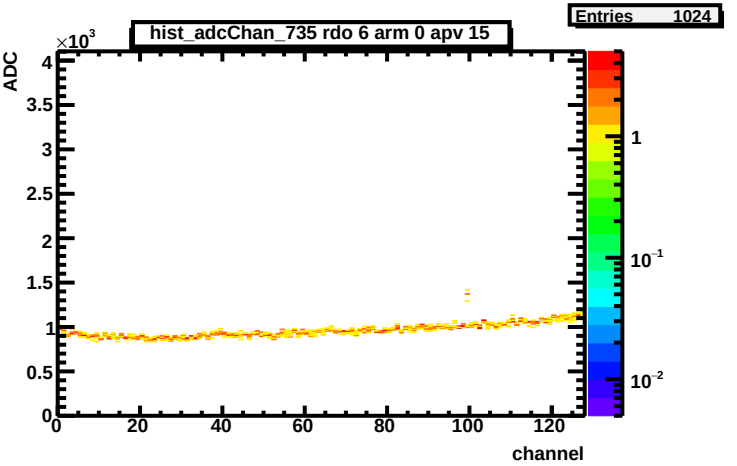

hist_adcChan_730 rdo 6 arm 0 apv 10
Entries 1024
ADC
channel

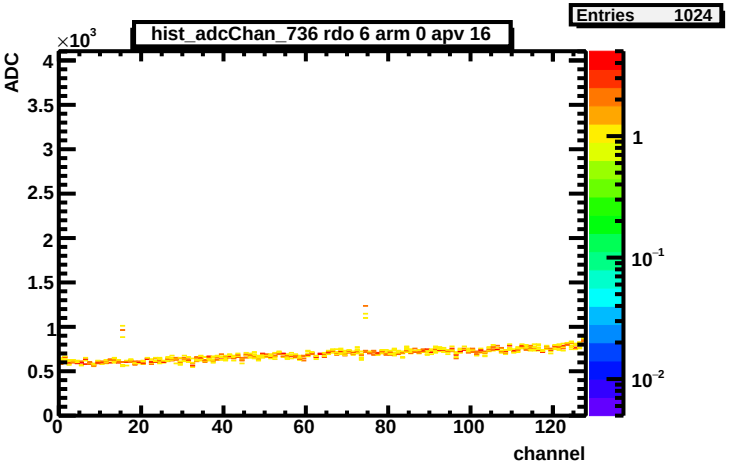

hist_adcChan_731 rdo 6 arm 0 apv 11
Entries 1024
ADC
channel

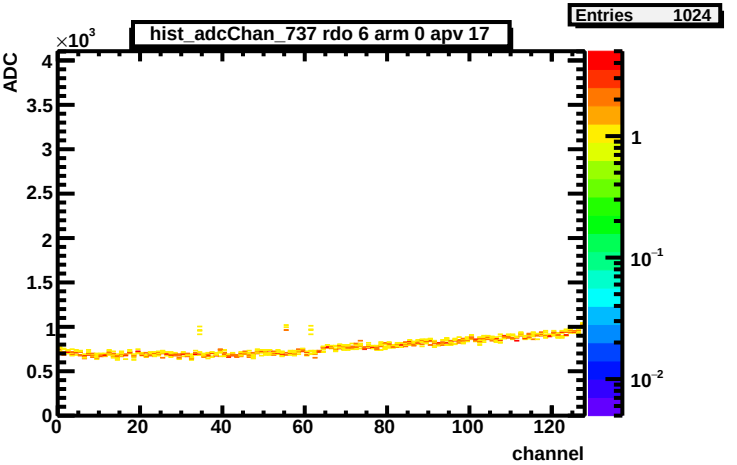

hist_adcChan_732 rdo 6 arm 0 apv 12
Entries 1024
ADC
channel

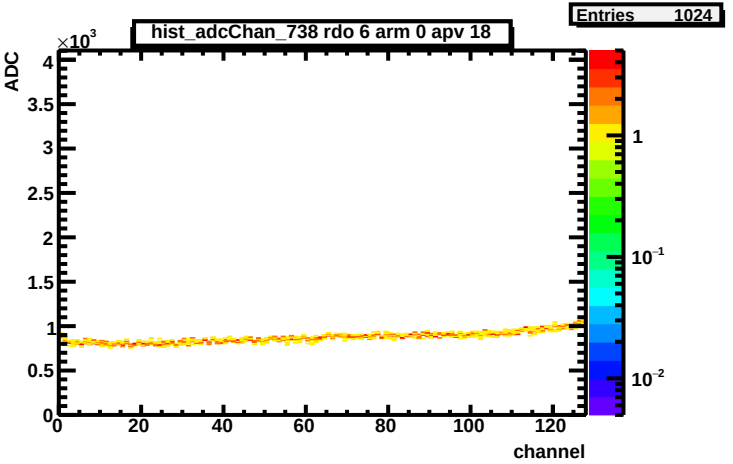

hist_adcChan_733 rdo 6 arm 0 apv 13
Entries 1024
ADC
channel

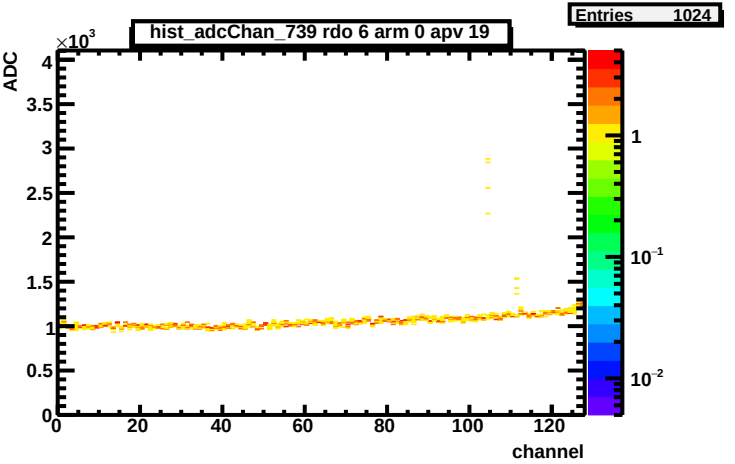

hist_adcChan_734 rdo 6 arm 0 apv 14
Entries 1024
ADC
channel

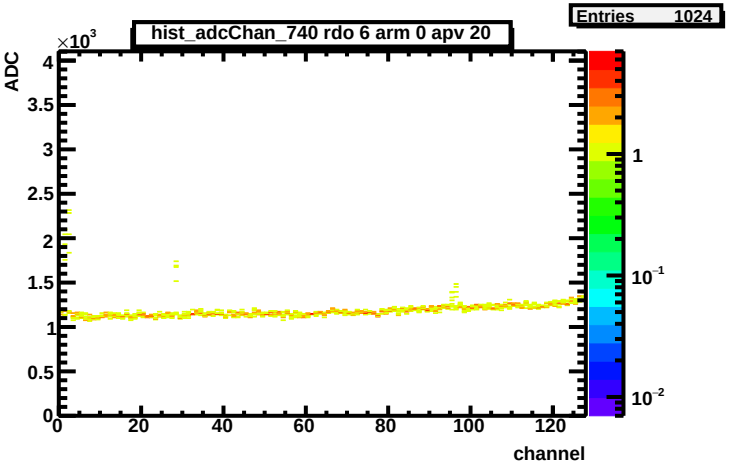

hist_adcChan_735 rdo 6 arm 0 apv 15
Entries 1024
ADC
channel

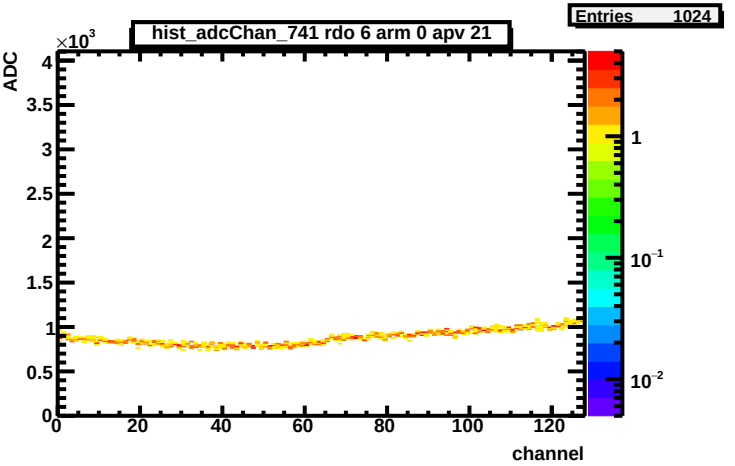

hist_adcChan_736 rdo 6 arm 0 apv 16
Entries 1024
ADC
channel

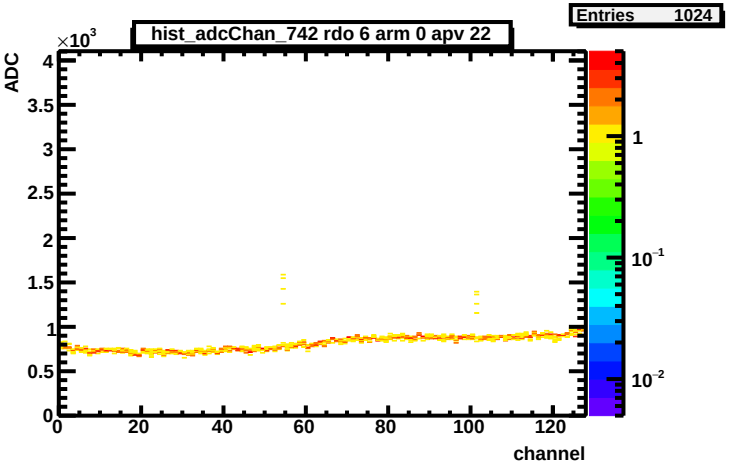

hist_adcChan_737 rdo 6 arm 0 apv 17
Entries 1024
ADC
channel

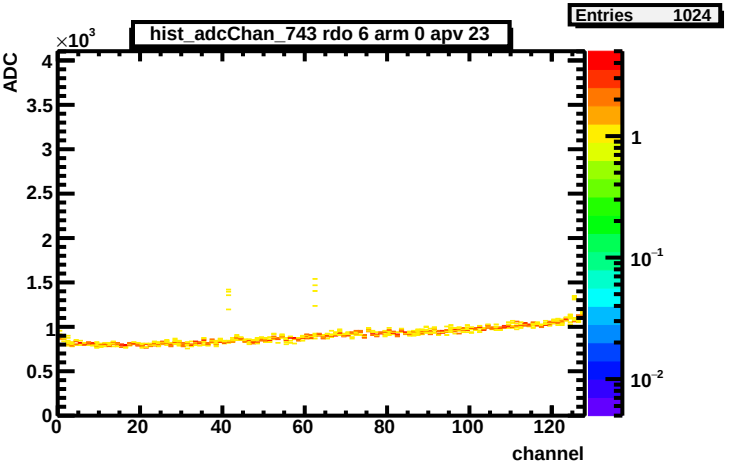

hist_adcChan_738 rdo 6 arm 0 apv 18
Entries 1024
ADC
channel

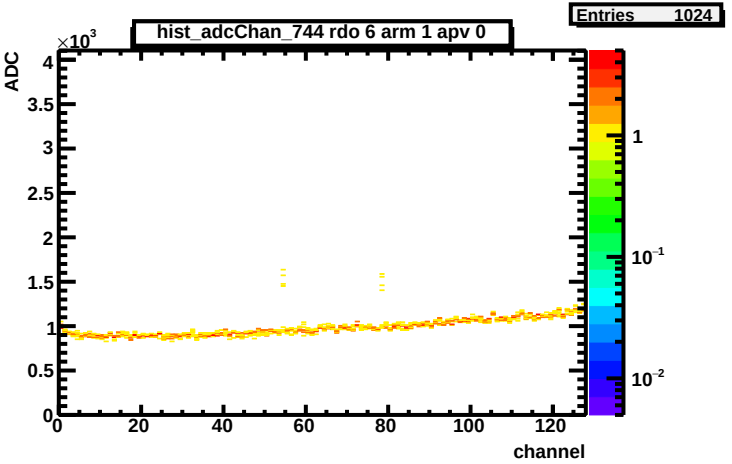

hist_adcChan_739 rdo 6 arm 0 apv 19
Entries 1024
ADC
channel

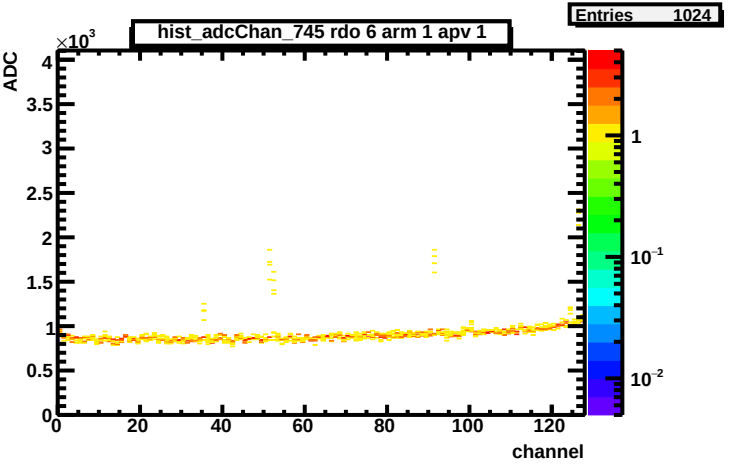

hist_adcChan_740 rdo 6 arm 0 apv 20
Entries 1024
ADC
channel

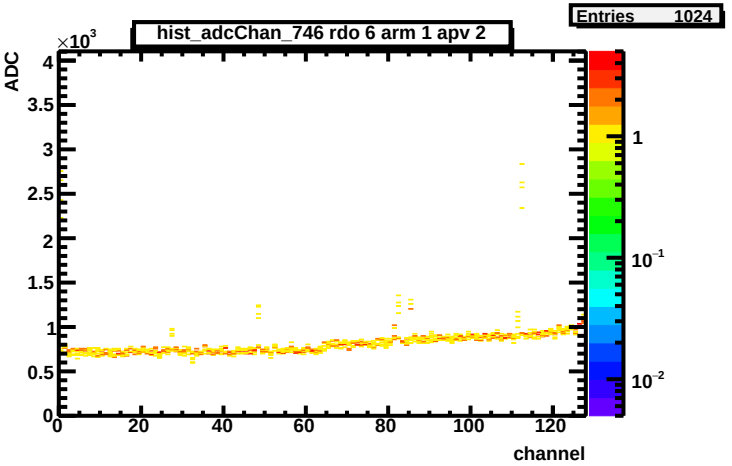

hist_adcChan_741 rdo 6 arm 0 apv 21
Entries 1024
ADC
channel

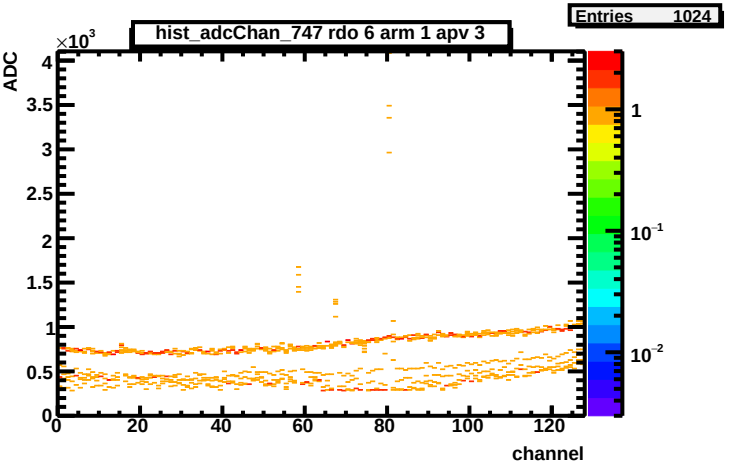

hist_adcChan_742 rdo 6 arm 0 apv 22
Entries 1024
ADC
channel

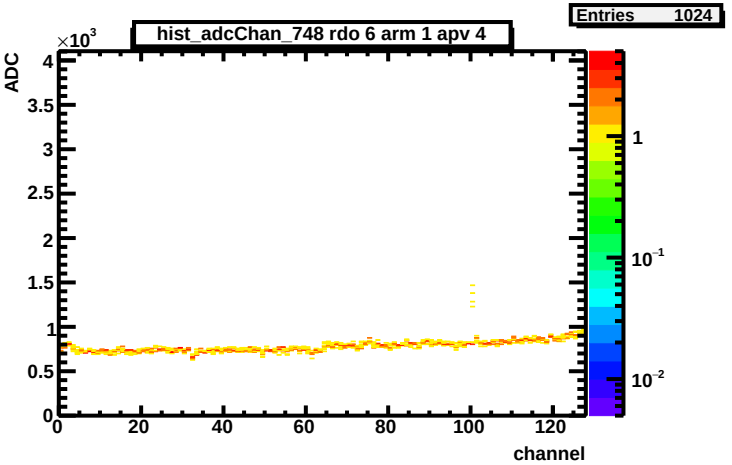

hist_adcChan_743 rdo 6 arm 0 apv 23
Entries 1024
ADC
channel

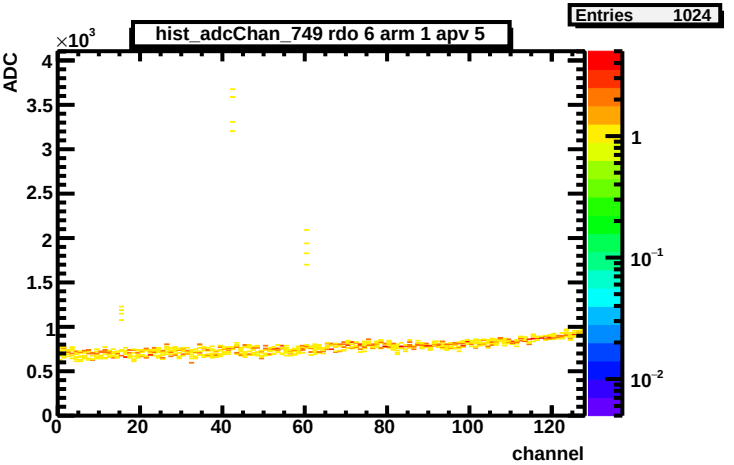

hist_adcChan_744 rdo 6 arm 1 apv 0
Entries 1024
ADC
×10^3
channel

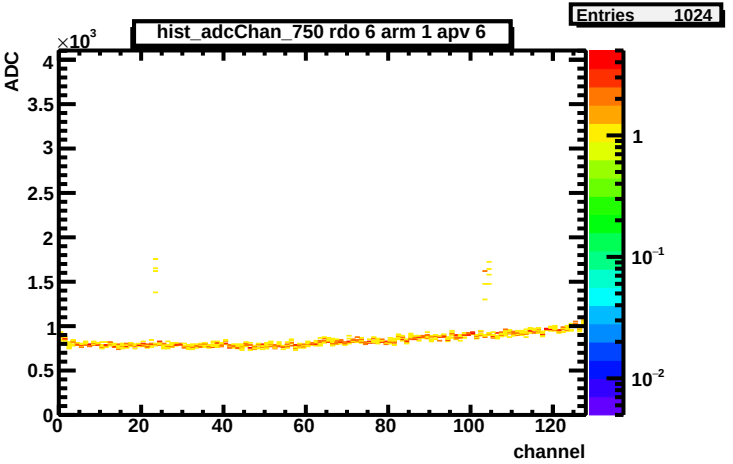

hist_adcChan_745 rdo 6 arm 1 apv 1
Entries 1024
ADC
×10^3
channel

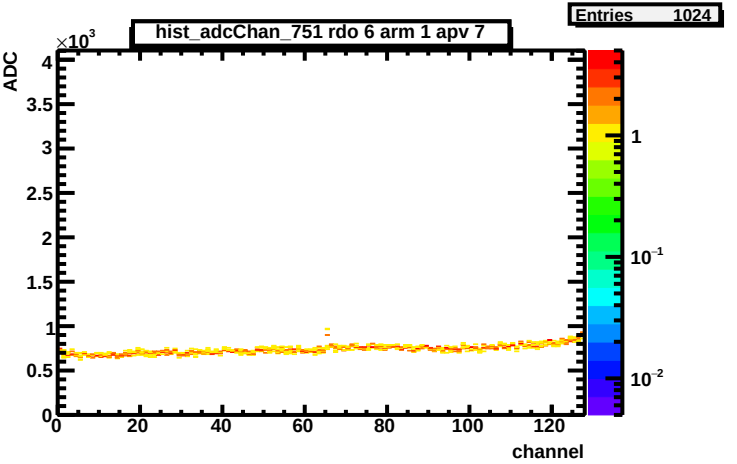

hist_adcChan_746 rdo 6 arm 1 apv 2
Entries 1024
ADC
×10^3
channel

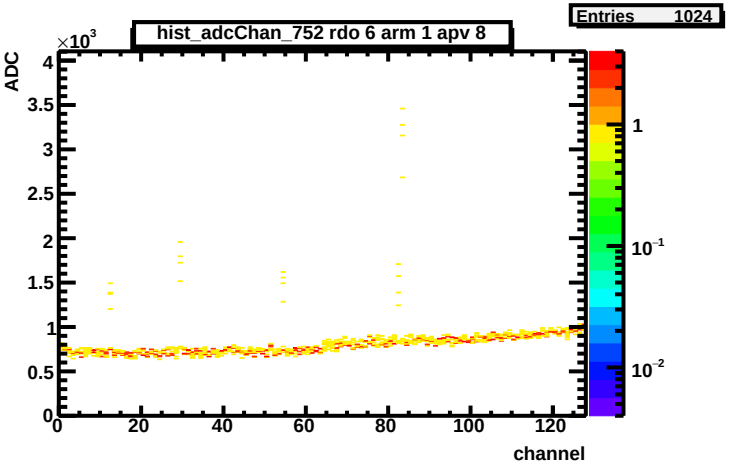

hist_adcChan_747 rdo 6 arm 1 apv 3
Entries 1024
ADC
×10^3
channel

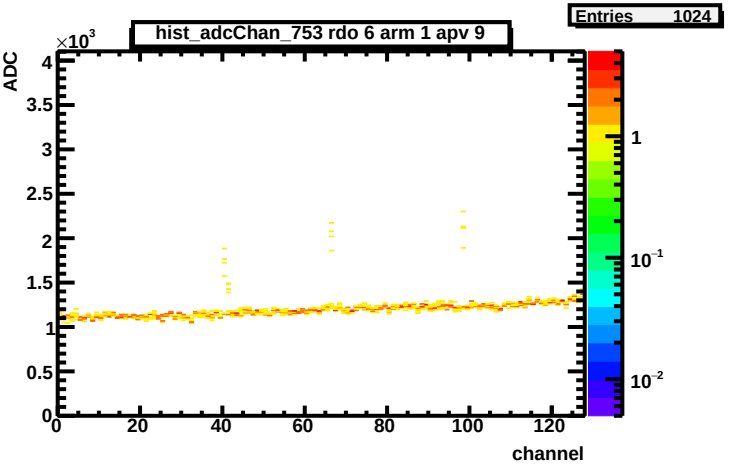

hist_adcChan_748 rdo 6 arm 1 apv 4
Entries 1024
ADC
×10^3
channel

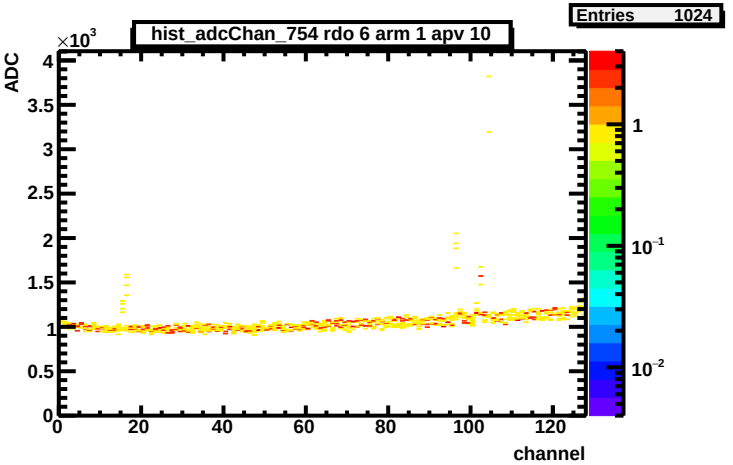

hist_adcChan_749 rdo 6 arm 1 apv 5
Entries 1024
ADC
×10^3
channel

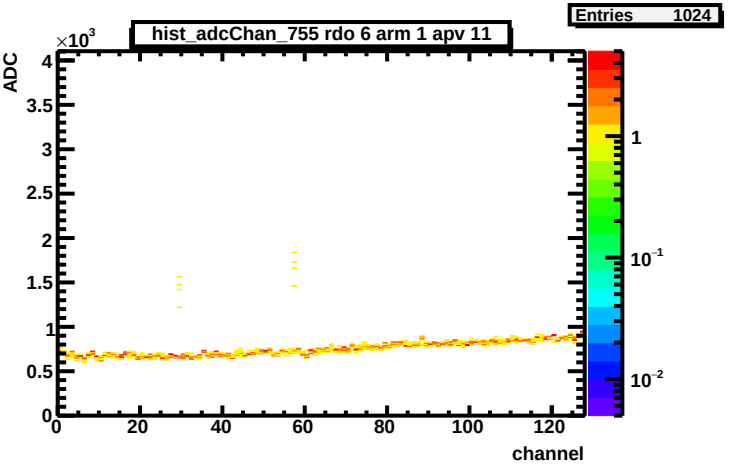

hist_adcChan_750 rdo 6 arm 1 apv 6
Entries 1024
ADC
×10^3
channel

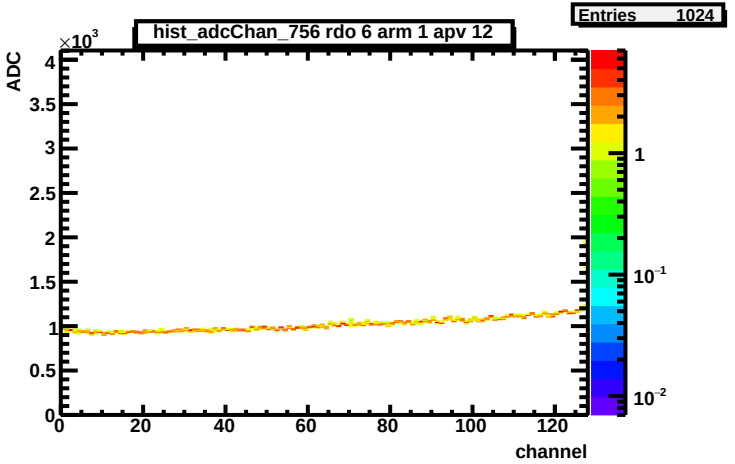

hist_adcChan_751 rdo 6 arm 1 apv 7
Entries 1024
ADC
×10^3
channel

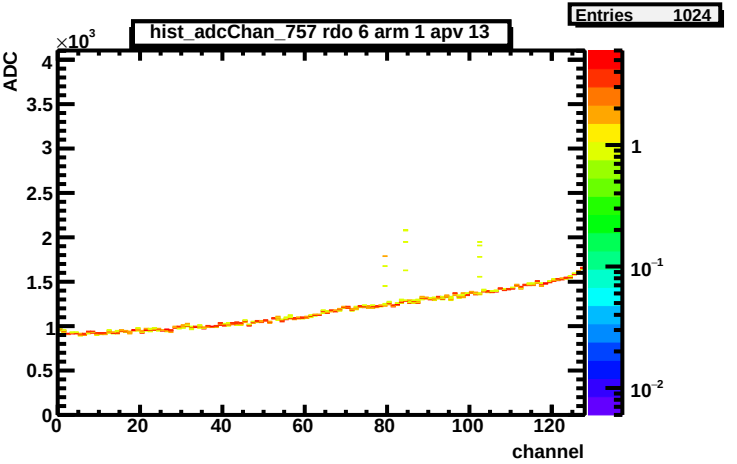

hist_adcChan_752 rdo 6 arm 1 apv 8
Entries 1024
ADC
×10^3
channel

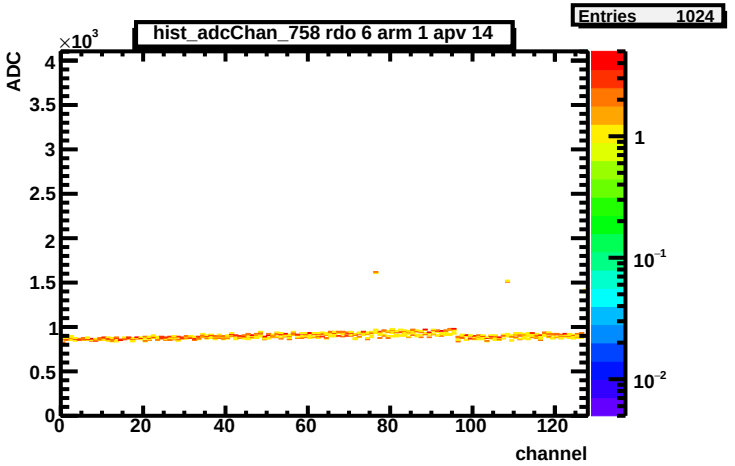

hist_adcChan_753 rdo 6 arm 1 apv 9
Entries 1024
ADC
×10^3
channel

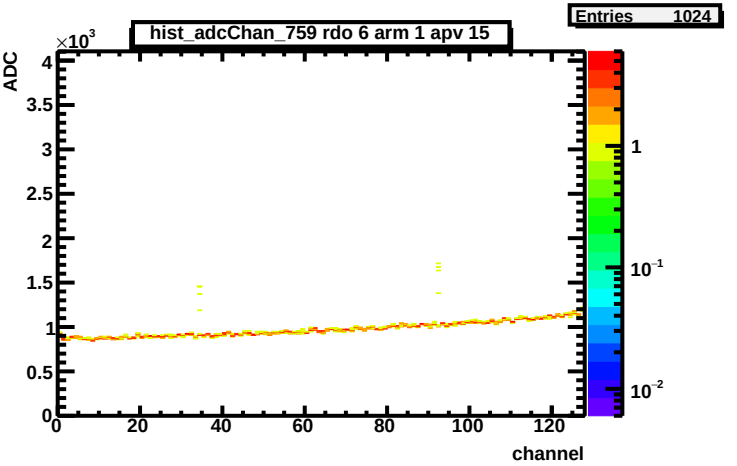

hist_adcChan_754 rdo 6 arm 1 apv 10
Entries 1024
ADC
×10^3
channel

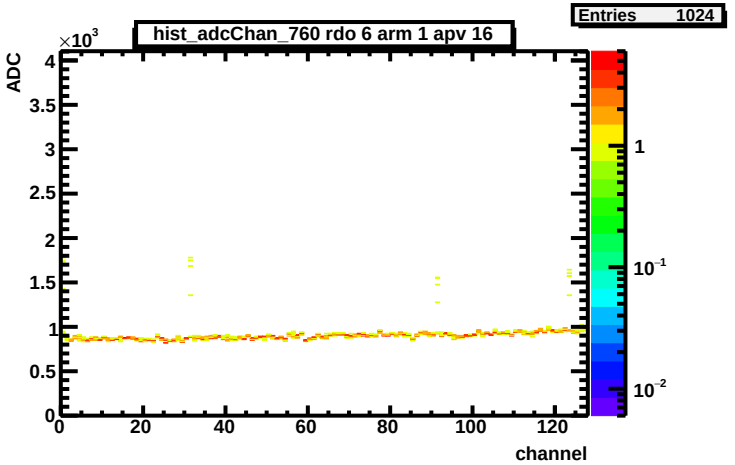

hist_adcChan_755 rdo 6 arm 1 apv 11
Entries 1024
ADC
×10^3
channel

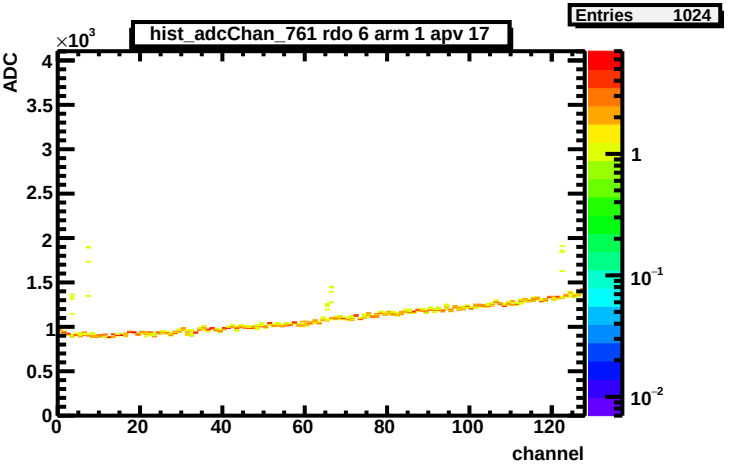

hist_adcChan_756 rdo 6 arm 1 apv 12
Entries 1024
ADC
channel

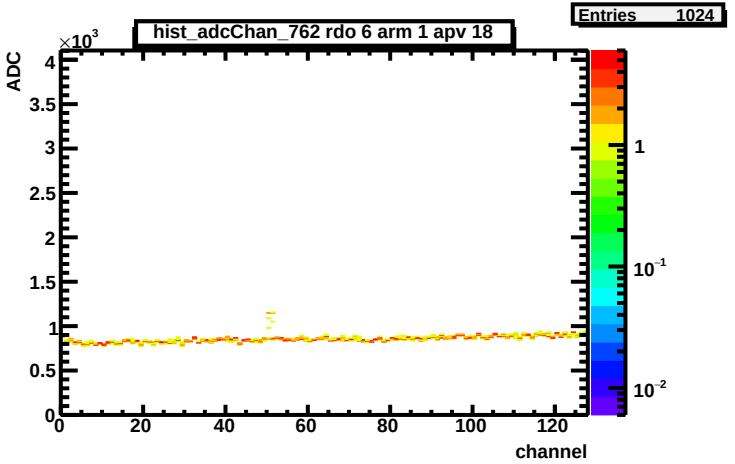

hist_adcChan_757 rdo 6 arm 1 apv 13
Entries 1024
ADC
channel

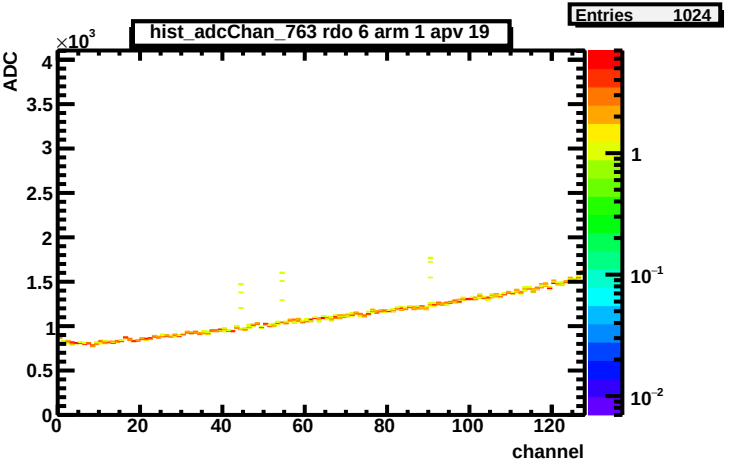

hist_adcChan_758 rdo 6 arm 1 apv 14
Entries 1024
ADC
channel

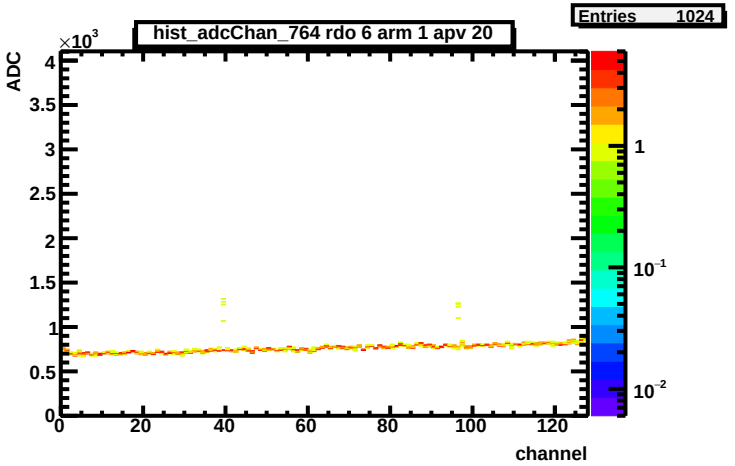

hist_adcChan_759 rdo 6 arm 1 apv 15
Entries 1024
ADC
channel

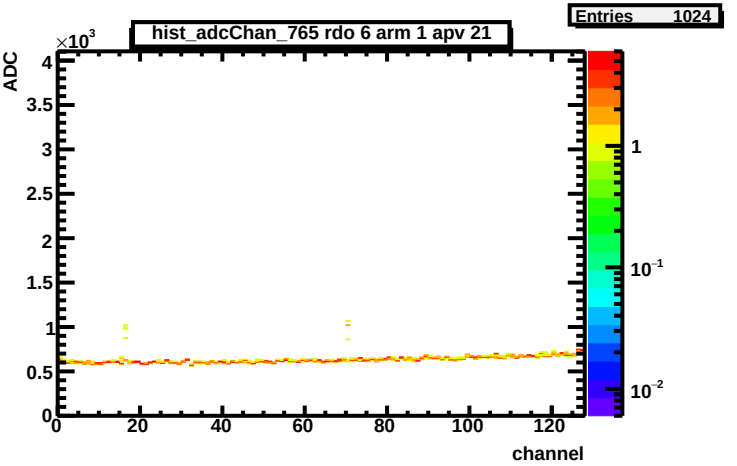

hist_adcChan_760 rdo 6 arm 1 apv 16
Entries 1024
ADC
channel

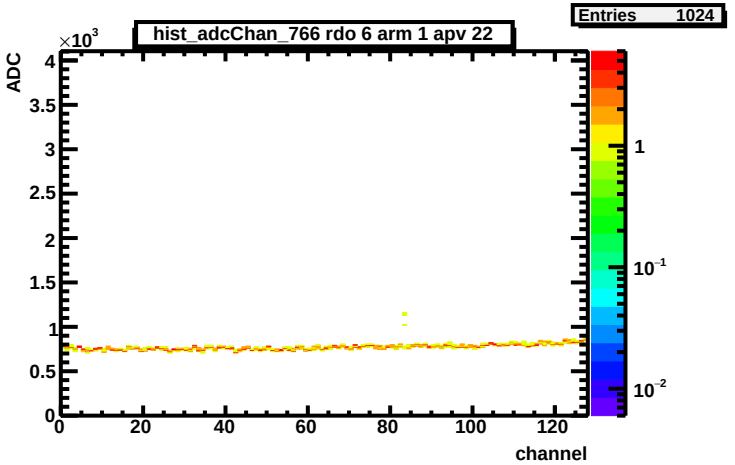

hist_adcChan_761 rdo 6 arm 1 apv 17
Entries 1024
ADC
channel

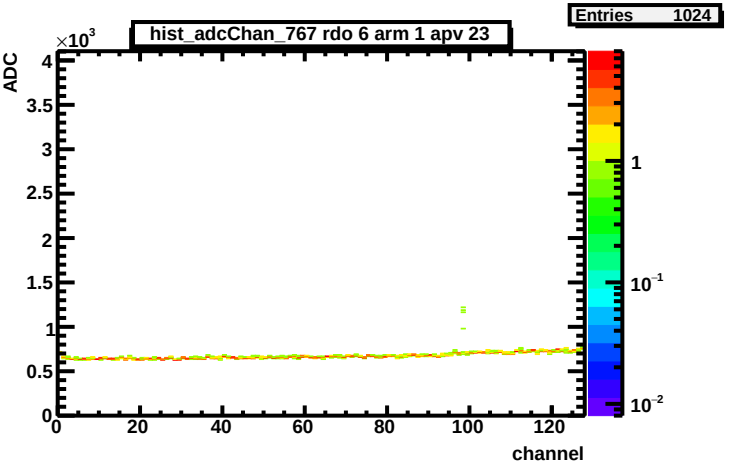

hist_adcChan_762 rdo 6 arm 1 apv 18
Entries 1024
ADC
×10^3
channel

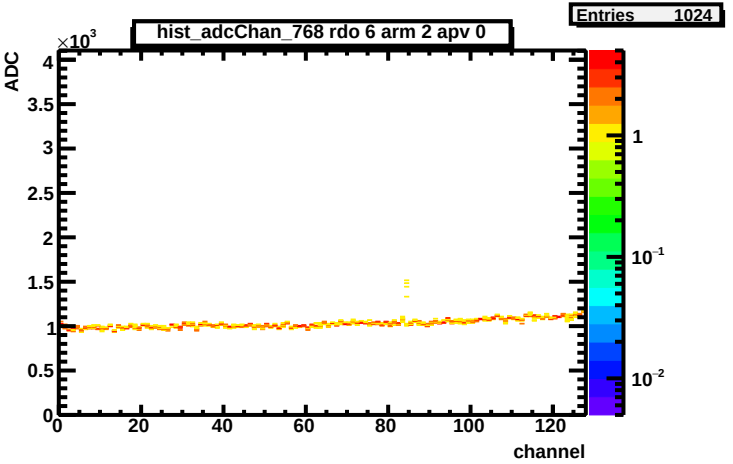

hist_adcChan_763 rdo 6 arm 1 apv 19
Entries 1024
ADC
×10^3
channel

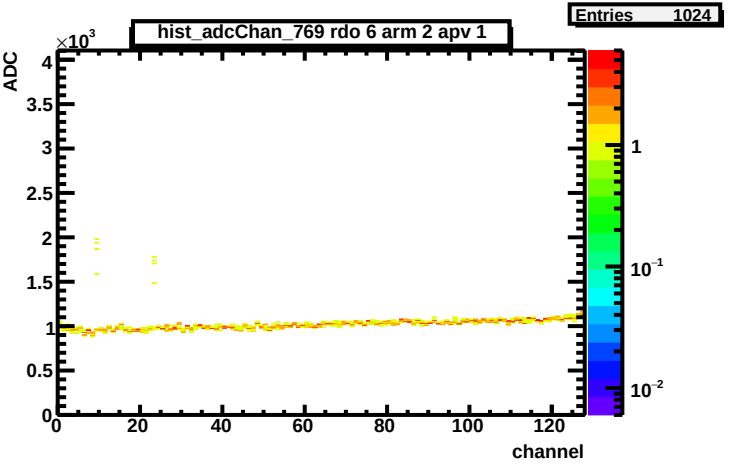

hist_adcChan_764 rdo 6 arm 1 apv 20
Entries 1024
ADC
×10^3
channel

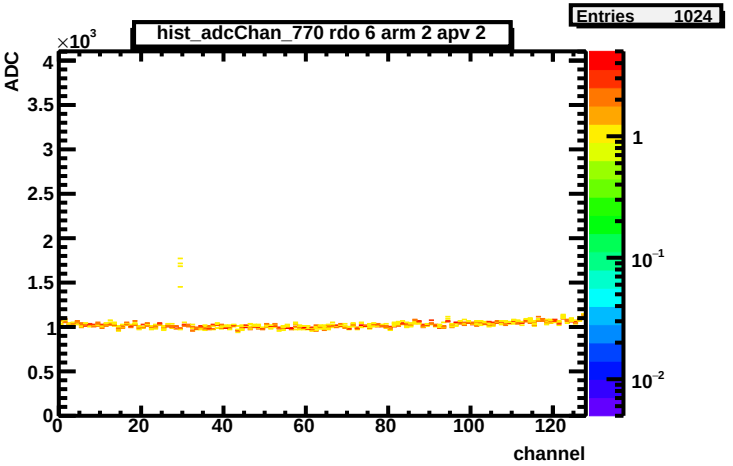

hist_adcChan_765 rdo 6 arm 1 apv 21
Entries 1024
ADC
×10^3
channel

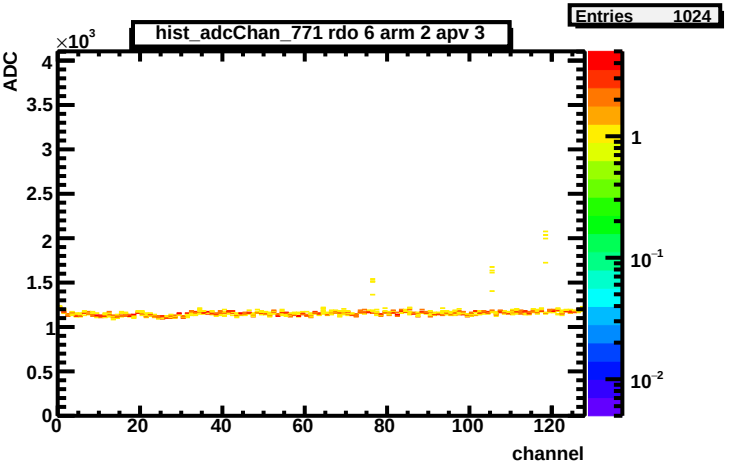

hist_adcChan_766 rdo 6 arm 1 apv 22
Entries 1024
ADC
×10^3
channel

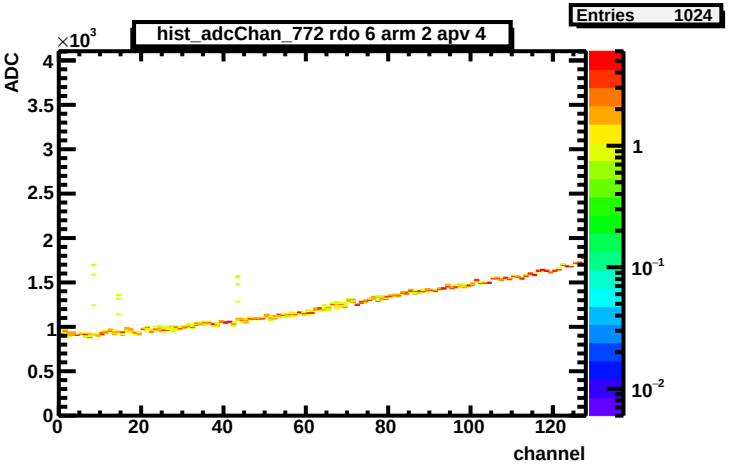

hist_adcChan_767 rdo 6 arm 1 apv 23
Entries 1024
ADC
×10^3
channel

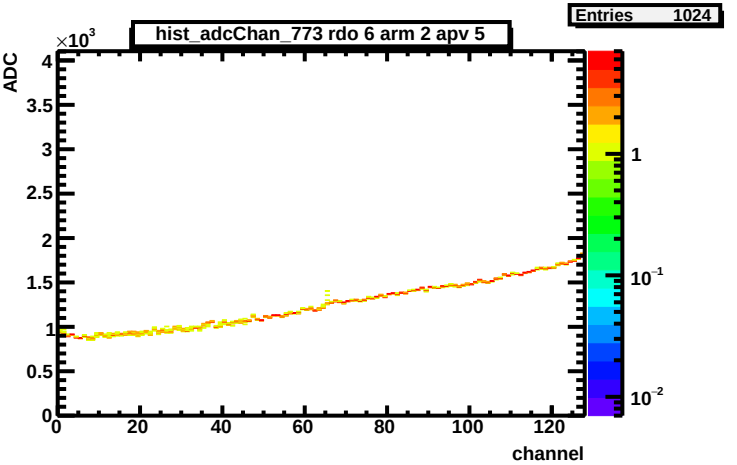

hist_adcChan_768 rdo 6 arm 2 apv 0
Entries 1024
ADC
channel

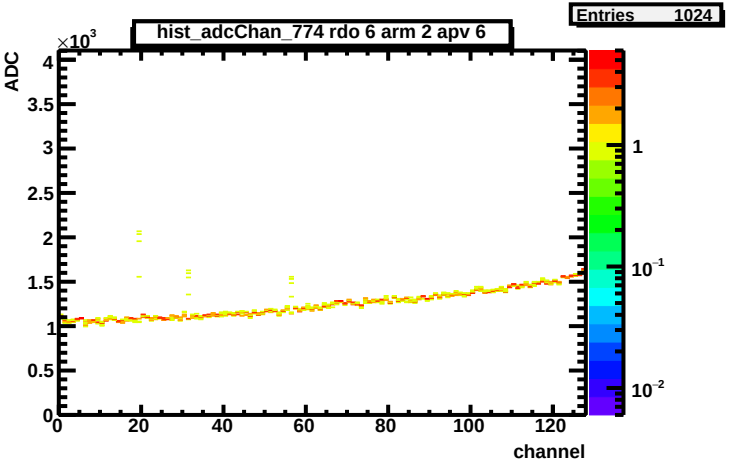

hist_adcChan_769 rdo 6 arm 2 apv 1
Entries 1024
ADC
channel

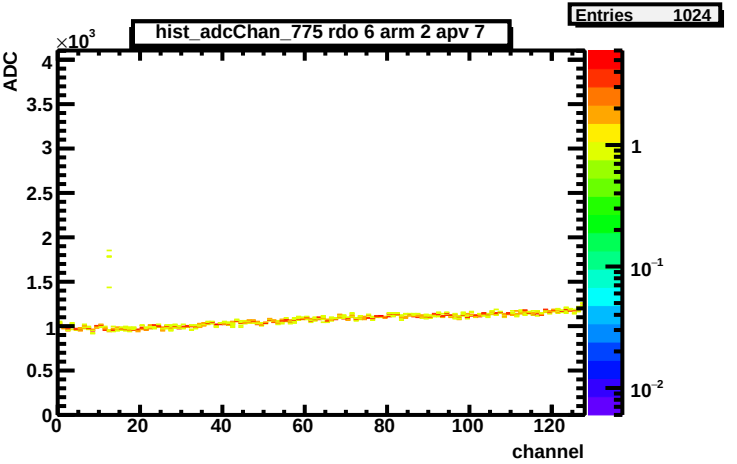

hist_adcChan_770 rdo 6 arm 2 apv 2
Entries 1024
ADC
channel

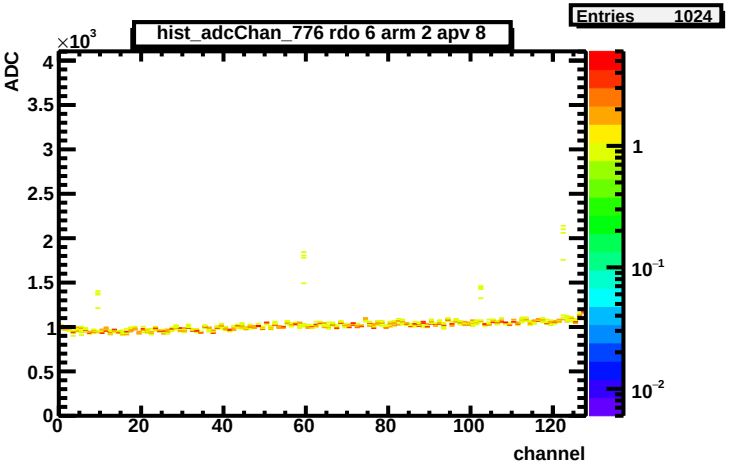

hist_adcChan_771 rdo 6 arm 2 apv 3
Entries 1024
ADC
channel

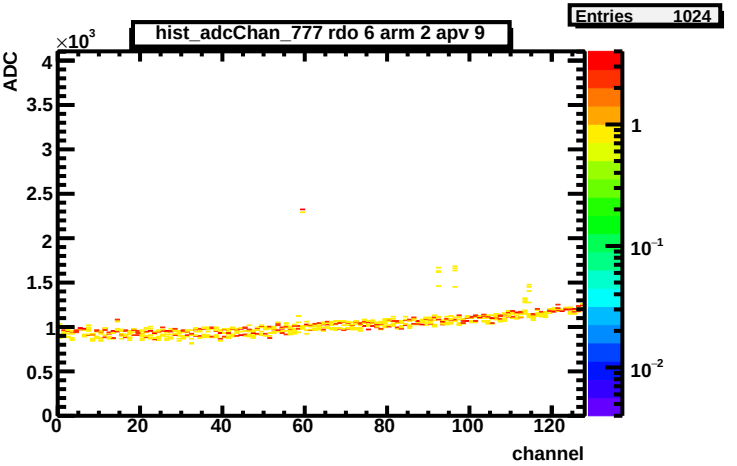

hist_adcChan_772 rdo 6 arm 2 apv 4
Entries 1024
ADC
channel

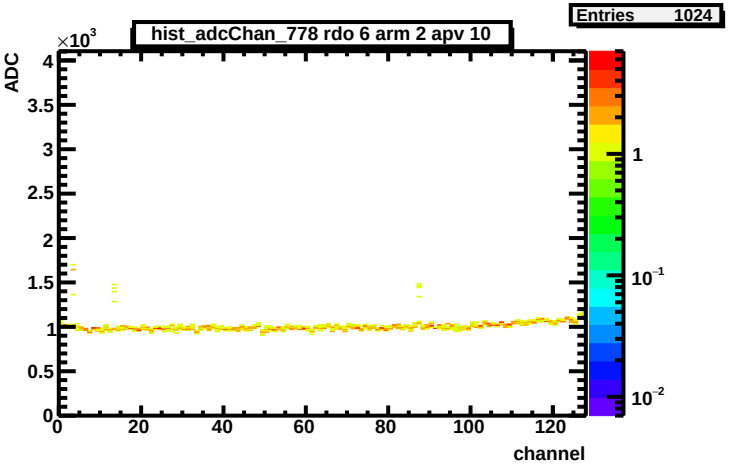

hist_adcChan_773 rdo 6 arm 2 apv 5
Entries 1024
ADC
channel

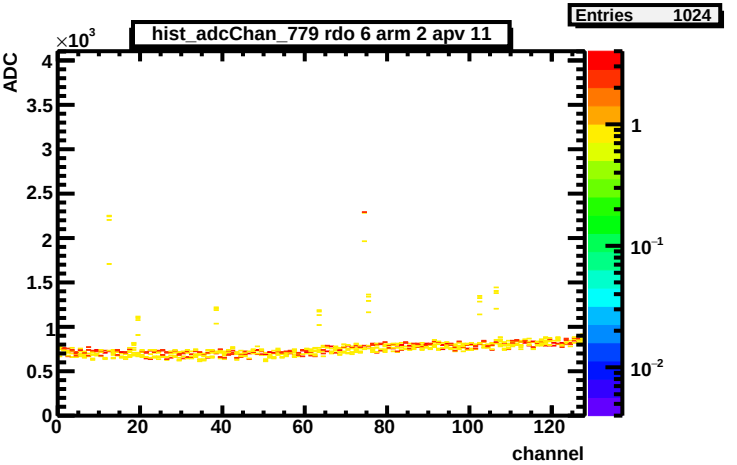

hist_adcChan_774 rdo 6 arm 2 apv 6
Entries 1024
ADC
×10^3
channel

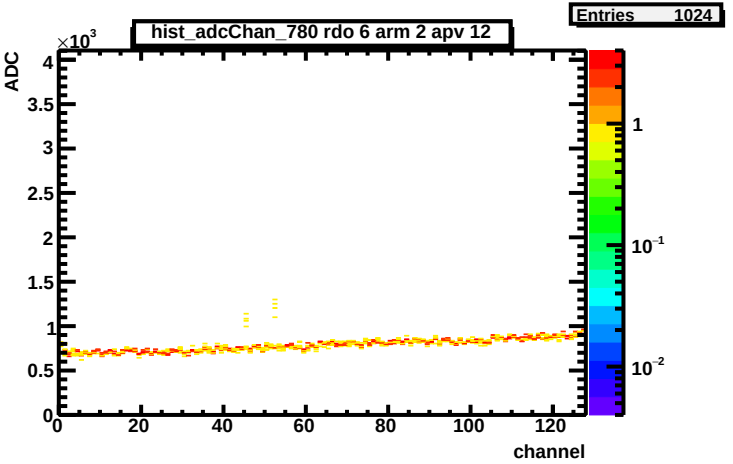

hist_adcChan_775 rdo 6 arm 2 apv 7
Entries 1024
ADC
×10^3
channel

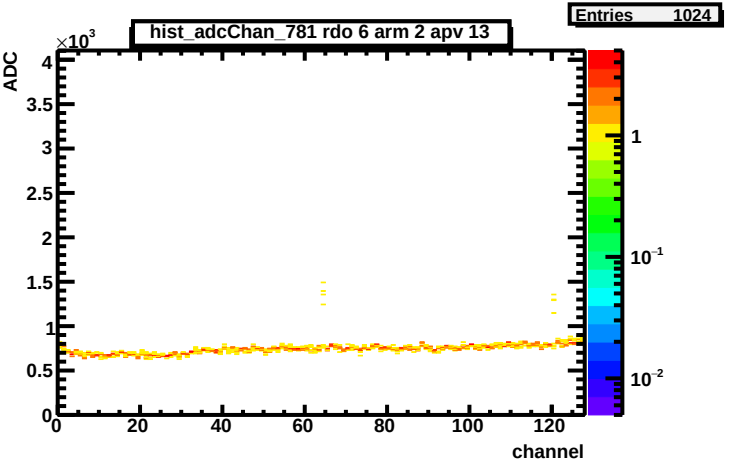

hist_adcChan_776 rdo 6 arm 2 apv 8
Entries 1024
ADC
×10^3
channel

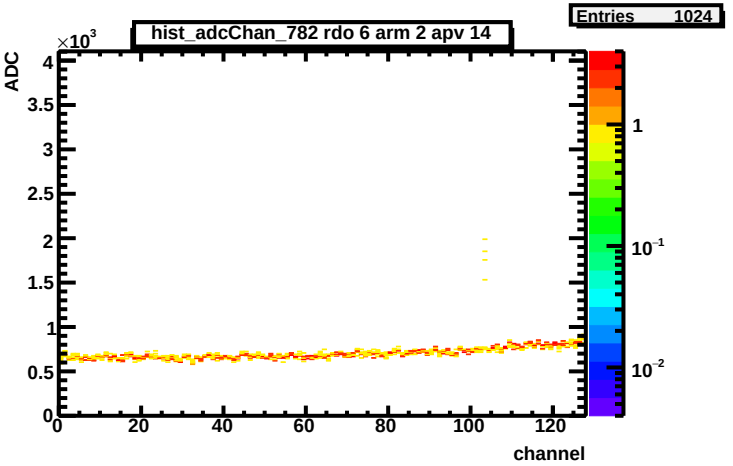

hist_adcChan_777 rdo 6 arm 2 apv 9
Entries 1024
ADC
×10^3
channel

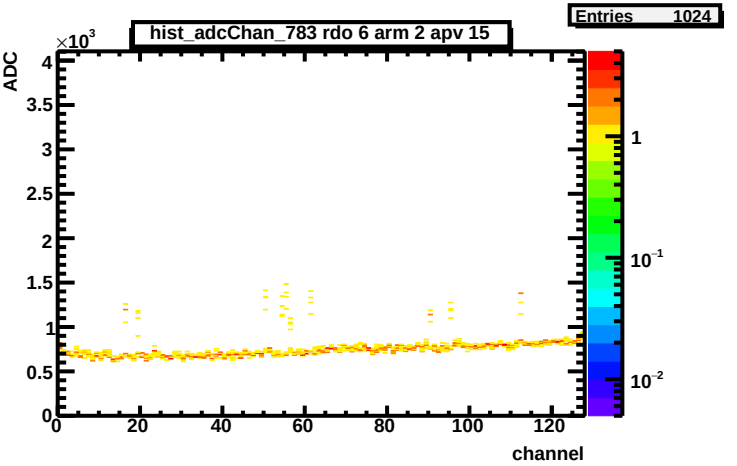

hist_adcChan_778 rdo 6 arm 2 apv 10
Entries 1024
ADC
×10^3
channel

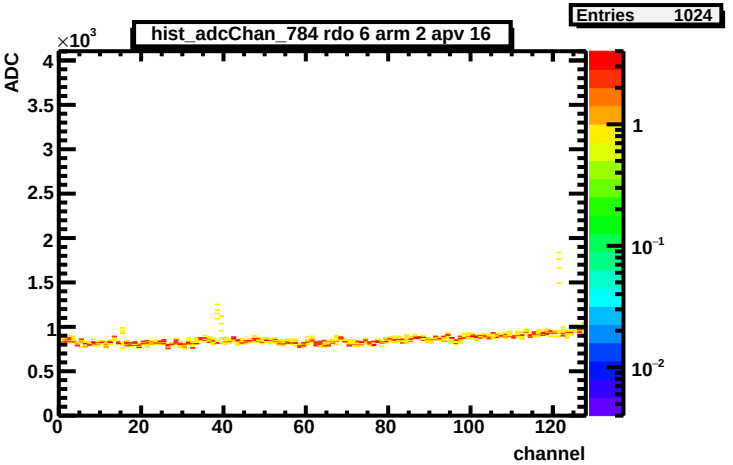

hist_adcChan_779 rdo 6 arm 2 apv 11
Entries 1024
ADC
×10^3
channel

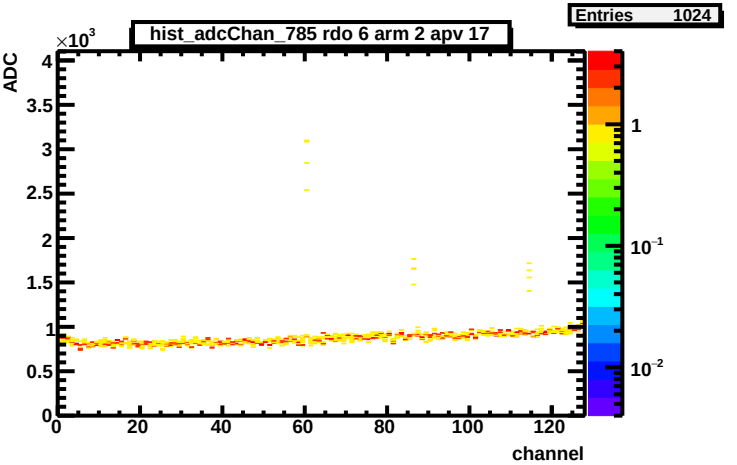

hist_adcChan_780 rdo 6 arm 2 apv 12
Entries 1024
ADC
×10^3
channel

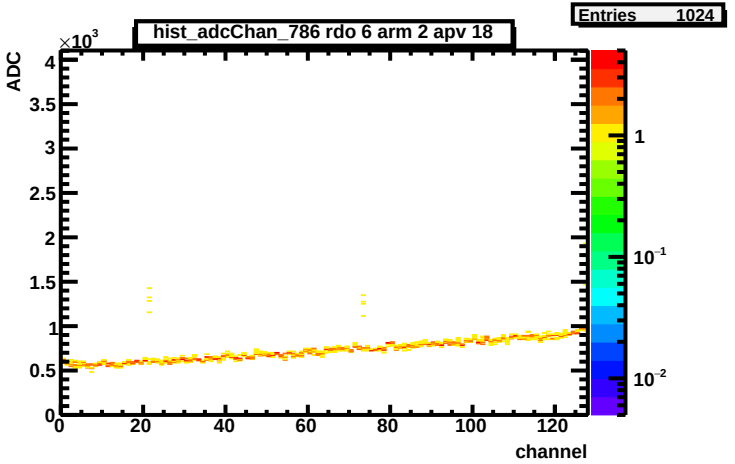

hist_adcChan_781 rdo 6 arm 2 apv 13
Entries 1024
ADC
×10^3
channel

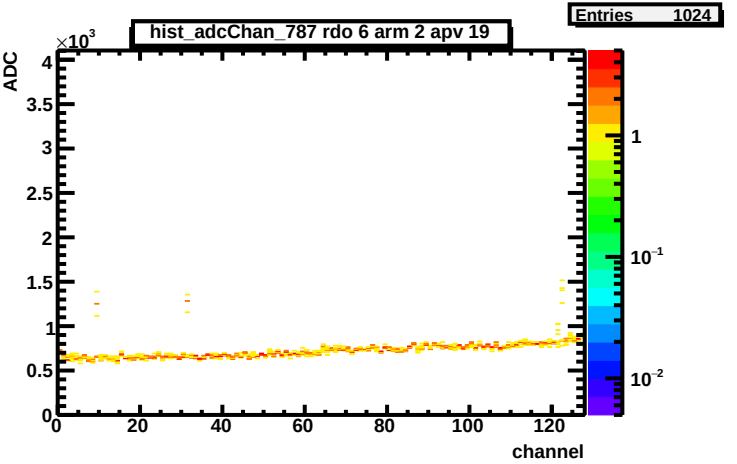

hist_adcChan_782 rdo 6 arm 2 apv 14
Entries 1024
ADC
×10^3
channel

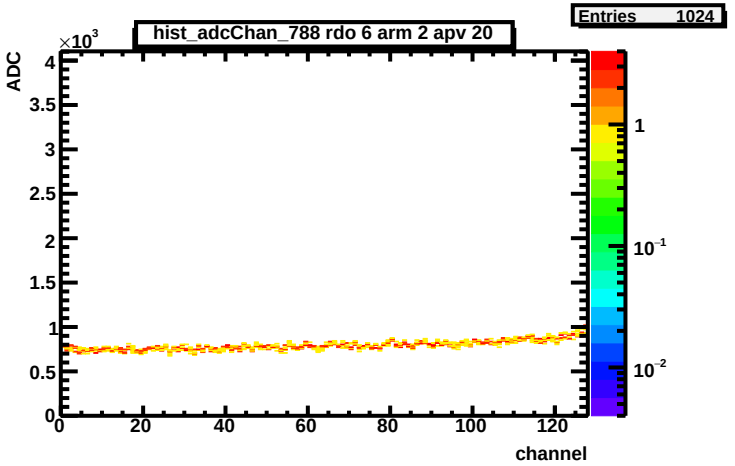

hist_adcChan_783 rdo 6 arm 2 apv 15
Entries 1024
ADC
×10^3
channel

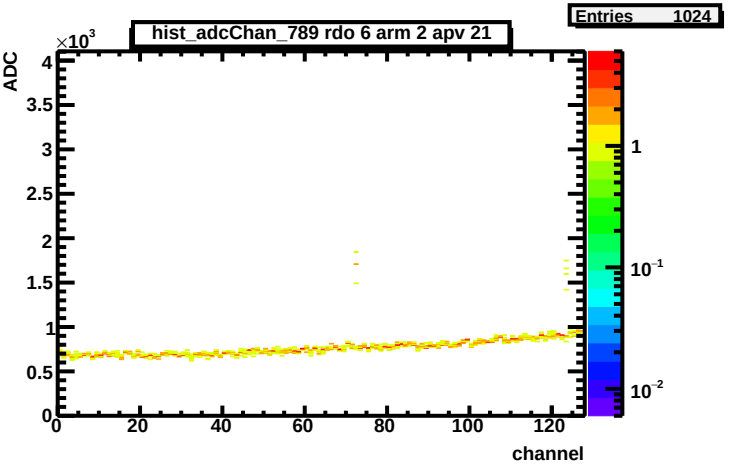

hist_adcChan_784 rdo 6 arm 2 apv 16
Entries 1024
ADC
×10^3
channel

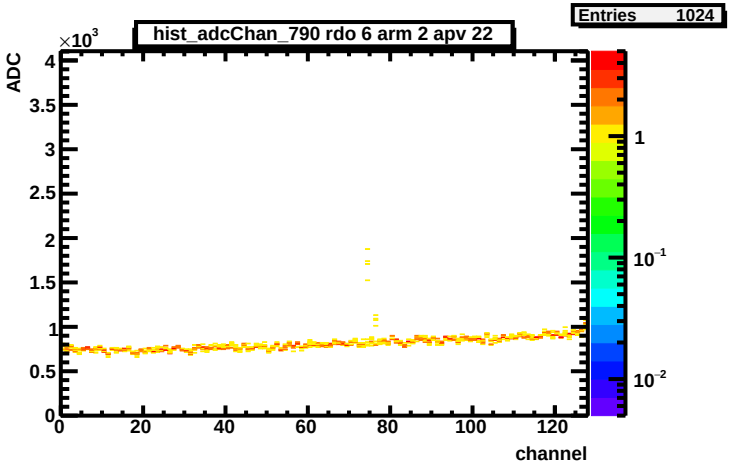

hist_adcChan_785 rdo 6 arm 2 apv 17
Entries 1024
ADC
×10^3
channel

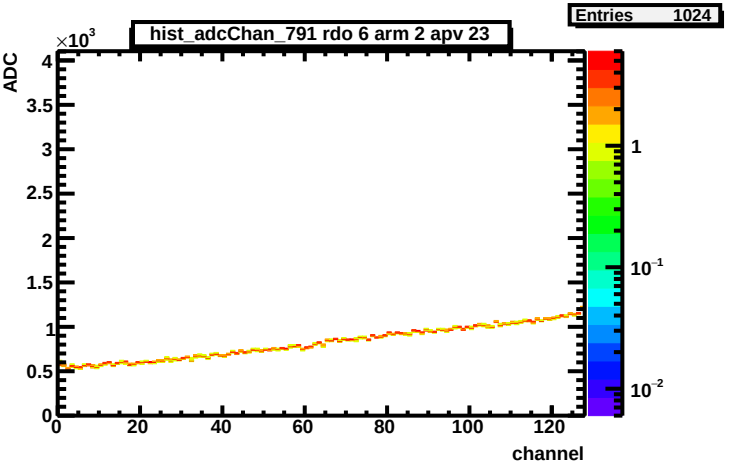

hist_adcChan_786 rdo 6 arm 2 apv 18
Entries 1024
ADC
channel

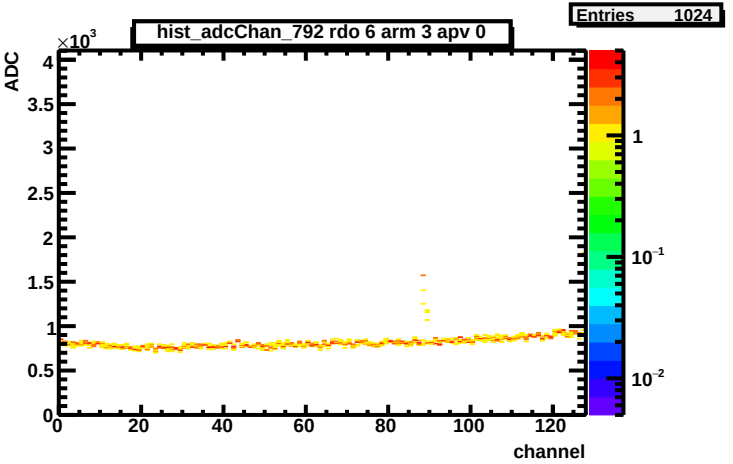

hist_adcChan_787 rdo 6 arm 2 apv 19
Entries 1024
ADC
channel

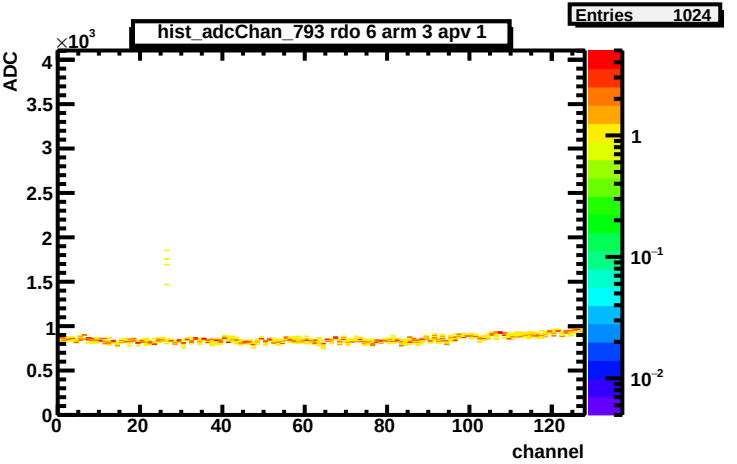

hist_adcChan_788 rdo 6 arm 2 apv 20
Entries 1024
ADC
channel

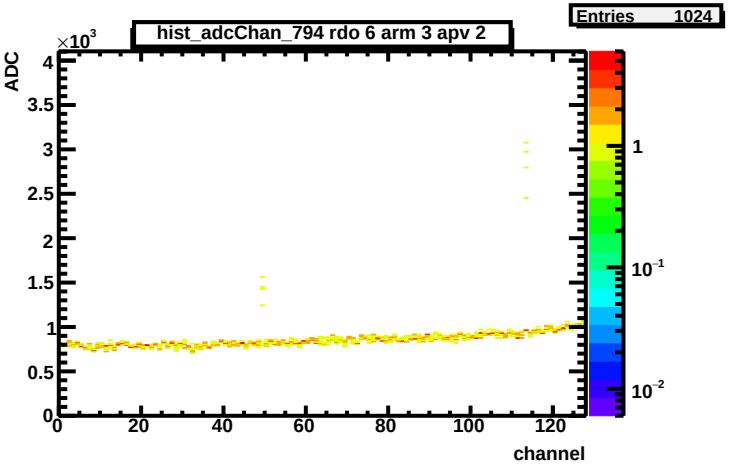

hist_adcChan_789 rdo 6 arm 2 apv 21
Entries 1024
ADC
channel

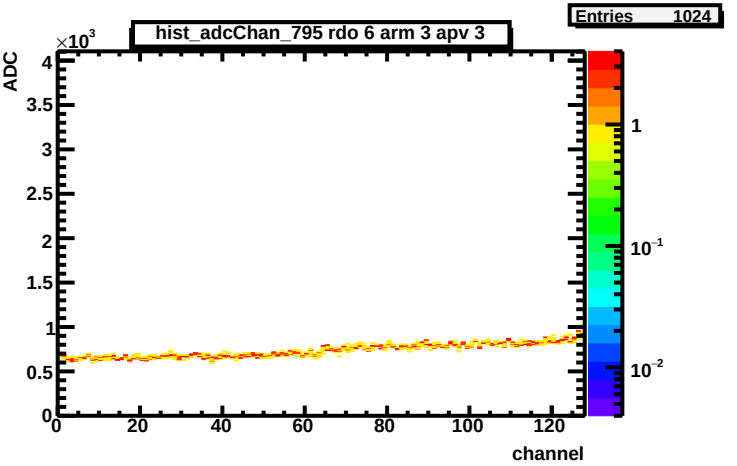

hist_adcChan_790 rdo 6 arm 2 apv 22
Entries 1024
ADC
channel

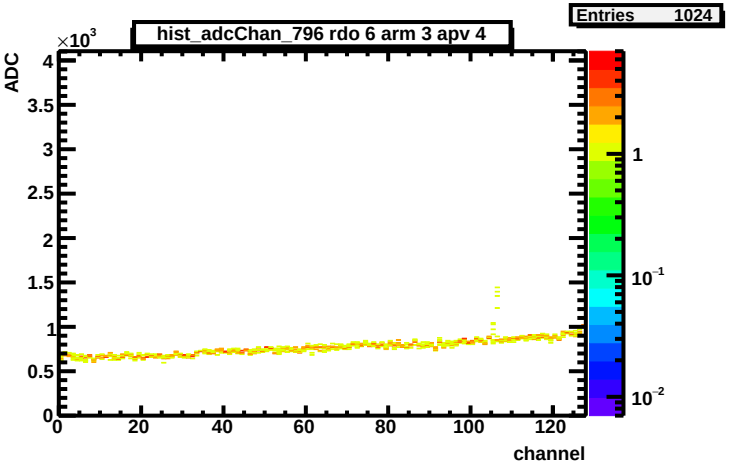

hist_adcChan_791 rdo 6 arm 2 apv 23
Entries 1024
ADC
channel

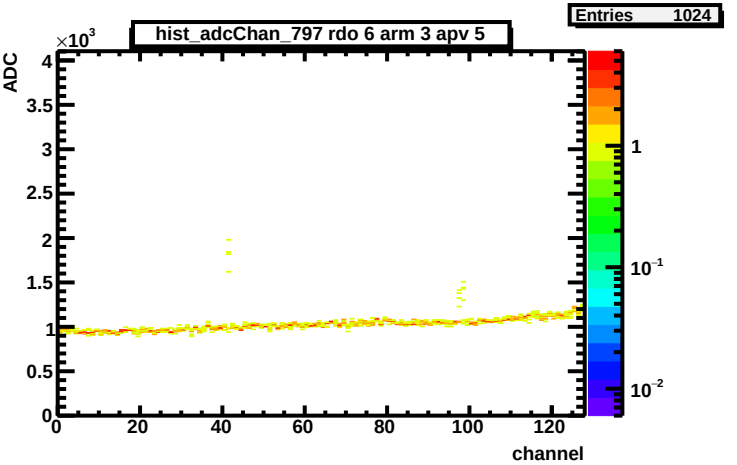

hist_adcChan_792 rdo 6 arm 3 apv 0
Entries 1024
ADC
×10^3
channel

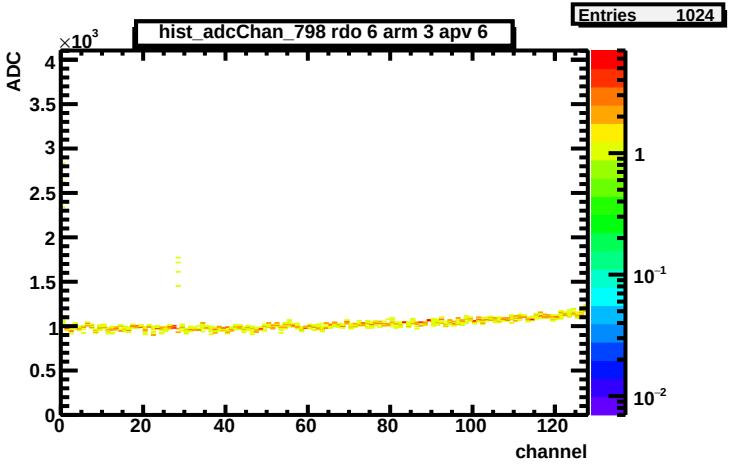

hist_adcChan_793 rdo 6 arm 3 apv 1
Entries 1024
ADC
×10^3
channel

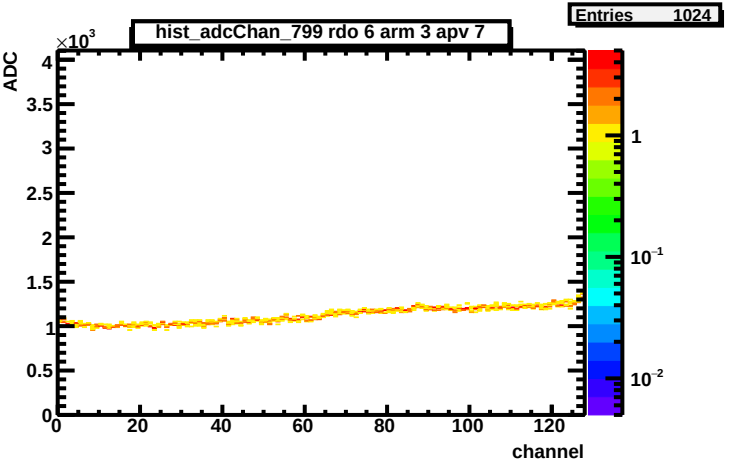

hist_adcChan_794 rdo 6 arm 3 apv 2
Entries 1024
ADC
×10^3
channel

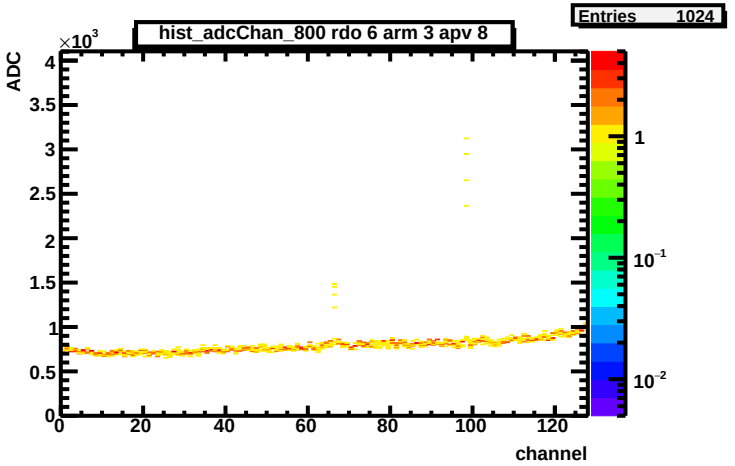

hist_adcChan_795 rdo 6 arm 3 apv 3
Entries 1024
ADC
×10^3
channel

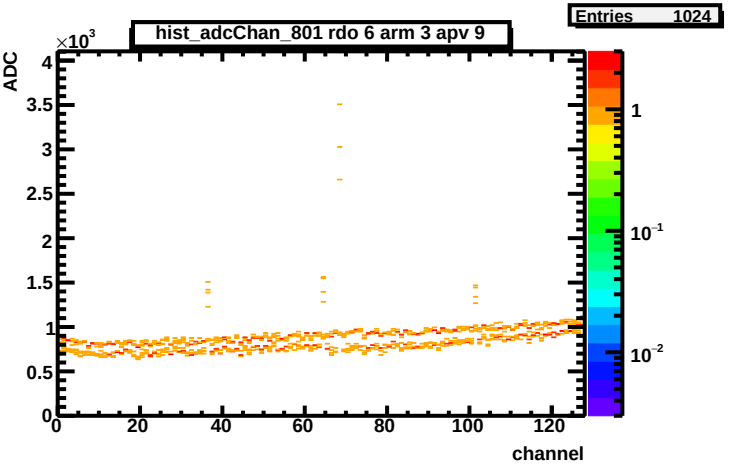

hist_adcChan_796 rdo 6 arm 3 apv 4
Entries 1024
ADC
×10^3
channel

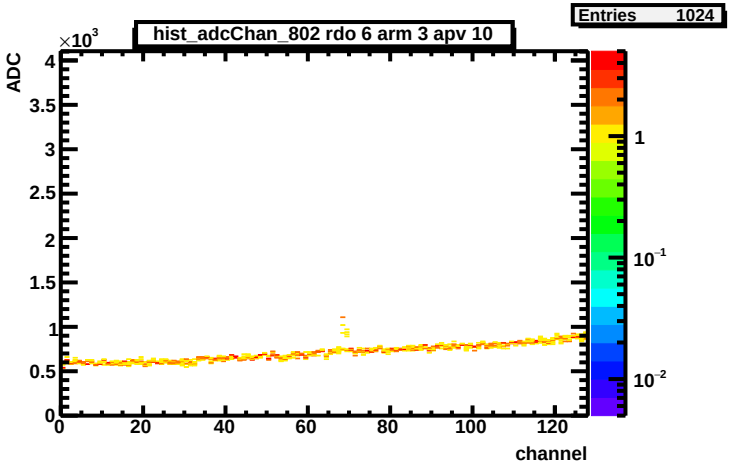

hist_adcChan_797 rdo 6 arm 3 apv 5
Entries 1024
ADC
×10^3
channel

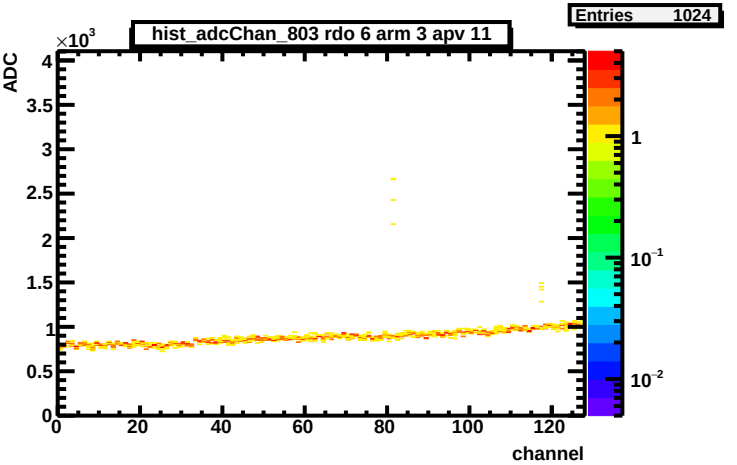

hist_adcChan_798 rdo 6 arm 3 apv 6
Entries 1024
ADC
×10^3
channel

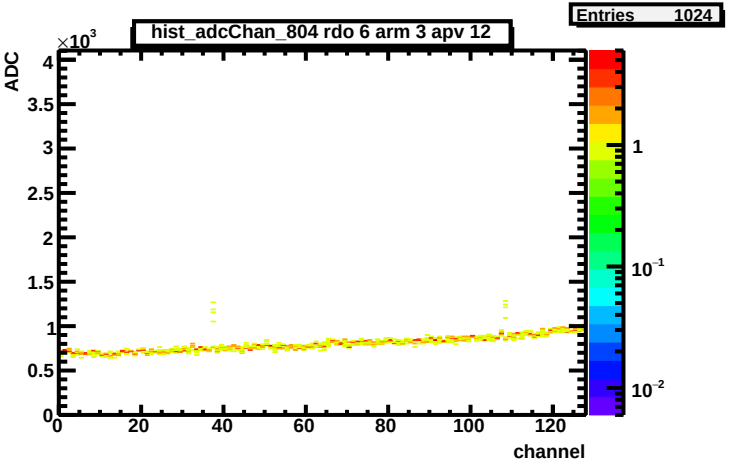

hist_adcChan_799 rdo 6 arm 3 apv 7
Entries 1024
ADC
×10^3
channel

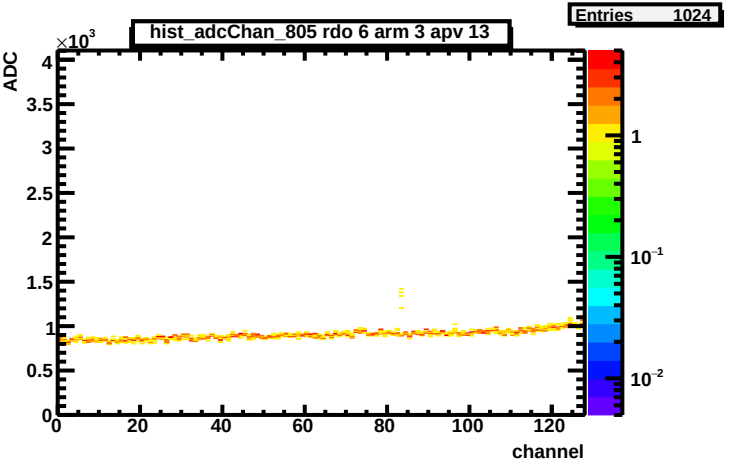

hist_adcChan_800 rdo 6 arm 3 apv 8
Entries 1024
ADC
×10^3
channel

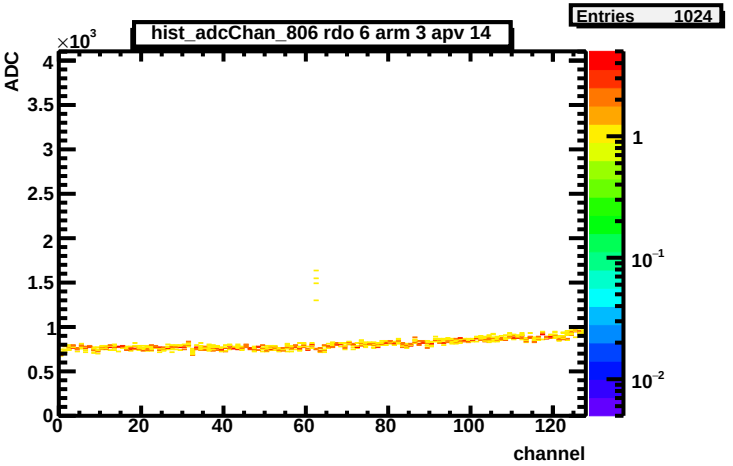

hist_adcChan_801 rdo 6 arm 3 apv 9
Entries 1024
ADC
×10^3
channel

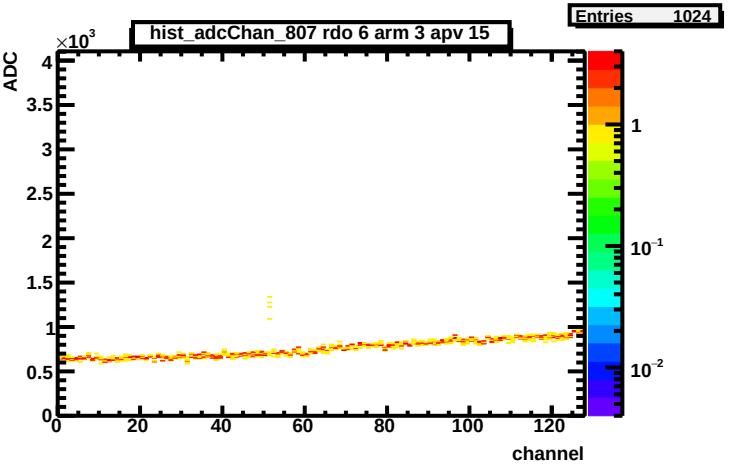

hist_adcChan_802 rdo 6 arm 3 apv 10
Entries 1024
ADC
×10^3
channel

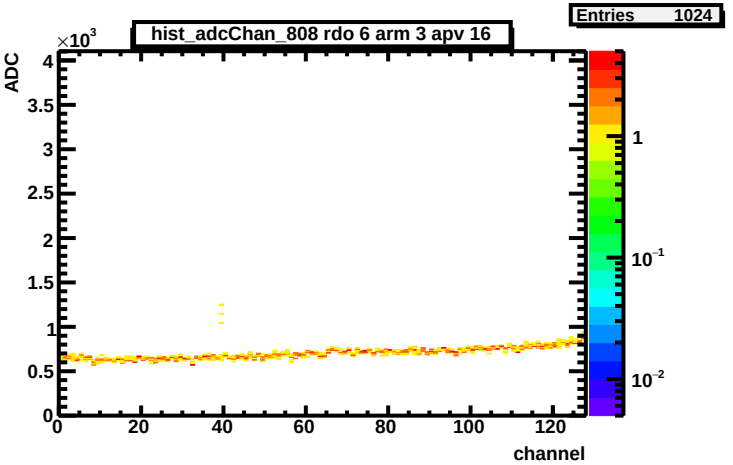

hist_adcChan_803 rdo 6 arm 3 apv 11
Entries 1024
ADC
×10^3
channel

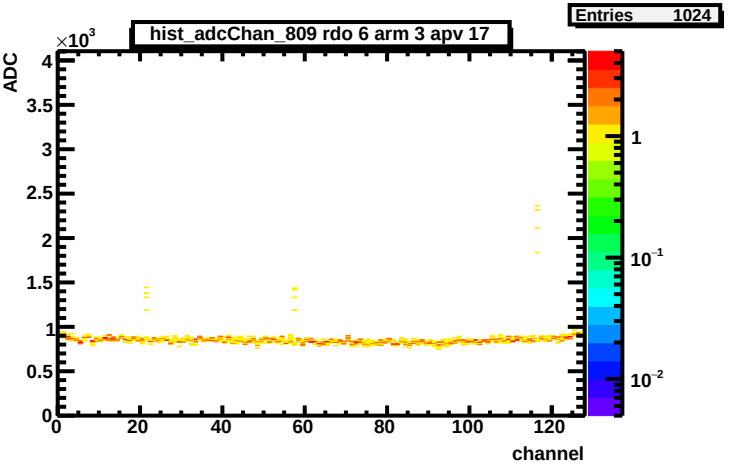

hist_adcChan_804 rdo 6 arm 3 apv 12
Entries 1024
ADC
channel

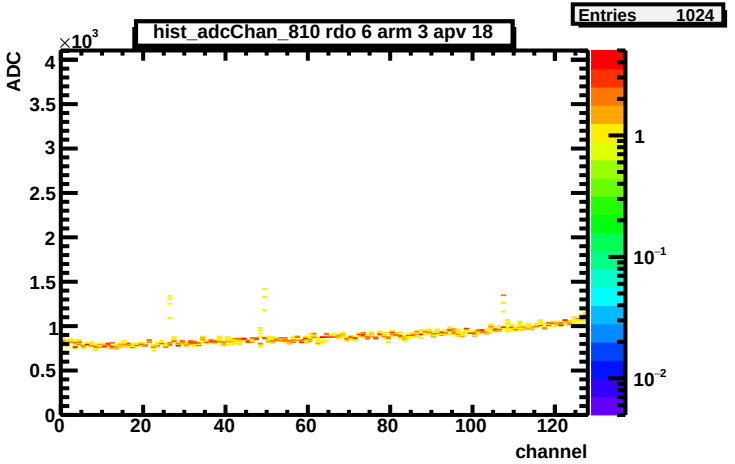

hist_adcChan_805 rdo 6 arm 3 apv 13
Entries 1024
ADC
channel

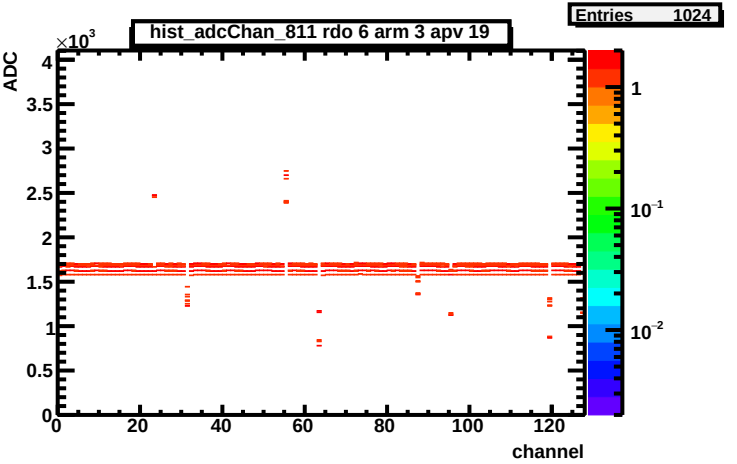

hist_adcChan_806 rdo 6 arm 3 apv 14
Entries 1024
ADC
channel

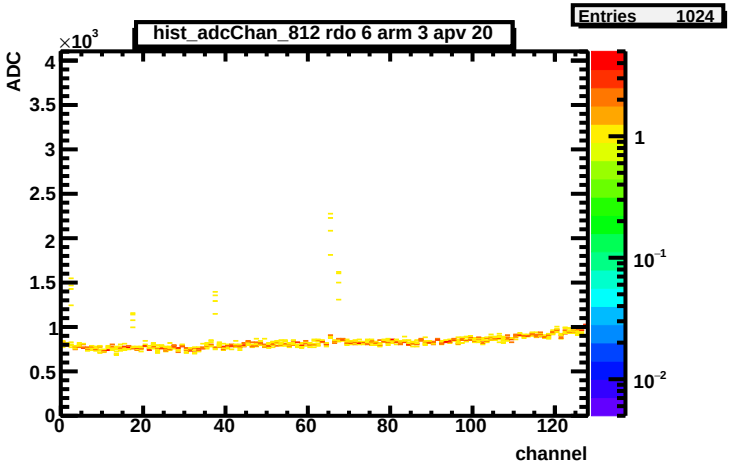

hist_adcChan_807 rdo 6 arm 3 apv 15
Entries 1024
ADC
channel

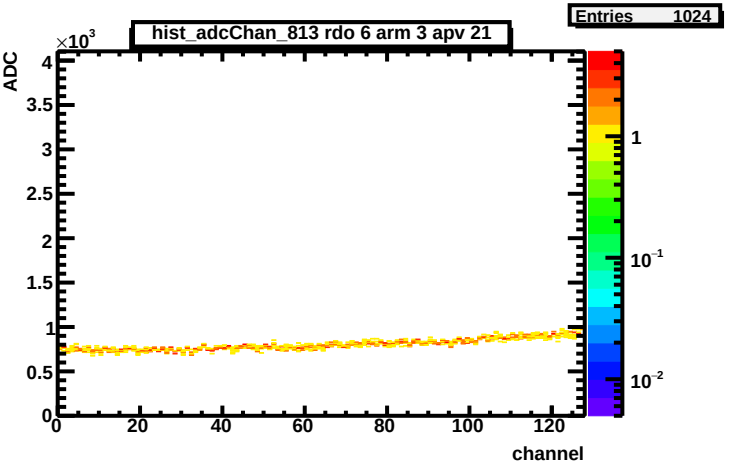

hist_adcChan_808 rdo 6 arm 3 apv 16
Entries 1024
ADC
channel

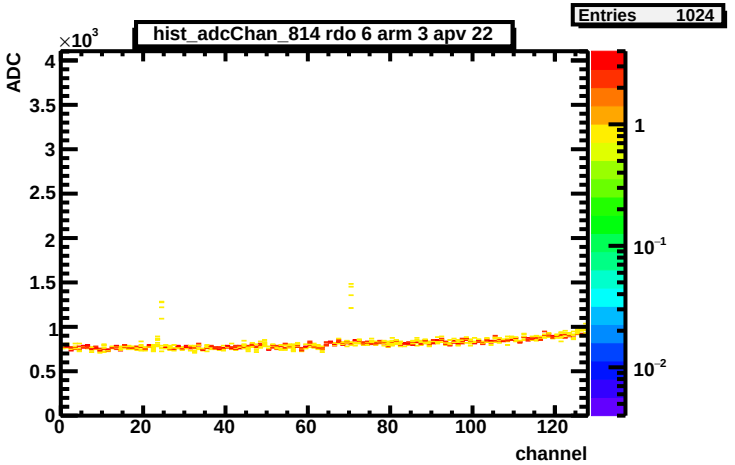

hist_adcChan_809 rdo 6 arm 3 apv 17
Entries 1024
ADC
channel

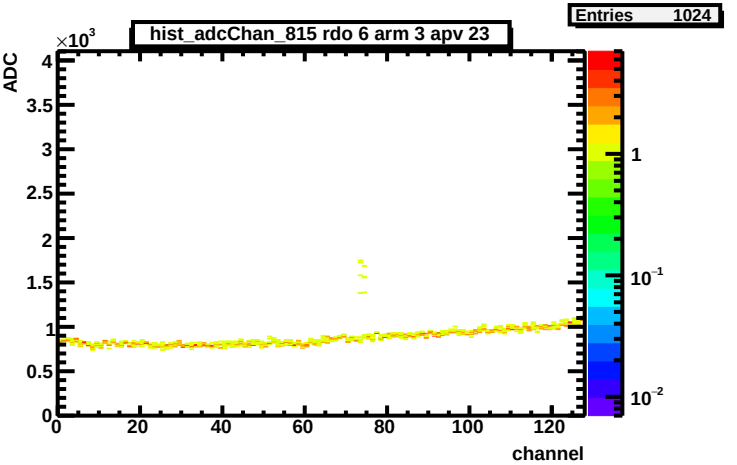

hist_adcChan_810 rdo 6 arm 3 apv 18
Entries 1024
ADC
×10^3
channel

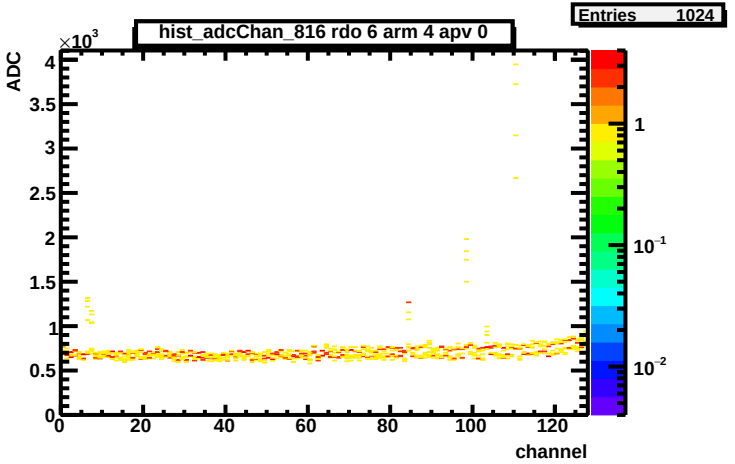

hist_adcChan_811 rdo 6 arm 3 apv 19
Entries 1024
ADC
×10^3
channel

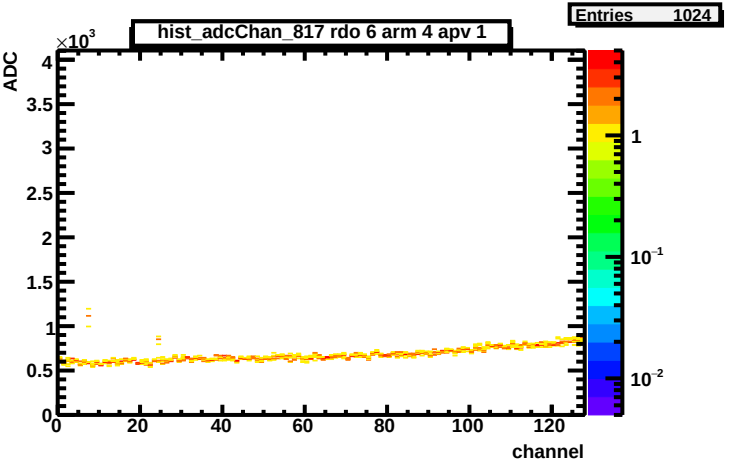

hist_adcChan_812 rdo 6 arm 3 apv 20
Entries 1024
ADC
×10^3
channel

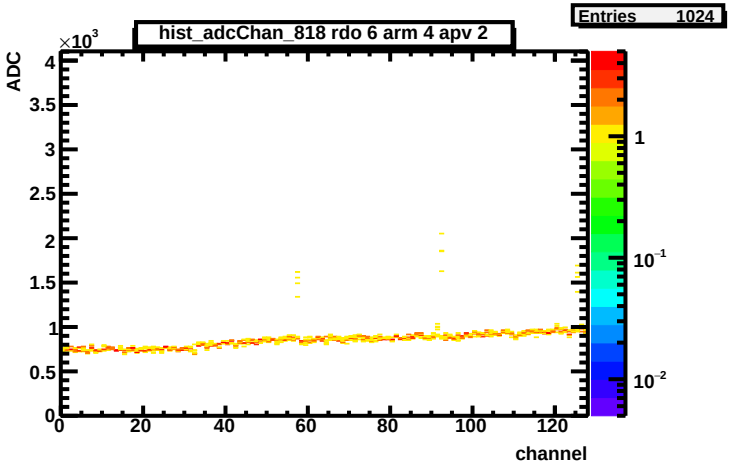

hist_adcChan_813 rdo 6 arm 3 apv 21
Entries 1024
ADC
×10^3
channel

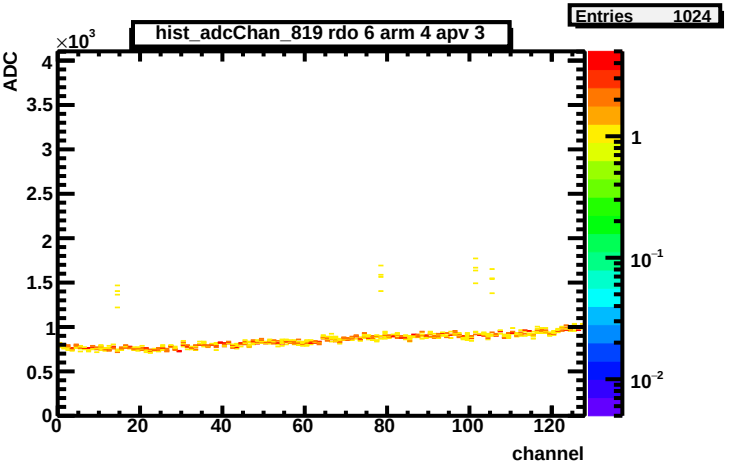

hist_adcChan_814 rdo 6 arm 3 apv 22
Entries 1024
ADC
×10^3
channel

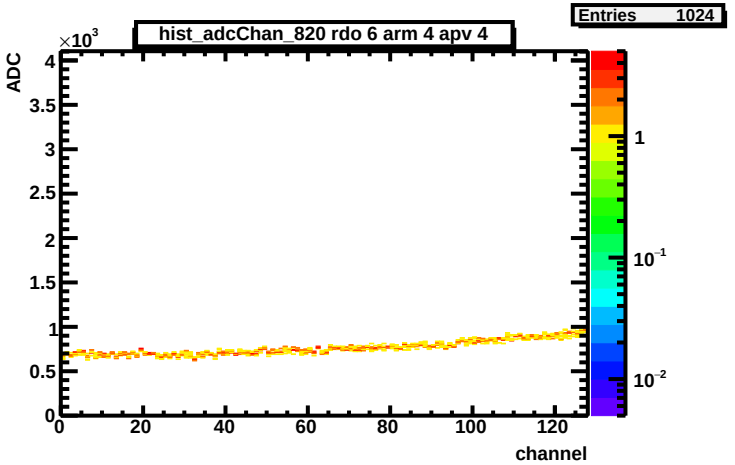

hist_adcChan_815 rdo 6 arm 3 apv 23
Entries 1024
ADC
×10^3
channel

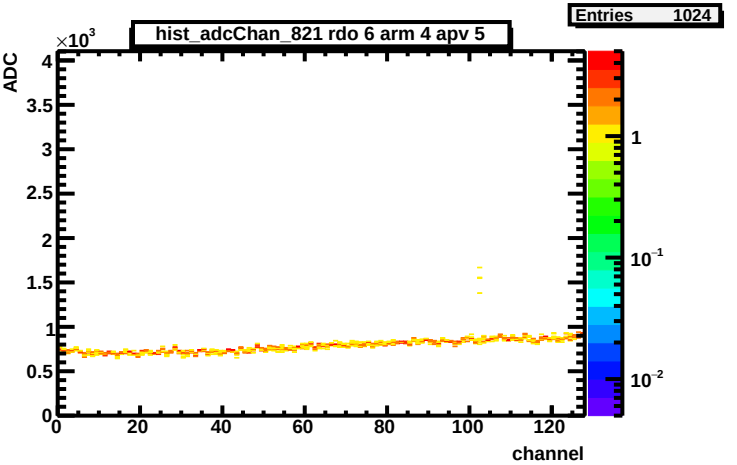

hist_adcChan_816 rdo 6 arm 4 apv 0
Entries 1024
ADC
×10^3
channel

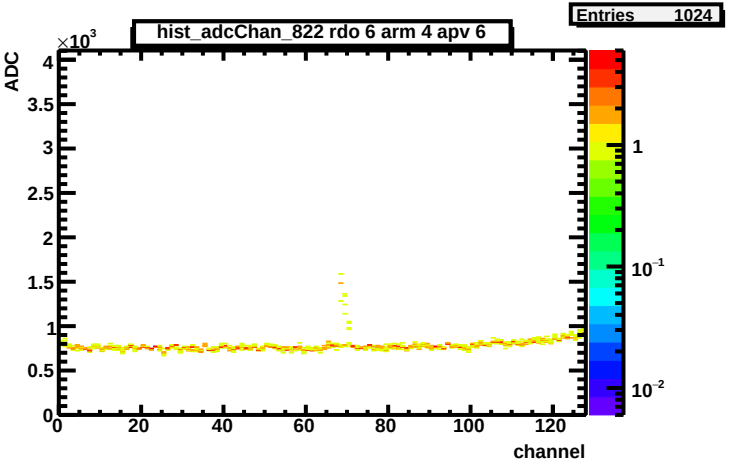

hist_adcChan_817 rdo 6 arm 4 apv 1
Entries 1024
ADC
×10^3
channel

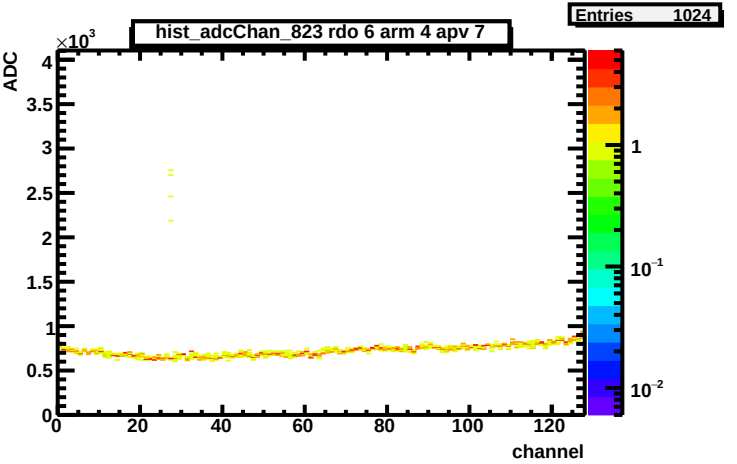

hist_adcChan_818 rdo 6 arm 4 apv 2
Entries 1024
ADC
×10^3
channel

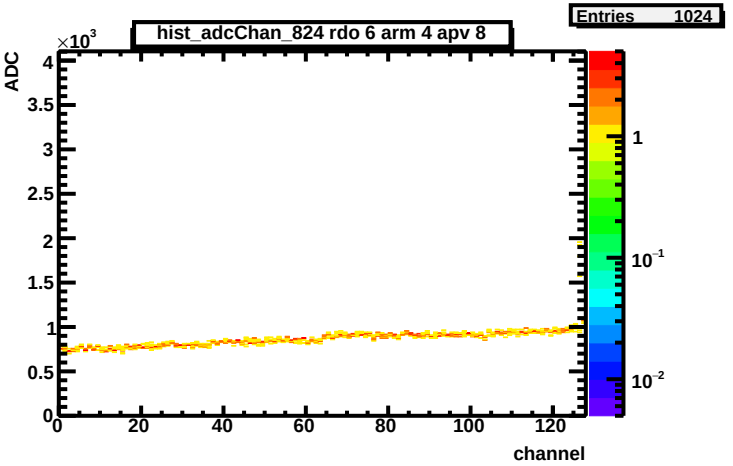

hist_adcChan_819 rdo 6 arm 4 apv 3
Entries 1024
ADC
×10^3
channel

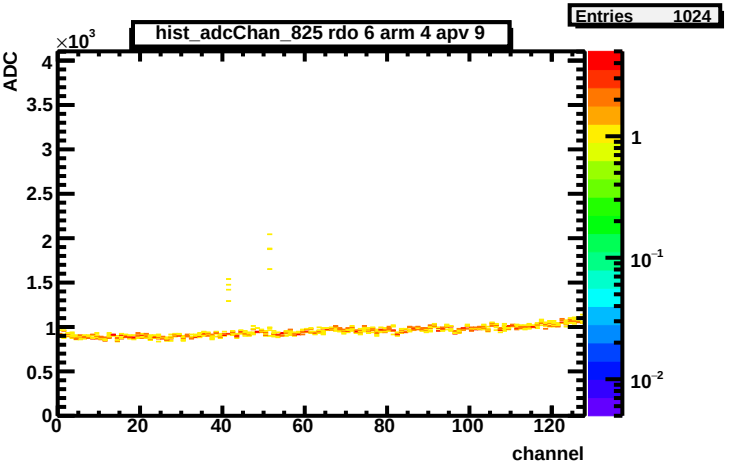

hist_adcChan_820 rdo 6 arm 4 apv 4
Entries 1024
ADC
×10^3
channel

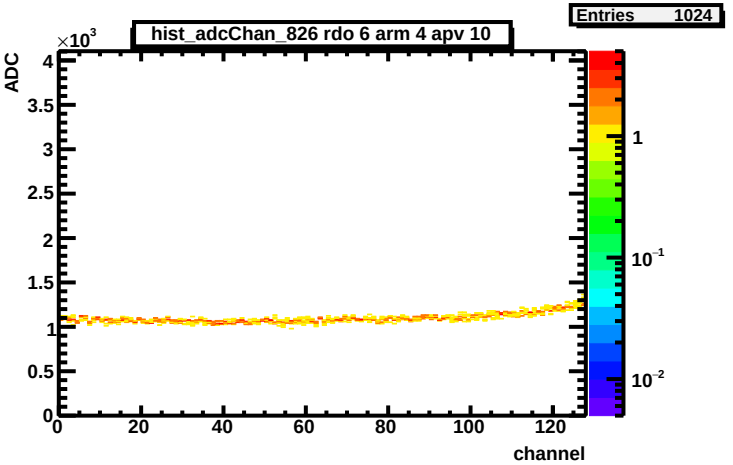

hist_adcChan_821 rdo 6 arm 4 apv 5
Entries 1024
ADC
×10^3
channel

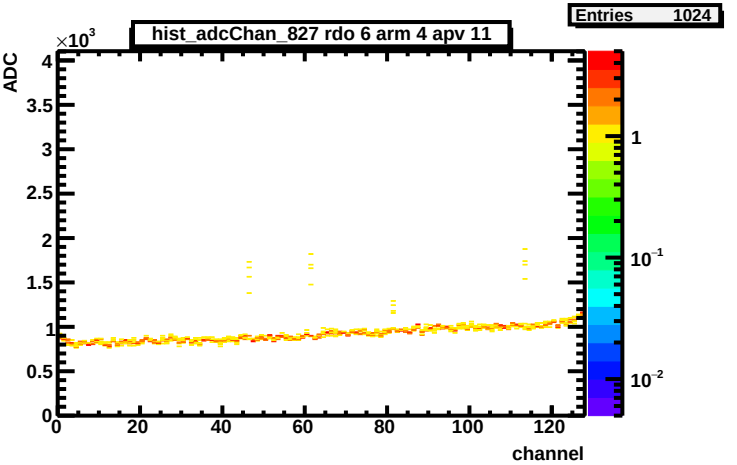

hist_adcChan_822 rdo 6 arm 4 apv 6
Entries 1024
ADC
×10^3
channel

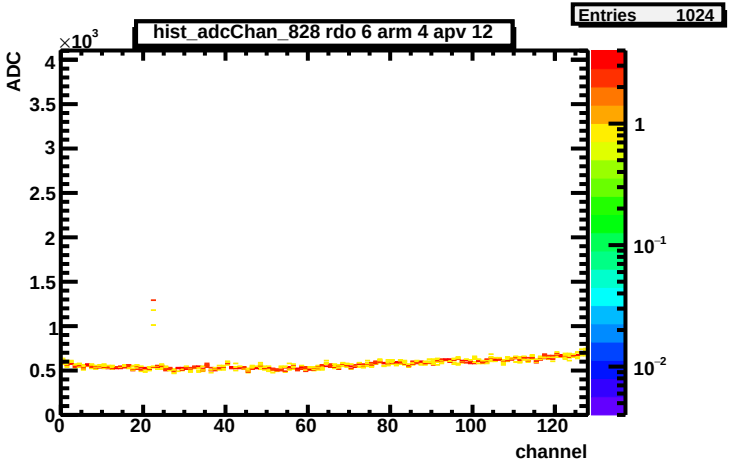

hist_adcChan_823 rdo 6 arm 4 apv 7
Entries 1024
ADC
×10^3
channel

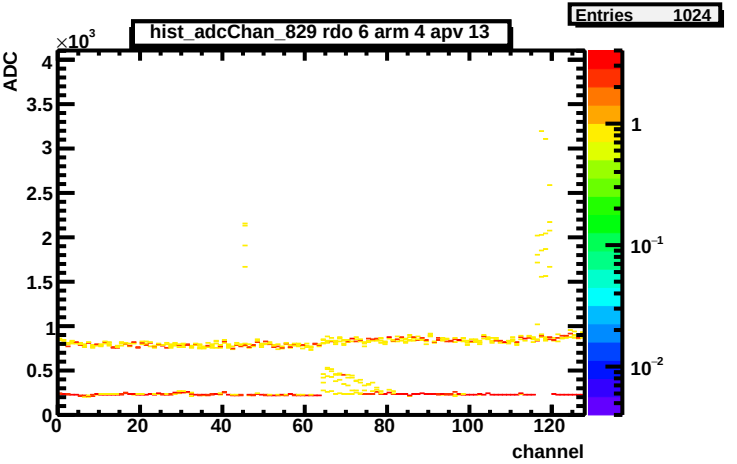

hist_adcChan_824 rdo 6 arm 4 apv 8
Entries 1024
ADC
×10^3
channel

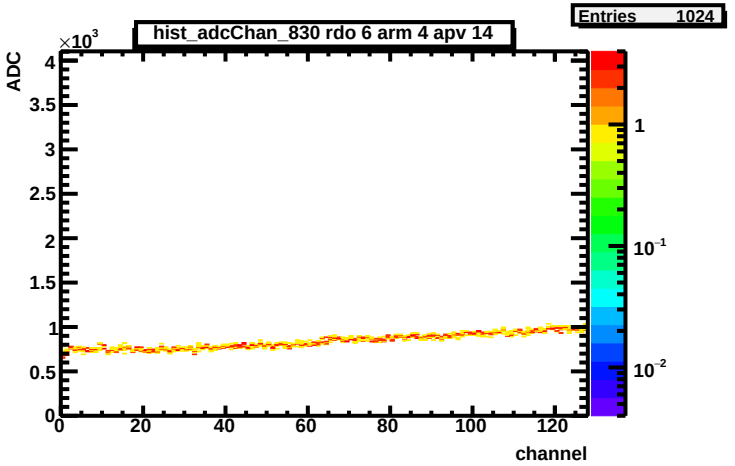

hist_adcChan_825 rdo 6 arm 4 apv 9
Entries 1024
ADC
×10^3
channel

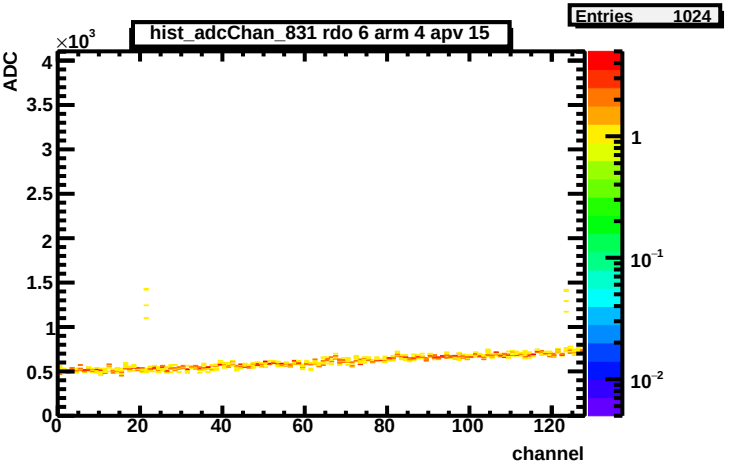

hist_adcChan_826 rdo 6 arm 4 apv 10
Entries 1024
ADC
×10^3
channel

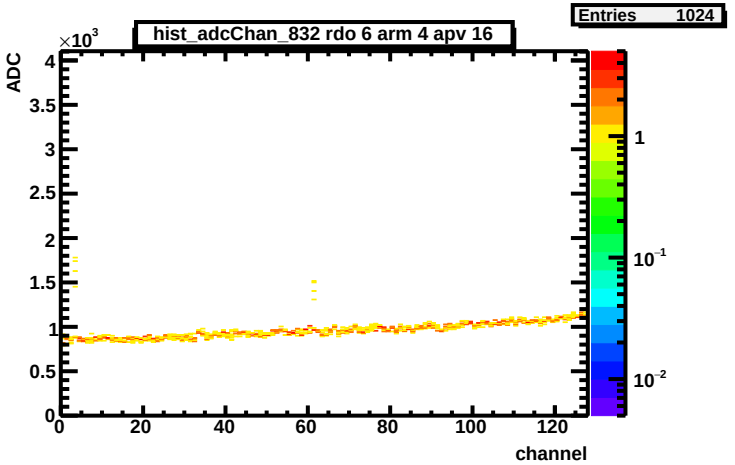

hist_adcChan_827 rdo 6 arm 4 apv 11
Entries 1024
ADC
×10^3
channel

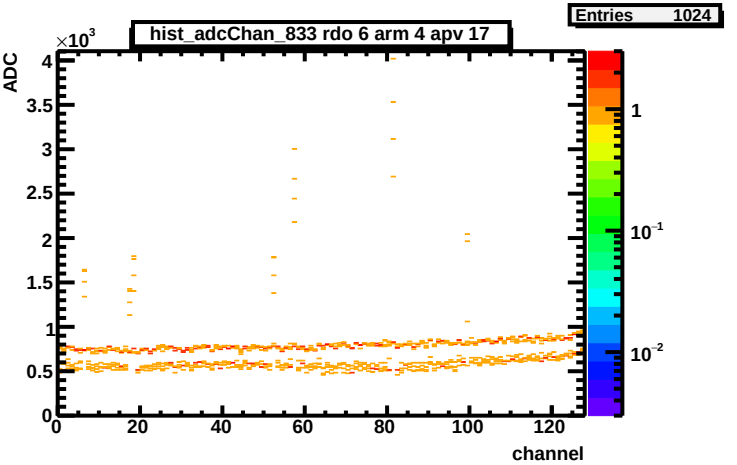

hist_adcChan_828 rdo 6 arm 4 apv 12
Entries 1024
ADC
×10^3
channel

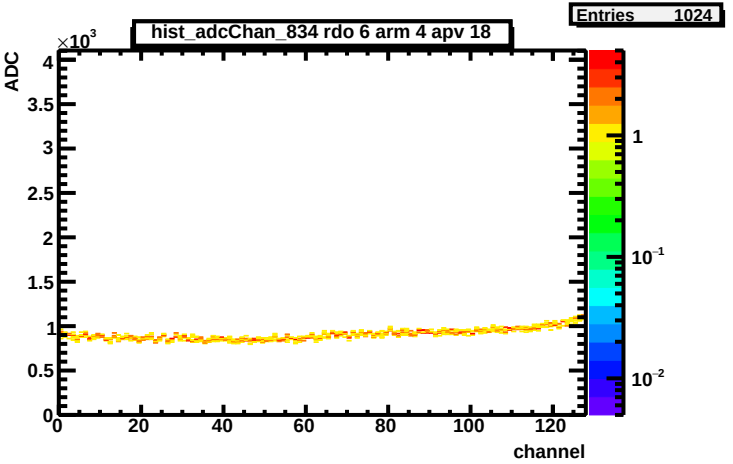

hist_adcChan_829 rdo 6 arm 4 apv 13
Entries 1024
ADC
×10^3
channel

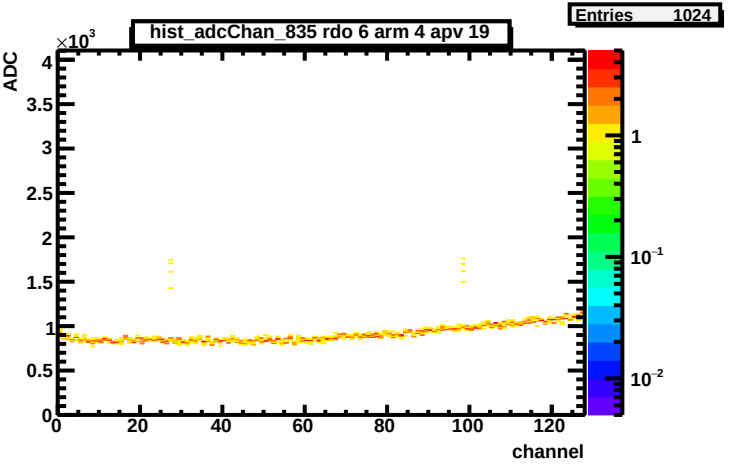

hist_adcChan_830 rdo 6 arm 4 apv 14
Entries 1024
ADC
×10^3
channel

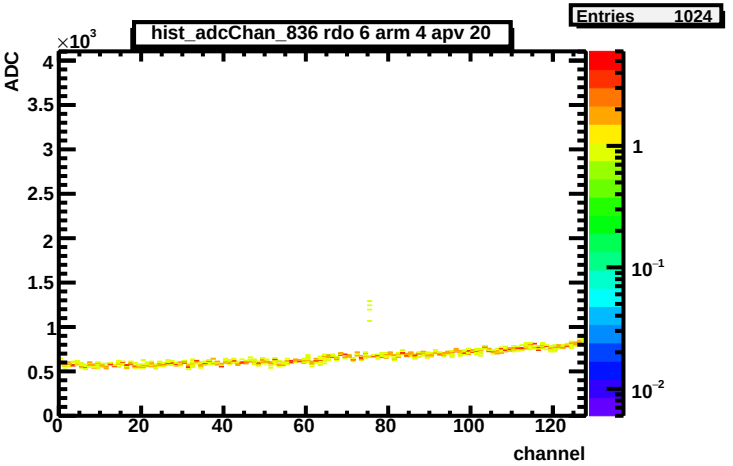

hist_adcChan_831 rdo 6 arm 4 apv 15
Entries 1024
ADC
×10^3
channel

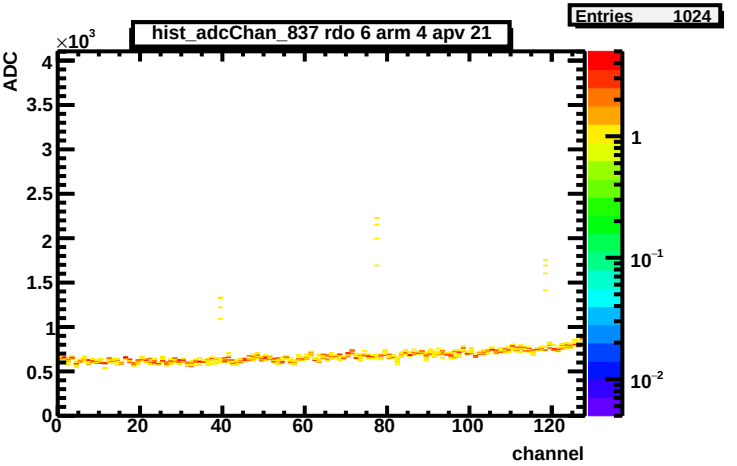

hist_adcChan_832 rdo 6 arm 4 apv 16
Entries 1024
ADC
×10^3
channel

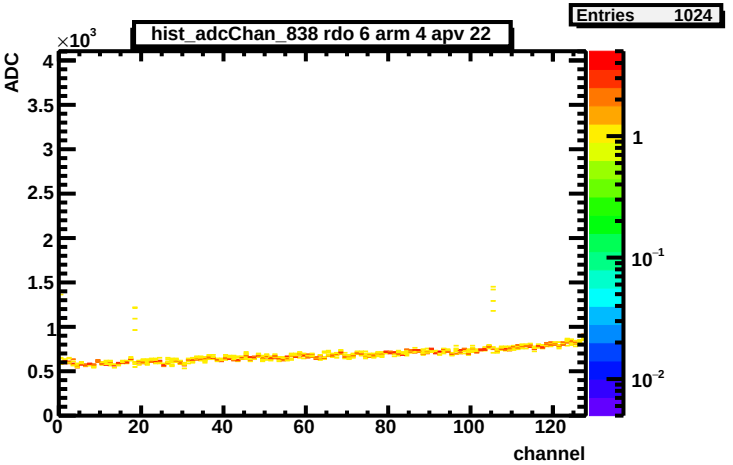

hist_adcChan_833 rdo 6 arm 4 apv 17
Entries 1024
ADC
×10^3
channel

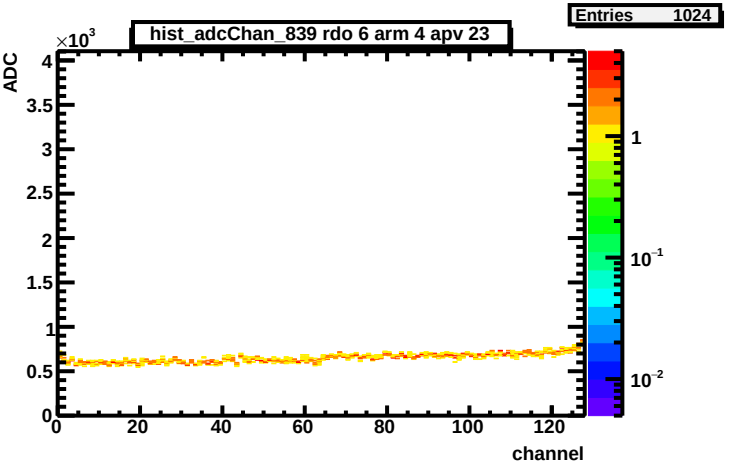

hist_adcChan_834 rdo 6 arm 4 apv 18
Entries 1024
ADC
×10^3
channel

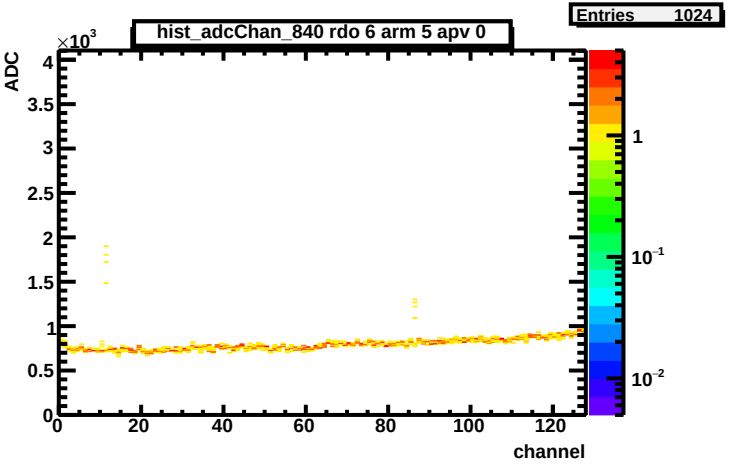

hist_adcChan_835 rdo 6 arm 4 apv 19
Entries 1024
ADC
×10^3
channel

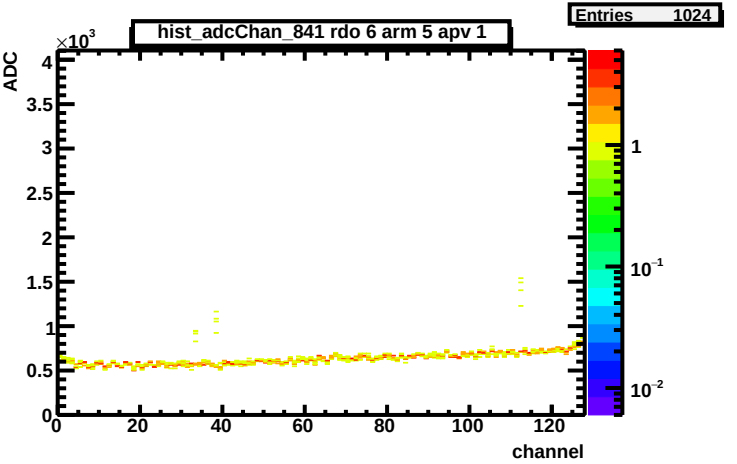

hist_adcChan_836 rdo 6 arm 4 apv 20
Entries 1024
ADC
×10^3
channel

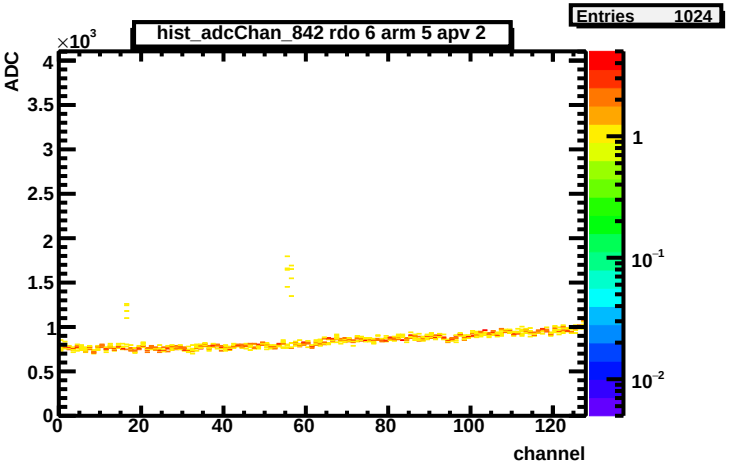

hist_adcChan_837 rdo 6 arm 4 apv 21
Entries 1024
ADC
×10^3
channel

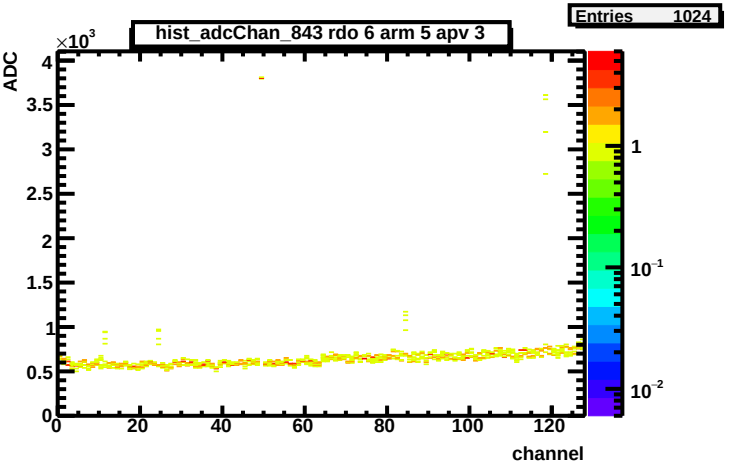

hist_adcChan_838 rdo 6 arm 4 apv 22
Entries 1024
ADC
×10^3
channel

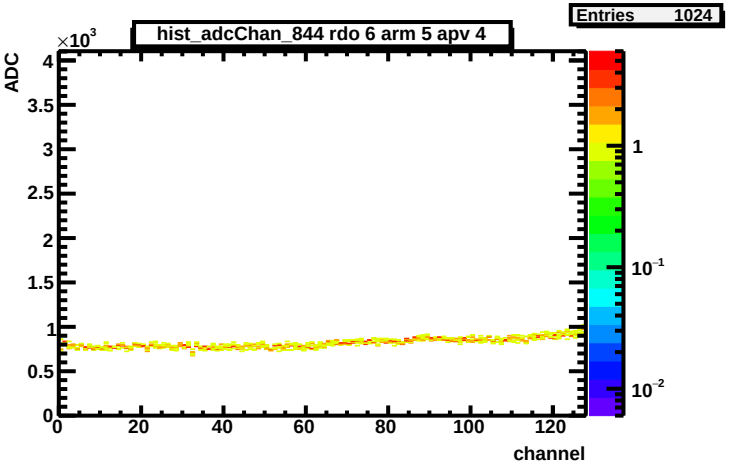

hist_adcChan_839 rdo 6 arm 4 apv 23
Entries 1024
ADC
×10^3
channel

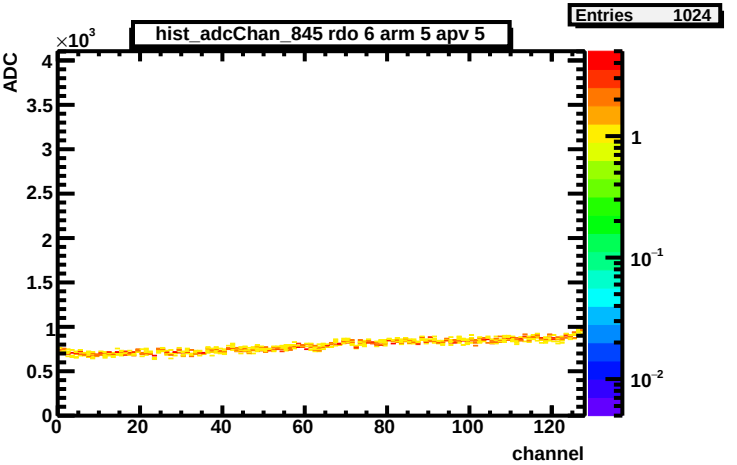

hist_adcChan_840 rdo 6 arm 5 apv 0
Entries 1024
ADC
×10^3
channel

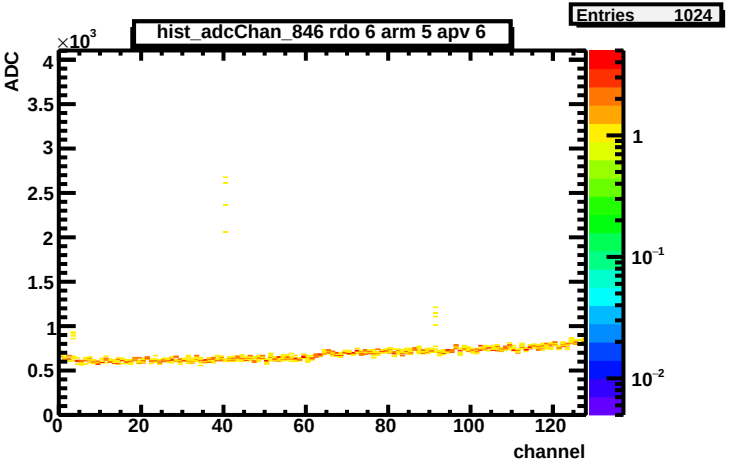

hist_adcChan_841 rdo 6 arm 5 apv 1
Entries 1024
ADC
×10^3
channel

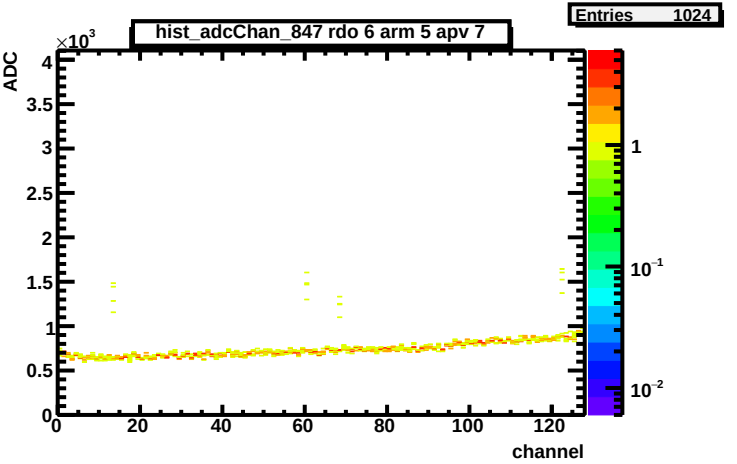

hist_adcChan_842 rdo 6 arm 5 apv 2
Entries 1024
ADC
×10^3
channel

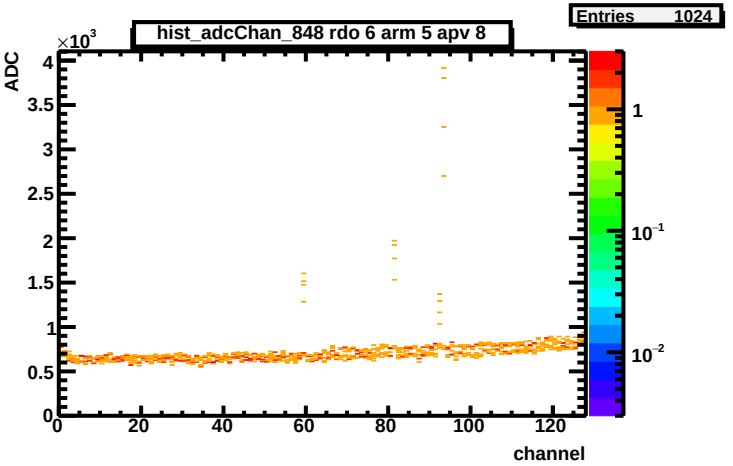

hist_adcChan_843 rdo 6 arm 5 apv 3
Entries 1024
ADC
×10^3
channel

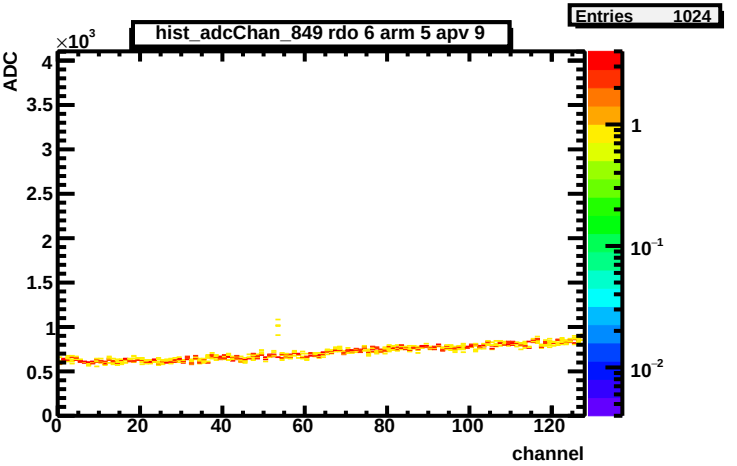

hist_adcChan_844 rdo 6 arm 5 apv 4
Entries 1024
ADC
×10^3
channel

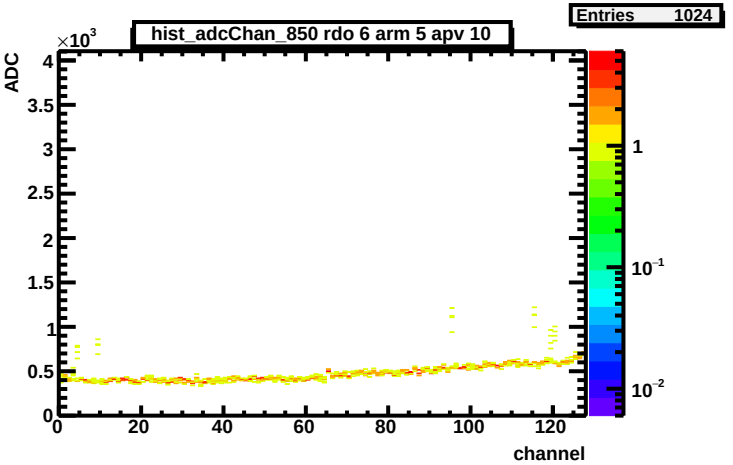

hist_adcChan_845 rdo 6 arm 5 apv 5
Entries 1024
ADC
×10^3
channel

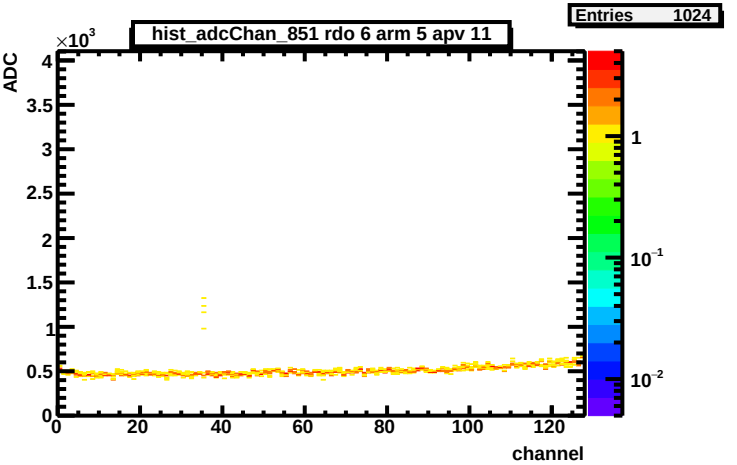

hist_adcChan_846 rdo 6 arm 5 apv 6
Entries 1024
ADC
×10^3
channel

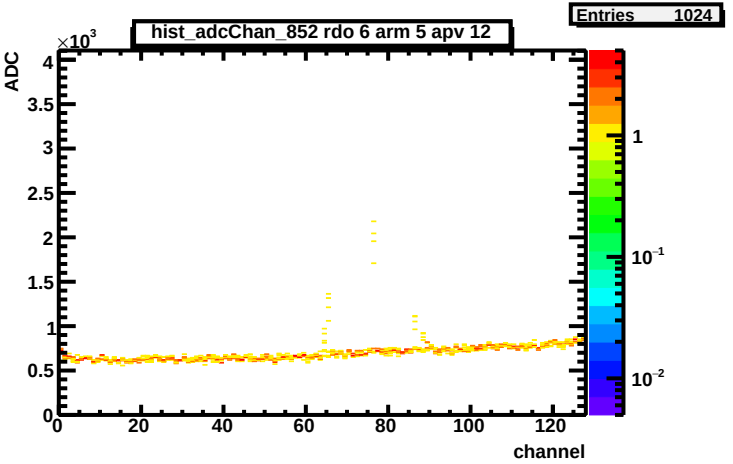

hist_adcChan_847 rdo 6 arm 5 apv 7
Entries 1024
ADC
×10^3
channel

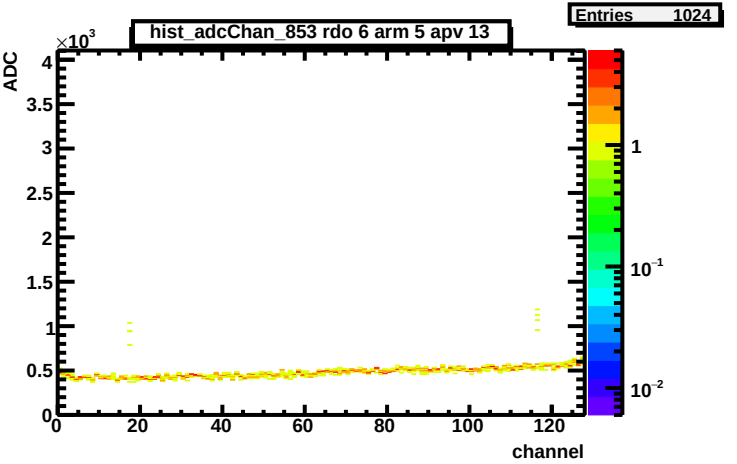

hist_adcChan_848 rdo 6 arm 5 apv 8
Entries 1024
ADC
×10^3
channel

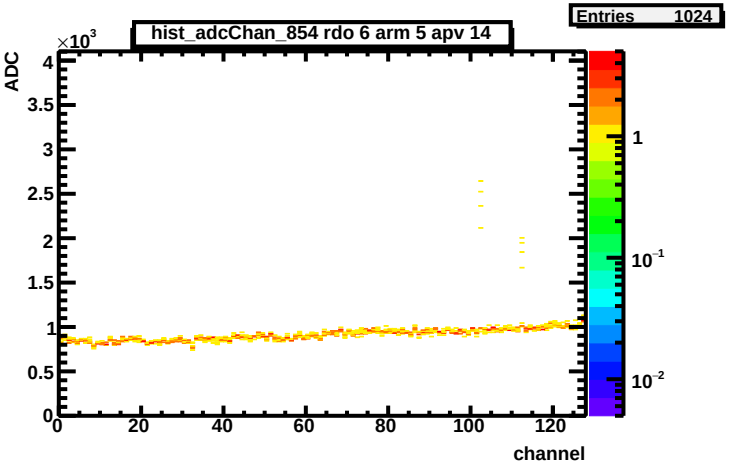

hist_adcChan_849 rdo 6 arm 5 apv 9
Entries 1024
ADC
×10^3
channel

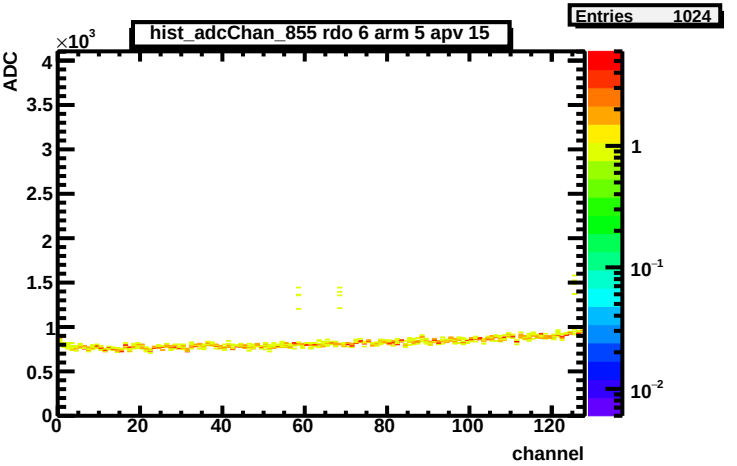

hist_adcChan_850 rdo 6 arm 5 apv 10
Entries 1024
ADC
×10^3
channel

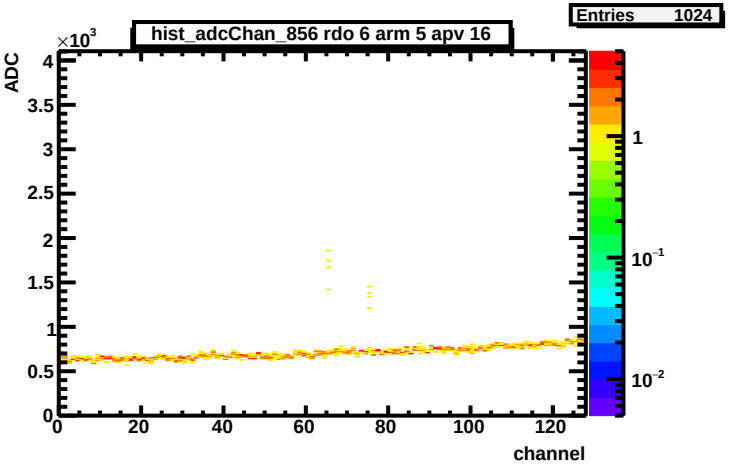

hist_adcChan_851 rdo 6 arm 5 apv 11
Entries 1024
ADC
×10^3
channel

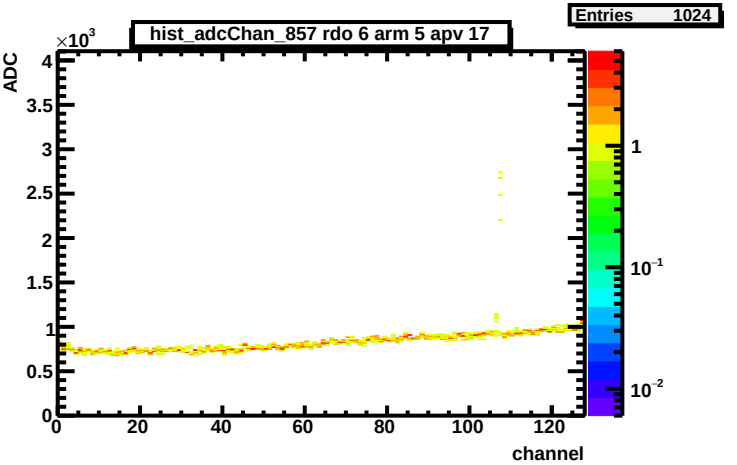

hist_adcChan_852 rdo 6 arm 5 apv 12
Entries 1024
ADC
×10³
channel

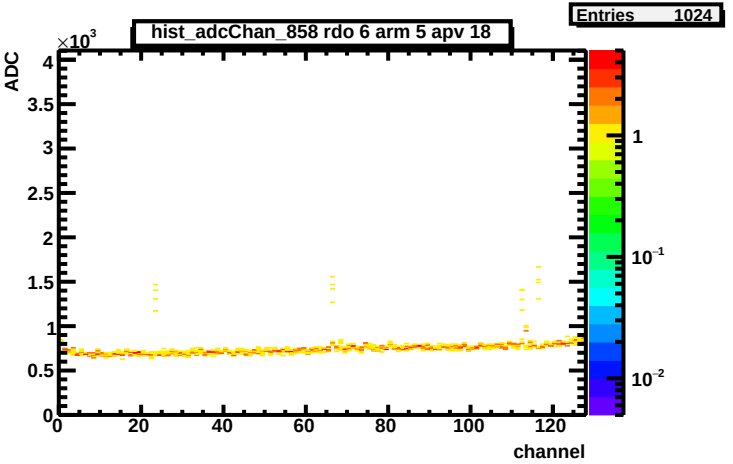

hist_adcChan_853 rdo 6 arm 5 apv 13
Entries 1024
ADC
×10³
channel

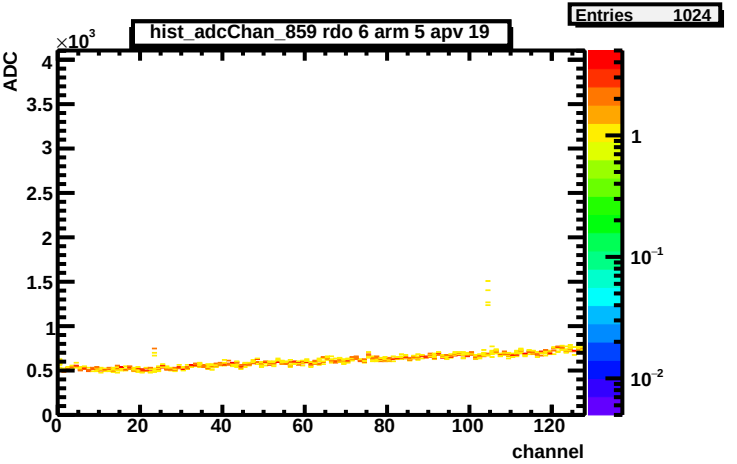

hist_adcChan_854 rdo 6 arm 5 apv 14
Entries 1024
ADC
×10³
channel

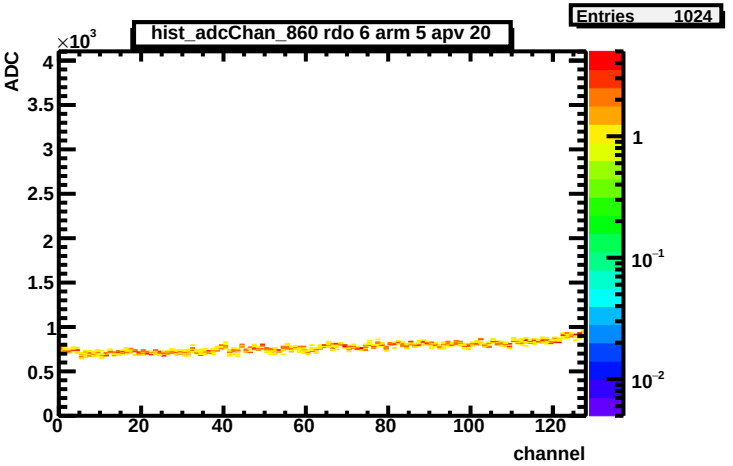

hist_adcChan_855 rdo 6 arm 5 apv 15
Entries 1024
ADC
×10³
channel

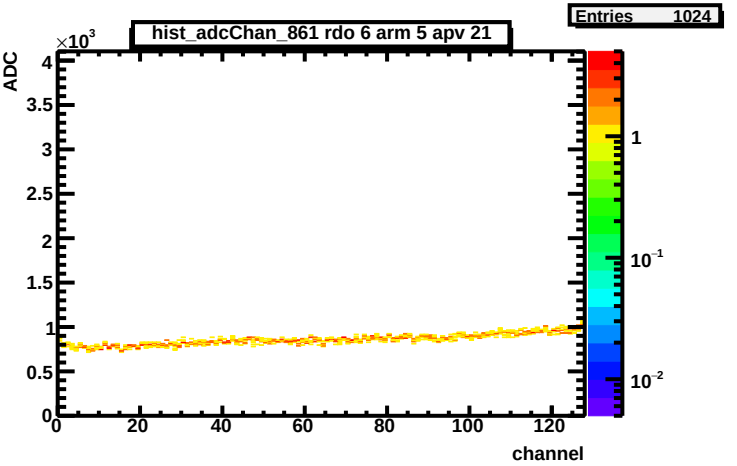

hist_adcChan_856 rdo 6 arm 5 apv 16
Entries 1024
ADC
×10³
channel

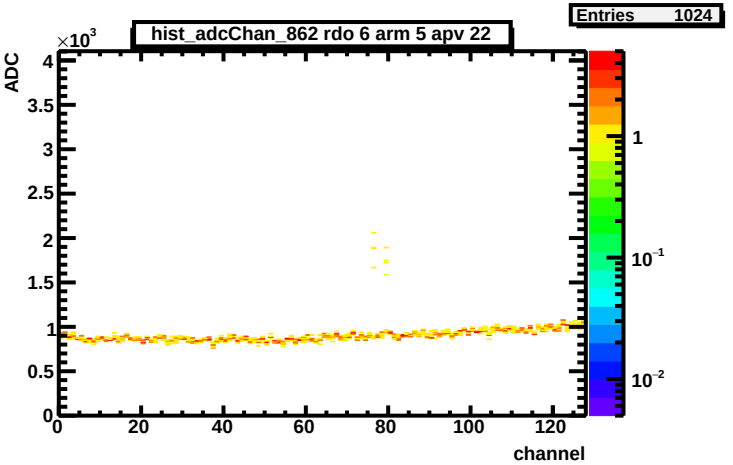

hist_adcChan_857 rdo 6 arm 5 apv 17
Entries 1024
ADC
×10³
channel

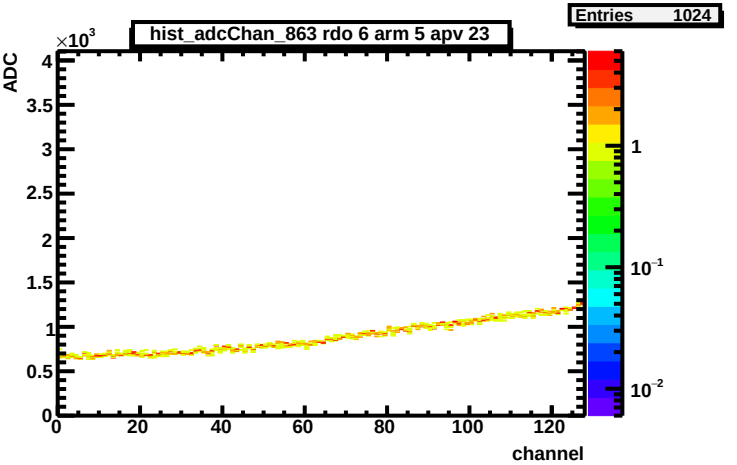

hist_adcChan_858 rdo 6 arm 5 apv 18
Entries 1024
ADC
×10^3
channel


hist_adcChan_859 rdo 6 arm 5 apv 19
Entries 1024
ADC
×10^3
channel


hist_adcChan_860 rdo 6 arm 5 apv 20
Entries 1024
ADC
×10^3
channel


hist_adcChan_861 rdo 6 arm 5 apv 21
Entries 1024
ADC
×10^3
channel


hist_adcChan_862 rdo 6 arm 5 apv 22
Entries 1024
ADC
×10^3
channel


hist_adcChan_863 rdo 6 arm 5 apv 23
Entries 1024
ADC
×10^3
channel