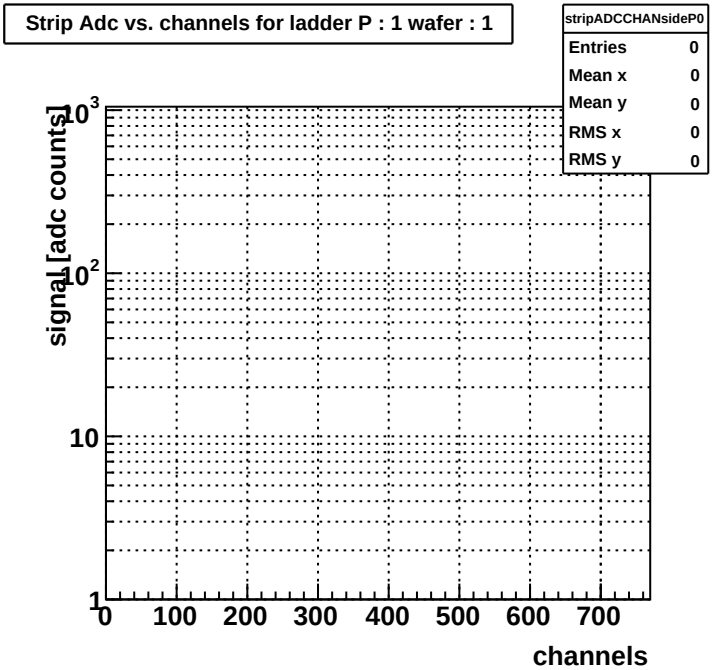
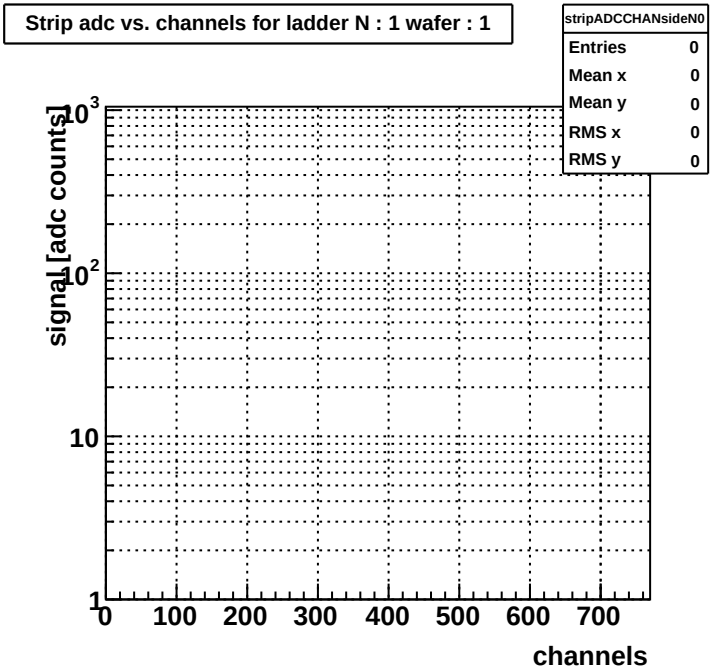
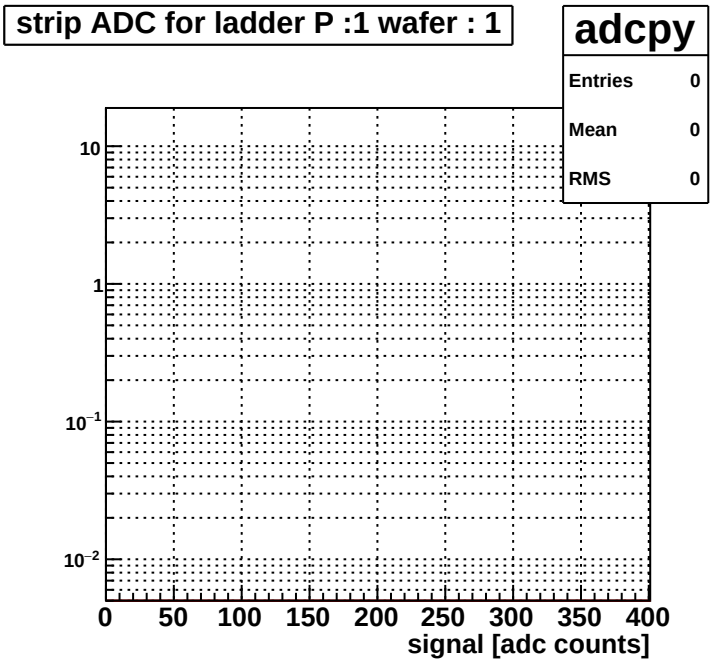
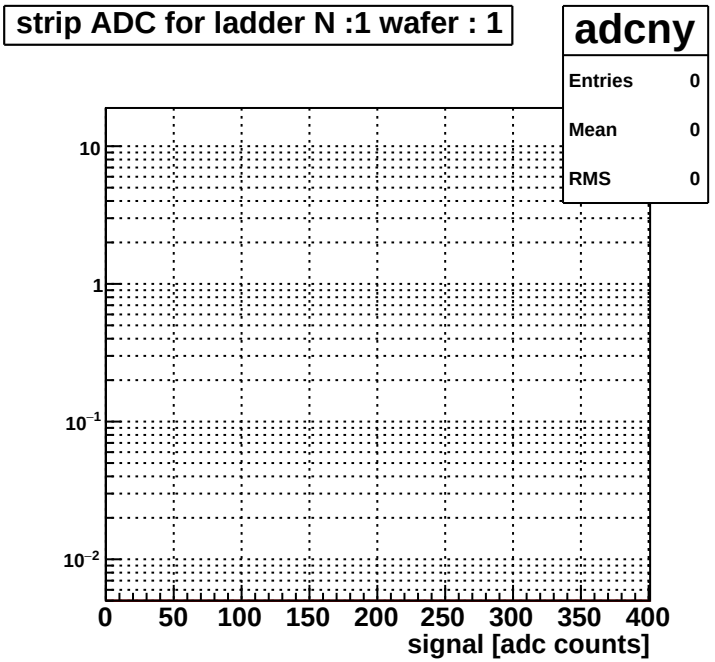
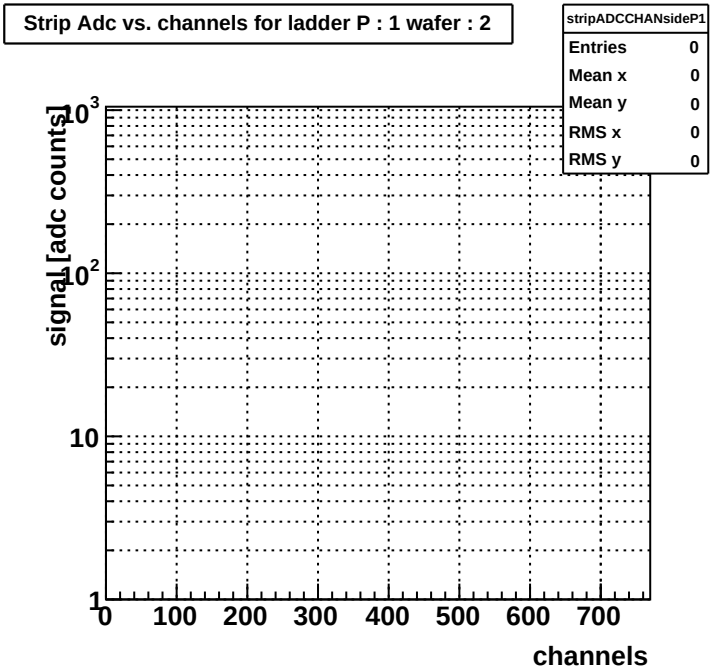
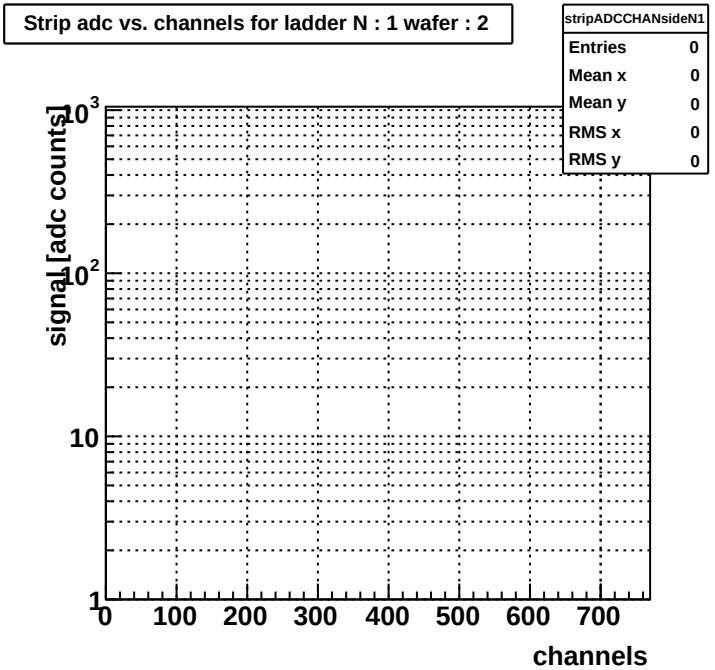

Strip Adc vs. channels for ladder P : 1 wafer : 1
stripADCCHANsideP0
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

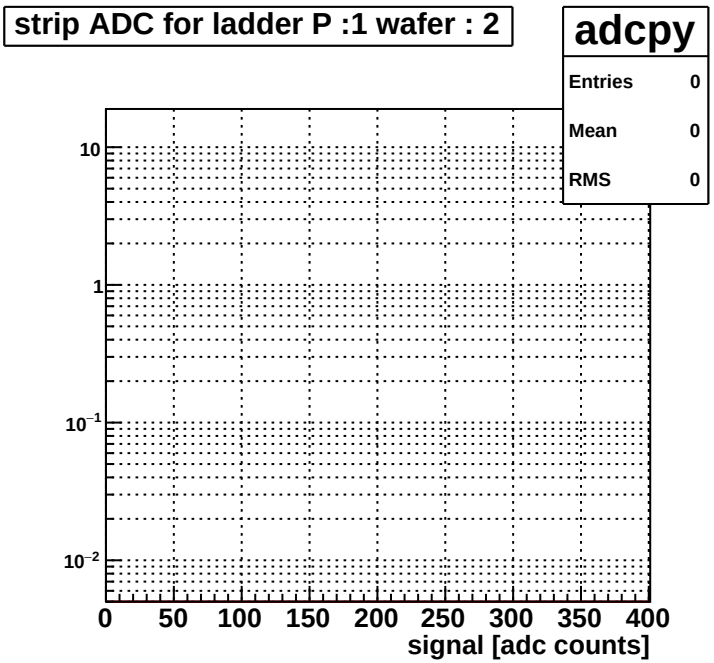

Strip adc vs. channels for ladder N : 1 wafer : 1
stripADCCHANsideN0
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

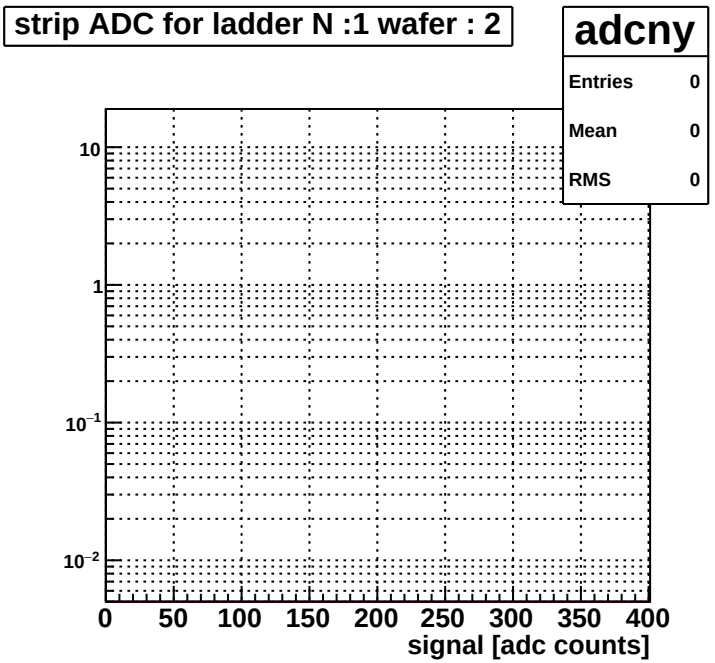

strip ADC for ladder P :1 wafer : 1
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

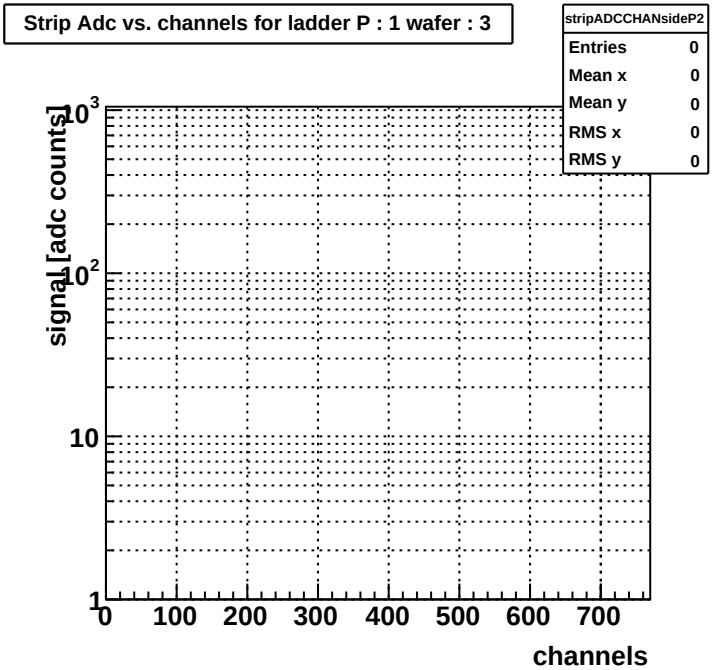

strip ADC for ladder N :1 wafer : 1
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

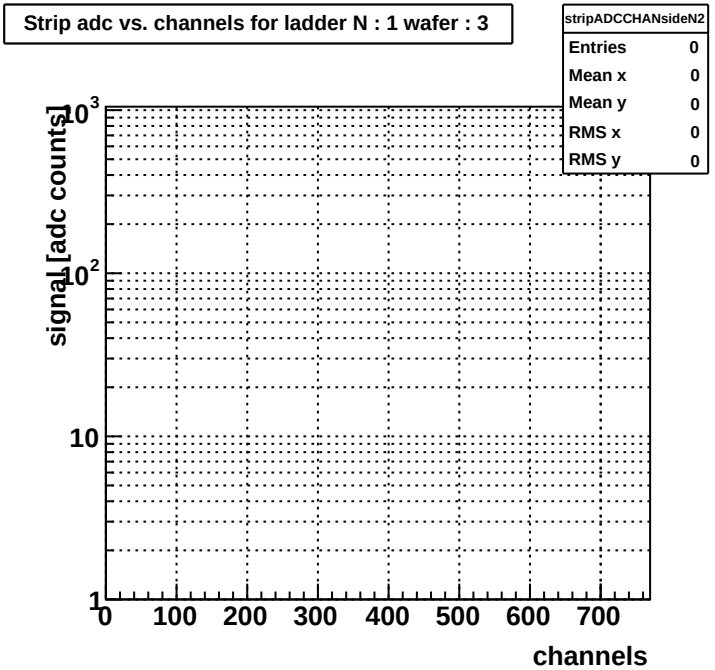

Strip Adc vs. channels for ladder P : 1 wafer : 2
stripADCCHANsideP1
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

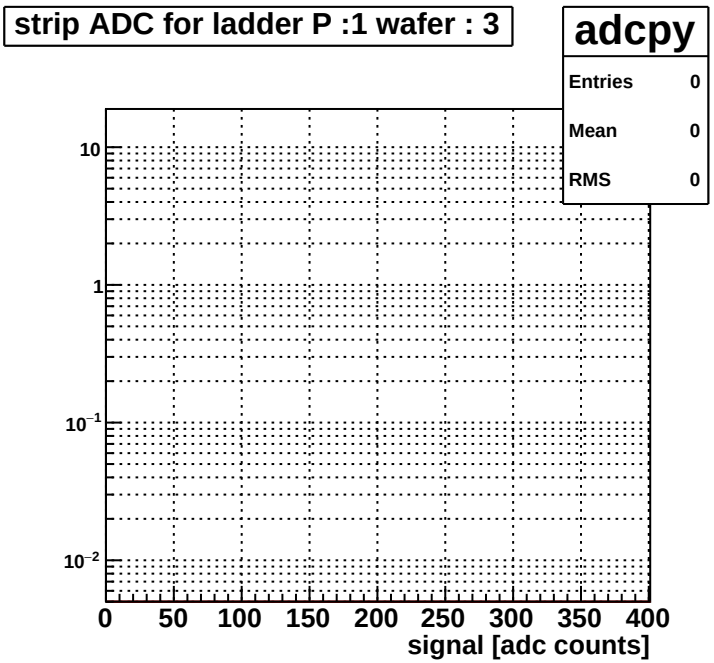

Strip adc vs. channels for ladder N : 1 wafer : 2
stripADCCHANsideN1
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

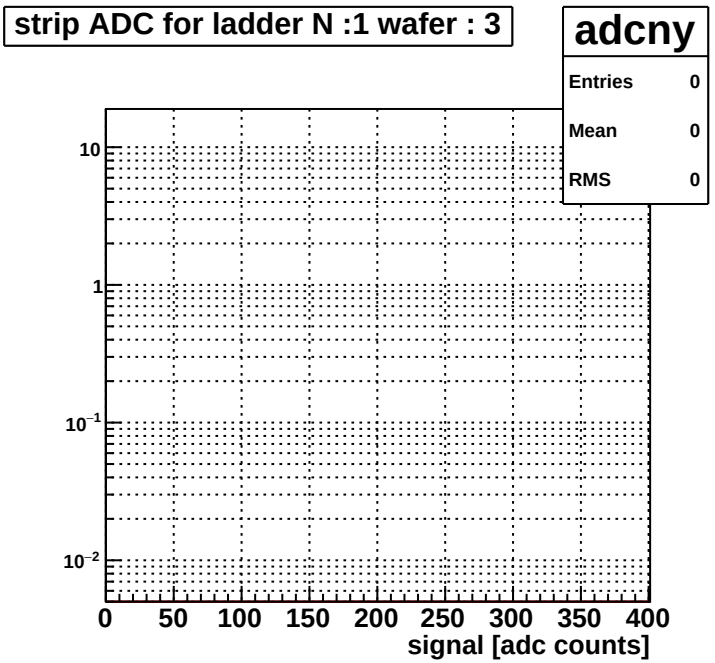

strip ADC for ladder P :1 wafer : 2
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

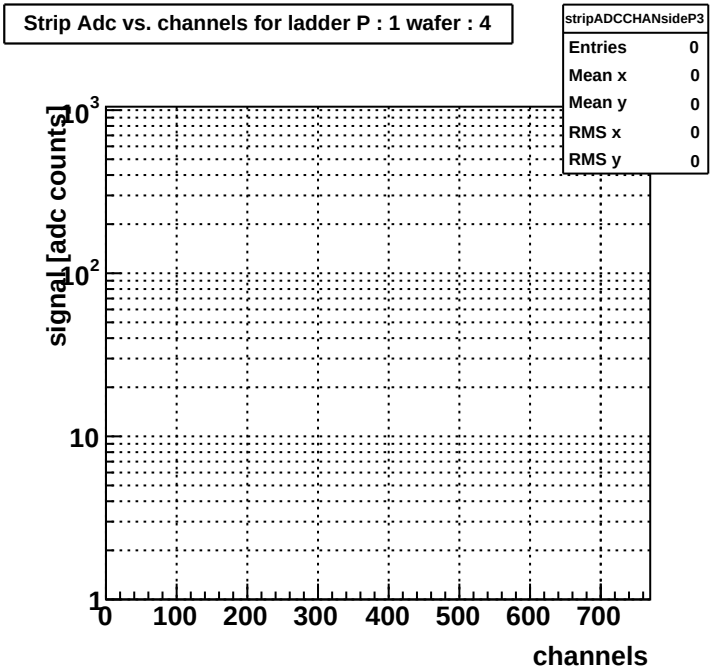

strip ADC for ladder N :1 wafer : 2
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

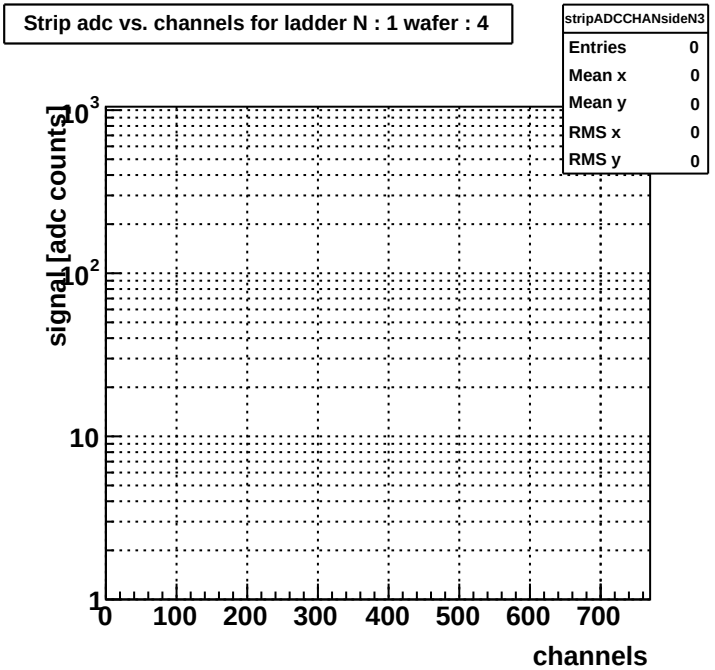

Strip Adc vs. channels for ladder P : 1 wafer : 3
stripADCCHANsideP2
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

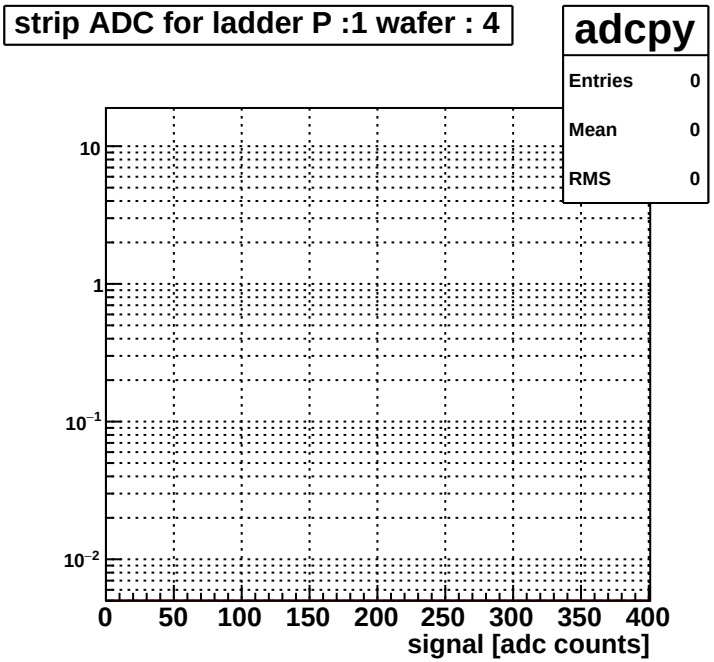

Strip adc vs. channels for ladder N : 1 wafer : 3
stripADCCHANsideN2
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

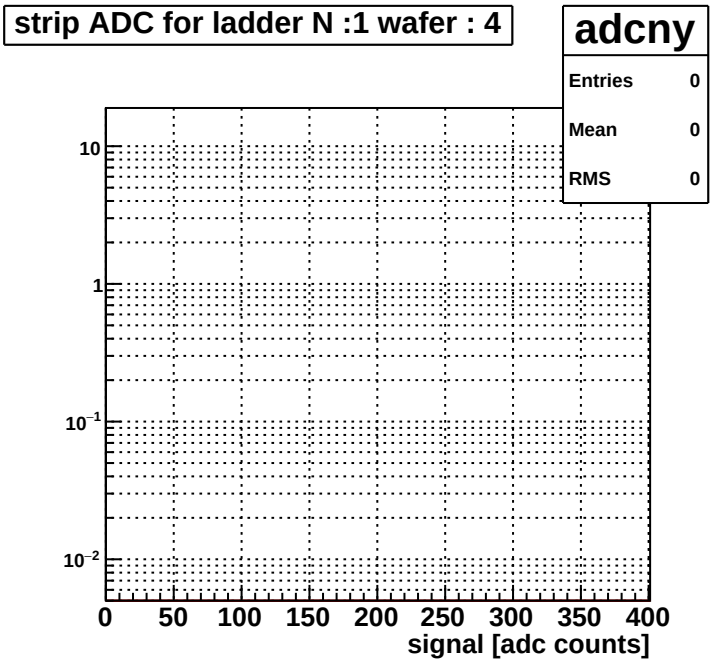

strip ADC for ladder P :1 wafer : 3
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

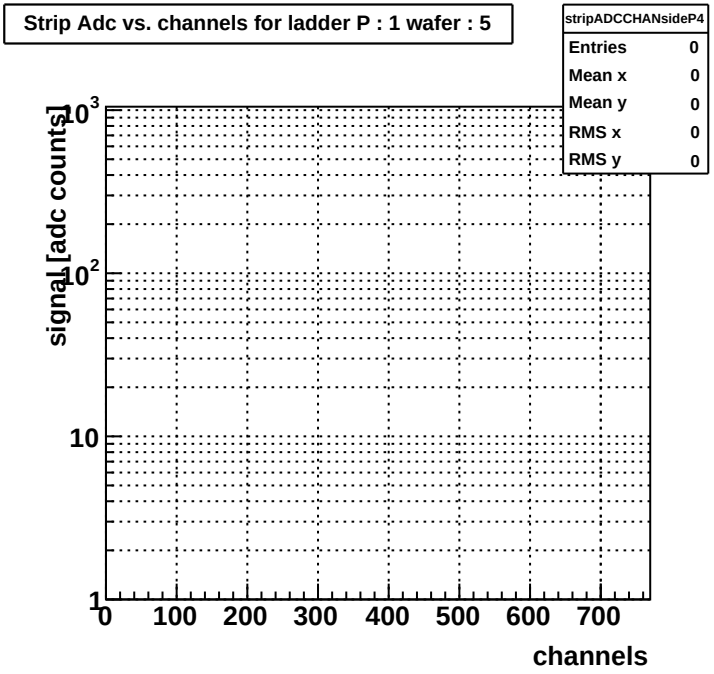

strip ADC for ladder N :1 wafer : 3
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

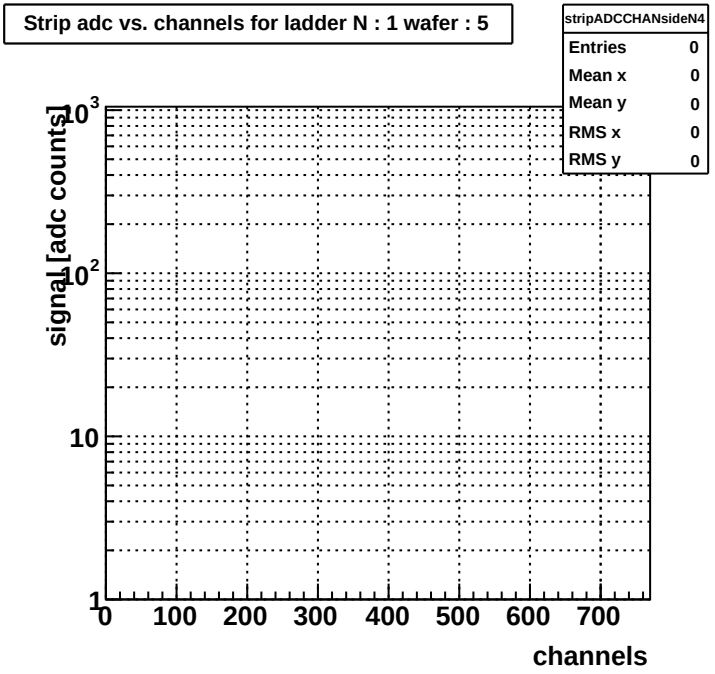

Strip Adc vs. channels for ladder P : 1 wafer : 4
stripADCCHANsideP3
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

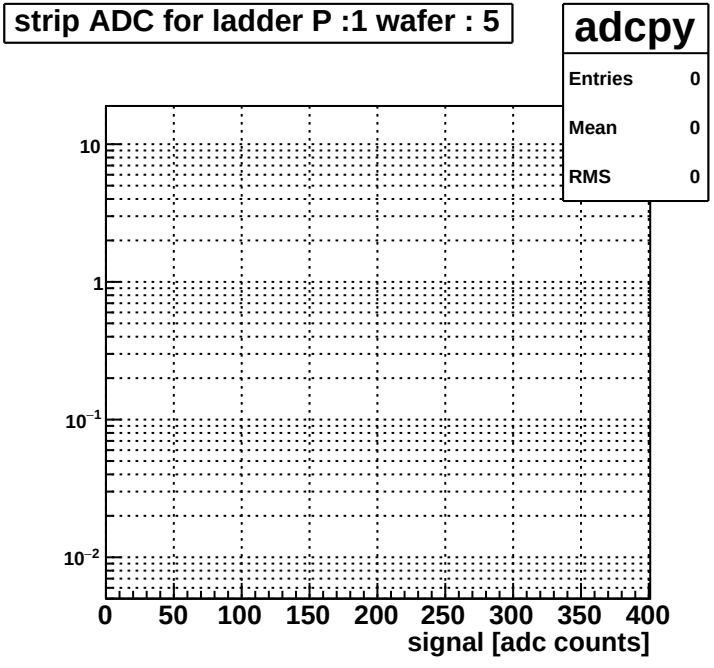

Strip adc vs. channels for ladder N : 1 wafer : 4
stripADCCHANsideN3
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

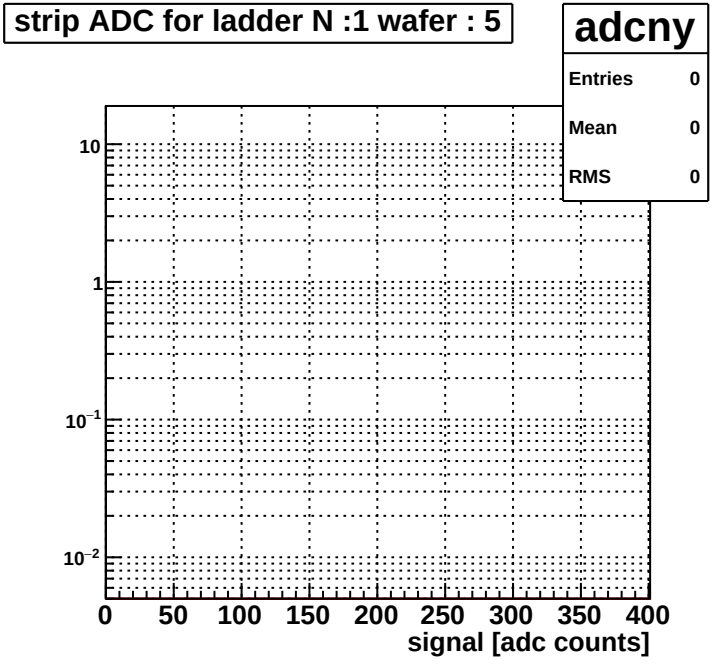

strip ADC for ladder P :1 wafer : 4
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

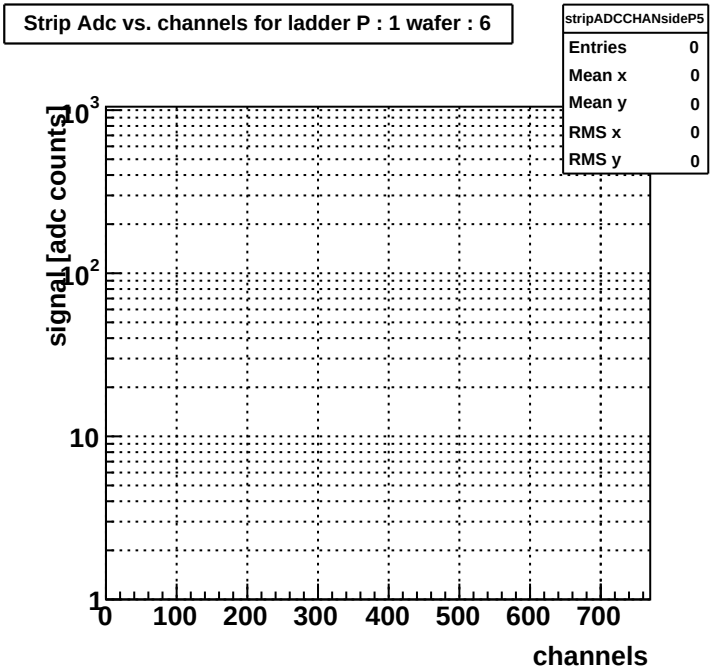

strip ADC for ladder N :1 wafer : 4
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

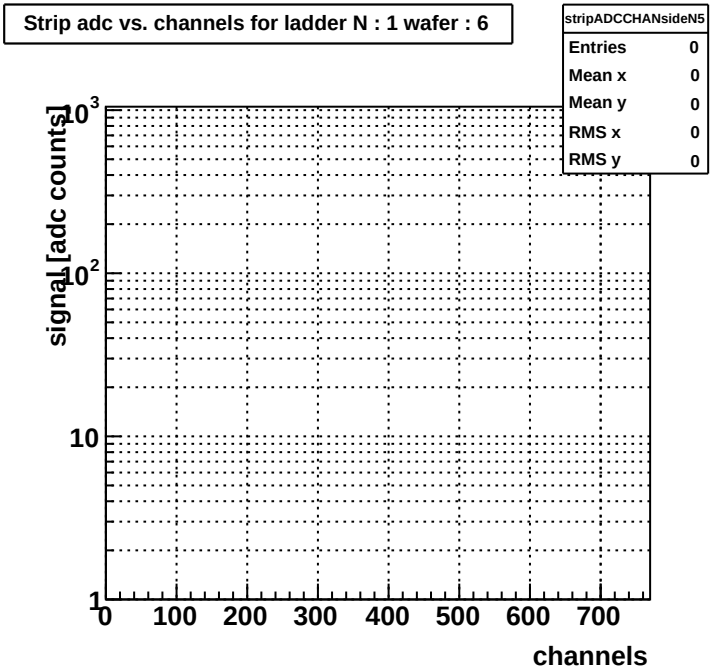

Strip Adc vs. channels for ladder P : 1 wafer : 5
stripADCCHANsideP4
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

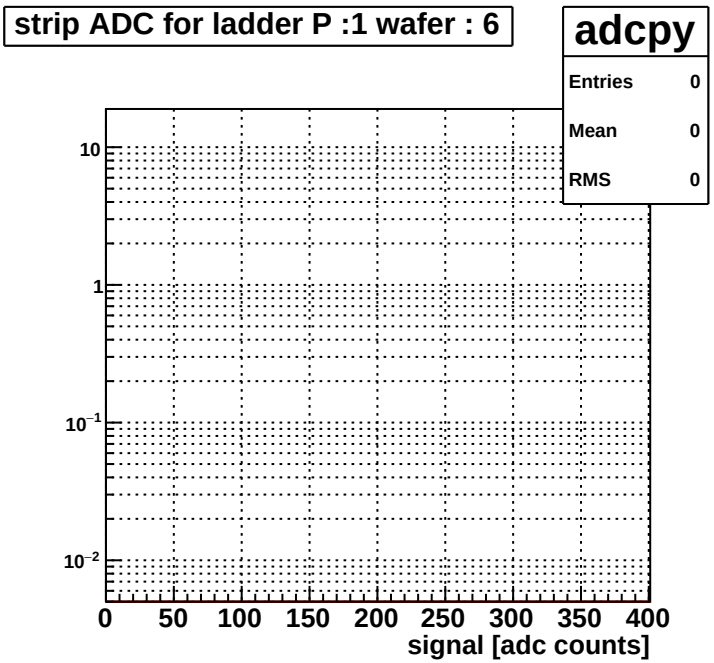

Strip adc vs. channels for ladder N : 1 wafer : 5
stripADCCHANsideN4
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

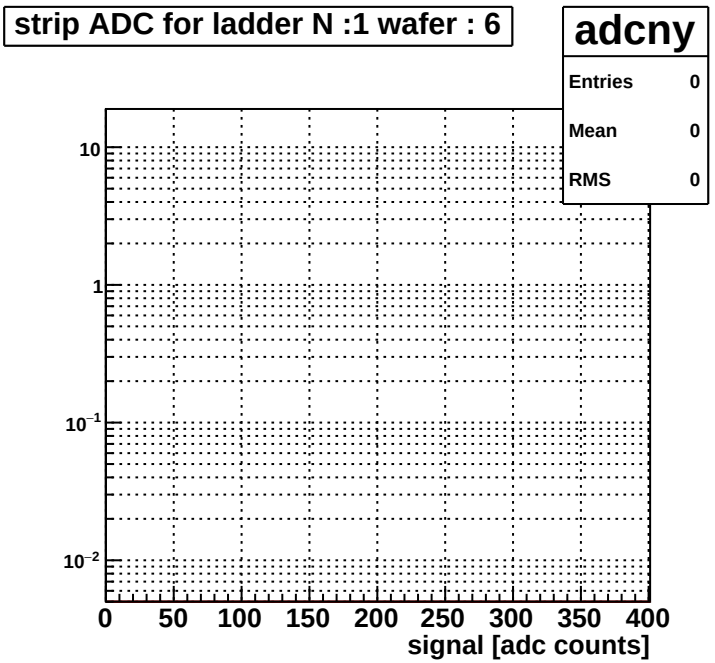

strip ADC for ladder P :1 wafer : 5
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

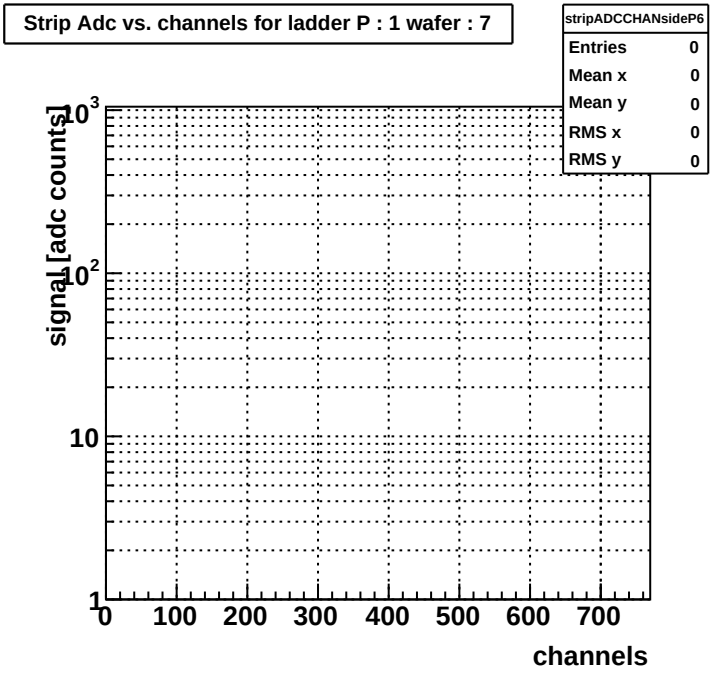

strip ADC for ladder N :1 wafer : 5
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

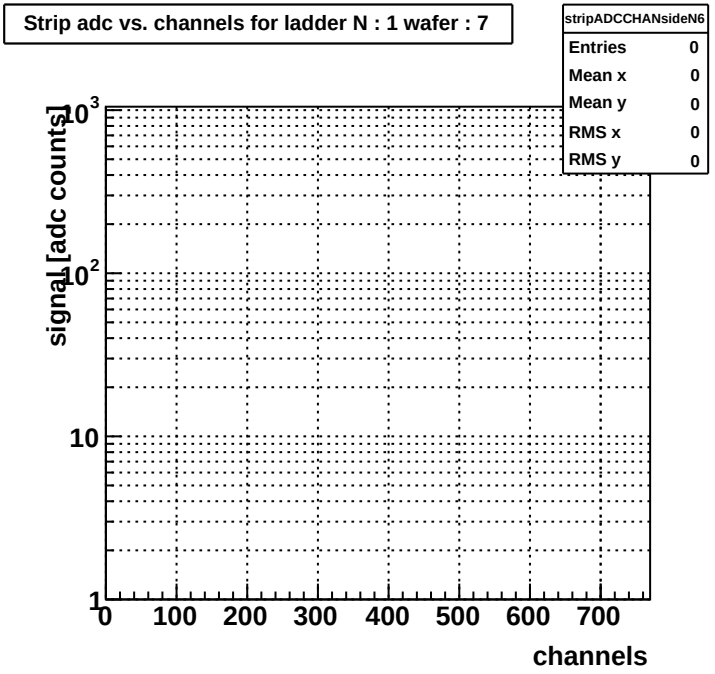

Strip Adc vs. channels for ladder P : 1 wafer : 6
stripADCCHANsideP5
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

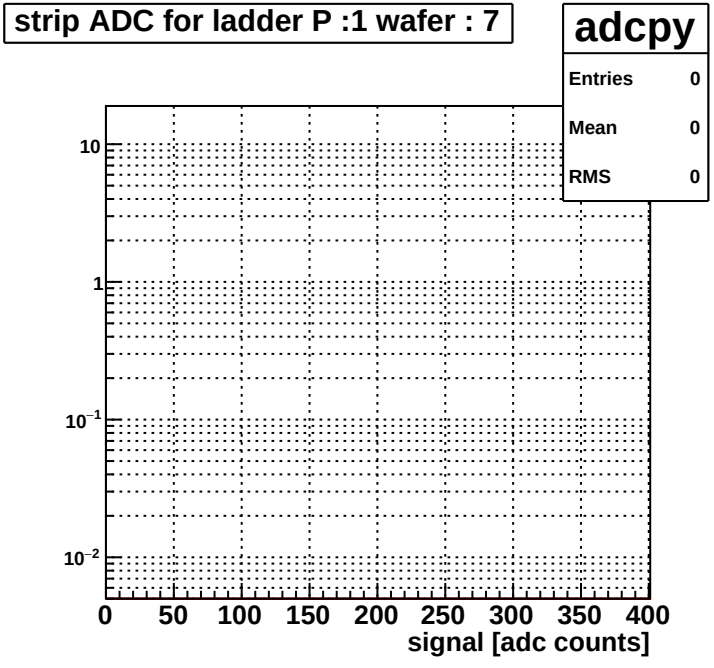

Strip adc vs. channels for ladder N : 1 wafer : 6
stripADCCHANsideN5
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

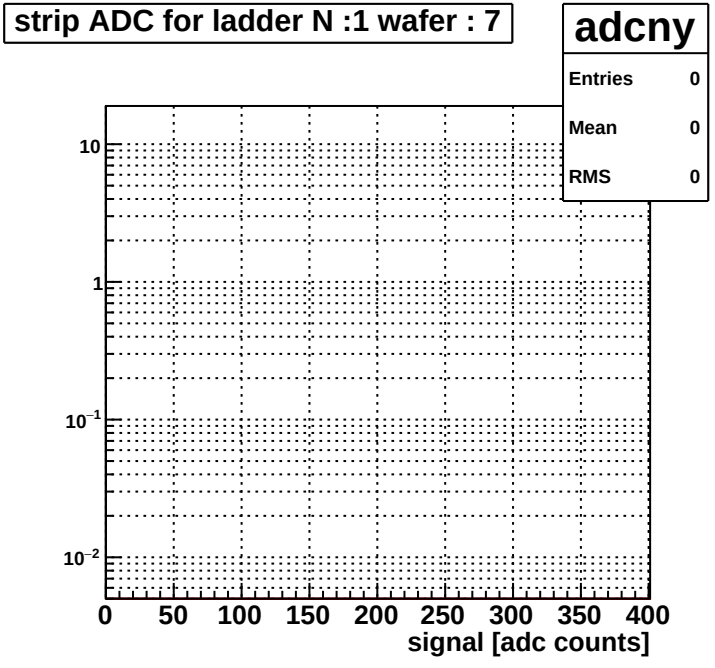

strip ADC for ladder P :1 wafer : 6
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

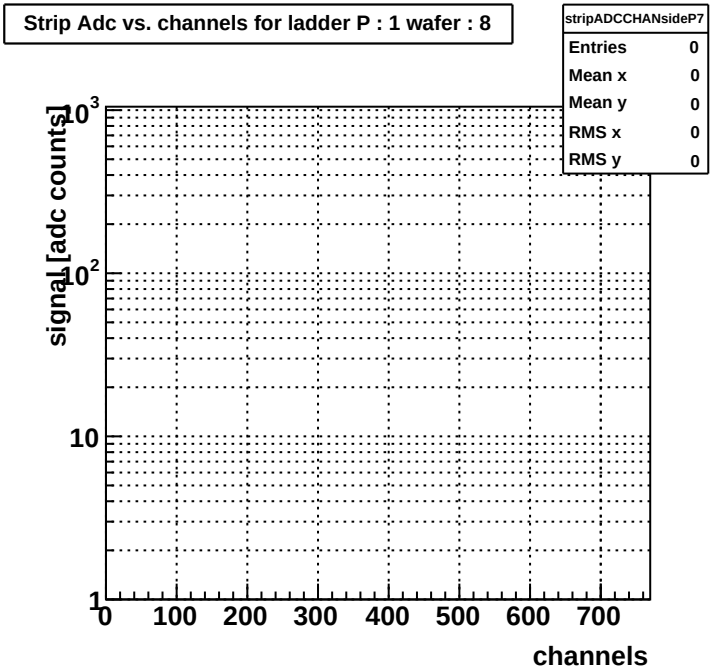

strip ADC for ladder N :1 wafer : 6
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

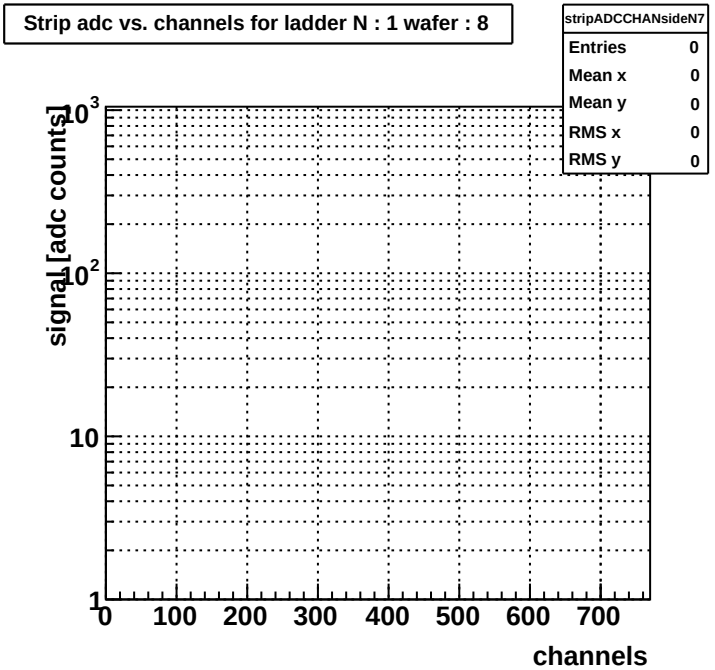

Strip Adc vs. channels for ladder P : 1 wafer : 7
stripADCCHANsideP6
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

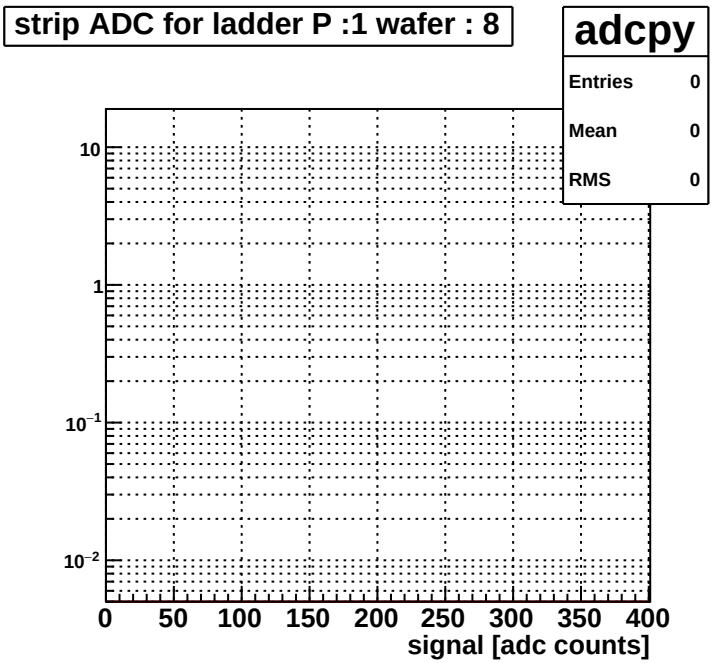

Strip adc vs. channels for ladder N : 1 wafer : 7
stripADCCHANsideN6
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

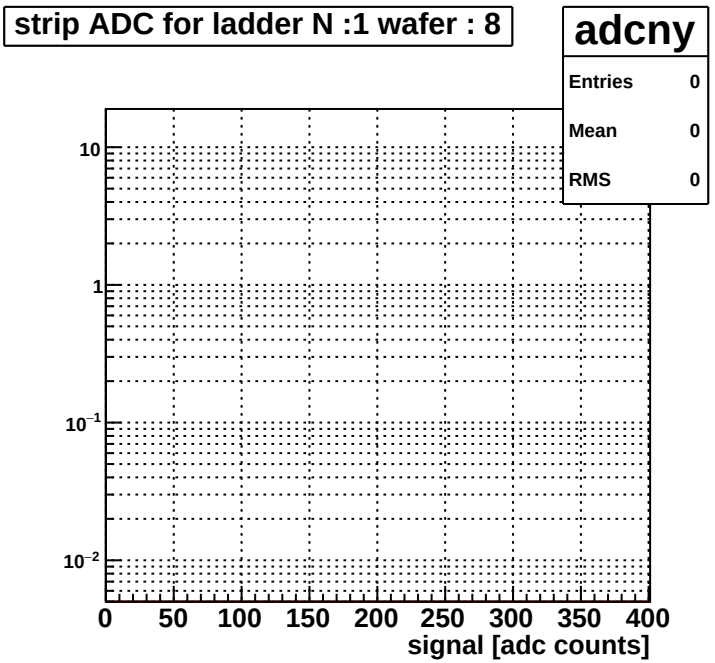

strip ADC for ladder P :1 wafer : 7
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

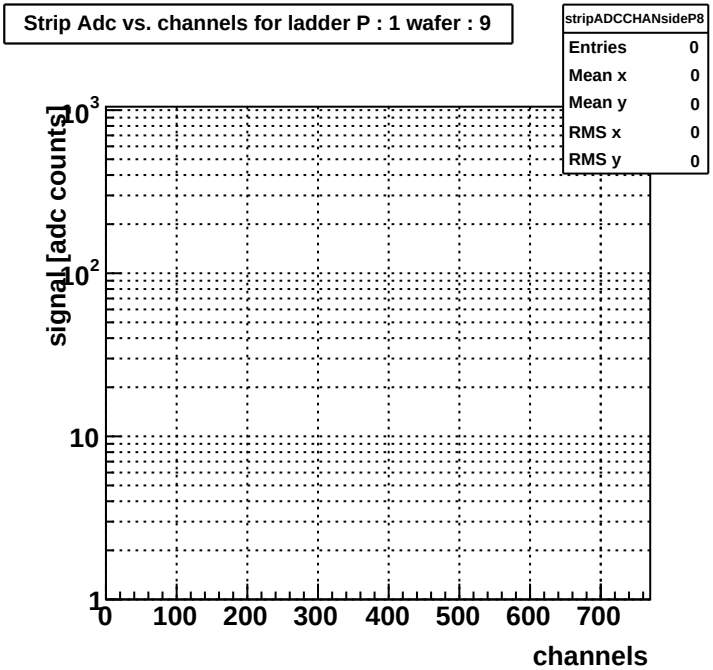

strip ADC for ladder N :1 wafer : 7
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

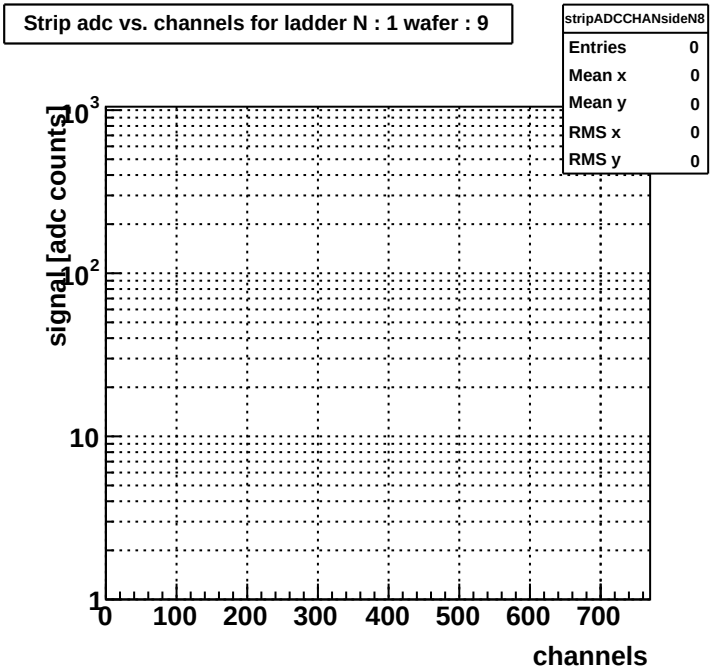

Strip Adc vs. channels for ladder P : 1 wafer : 8
stripADCCHANsideP7
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

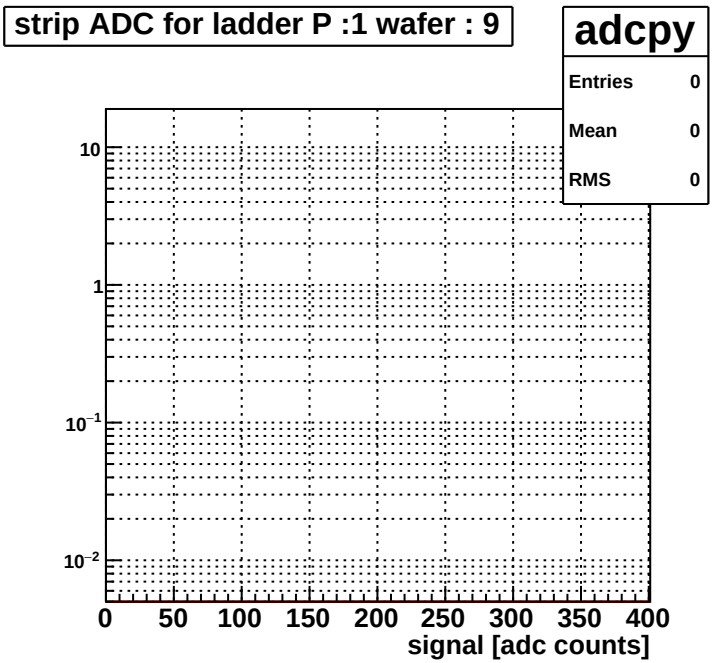

Strip adc vs. channels for ladder N : 1 wafer : 8
stripADCCHANsideN7
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

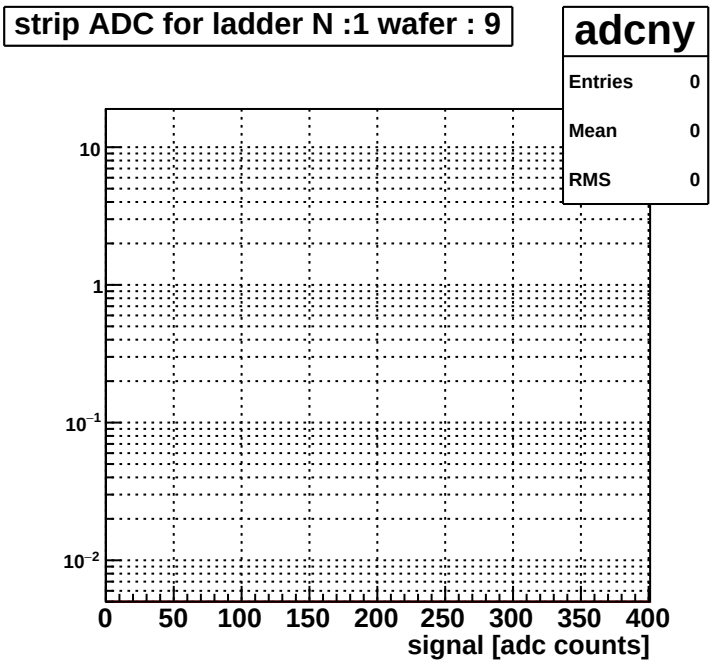

strip ADC for ladder P :1 wafer : 8
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

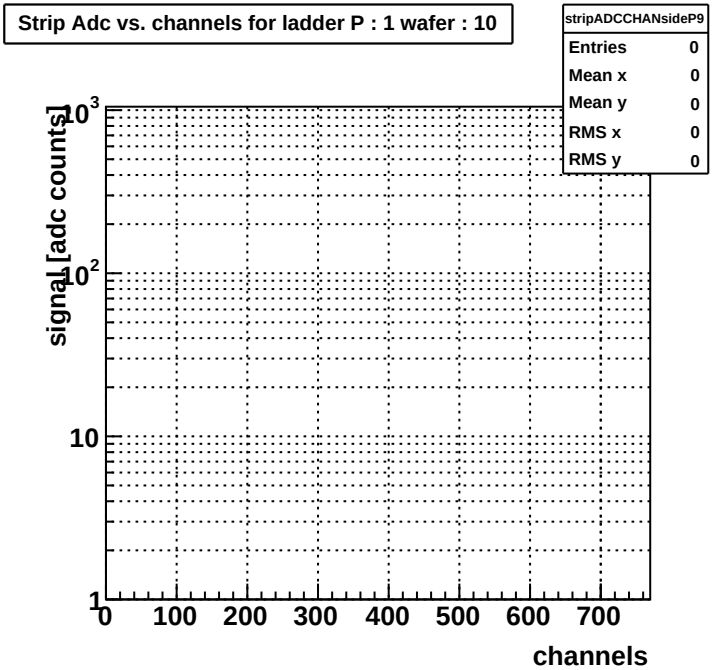

strip ADC for ladder N :1 wafer : 8
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

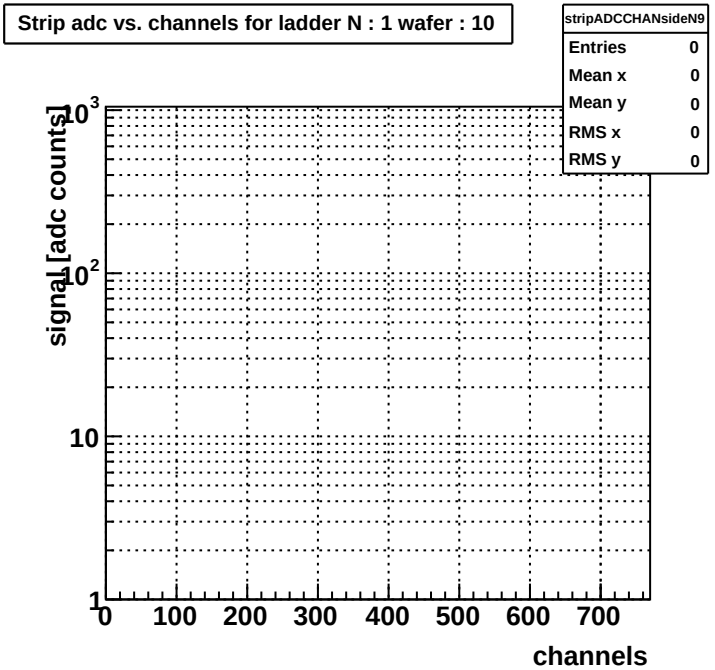

Strip Adc vs. channels for ladder P : 1 wafer : 9
stripADCCHANsideP8
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

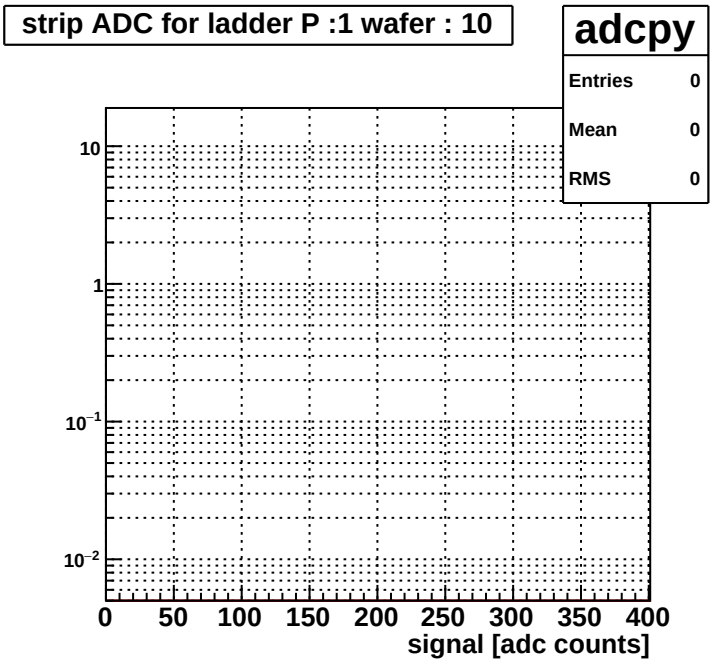

Strip adc vs. channels for ladder N : 1 wafer : 9
stripADCCHANsideN8
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

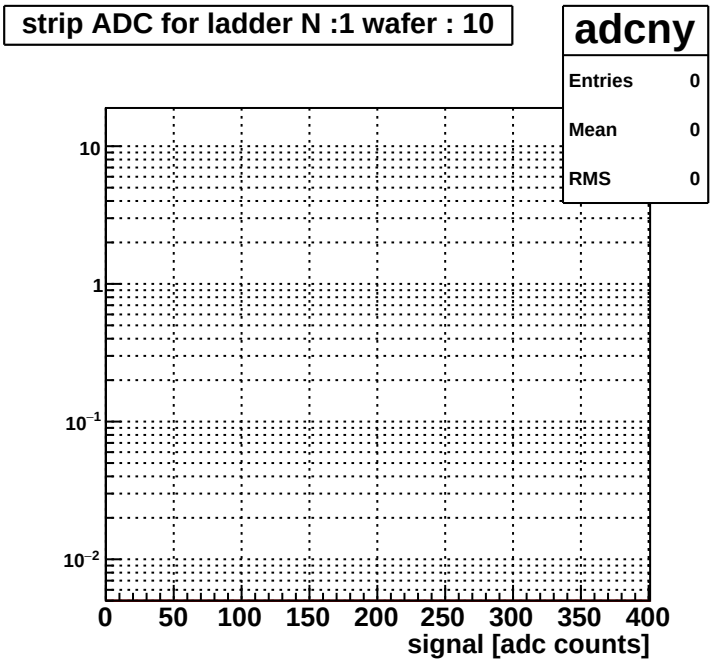

strip ADC for ladder P :1 wafer : 9
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

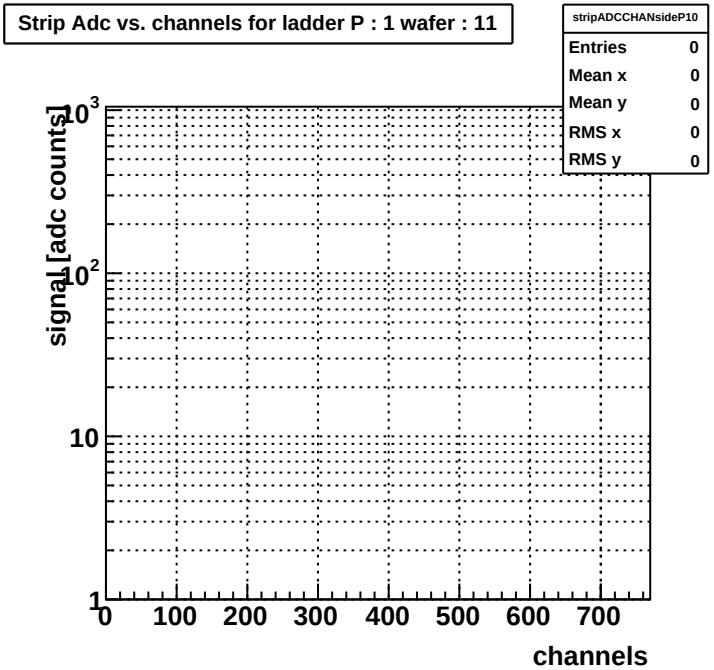

strip ADC for ladder N :1 wafer : 9
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

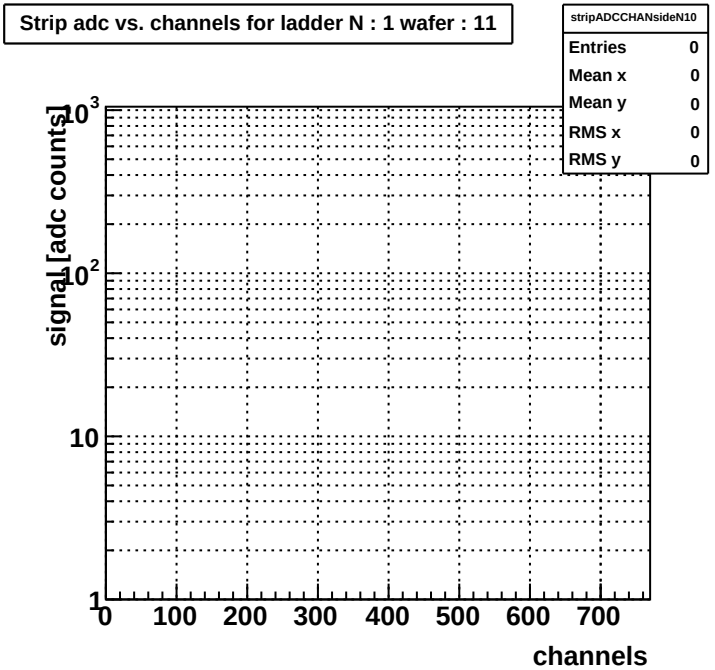

Strip Adc vs. channels for ladder P : 1 wafer : 10
stripADCCHANsideP9
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

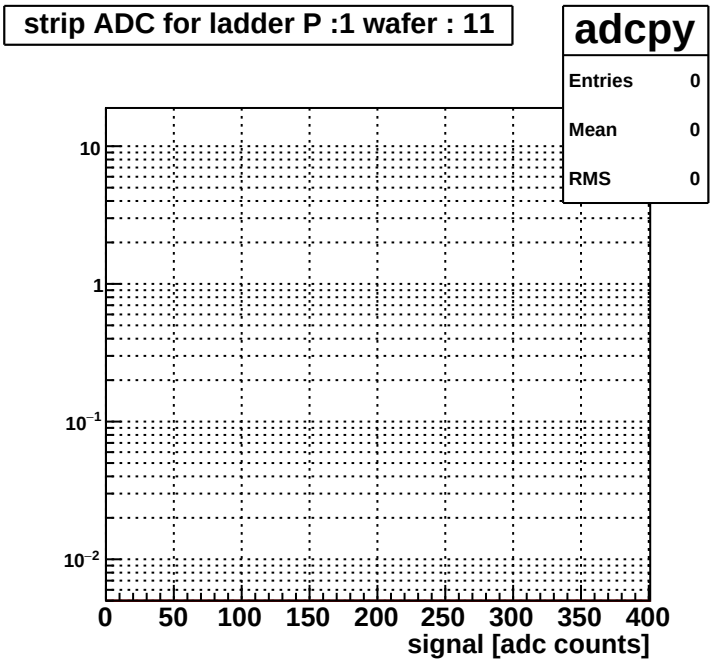

Strip adc vs. channels for ladder N : 1 wafer : 10
stripADCCHANsideN9
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

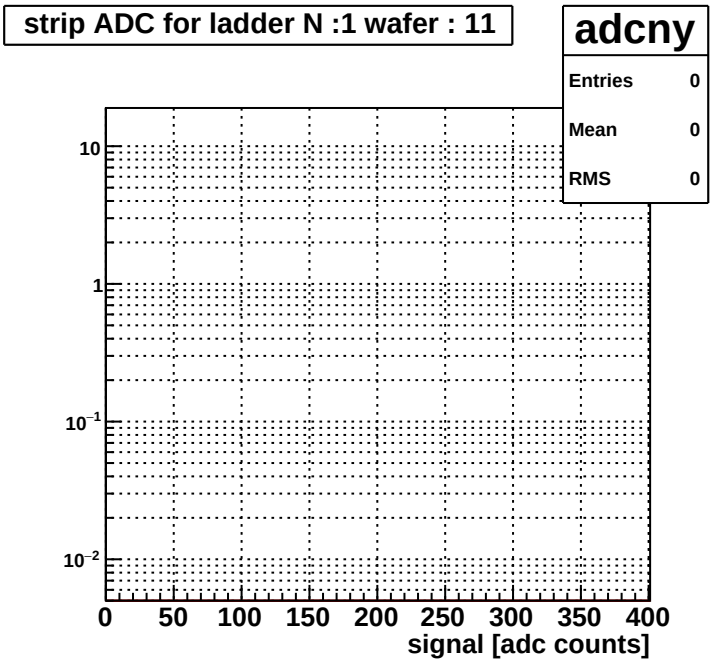

strip ADC for ladder P :1 wafer : 10
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

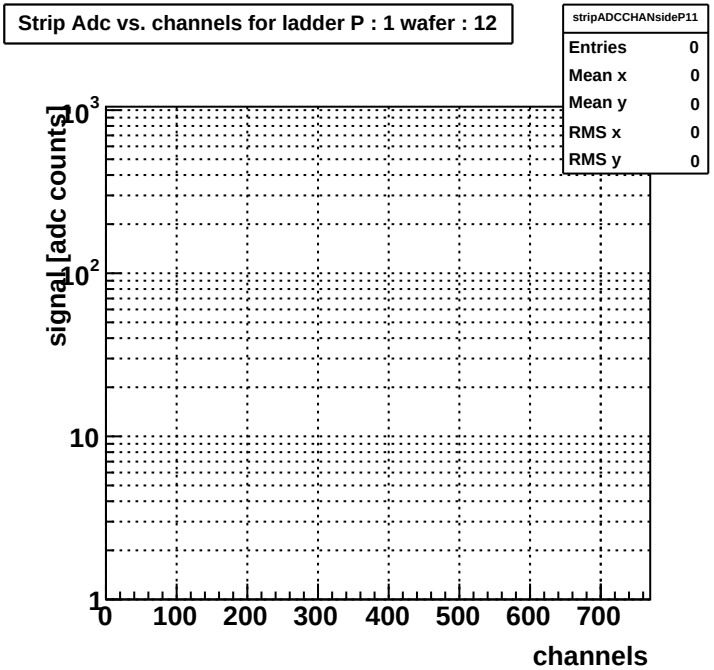

strip ADC for ladder N :1 wafer : 10
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

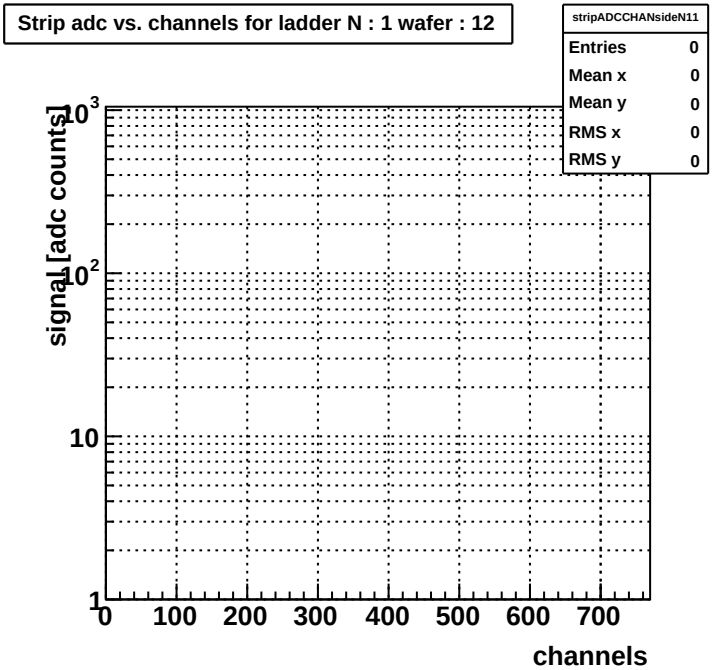

Strip Adc vs. channels for ladder P : 1 wafer : 11
stripADCCHANsideP10
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

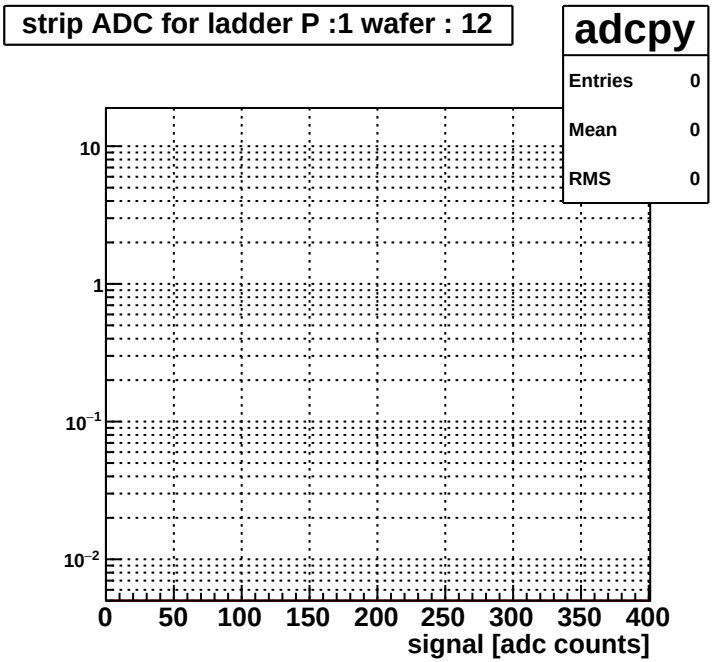

Strip adc vs. channels for ladder N : 1 wafer : 11
stripADCCHANsideN10
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

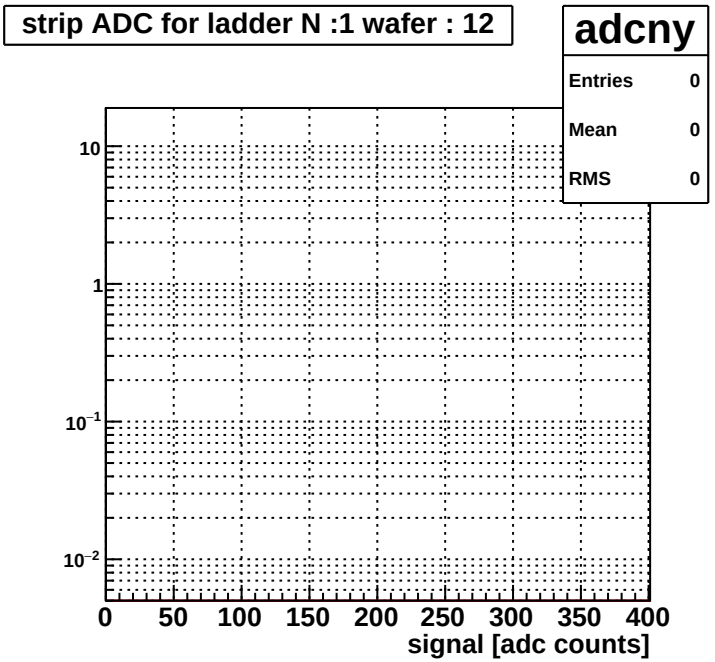

strip ADC for ladder P :1 wafer : 11
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

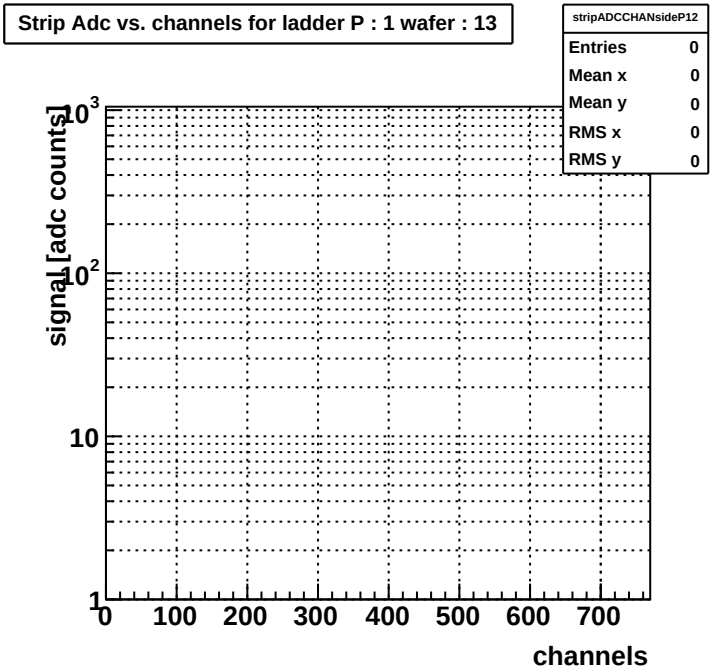

strip ADC for ladder N :1 wafer : 11
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

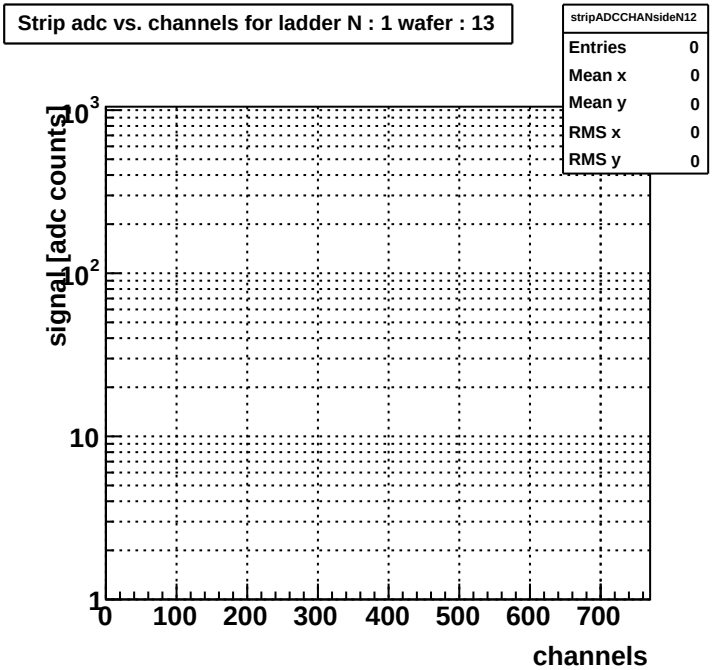

Strip Adc vs. channels for ladder P : 1 wafer : 12
stripADCCHANsideP11
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

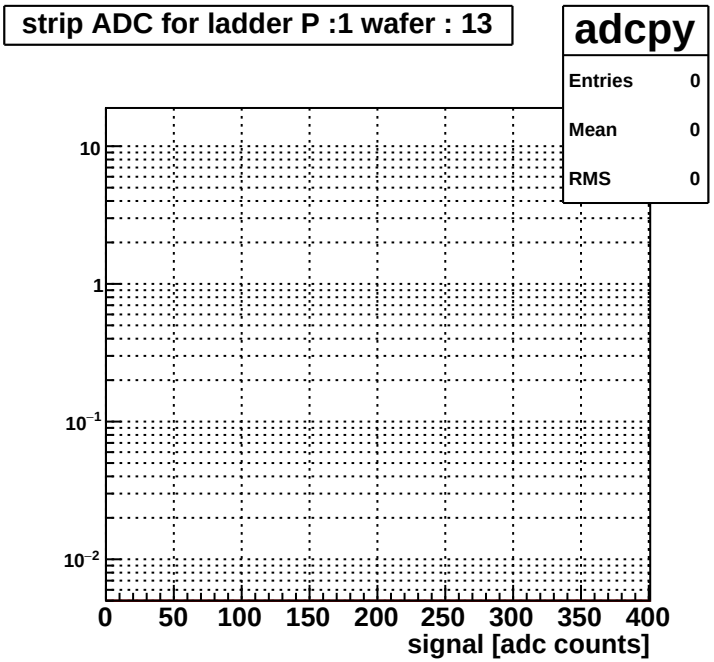

Strip adc vs. channels for ladder N : 1 wafer : 12
stripADCCHANsideN11
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

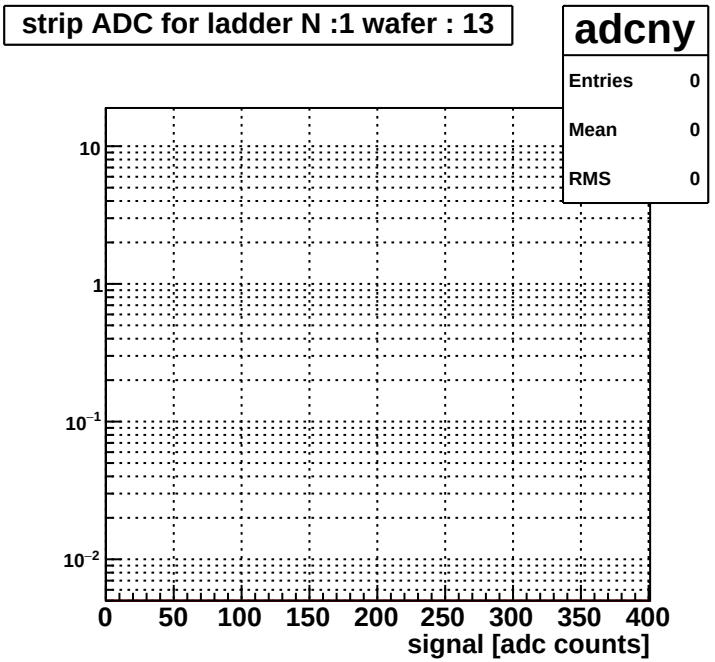

strip ADC for ladder P :1 wafer : 12
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

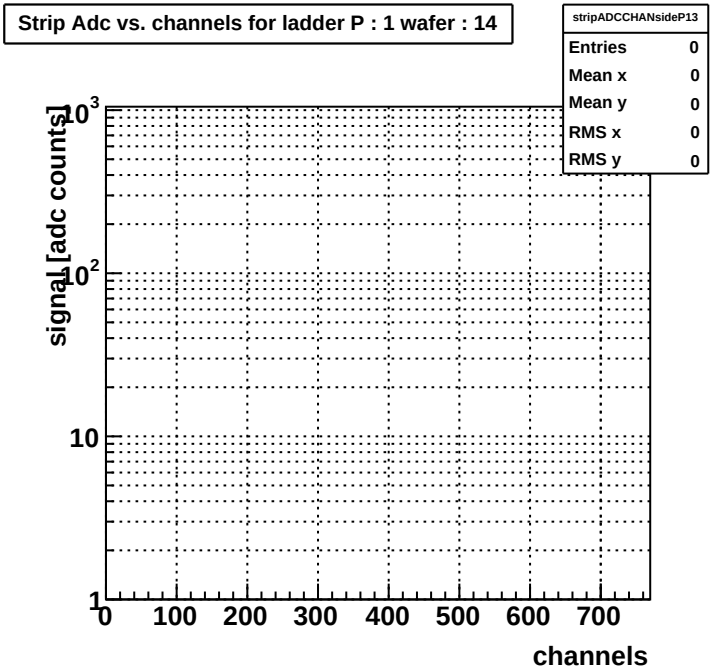

strip ADC for ladder N :1 wafer : 12
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

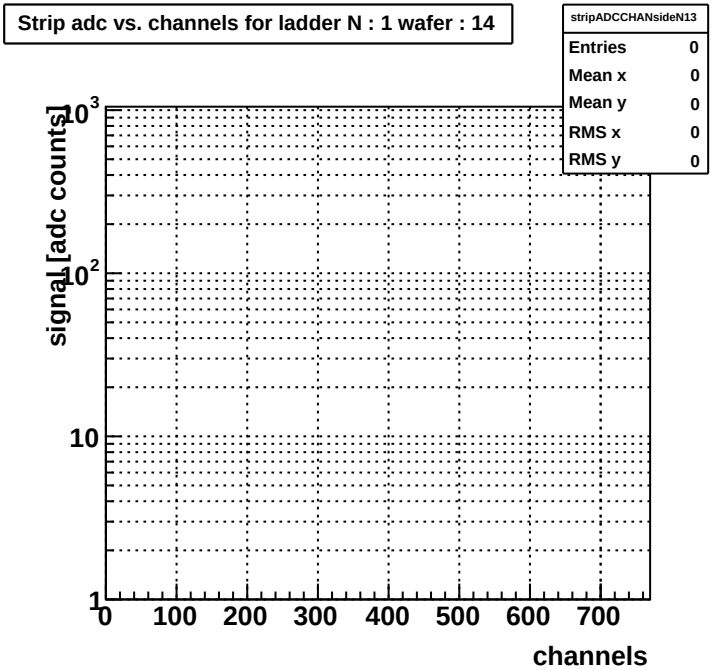

Strip Adc vs. channels for ladder P : 1 wafer : 13
stripADCCHANsideP12
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

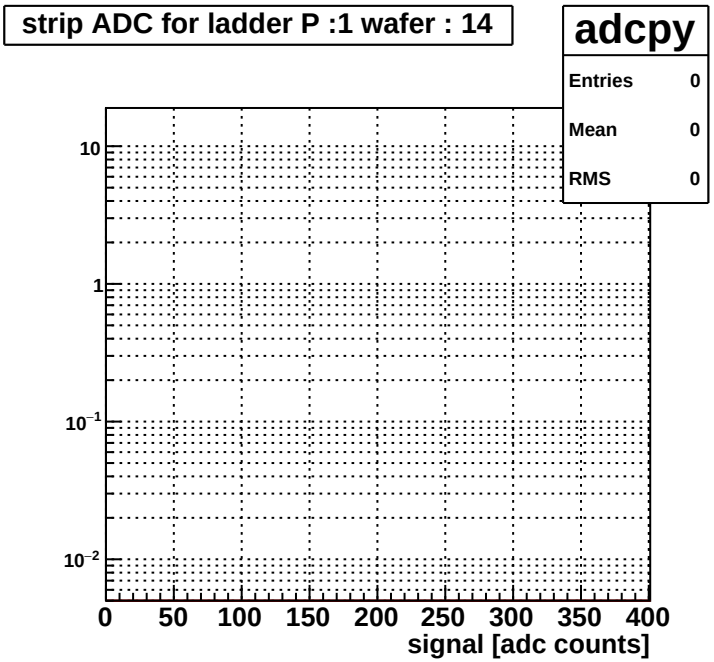

Strip adc vs. channels for ladder N : 1 wafer : 13
stripADCCHANsideN12
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

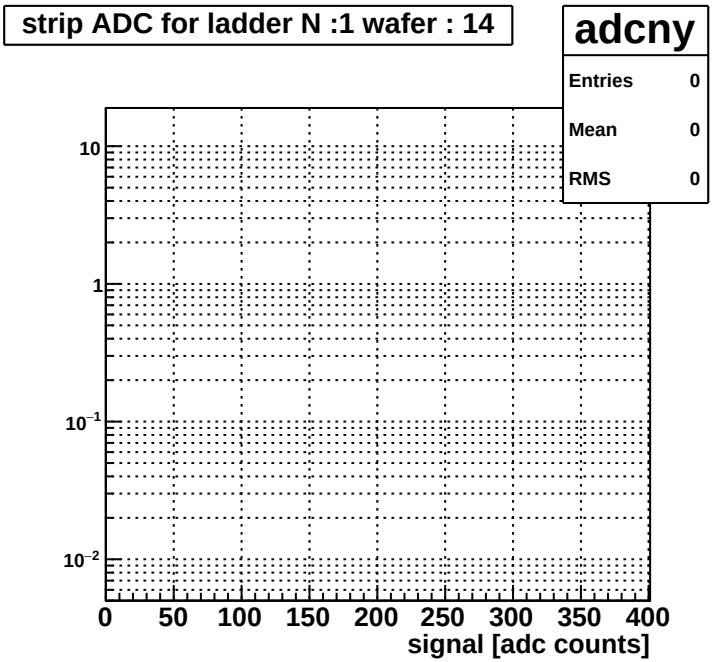

strip ADC for ladder P :1 wafer : 13
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

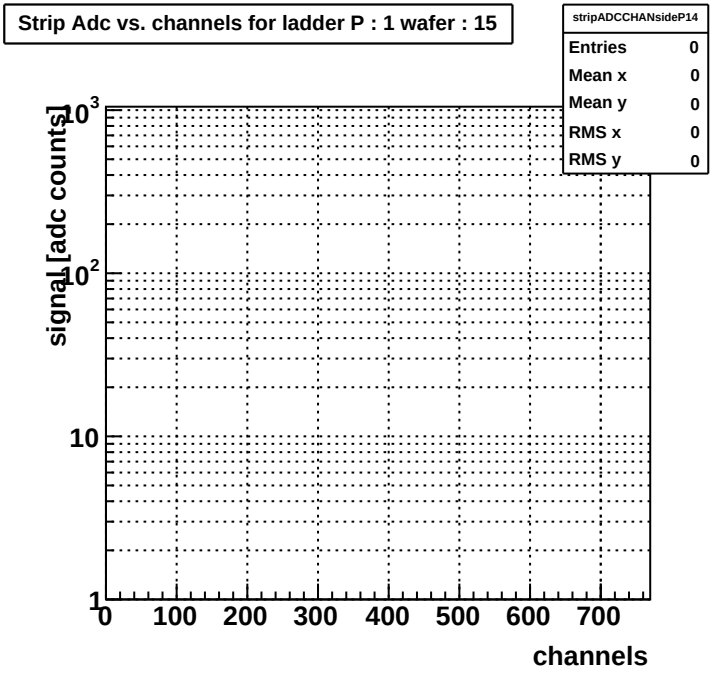

strip ADC for ladder N :1 wafer : 13
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

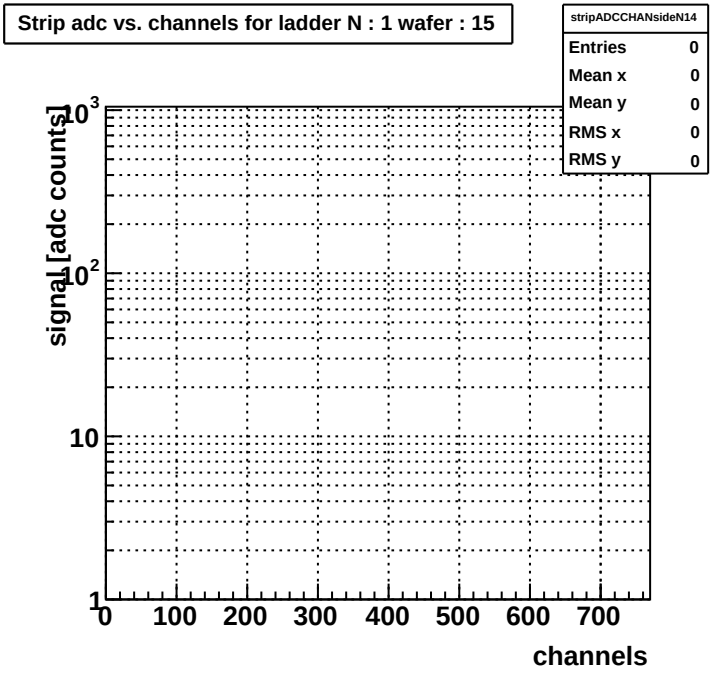

Strip Adc vs. channels for ladder P : 1 wafer : 14
stripADCCHANsideP13
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

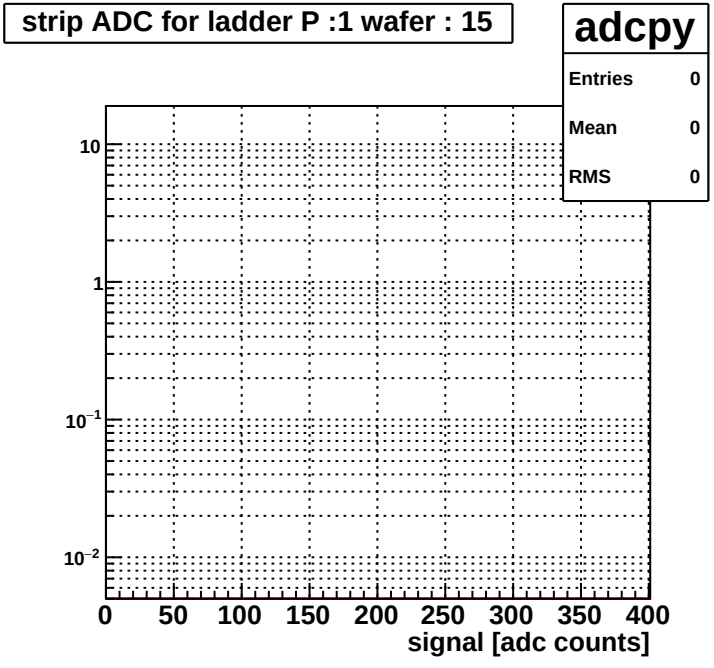

Strip adc vs. channels for ladder N : 1 wafer : 14
stripADCCHANsideN13
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

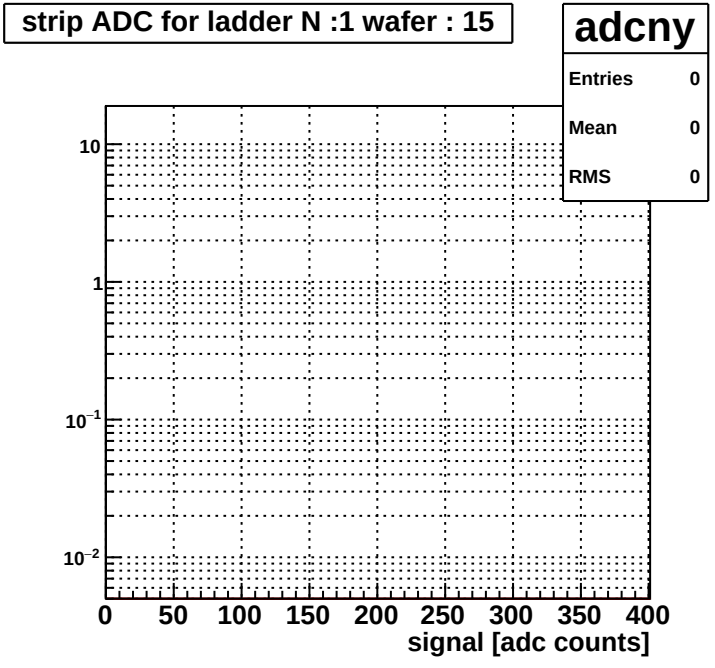

strip ADC for ladder P :1 wafer : 14
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

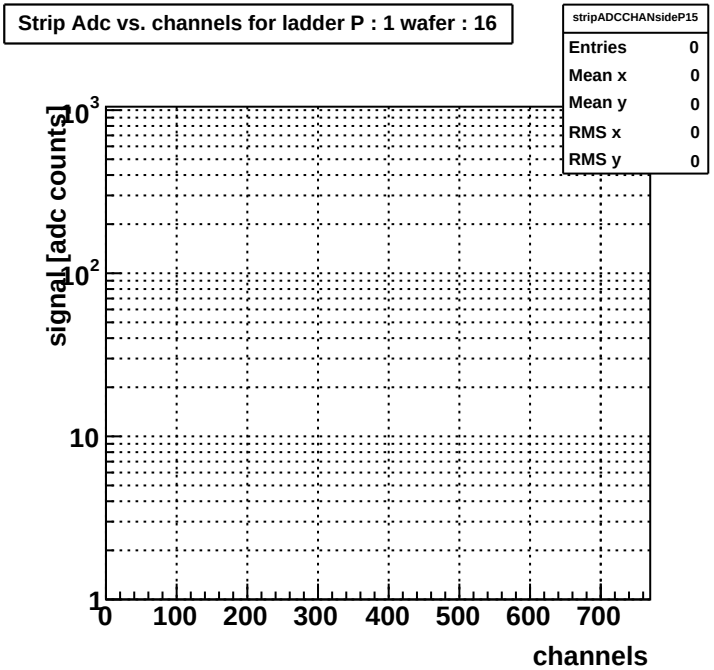

strip ADC for ladder N :1 wafer : 14
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

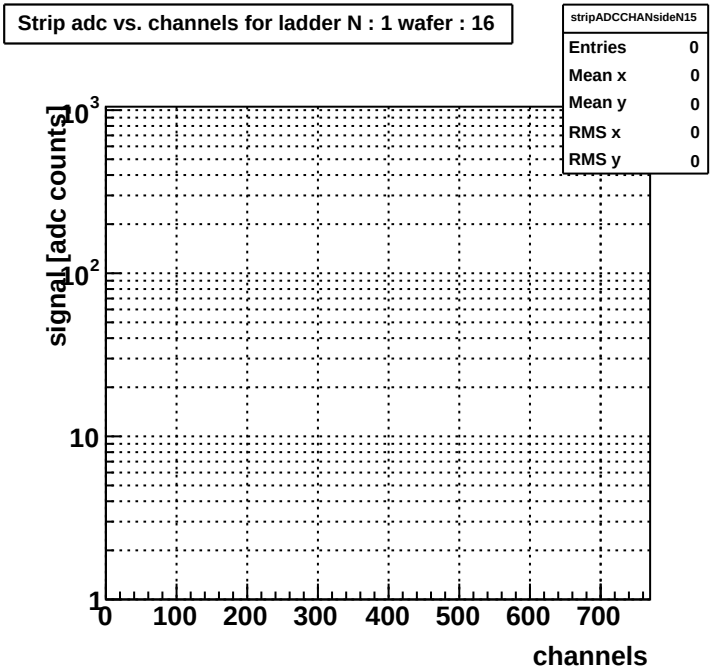

Strip Adc vs. channels for ladder P : 1 wafer : 15
stripADCCHANsideP14
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

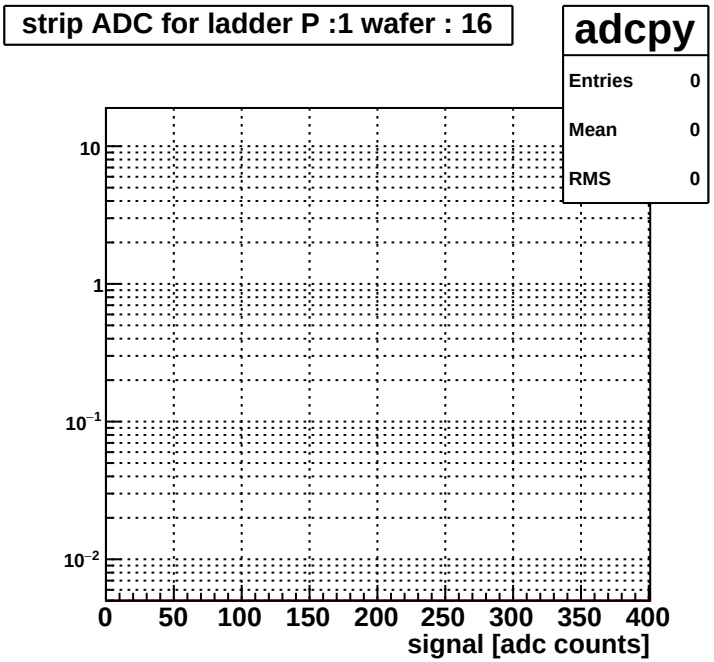

Strip adc vs. channels for ladder N : 1 wafer : 15
stripADCCHANsideN14
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

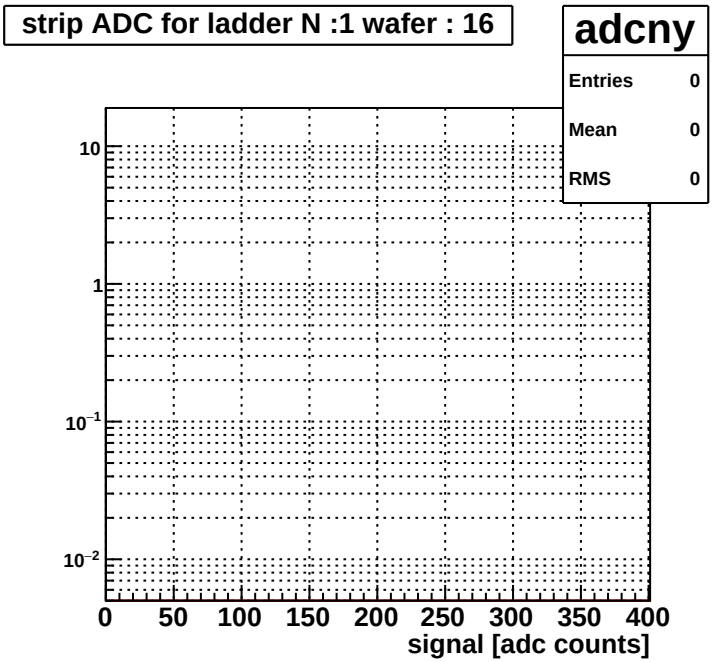

strip ADC for ladder P :1 wafer : 15
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

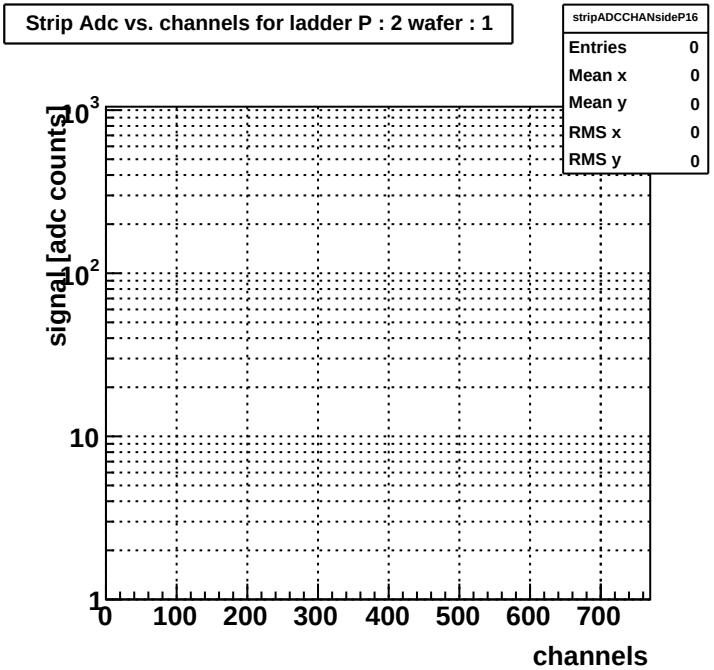

strip ADC for ladder N :1 wafer : 15
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

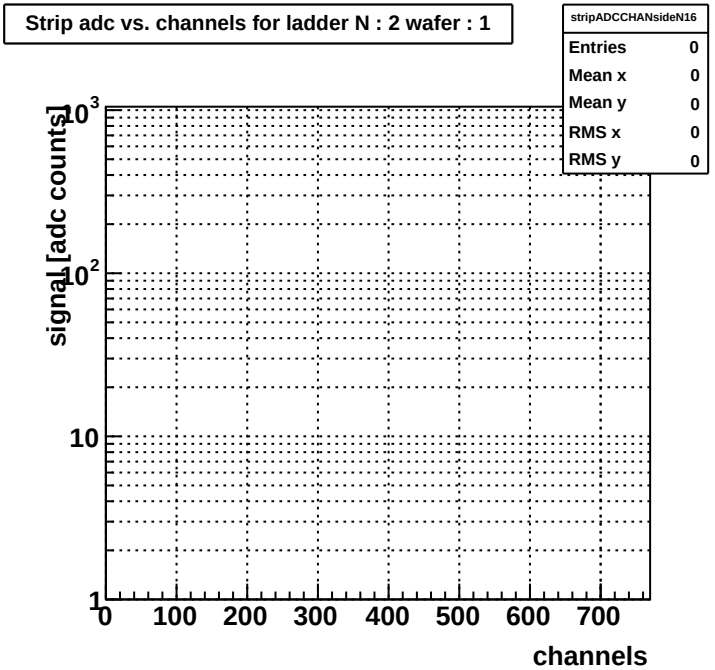

Strip Adc vs. channels for ladder P : 1 wafer : 16
stripADCCHANsideP15
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

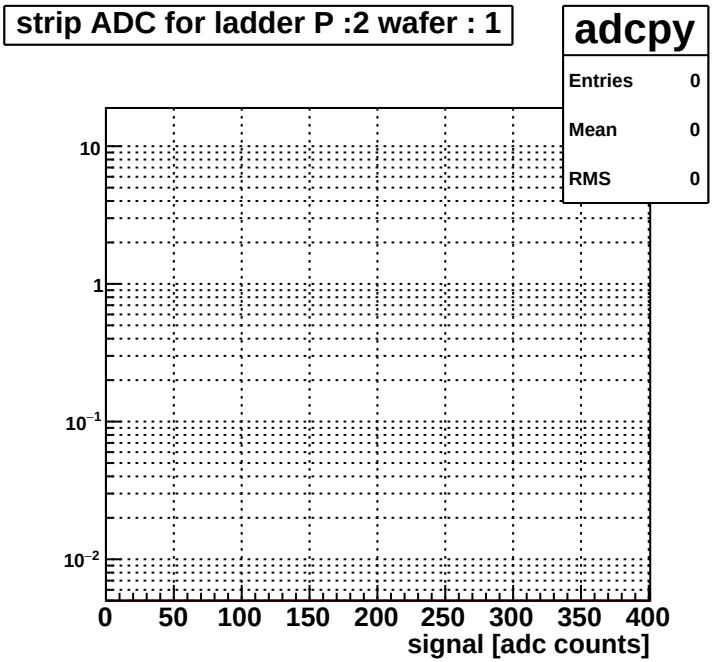

Strip adc vs. channels for ladder N : 1 wafer : 16
stripADCCHANsideN15
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

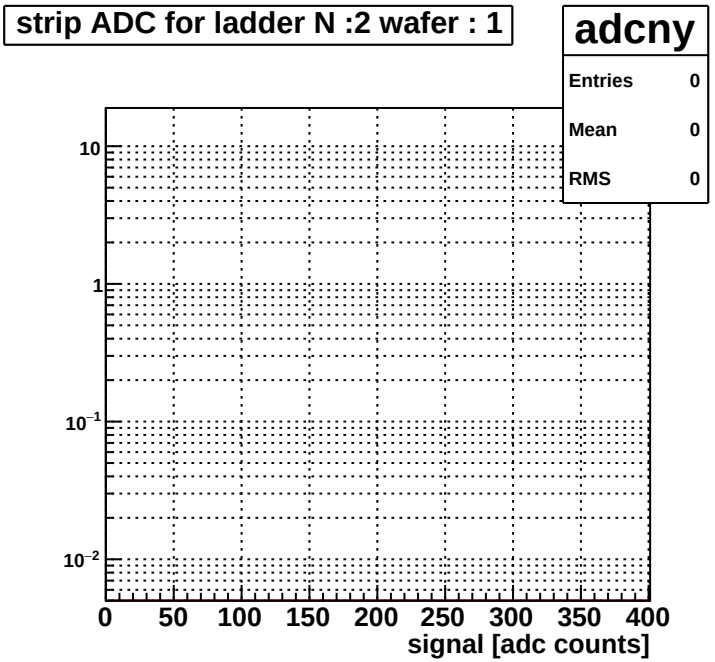

strip ADC for ladder P :1 wafer : 16
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

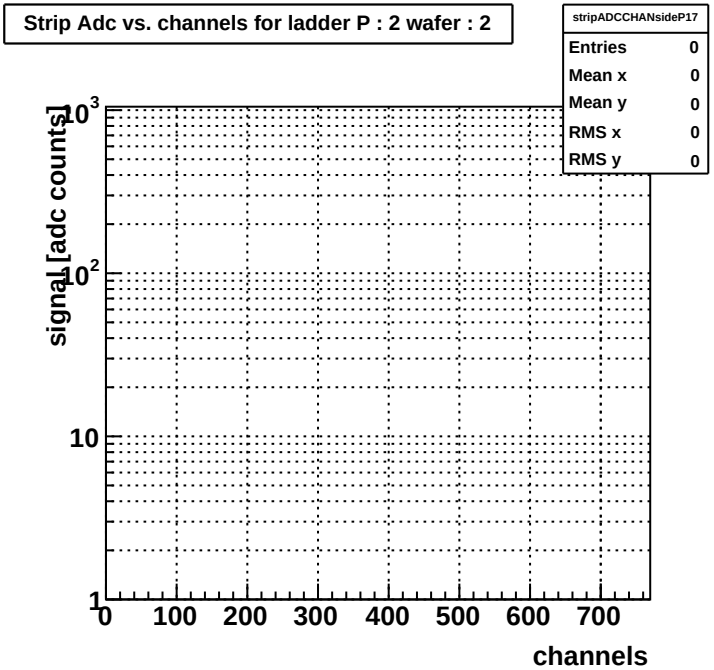

strip ADC for ladder N :1 wafer : 16
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

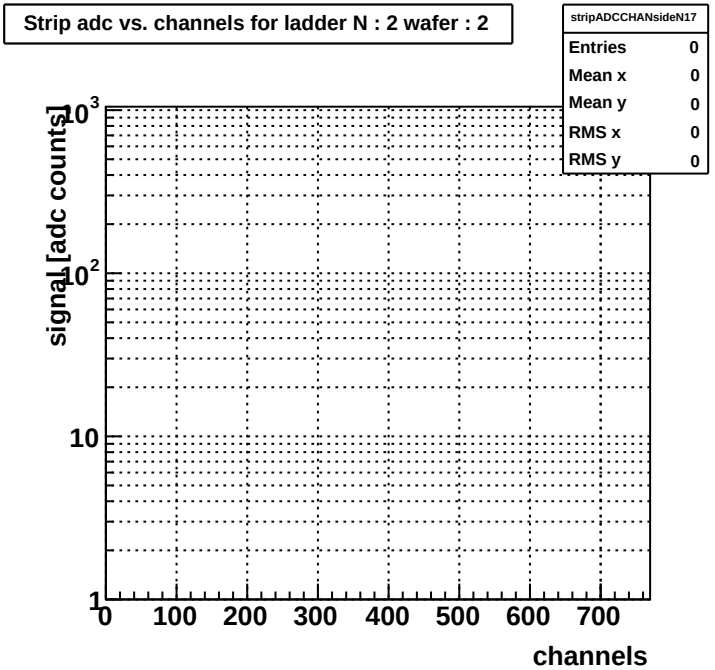

Strip Adc vs. channels for ladder P : 2 wafer : 1
stripADCCHANsideP16
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

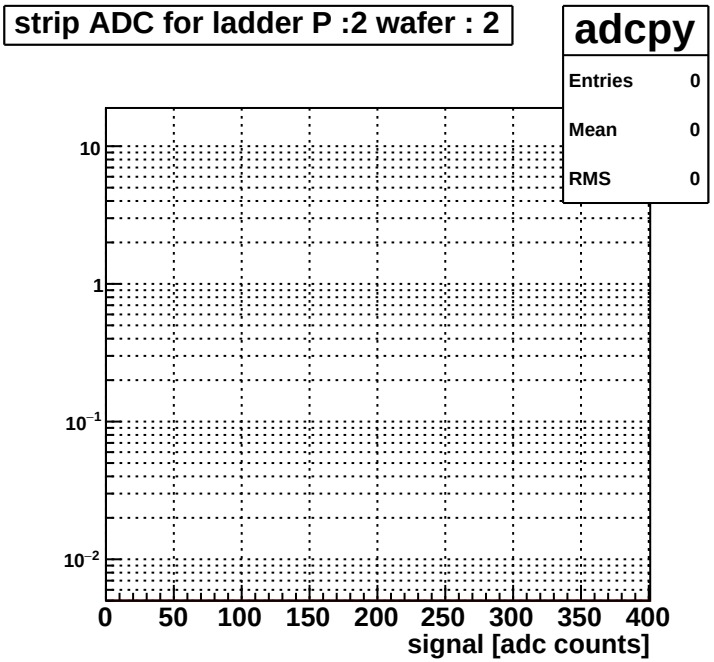

Strip adc vs. channels for ladder N : 2 wafer : 1
stripADCCHANsideN16
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

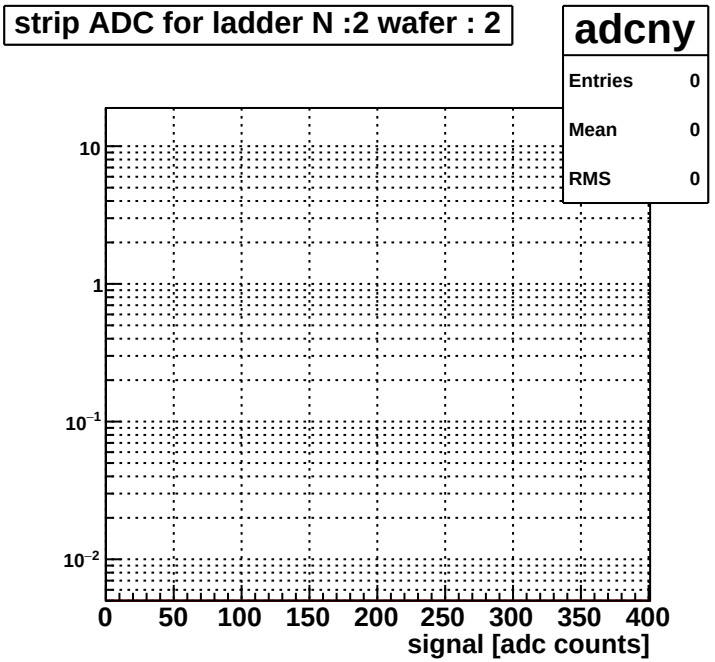

strip ADC for ladder P :2 wafer : 1
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

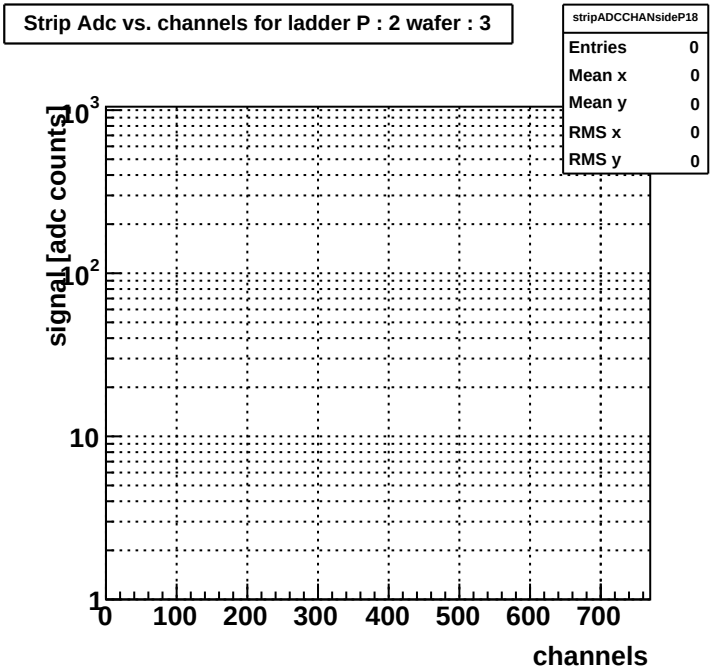

strip ADC for ladder N :2 wafer : 1
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

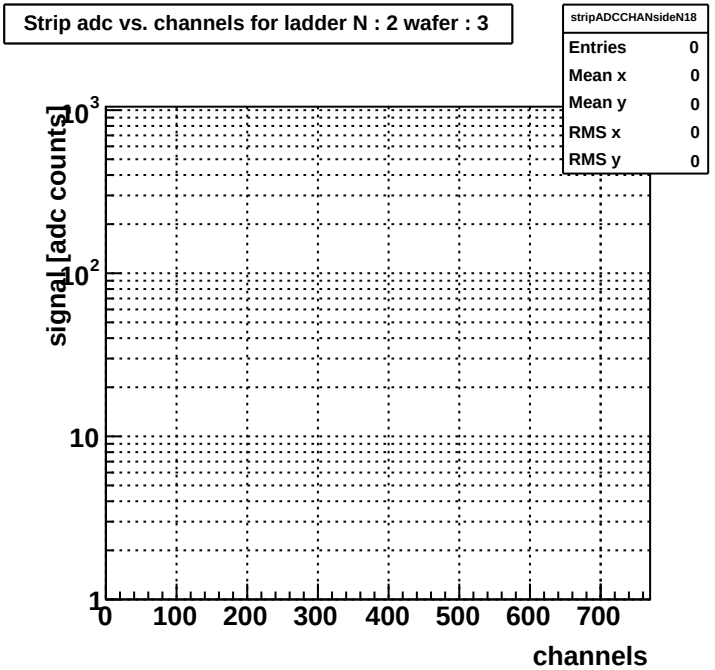

Strip Adc vs. channels for ladder P : 2 wafer : 2
stripADCCHANsideP17
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

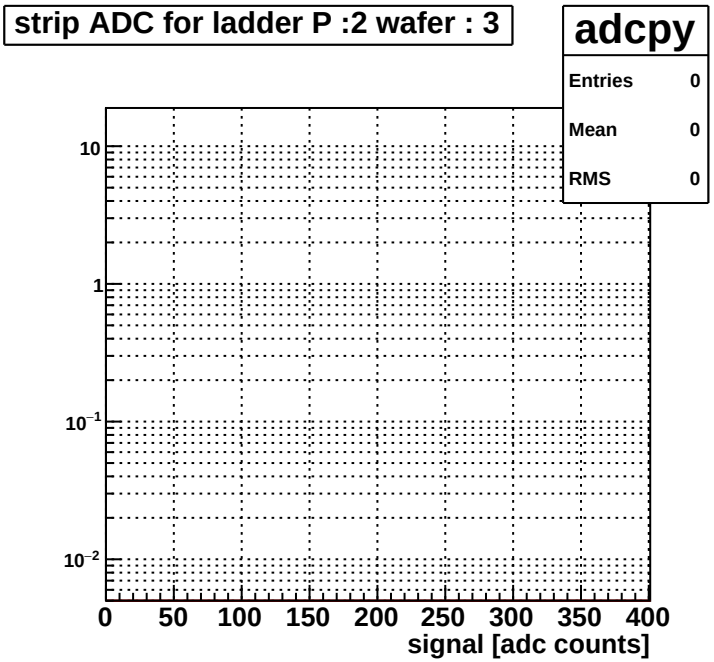

Strip adc vs. channels for ladder N : 2 wafer : 2
stripADCCHANsideN17
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

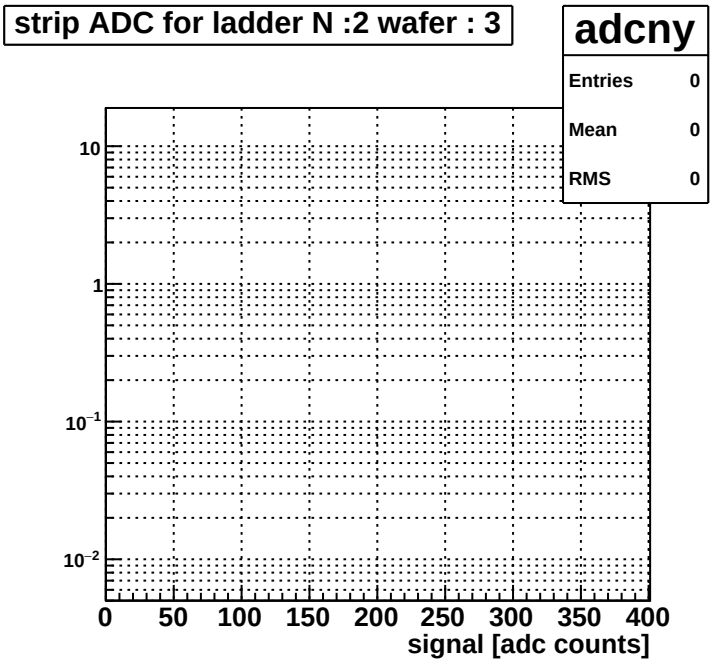

strip ADC for ladder P :2 wafer : 2
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

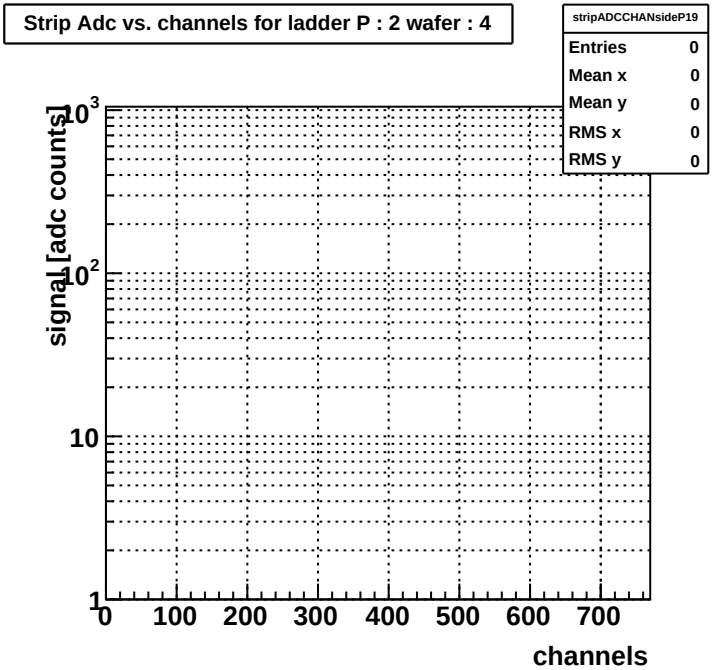

strip ADC for ladder N :2 wafer : 2
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

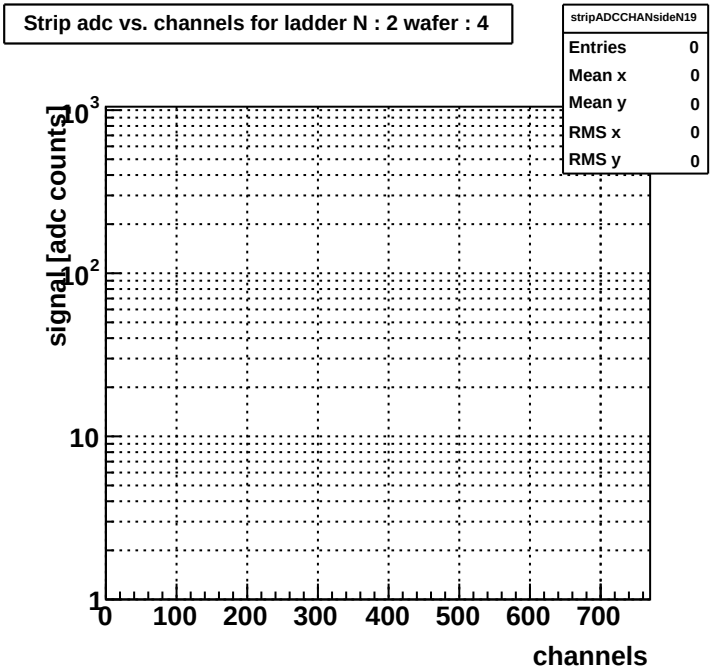

Strip Adc vs. channels for ladder P : 2 wafer : 3
stripADCCHANsideP18
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

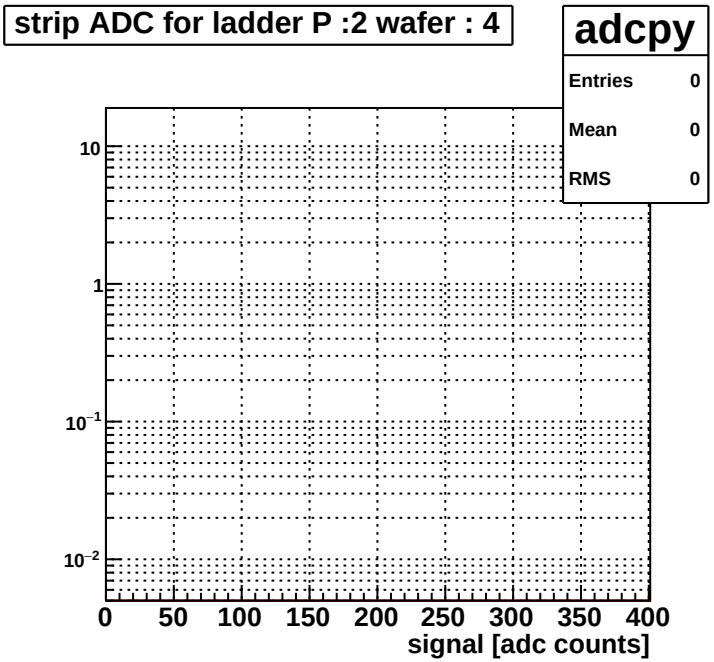

Strip adc vs. channels for ladder N : 2 wafer : 3
stripADCCHANsideN18
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

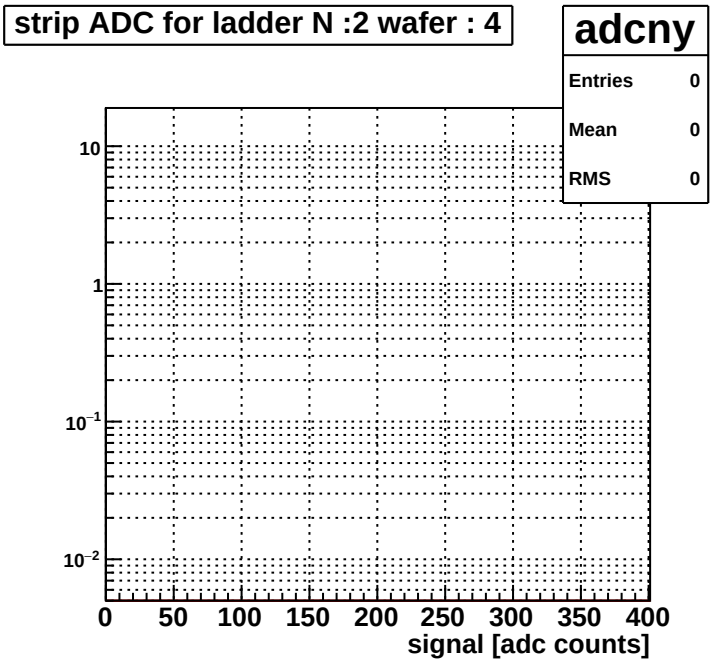

strip ADC for ladder P :2 wafer : 3
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

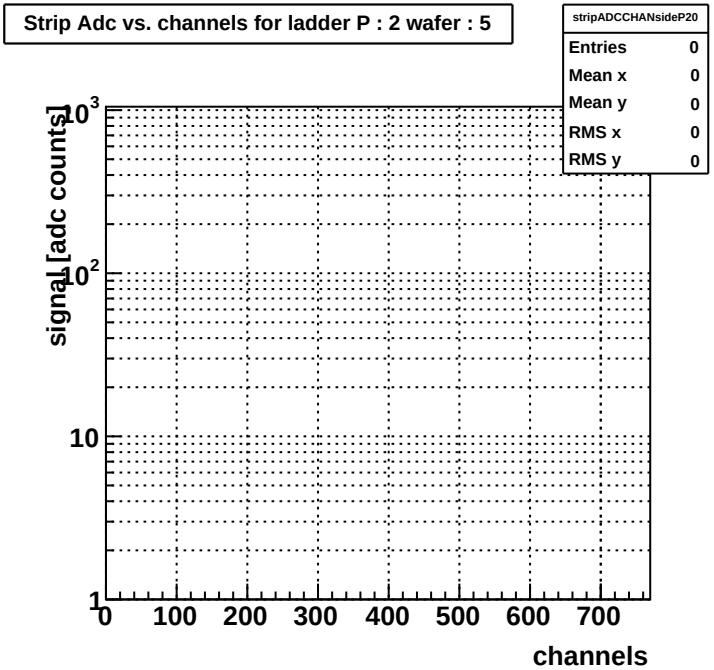

strip ADC for ladder N :2 wafer : 3
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

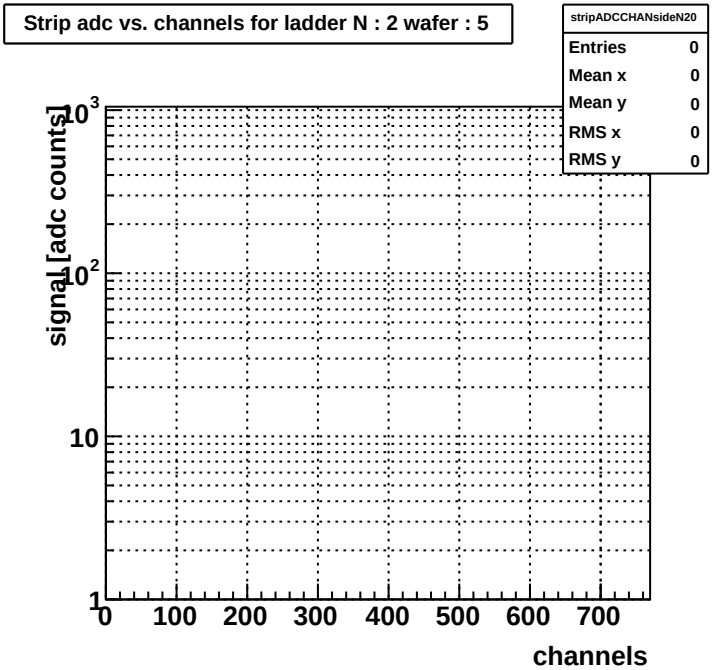

Strip Adc vs. channels for ladder P : 2 wafer : 4
stripADCCHANsideP19
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

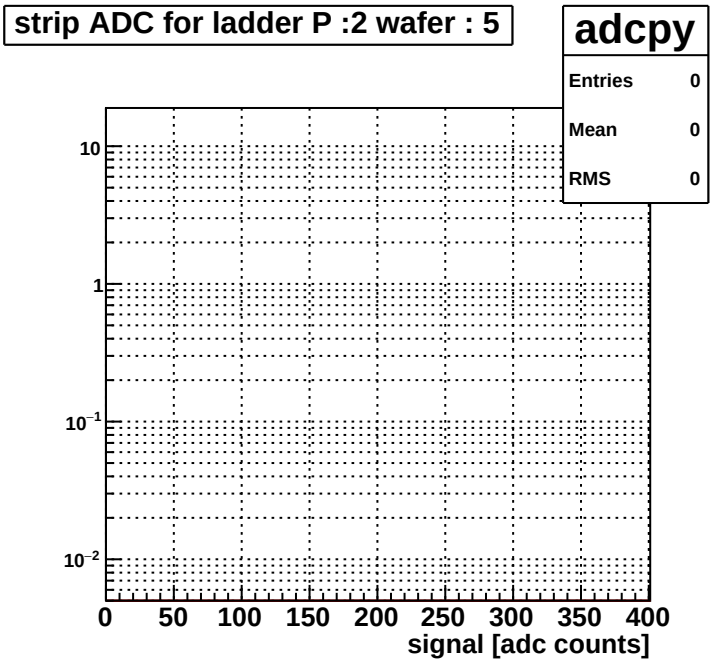

Strip adc vs. channels for ladder N : 2 wafer : 4
stripADCCHANsideN19
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

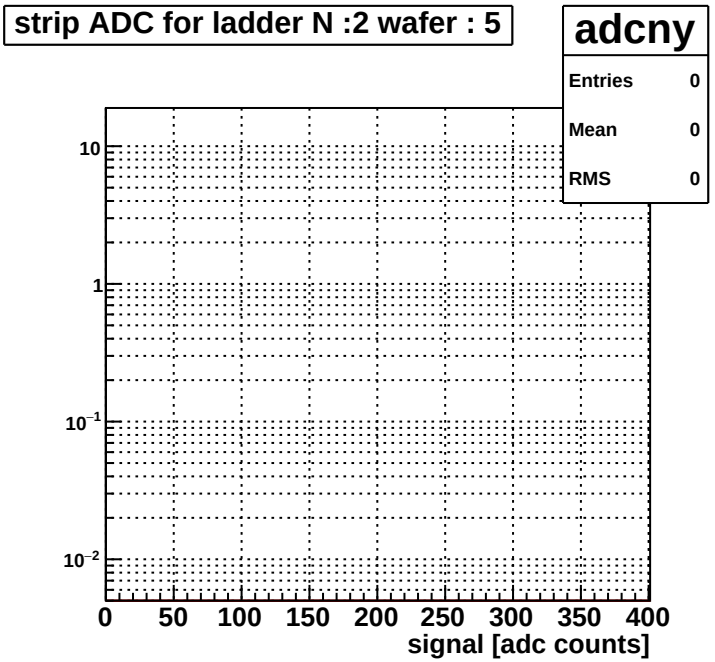

strip ADC for ladder P :2 wafer : 4
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

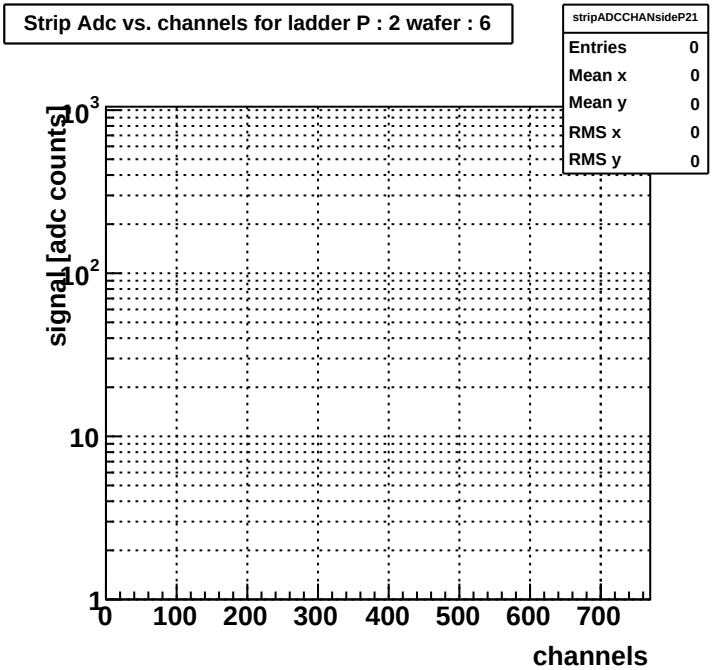

strip ADC for ladder N :2 wafer : 4
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

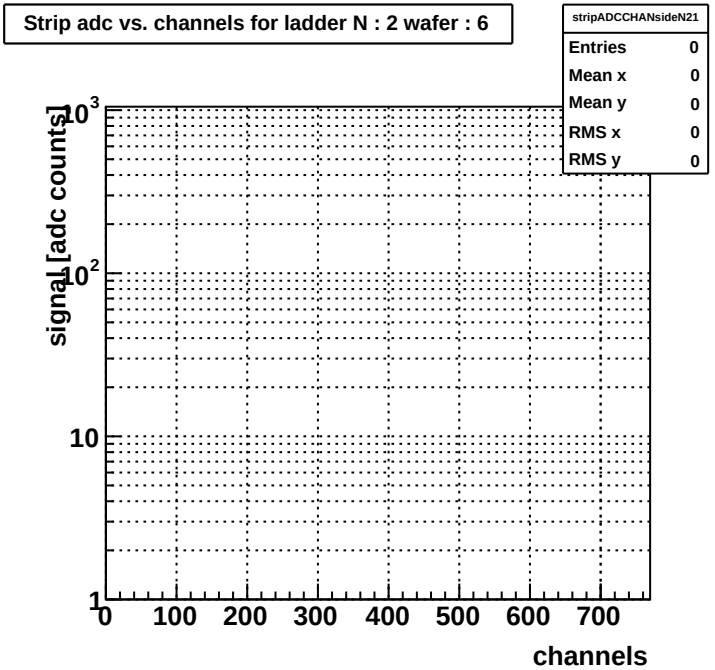

Strip Adc vs. channels for ladder P : 2 wafer : 5
stripADCCHANsideP20
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

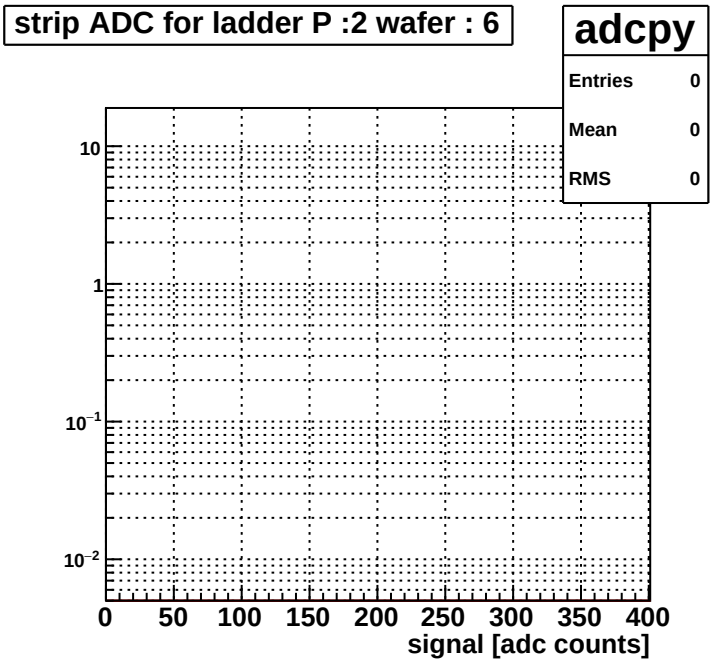

Strip adc vs. channels for ladder N : 2 wafer : 5
stripADCCHANsideN20
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

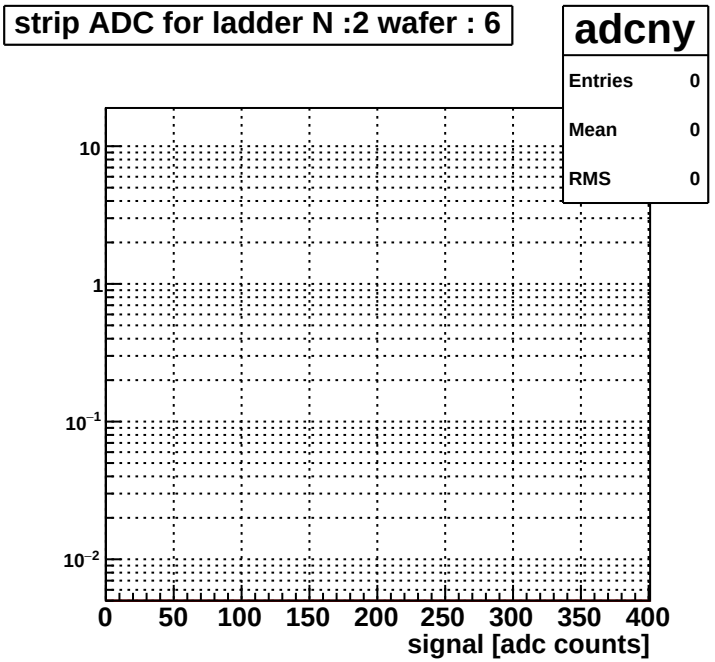

strip ADC for ladder P :2 wafer : 5
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

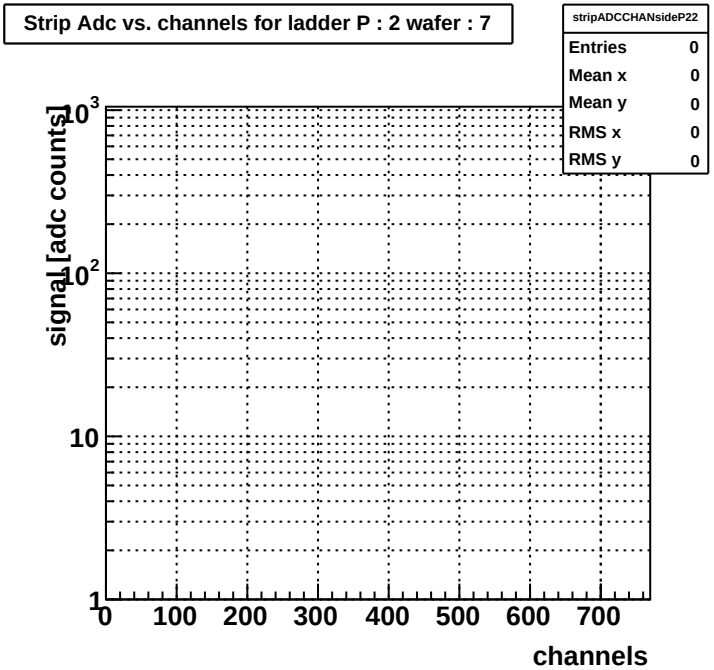

strip ADC for ladder N :2 wafer : 5
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

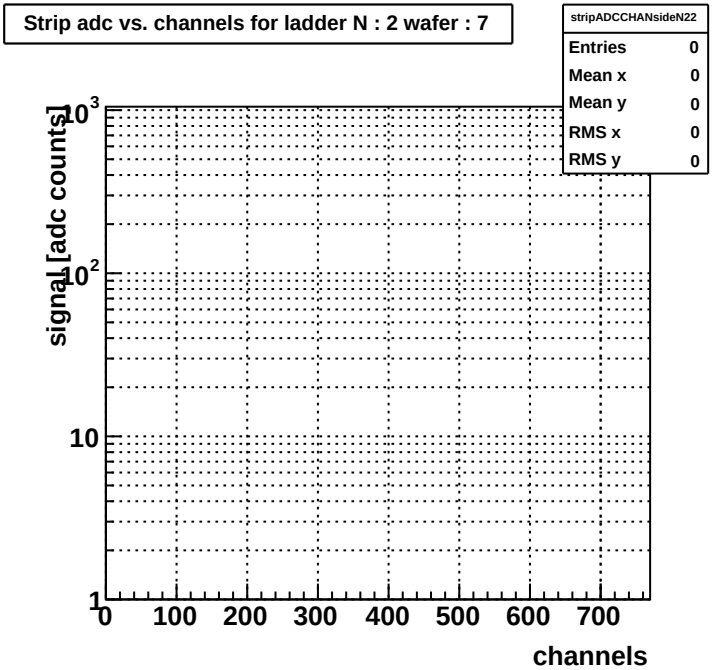

Strip Adc vs. channels for ladder P : 2 wafer : 6
stripADCCHANsideP21
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

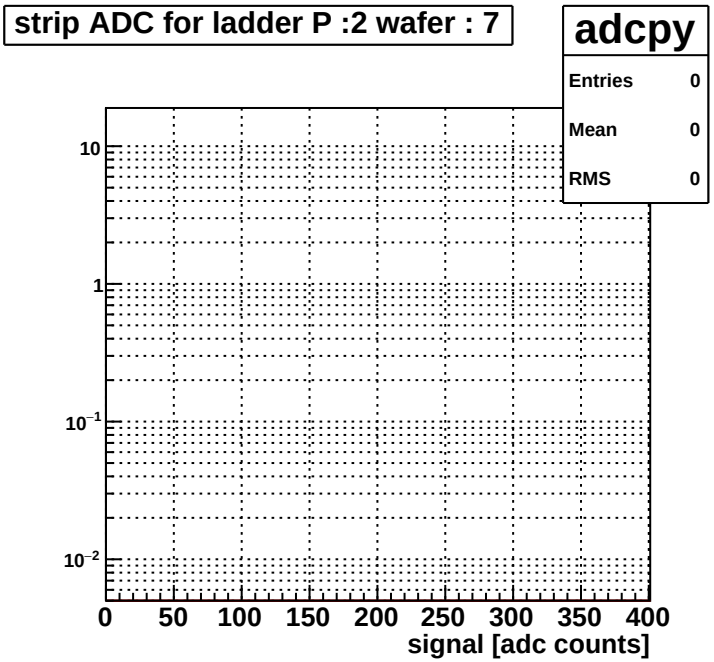

Strip adc vs. channels for ladder N : 2 wafer : 6
stripADCCHANsideN21
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

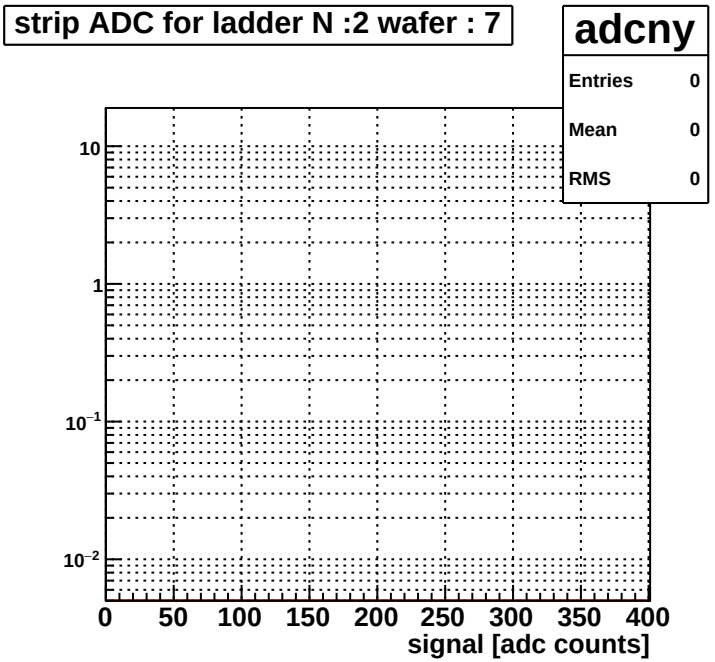

strip ADC for ladder P :2 wafer : 6
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

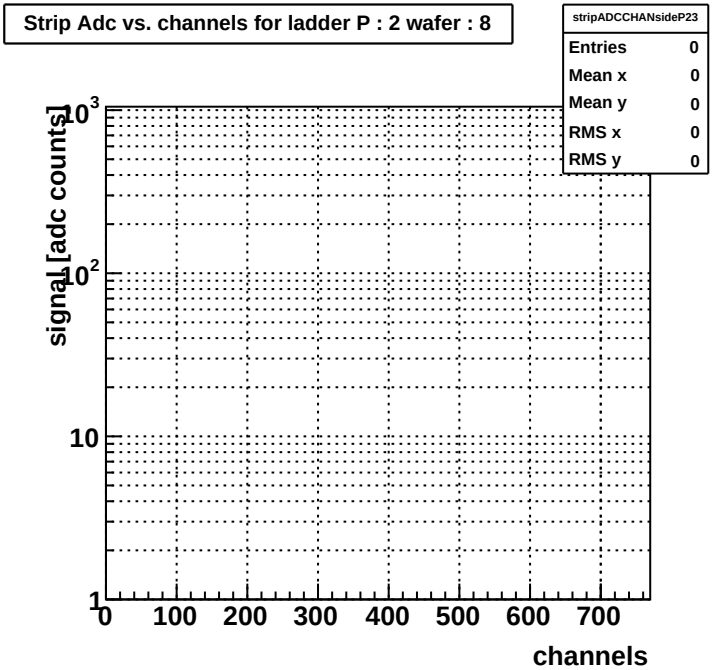

strip ADC for ladder N :2 wafer : 6
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

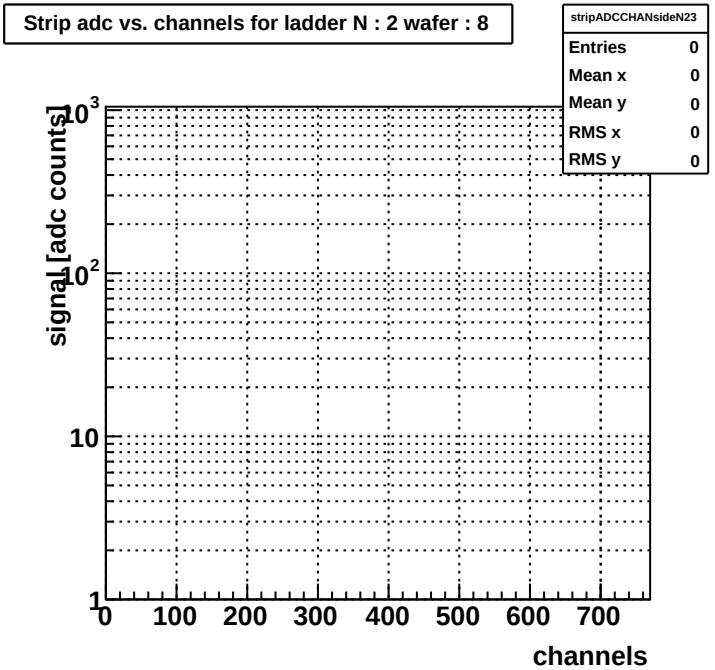

Strip Adc vs. channels for ladder P : 2 wafer : 7
stripADCCHANsideP22
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

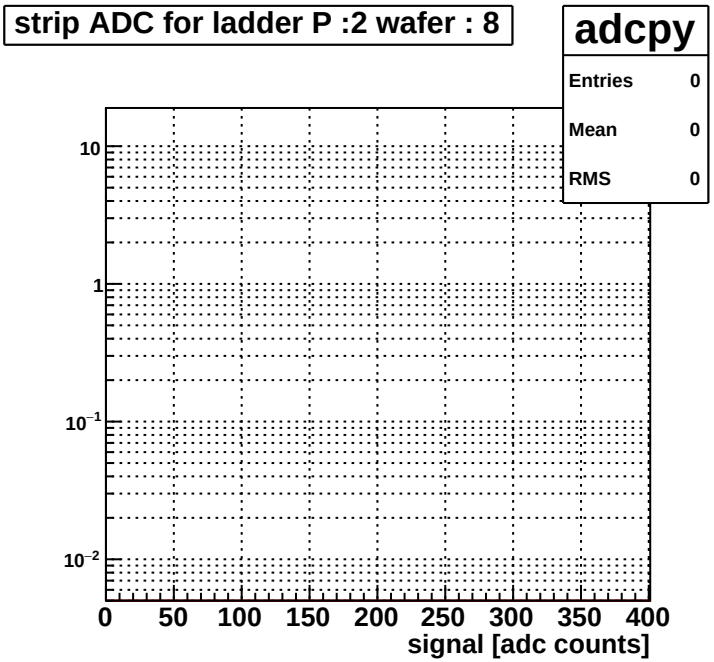

Strip adc vs. channels for ladder N : 2 wafer : 7
stripADCCHANsideN22
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

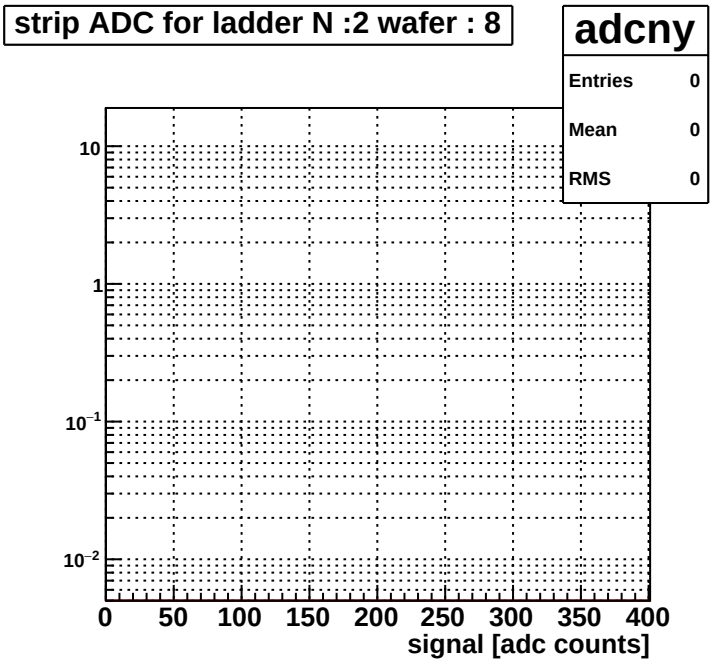

strip ADC for ladder P :2 wafer : 7
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

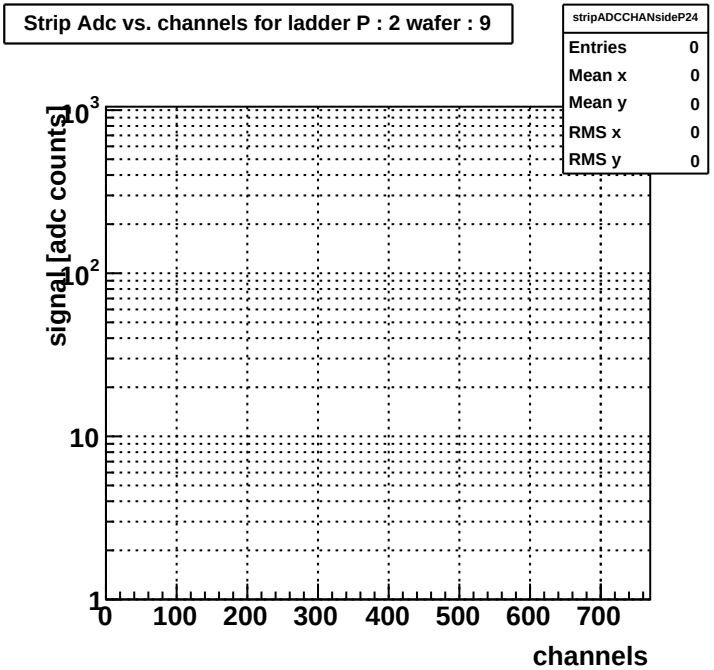

strip ADC for ladder N :2 wafer : 7
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

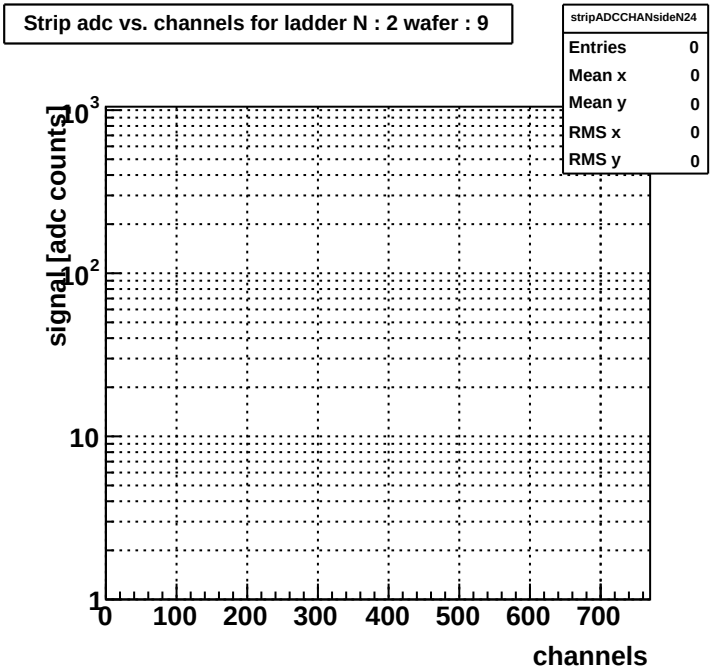

Strip Adc vs. channels for ladder P : 2 wafer : 8
stripADCCHANsideP23
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

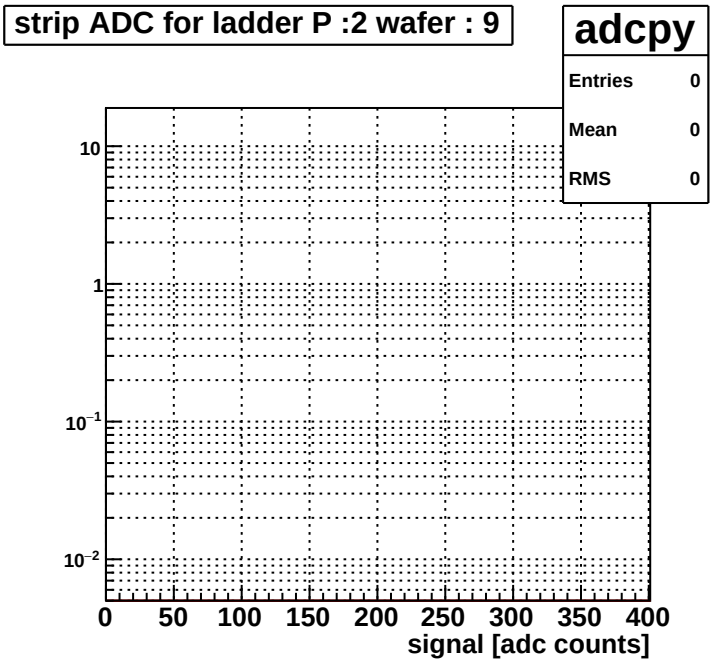

Strip adc vs. channels for ladder N : 2 wafer : 8
stripADCCHANsideN23
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

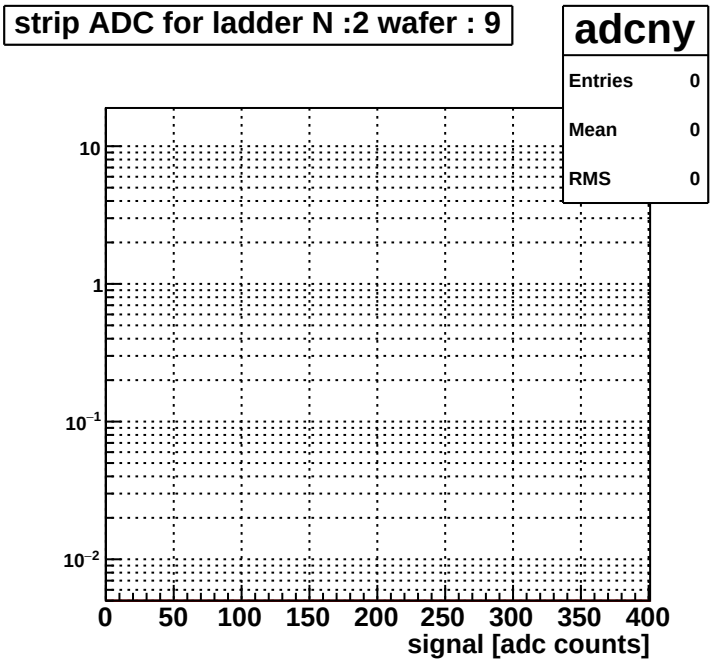

strip ADC for ladder P :2 wafer : 8
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

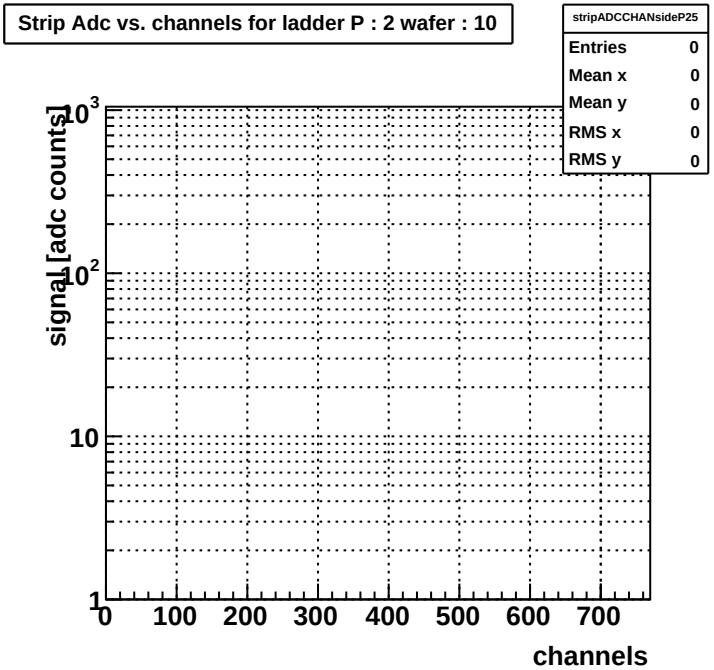

strip ADC for ladder N :2 wafer : 8
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

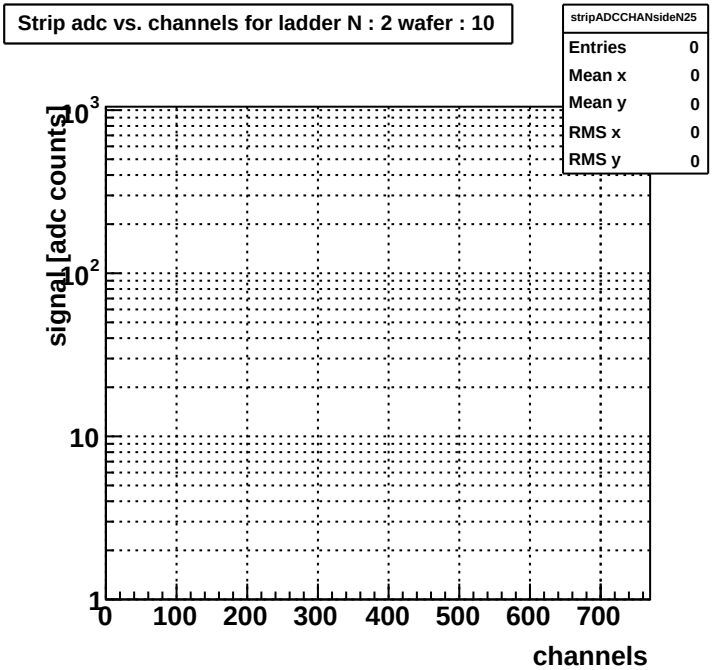

Strip Adc vs. channels for ladder P : 2 wafer : 9
stripADCCHANsideP24
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

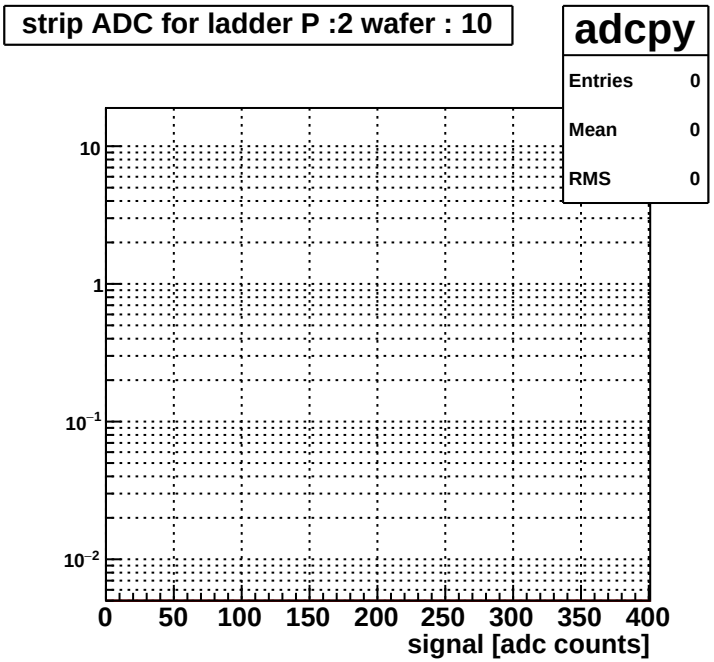

Strip adc vs. channels for ladder N : 2 wafer : 9
stripADCCHANsideN24
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

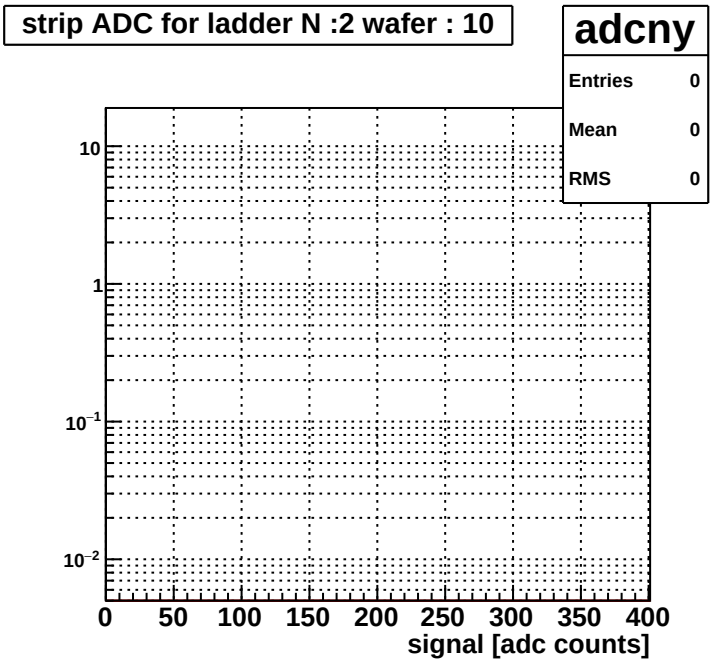

strip ADC for ladder P :2 wafer : 9
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

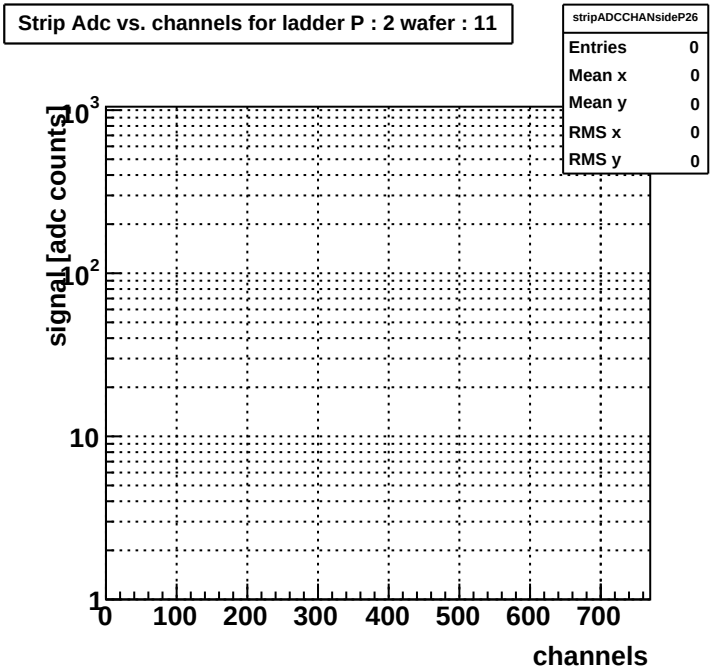

strip ADC for ladder N :2 wafer : 9
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

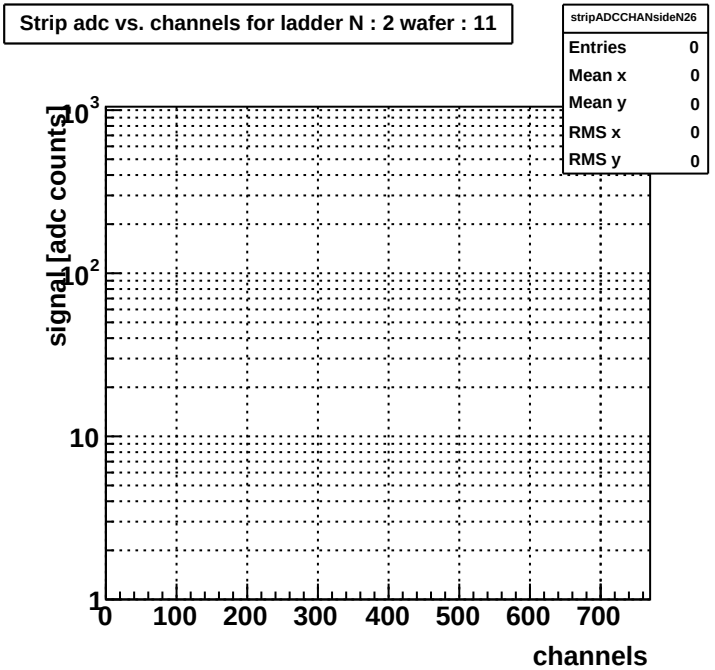

Strip Adc vs. channels for ladder P : 2 wafer : 10
stripADCCHANsideP25
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

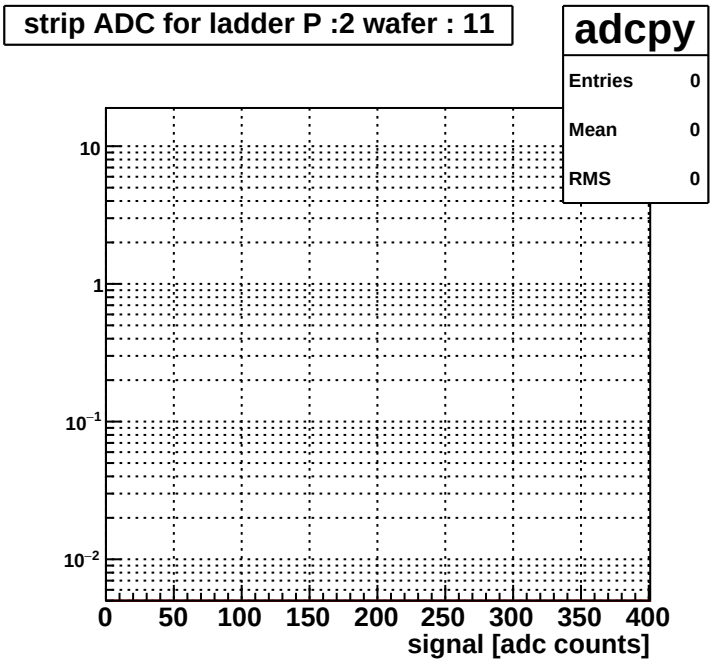

Strip adc vs. channels for ladder N : 2 wafer : 10
stripADCCHANsideN25
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

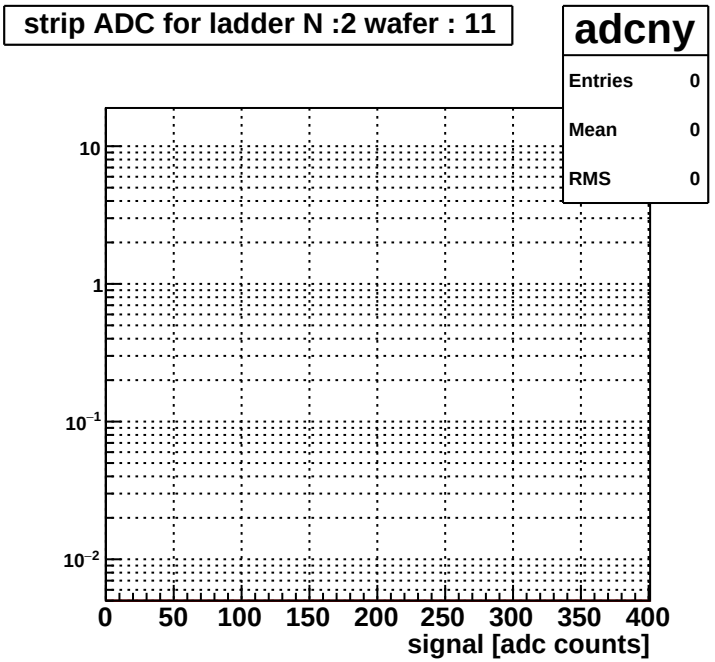

strip ADC for ladder P :2 wafer : 10
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

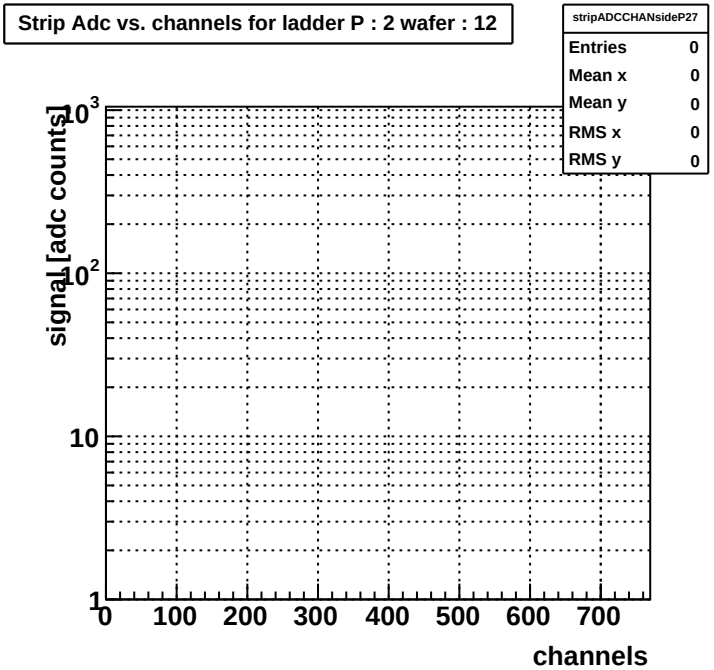

strip ADC for ladder N :2 wafer : 10
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

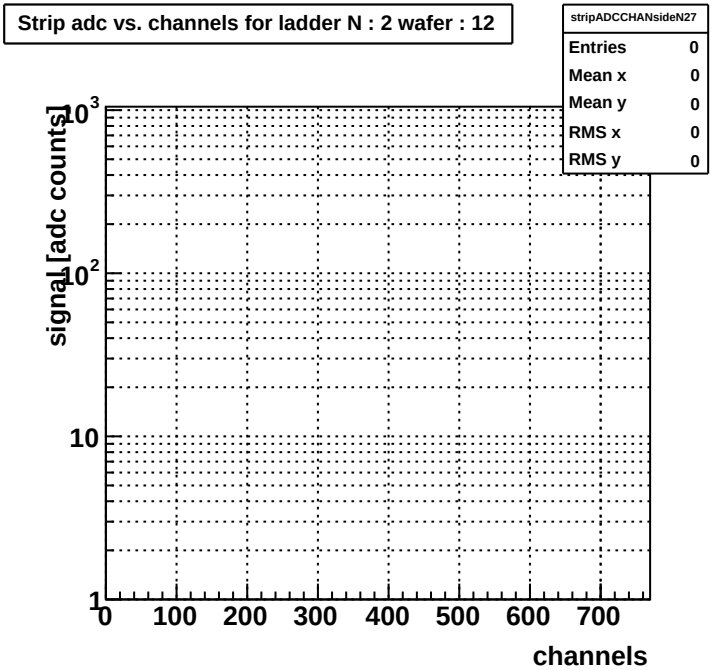

Strip Adc vs. channels for ladder P : 2 wafer : 11
stripADCCHANsideP26
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

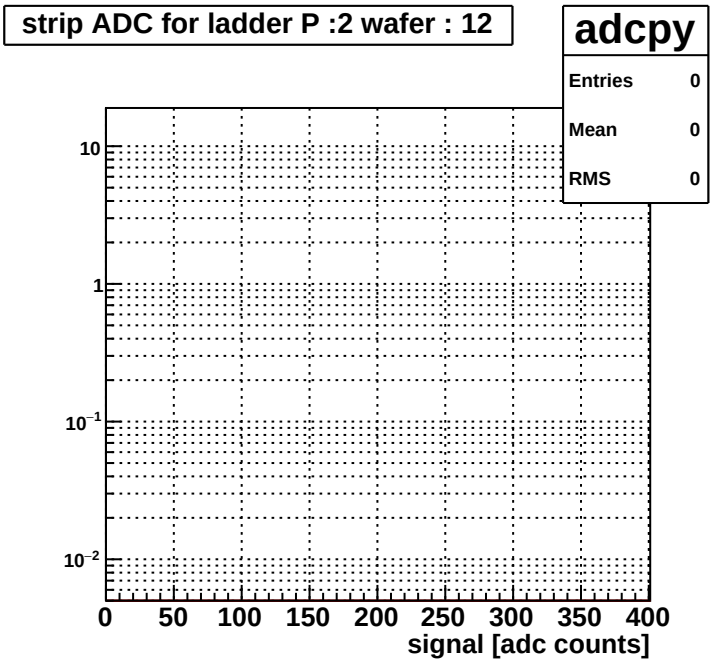

Strip adc vs. channels for ladder N : 2 wafer : 11
stripADCCHANsideN26
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

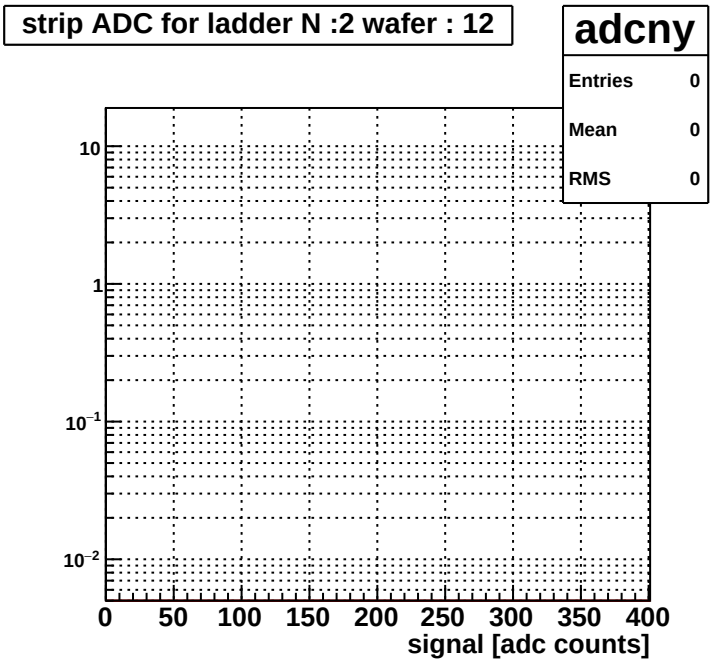

strip ADC for ladder P :2 wafer : 11
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

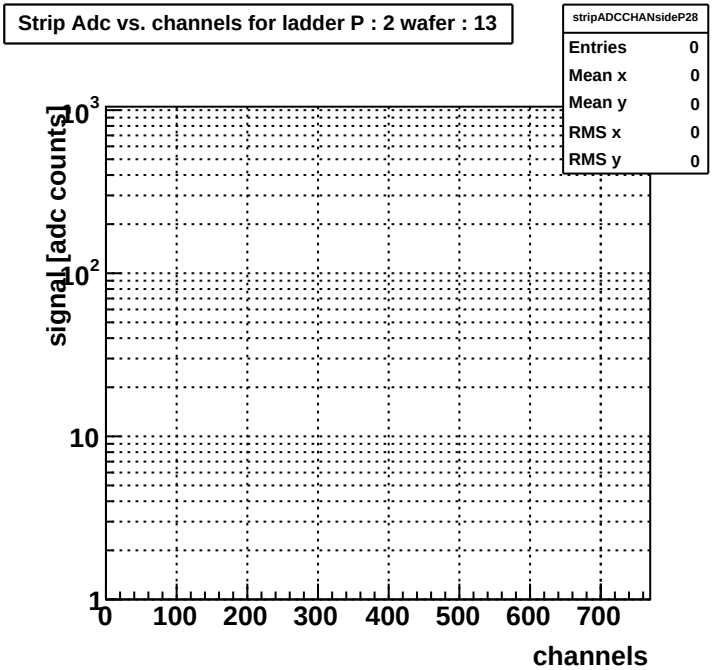

strip ADC for ladder N :2 wafer : 11
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

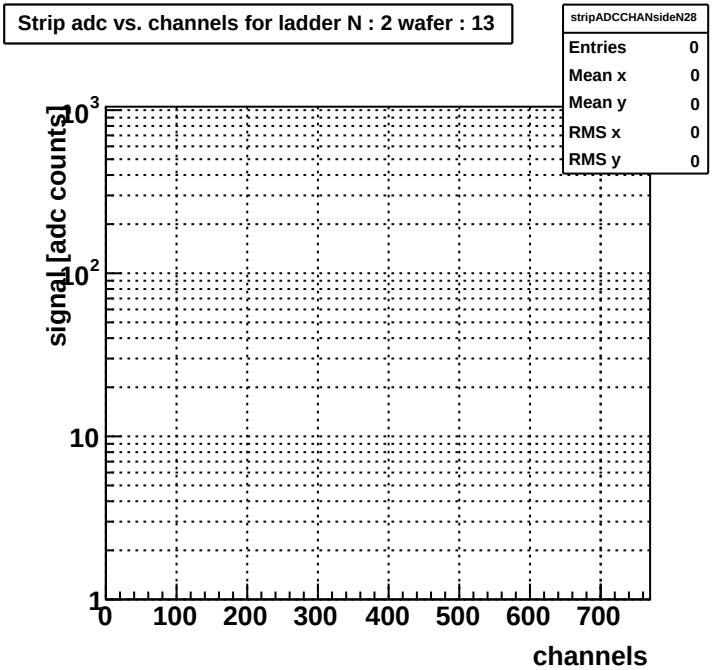

Strip Adc vs. channels for ladder P : 2 wafer : 12
stripADCCHANsideP27
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

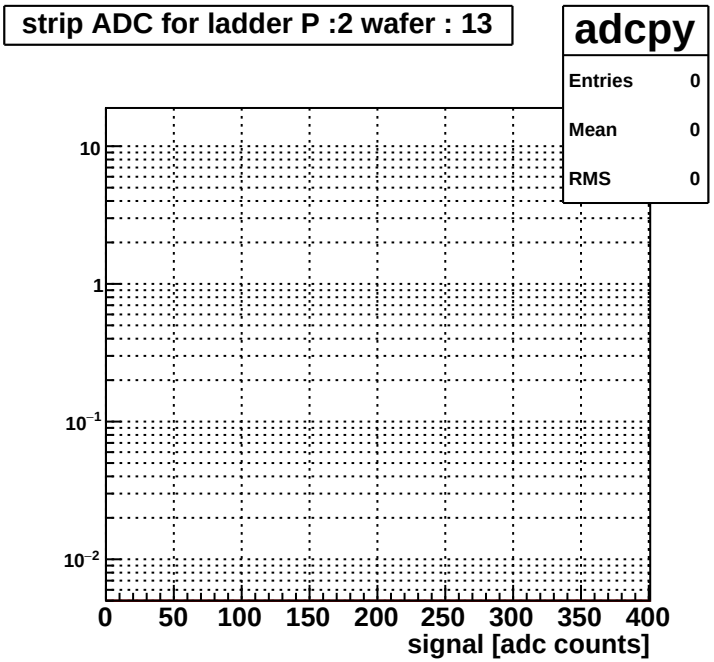

Strip adc vs. channels for ladder N : 2 wafer : 12
stripADCCHANsideN27
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

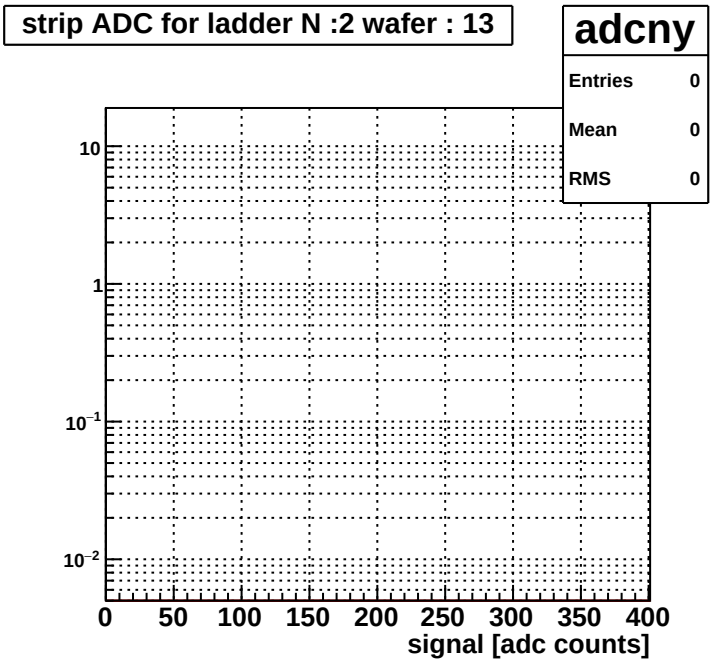

strip ADC for ladder P :2 wafer : 12
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

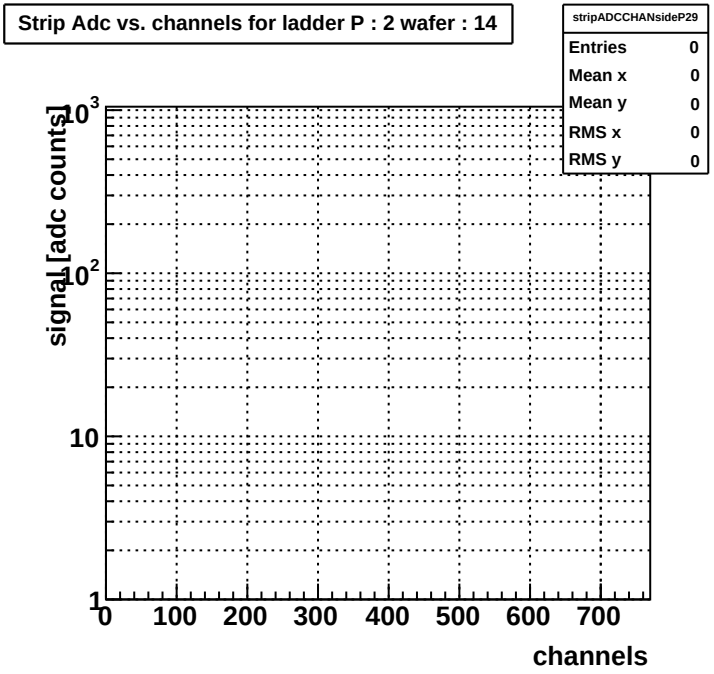

strip ADC for ladder N :2 wafer : 12
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

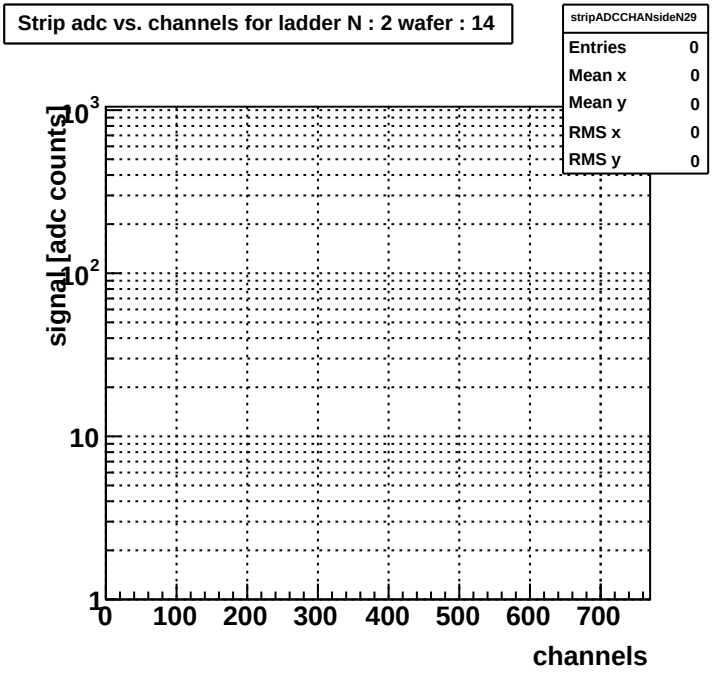

Strip Adc vs. channels for ladder P : 2 wafer : 13
stripADCCHANsideP28
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

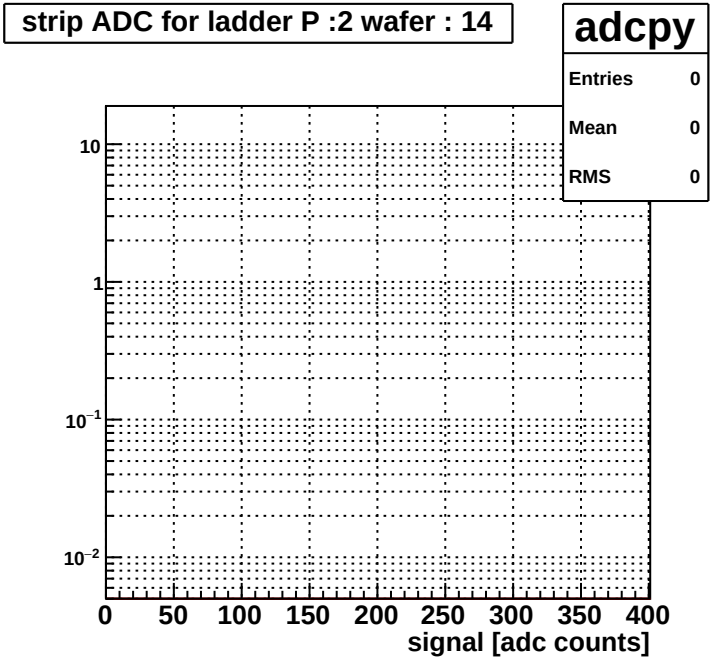

Strip adc vs. channels for ladder N : 2 wafer : 13
stripADCCHANsideN28
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

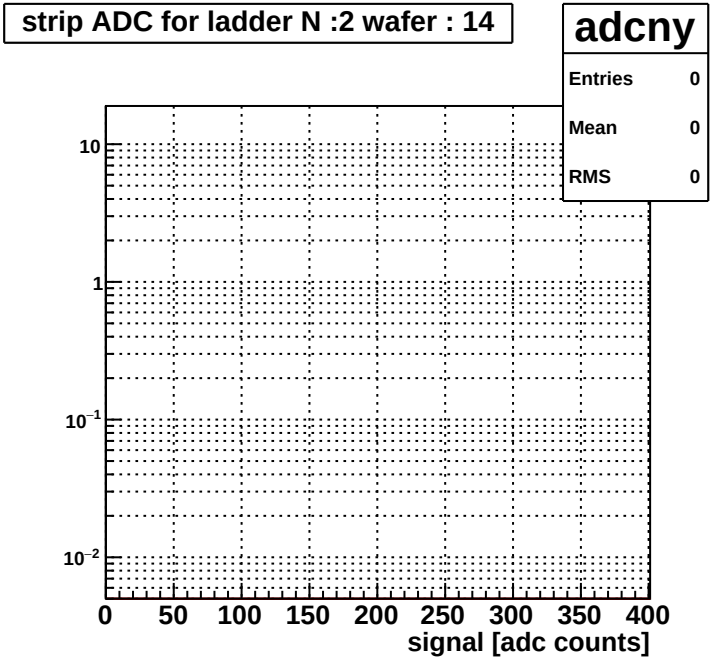

strip ADC for ladder P :2 wafer : 13
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

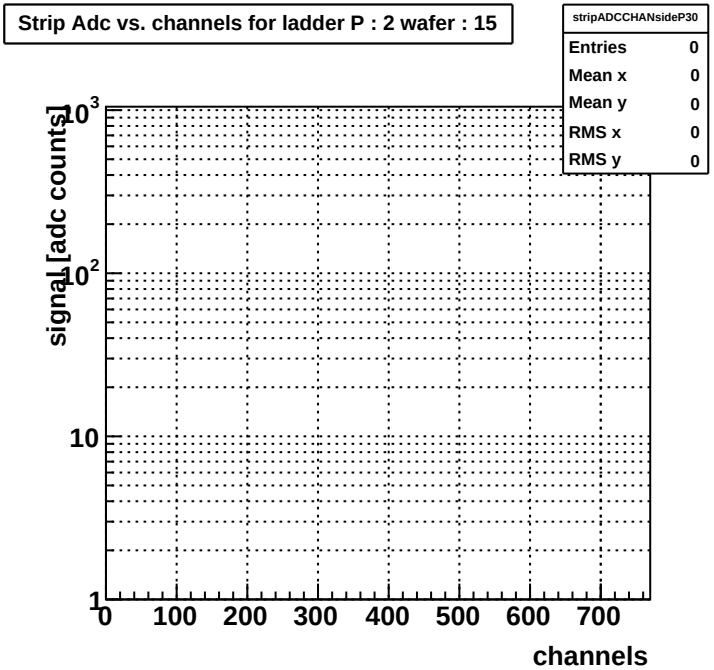

strip ADC for ladder N :2 wafer : 13
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

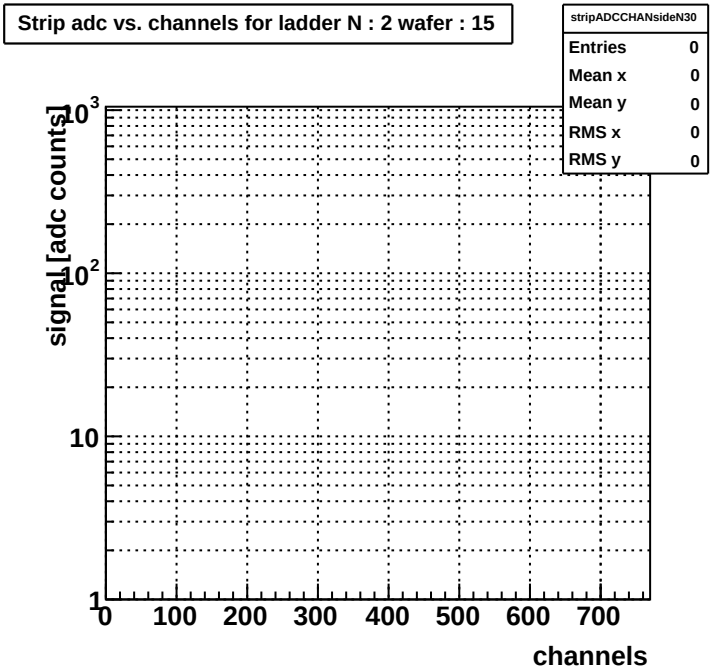

Strip Adc vs. channels for ladder P : 2 wafer : 14
stripADCCHANsideP29
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

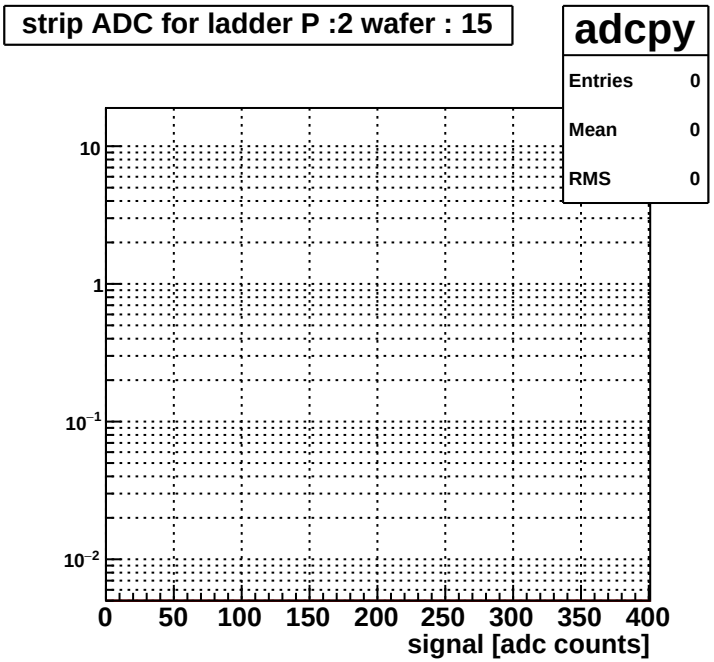

Strip adc vs. channels for ladder N : 2 wafer : 14
stripADCCHANsideN29
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

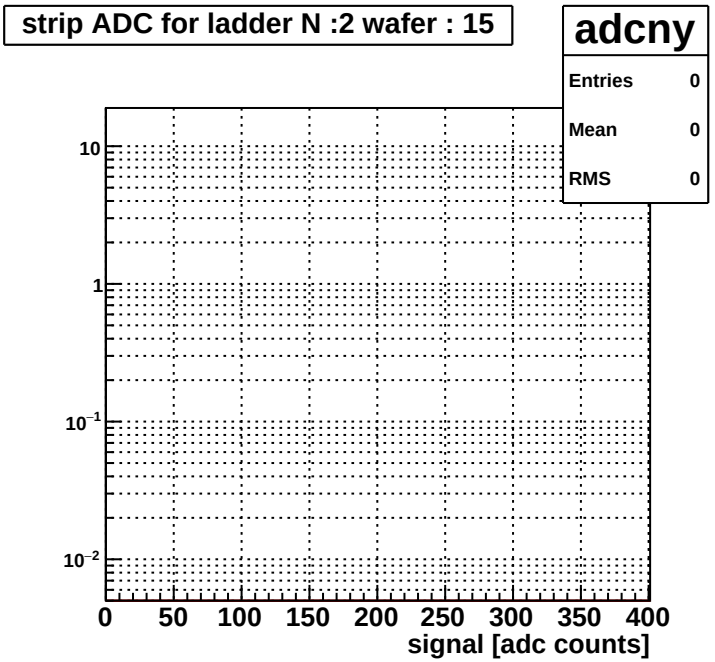

strip ADC for ladder P :2 wafer : 14
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

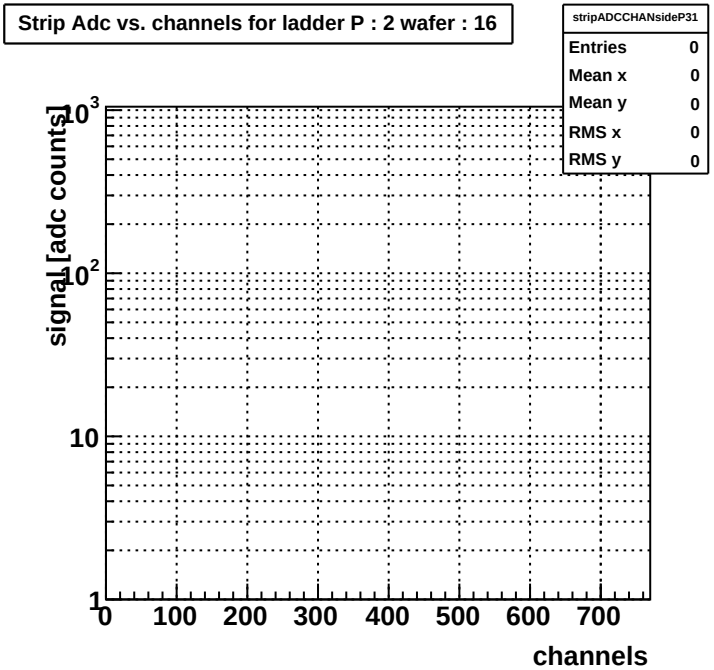

strip ADC for ladder N :2 wafer : 14
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

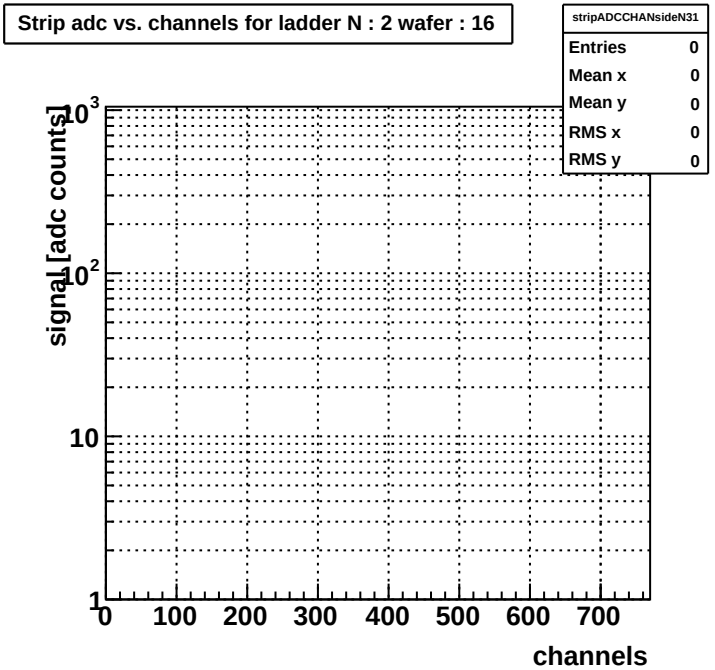

Strip Adc vs. channels for ladder P : 2 wafer : 15
stripADCCHANsideP30
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

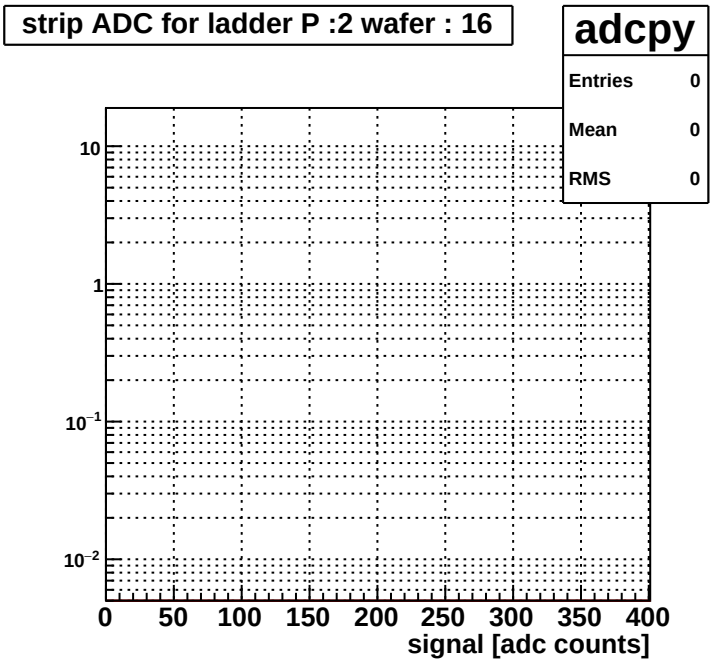

Strip adc vs. channels for ladder N : 2 wafer : 15
stripADCCHANsideN30
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

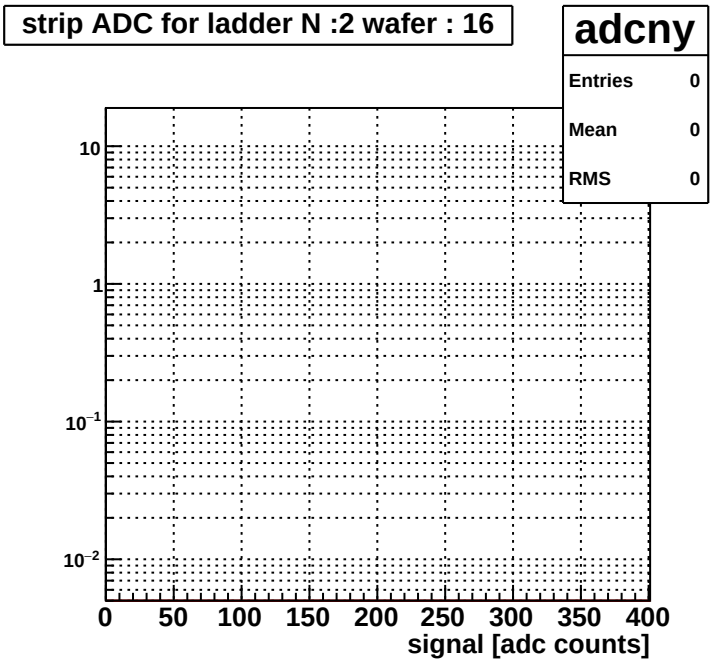

strip ADC for ladder P :2 wafer : 15
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

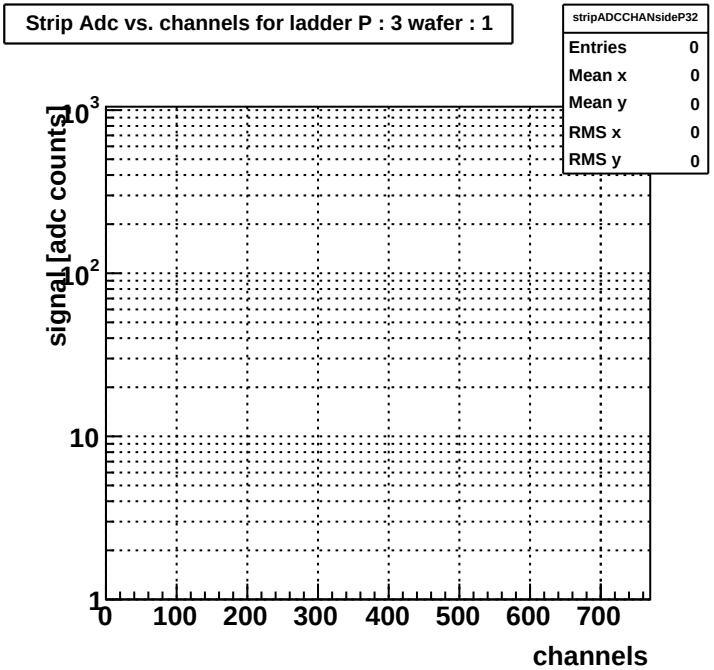

strip ADC for ladder N :2 wafer : 15
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

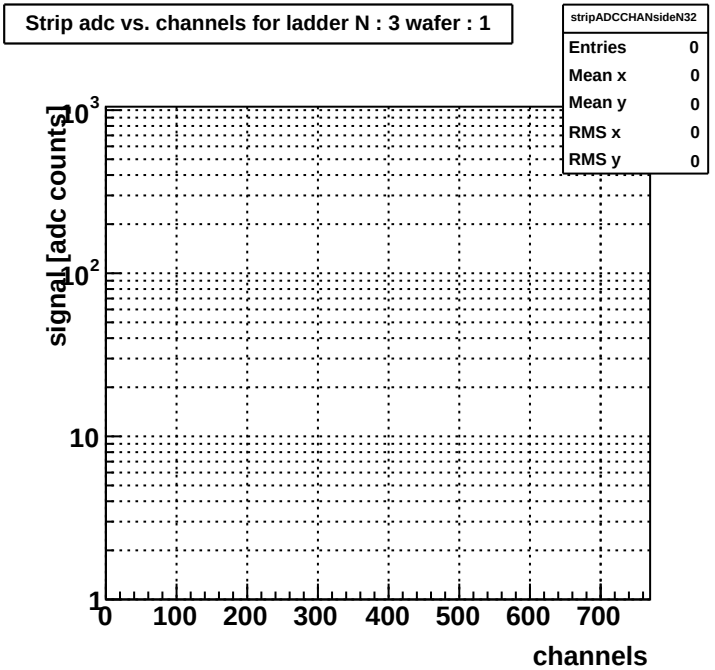

Strip Adc vs. channels for ladder P : 2 wafer : 16
stripADCCHANsideP31
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

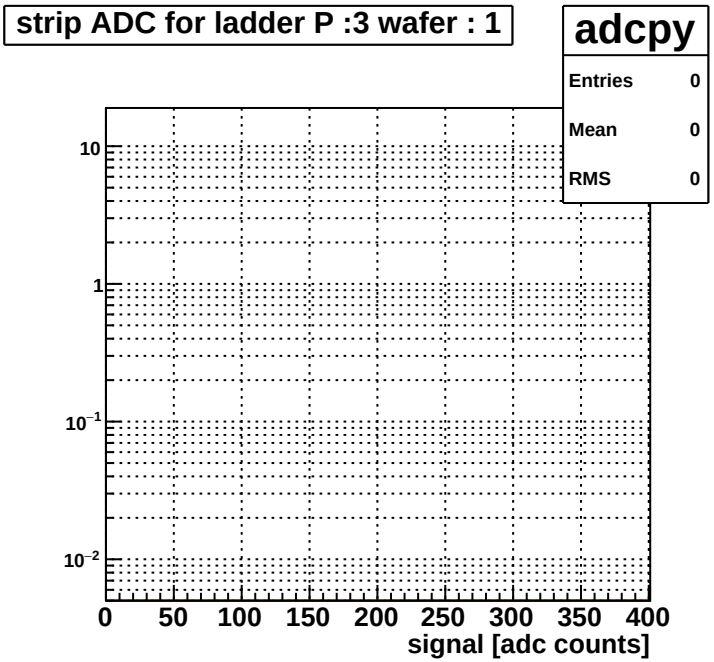

Strip adc vs. channels for ladder N : 2 wafer : 16
stripADCCHANsideN31
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

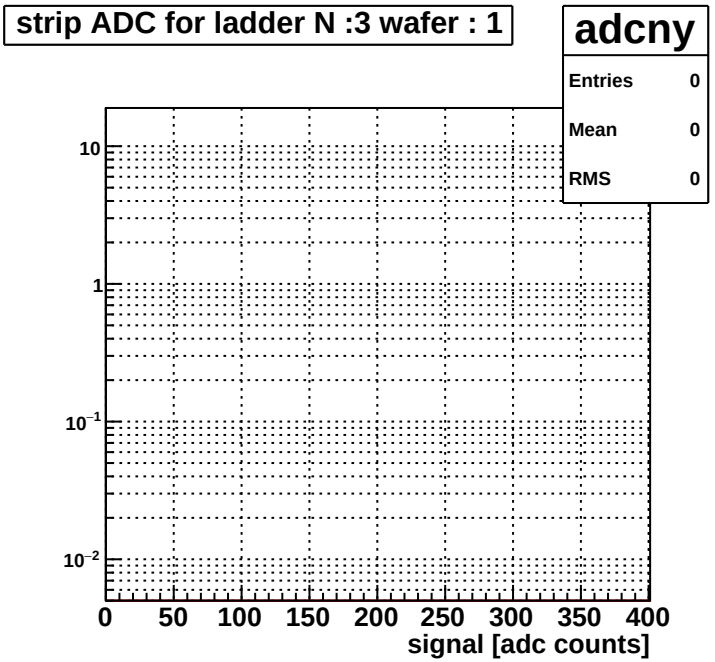

strip ADC for ladder P :2 wafer : 16
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

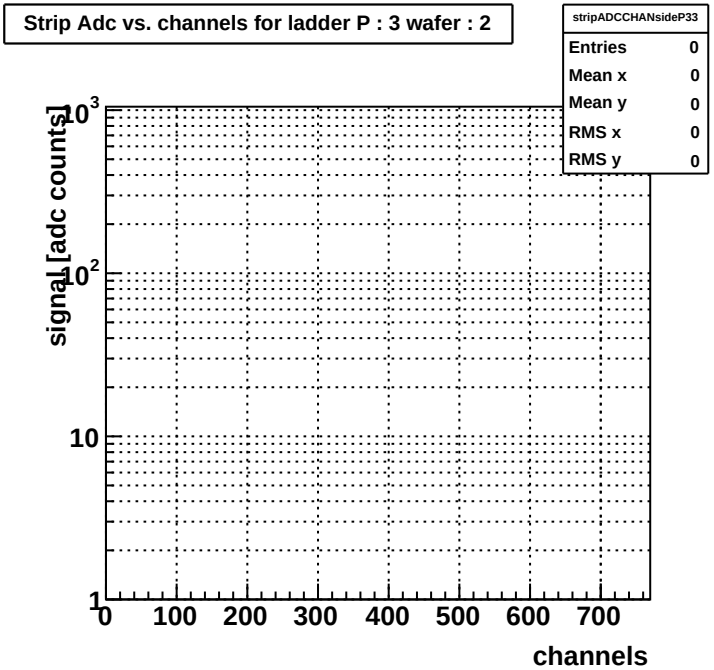

strip ADC for ladder N :2 wafer : 16
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

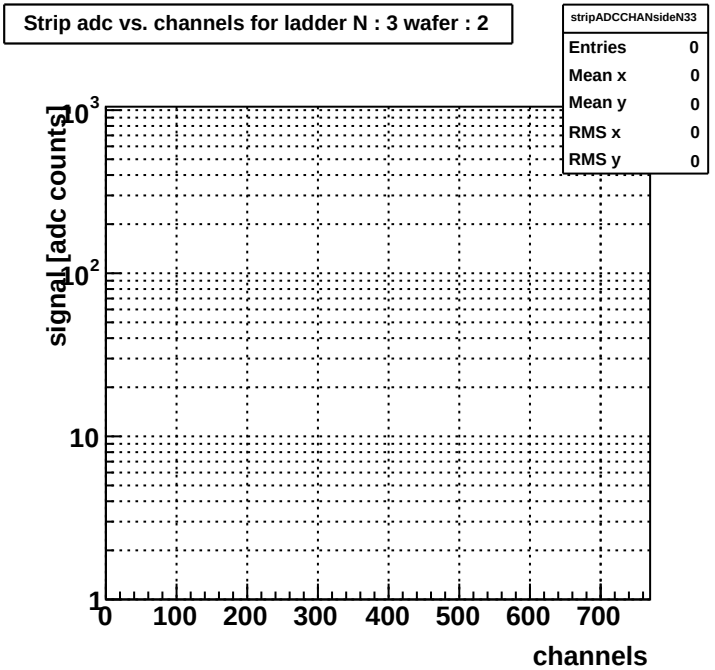

Strip Adc vs. channels for ladder P : 3 wafer : 1
stripADCCHANsideP32
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

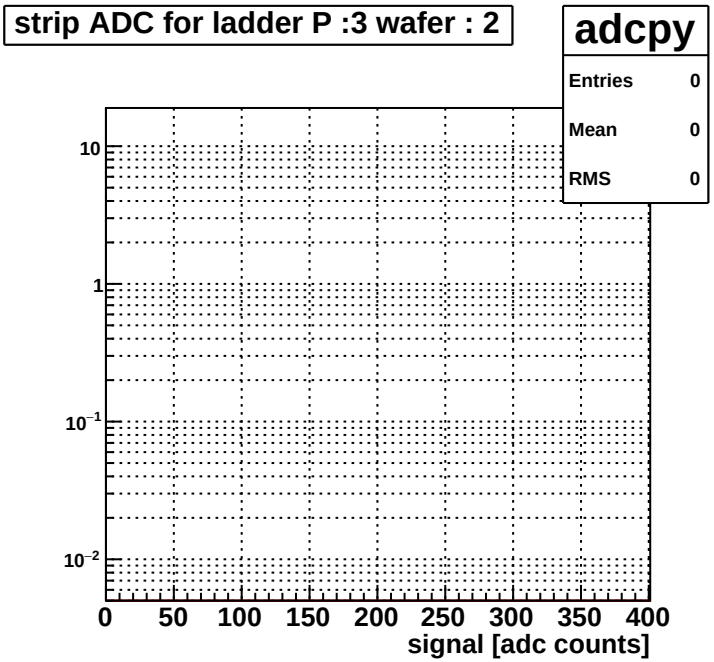

Strip adc vs. channels for ladder N : 3 wafer : 1
stripADCCHANsideN32
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

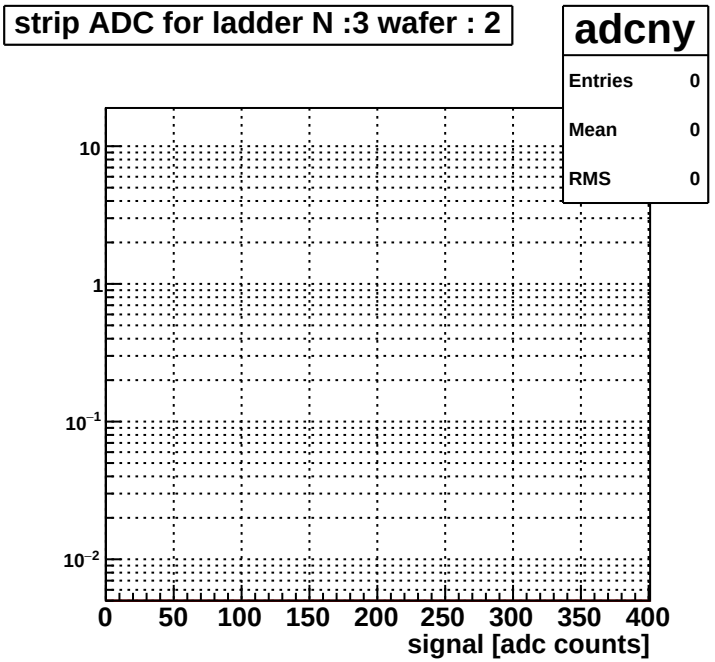

strip ADC for ladder P :3 wafer : 1
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

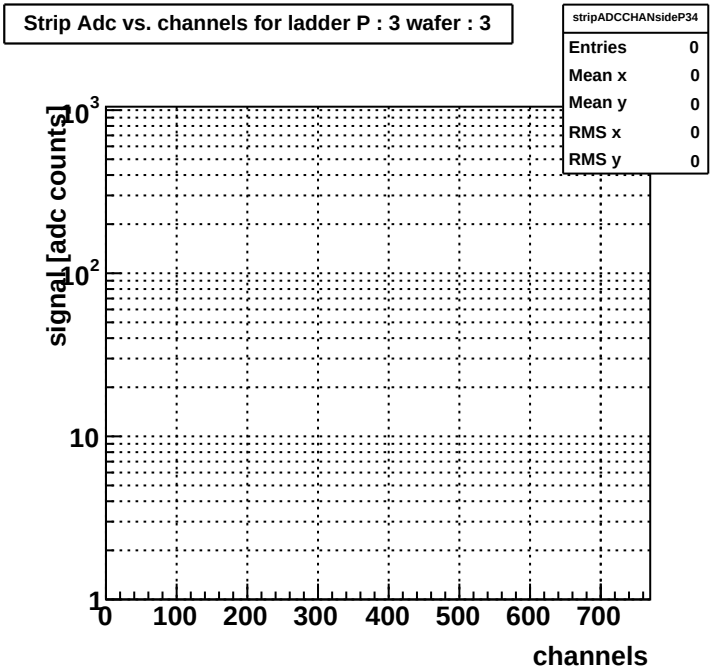

strip ADC for ladder N :3 wafer : 1
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

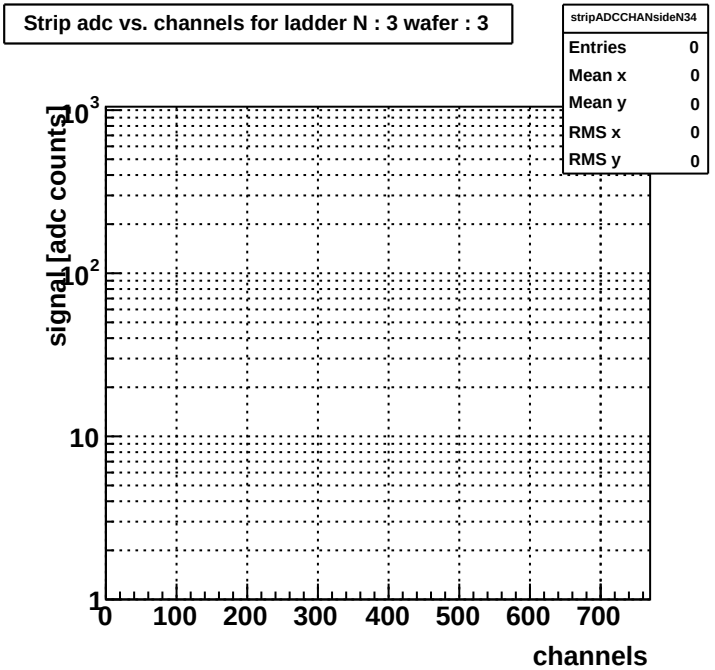

Strip Adc vs. channels for ladder P : 3 wafer : 2
stripADCCHANsideP33
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

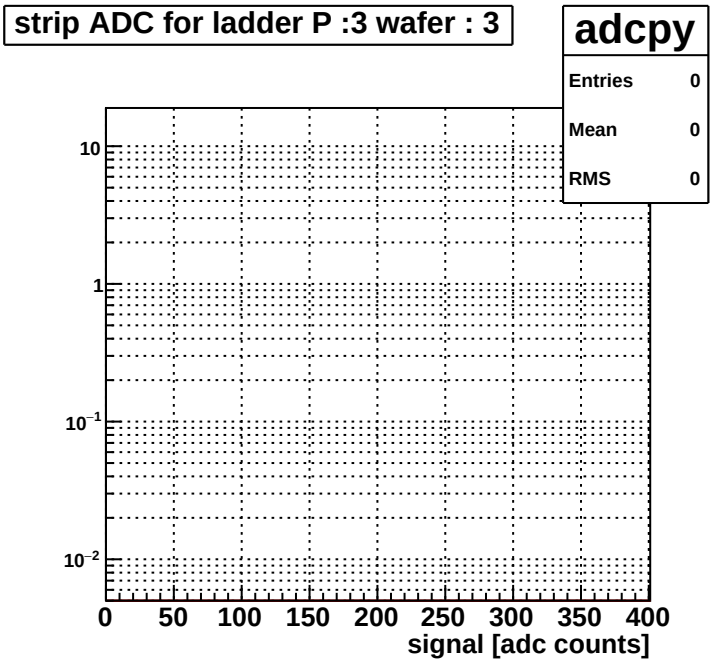

Strip adc vs. channels for ladder N : 3 wafer : 2
stripADCCHANsideN33
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

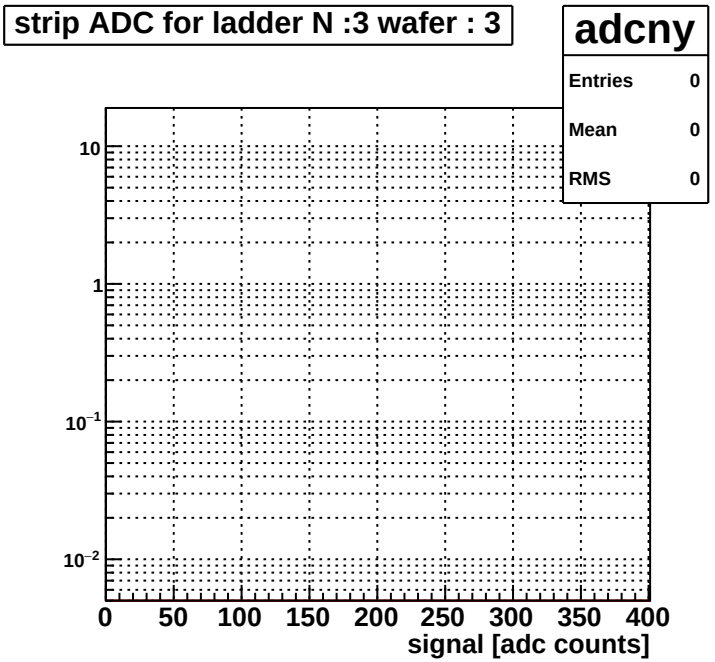

strip ADC for ladder P :3 wafer : 2
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

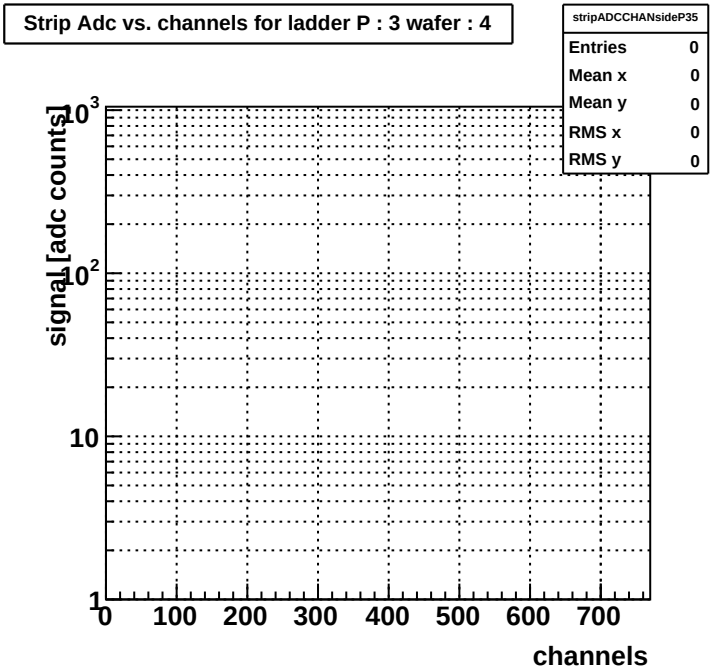

strip ADC for ladder N :3 wafer : 2
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

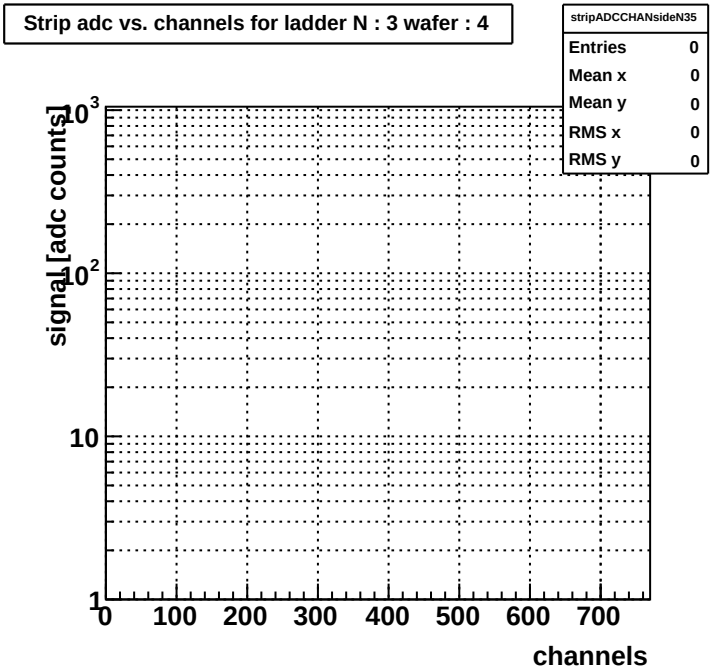

Strip Adc vs. channels for ladder P : 3 wafer : 3
stripADCCHANsideP34
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

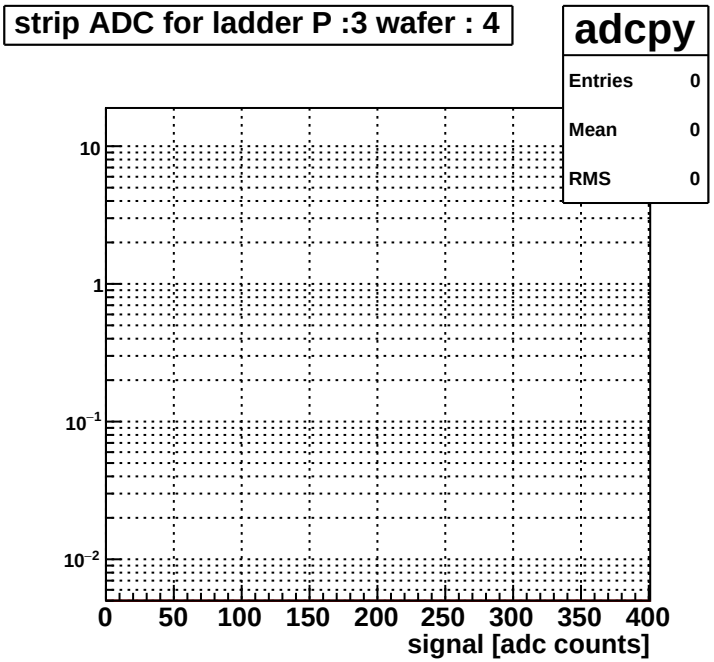

Strip adc vs. channels for ladder N : 3 wafer : 3
stripADCCHANsideN34
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

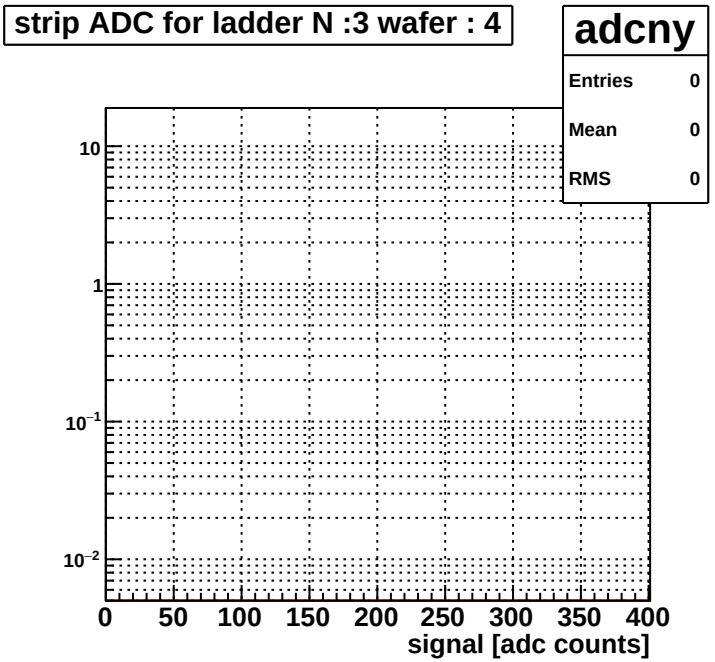

strip ADC for ladder P :3 wafer : 3
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

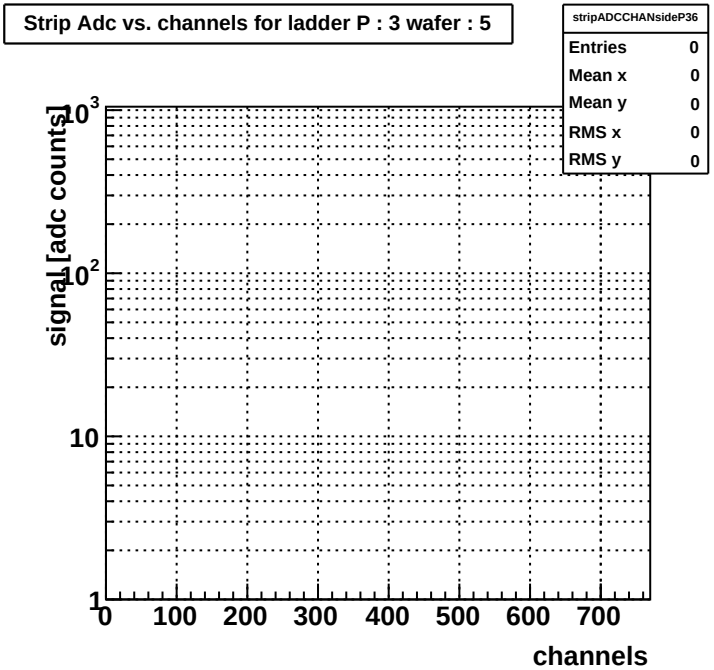

strip ADC for ladder N :3 wafer : 3
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

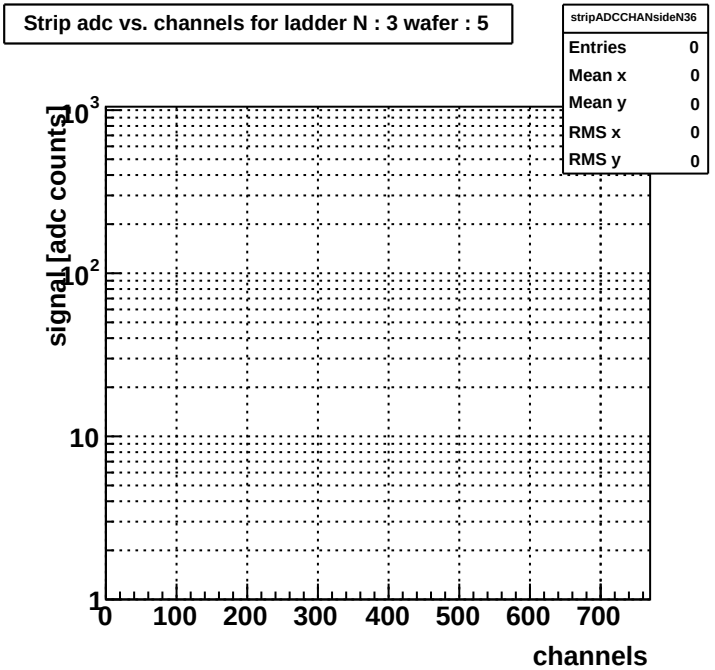

Strip Adc vs. channels for ladder P : 3 wafer : 4
stripADCCHANsideP35
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

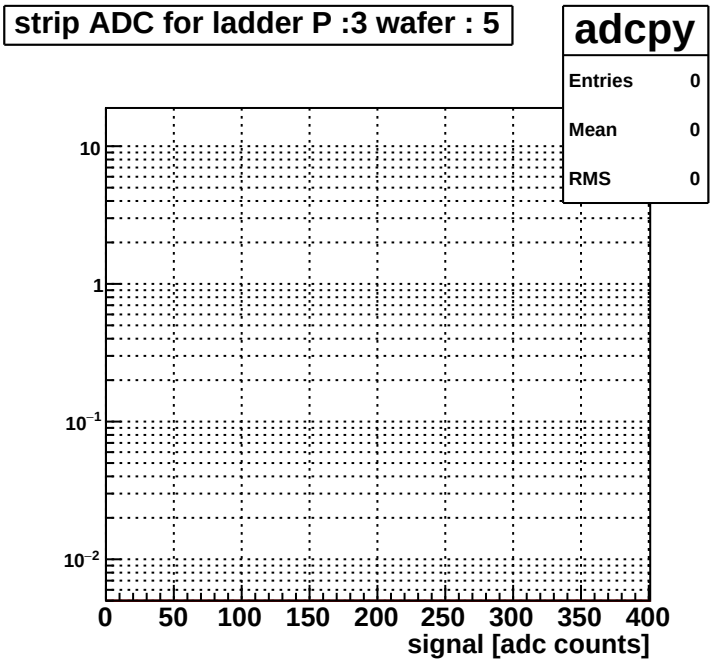

Strip adc vs. channels for ladder N : 3 wafer : 4
stripADCCHANsideN35
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

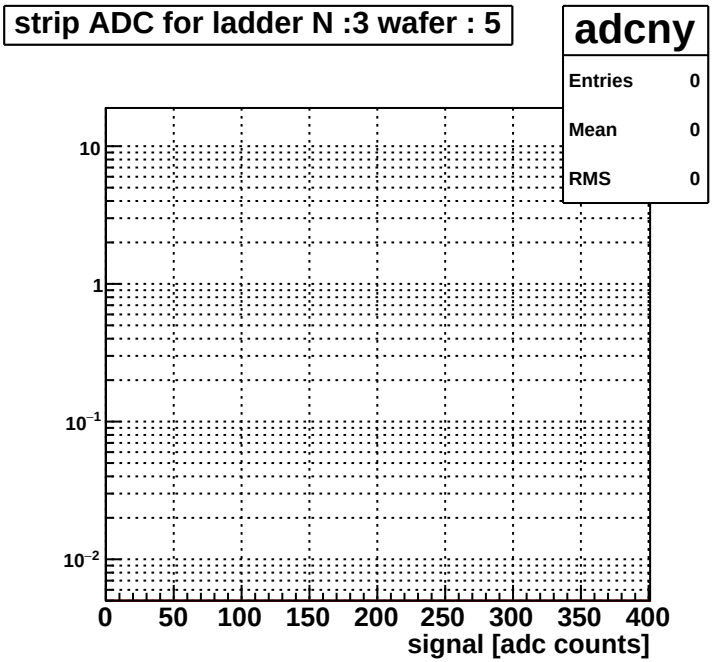

strip ADC for ladder P :3 wafer : 4
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

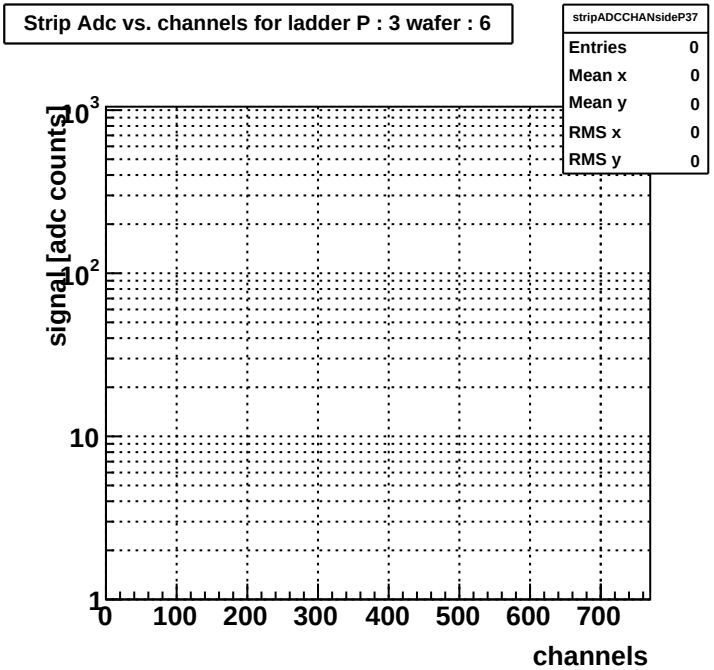

strip ADC for ladder N :3 wafer : 4
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

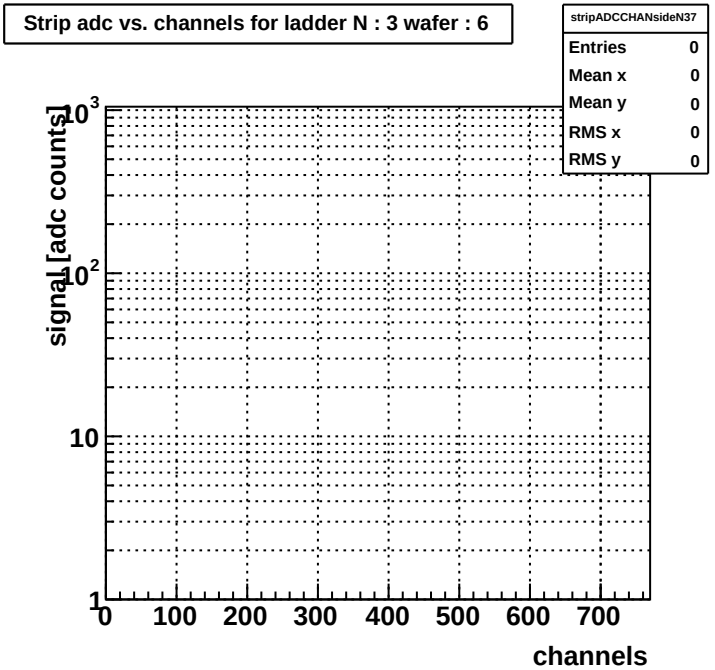

Strip Adc vs. channels for ladder P : 3 wafer : 5
stripADCCHANsideP36
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

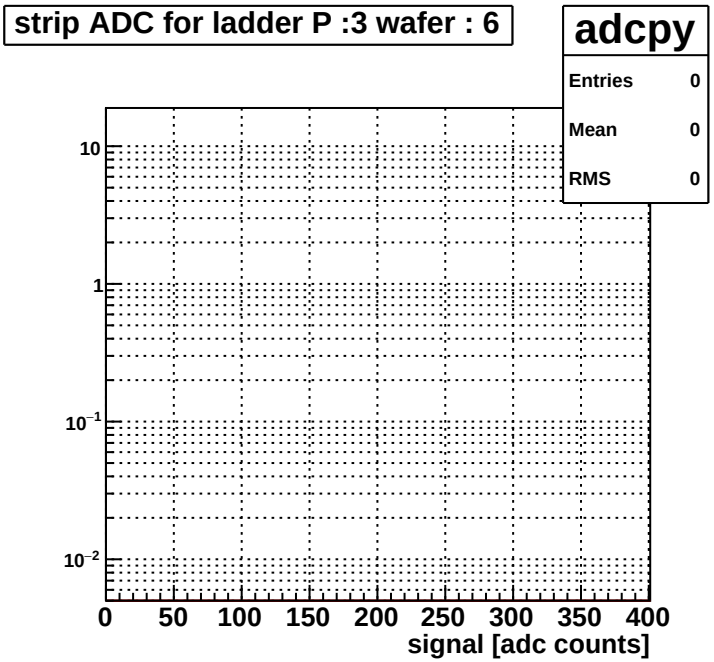

Strip adc vs. channels for ladder N : 3 wafer : 5
stripADCCHANsideN36
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

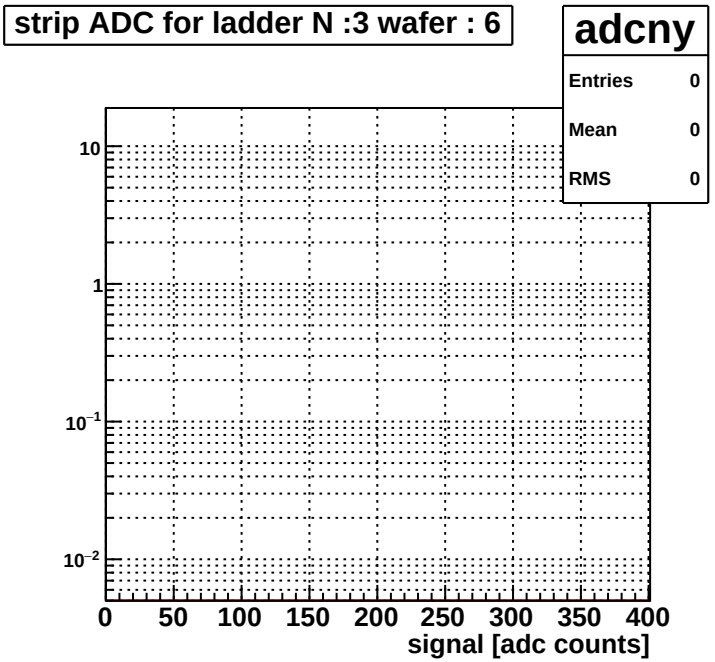

strip ADC for ladder P :3 wafer : 5
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

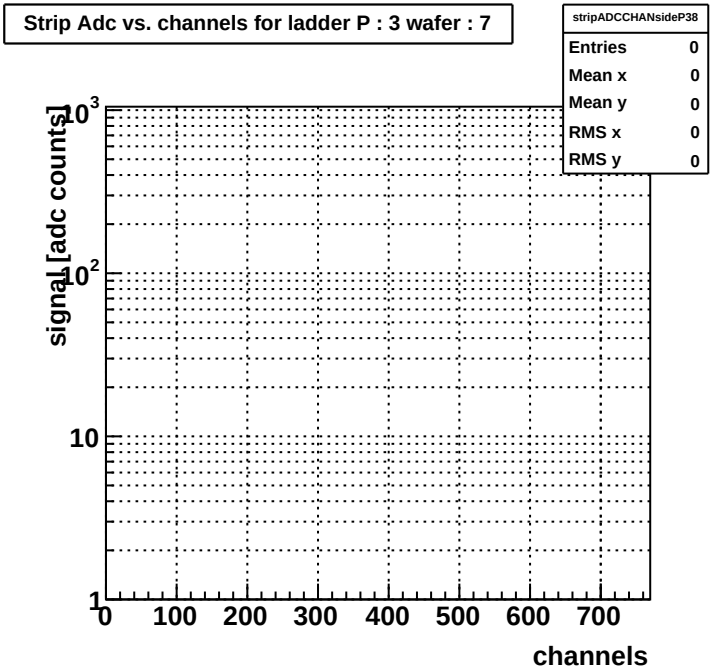

strip ADC for ladder N :3 wafer : 5
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

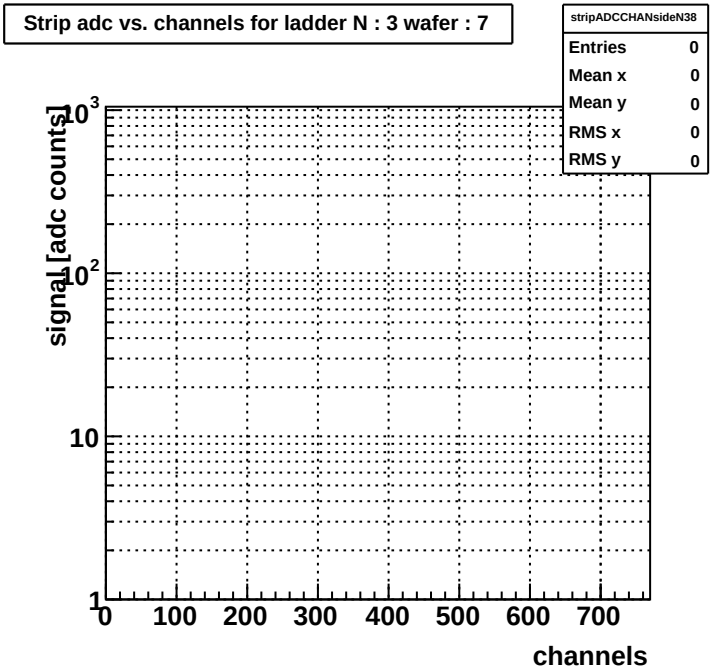

Strip Adc vs. channels for ladder P : 3 wafer : 6
stripADCCHANsideP37
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

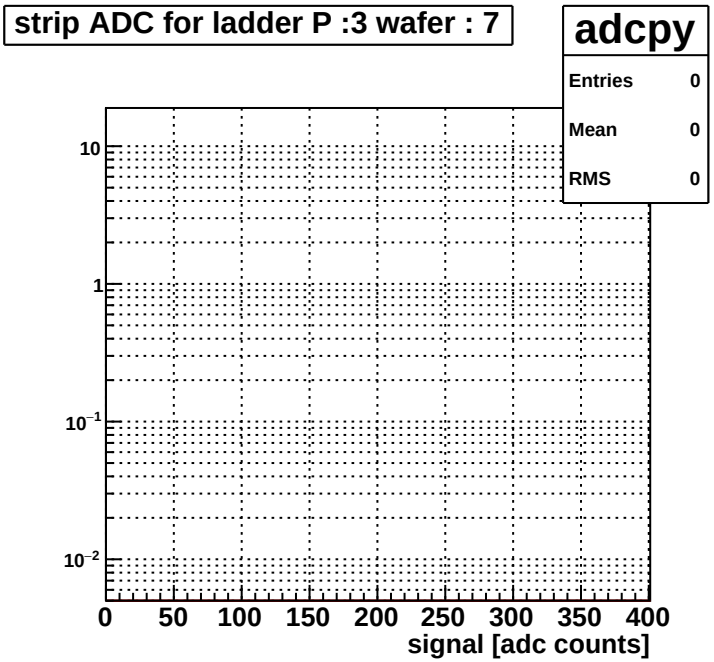

Strip adc vs. channels for ladder N : 3 wafer : 6
stripADCCHANsideN37
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

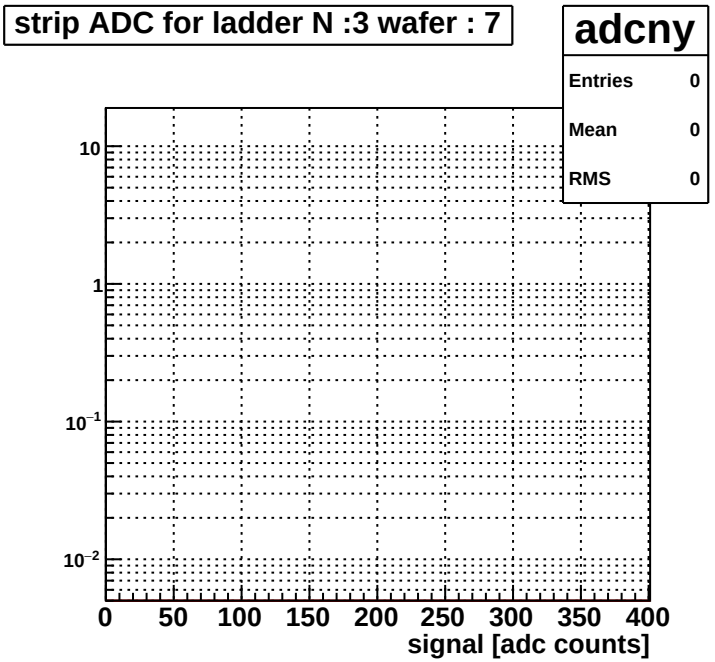

strip ADC for ladder P :3 wafer : 6
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

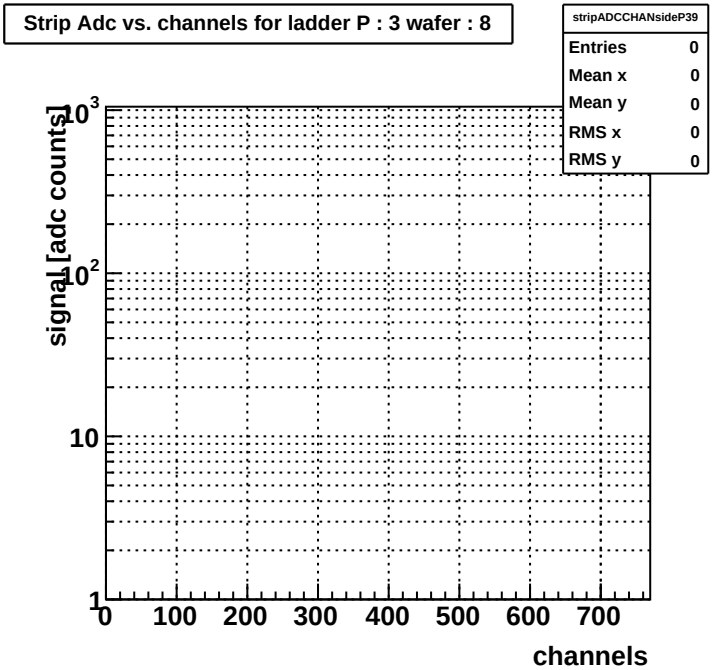

strip ADC for ladder N :3 wafer : 6
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

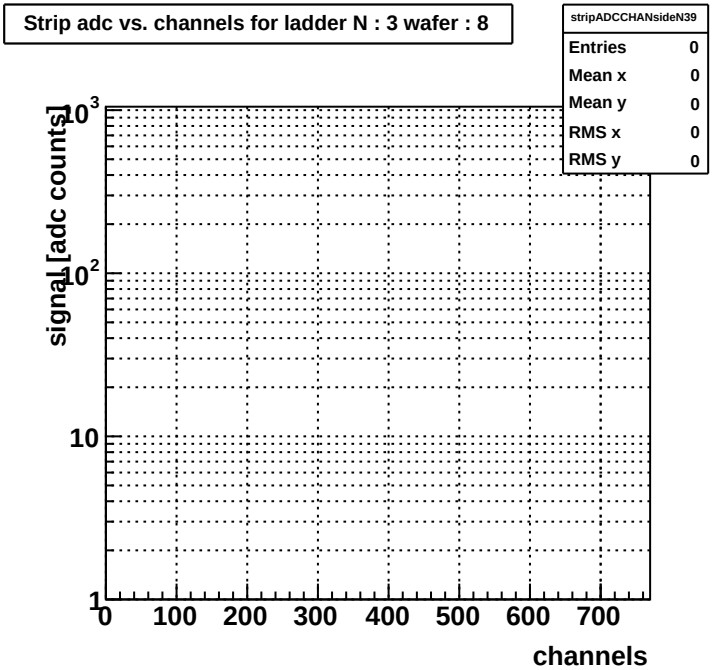

Strip Adc vs. channels for ladder P : 3 wafer : 7
stripADCCHANsideP38
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

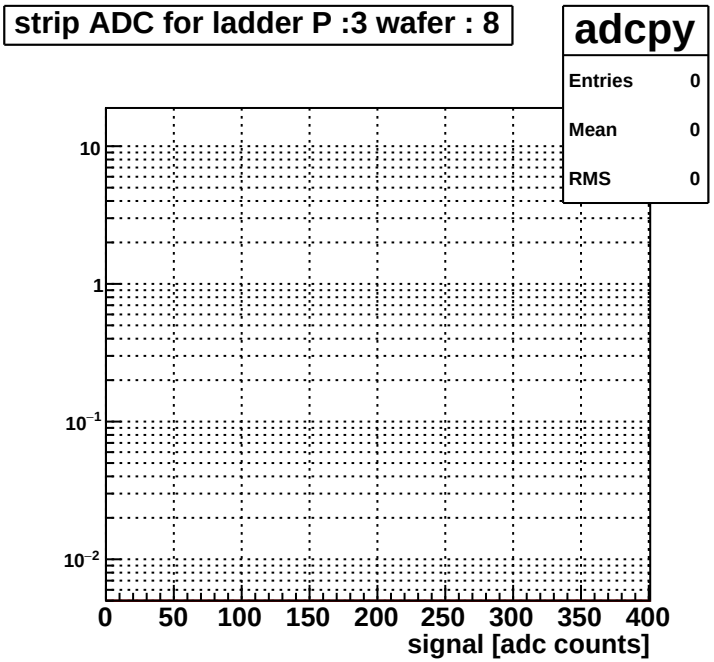

Strip adc vs. channels for ladder N : 3 wafer : 7
stripADCCHANsideN38
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

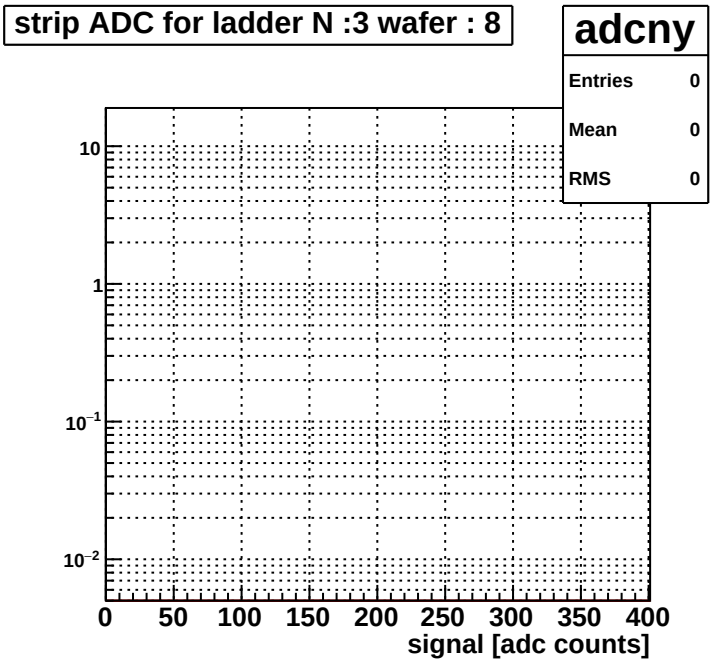

strip ADC for ladder P :3 wafer : 7
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

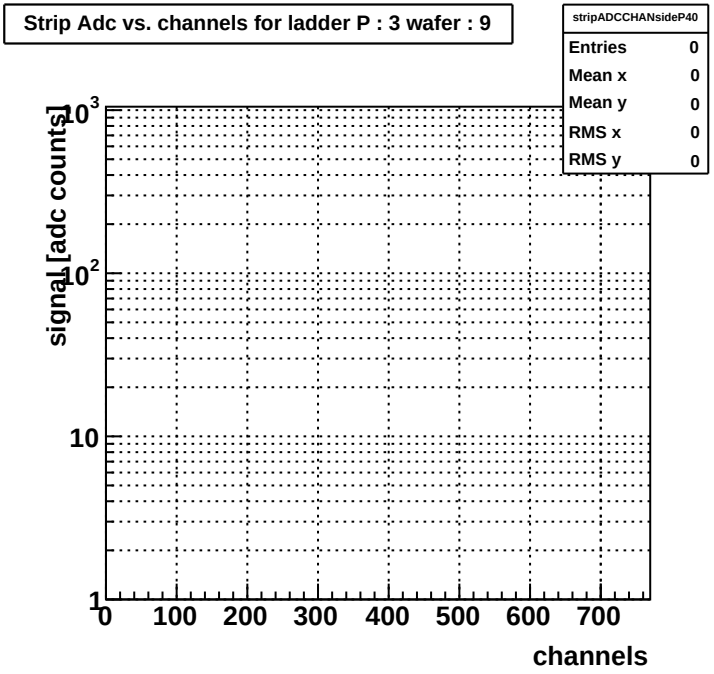

strip ADC for ladder N :3 wafer : 7
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

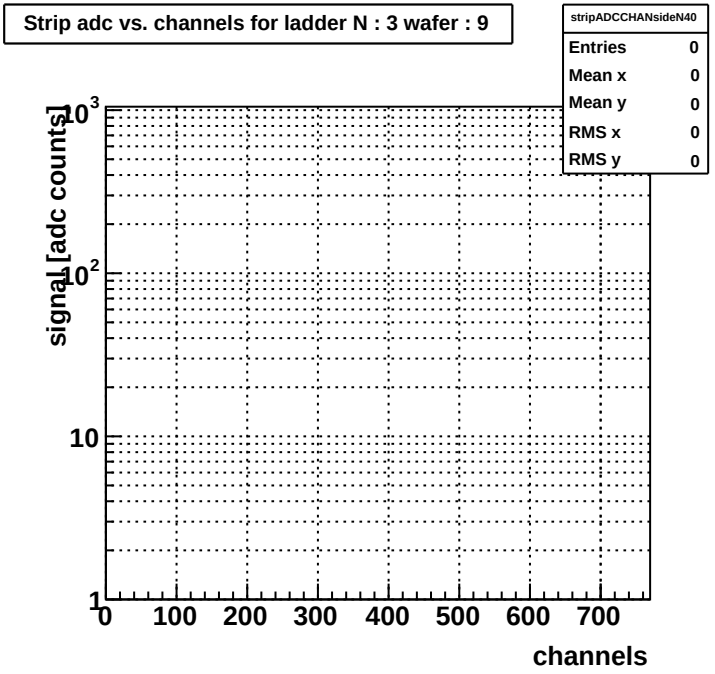

Strip Adc vs. channels for ladder P : 3 wafer : 8
stripADCCHANsideP39
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

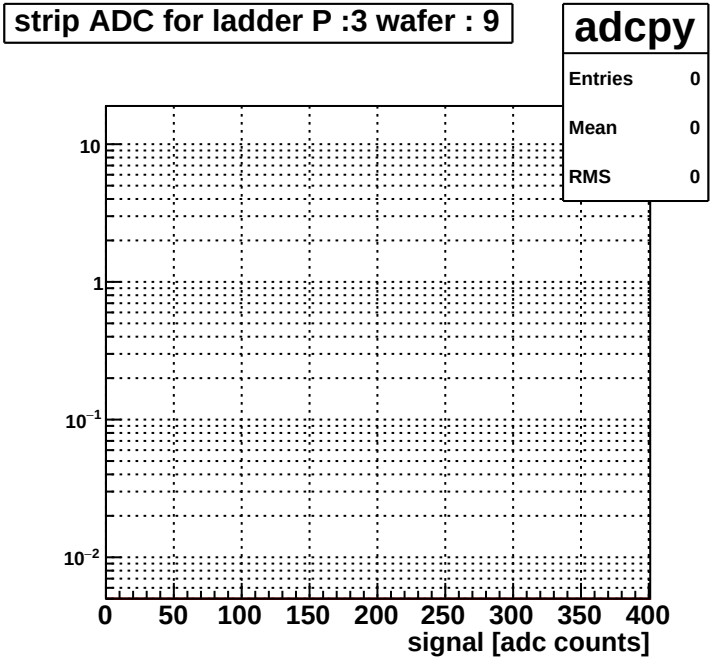

Strip adc vs. channels for ladder N : 3 wafer : 8
stripADCCHANsideN39
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

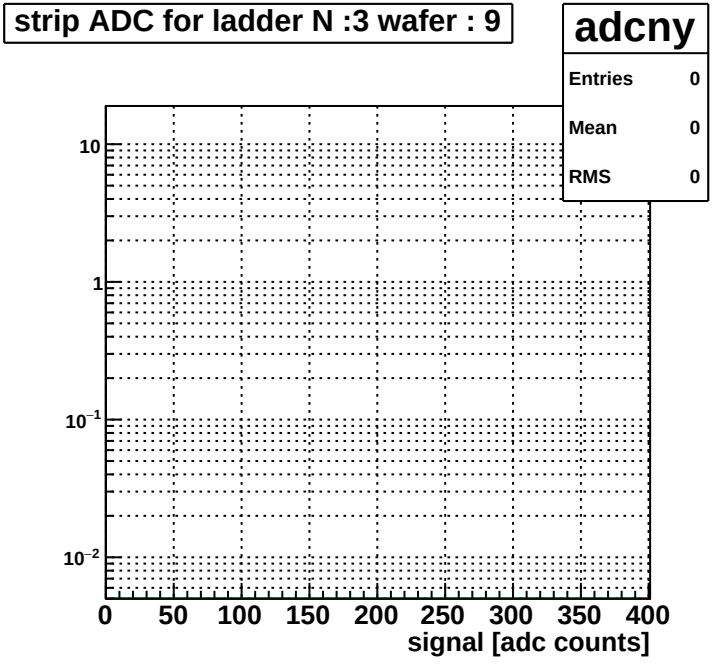

strip ADC for ladder P :3 wafer : 8
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

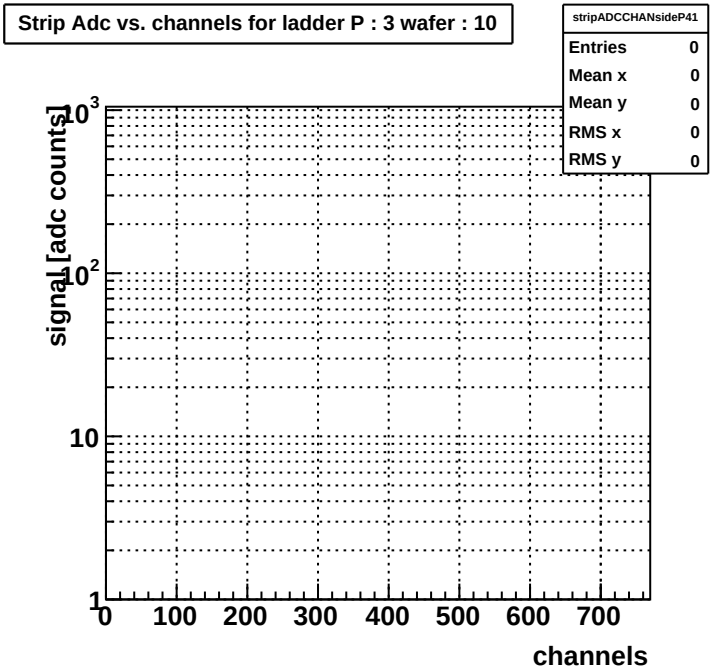

strip ADC for ladder N :3 wafer : 8
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

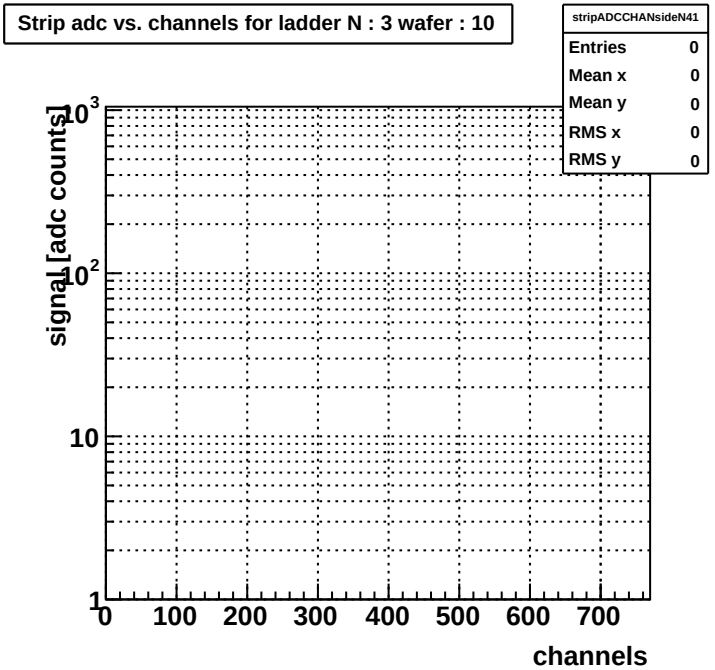

Strip Adc vs. channels for ladder P : 3 wafer : 9
stripADCCHANsideP40
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

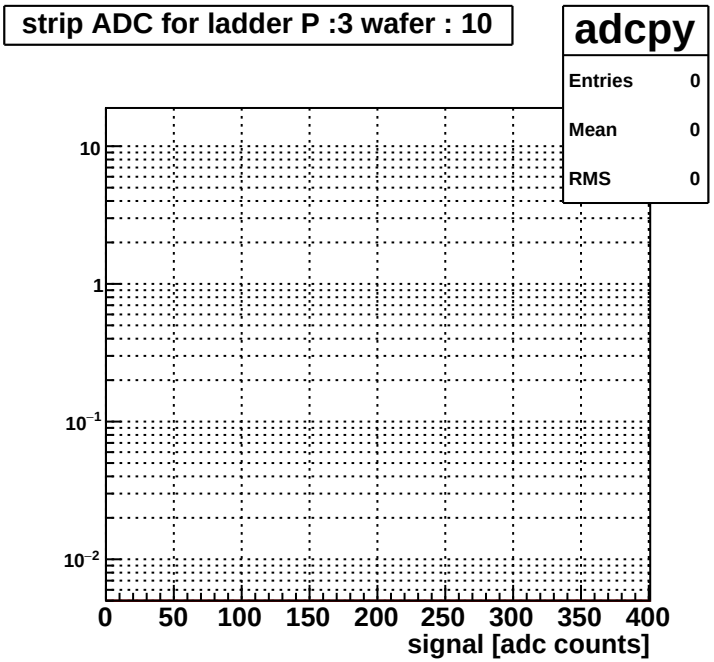

Strip adc vs. channels for ladder N : 3 wafer : 9
stripADCCHANsideN40
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

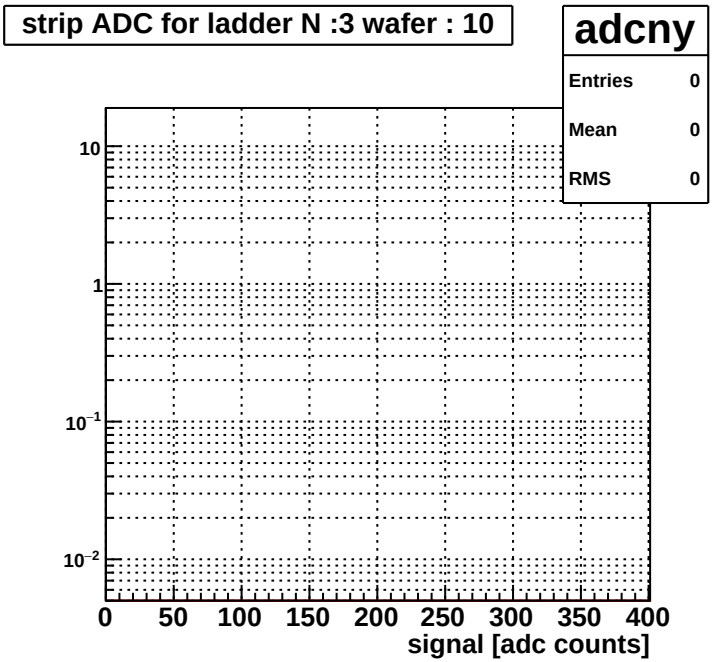

strip ADC for ladder P :3 wafer : 9
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

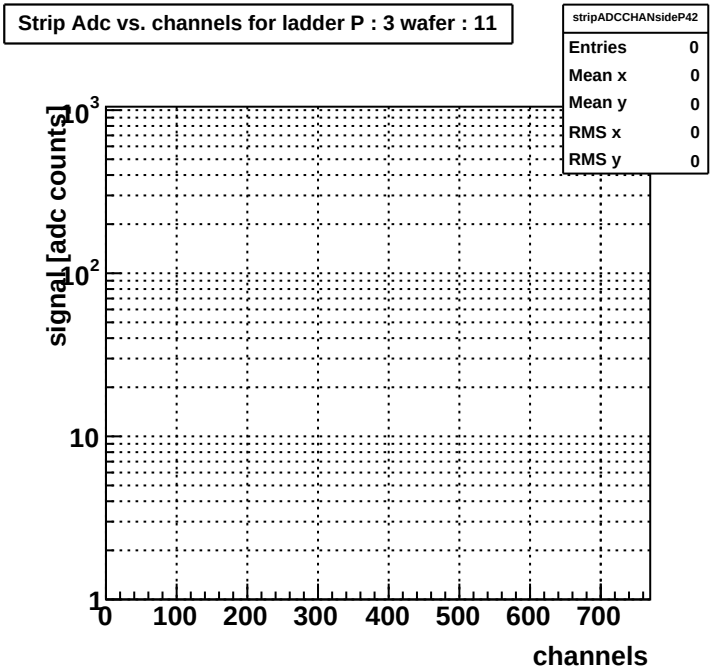

strip ADC for ladder N :3 wafer : 9
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

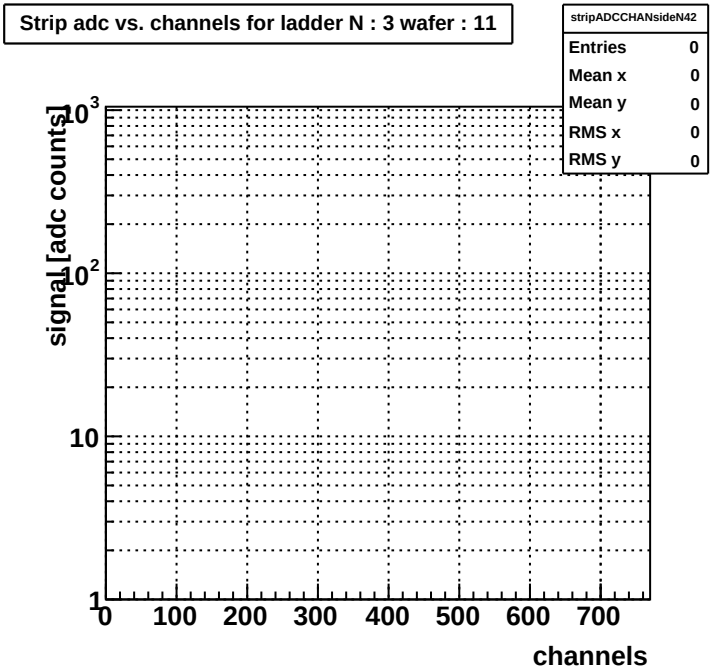

Strip Adc vs. channels for ladder P : 3 wafer : 10
stripADCCHANsideP41
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

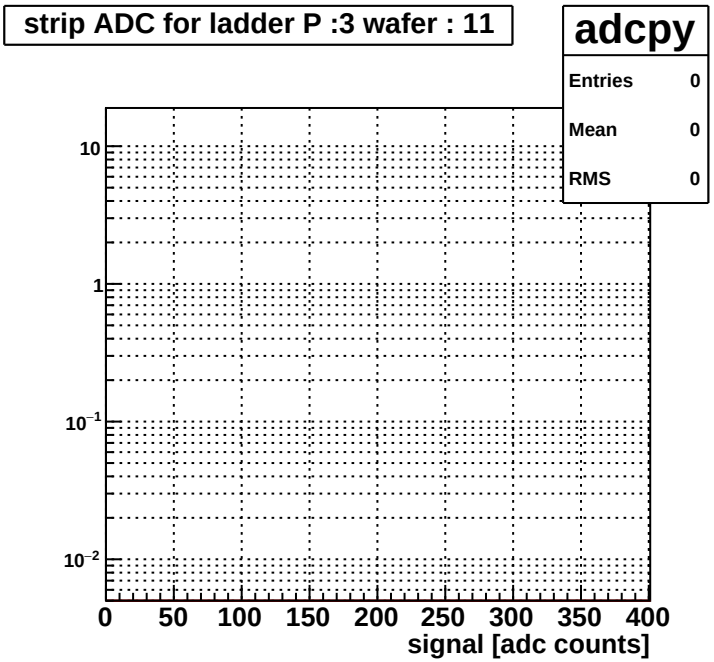

Strip adc vs. channels for ladder N : 3 wafer : 10
stripADCCHANsideN41
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

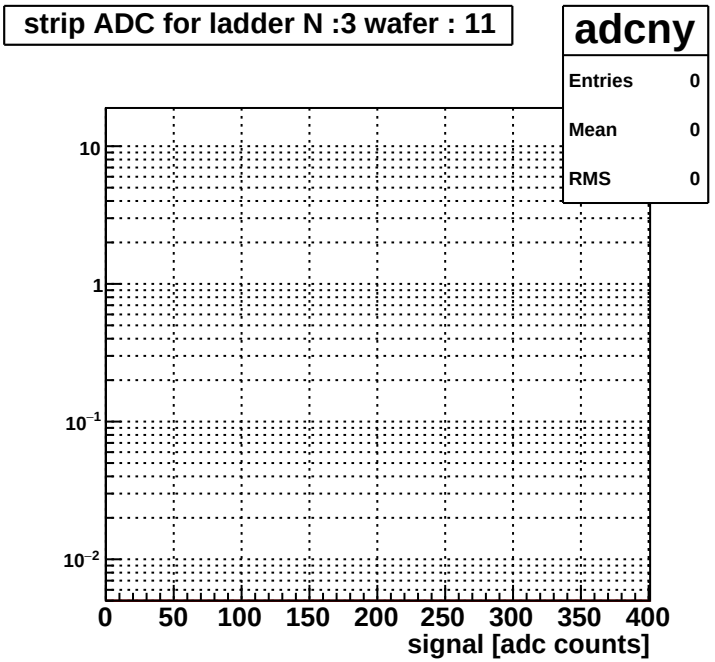

strip ADC for ladder P :3 wafer : 10
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

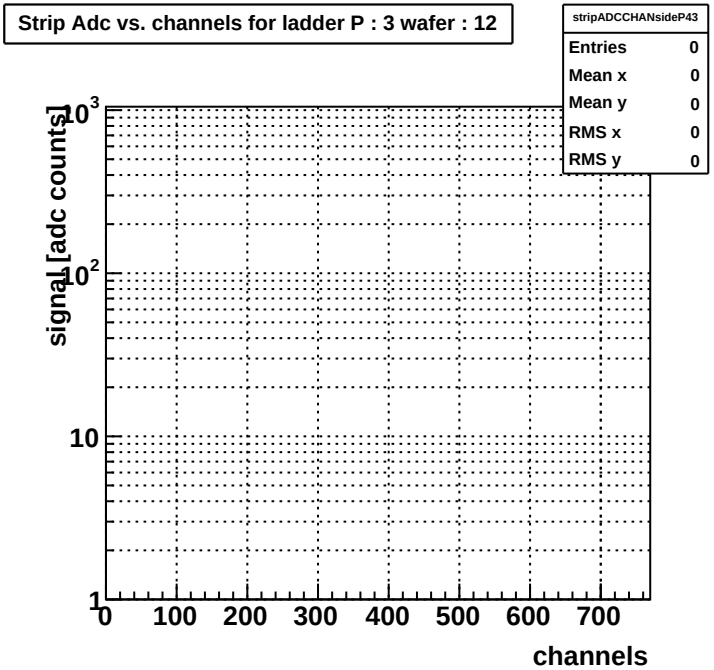

strip ADC for ladder N :3 wafer : 10
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

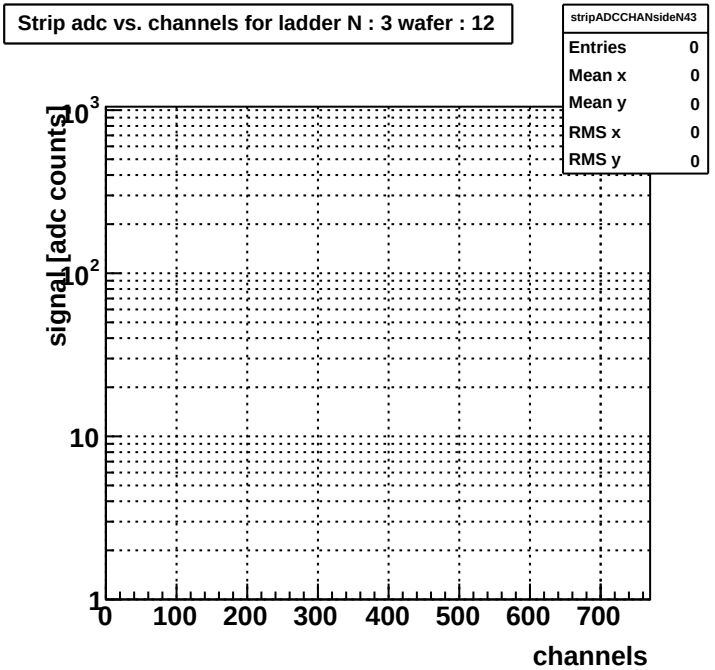

Strip Adc vs. channels for ladder P : 3 wafer : 11
stripADCCHANsideP42
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

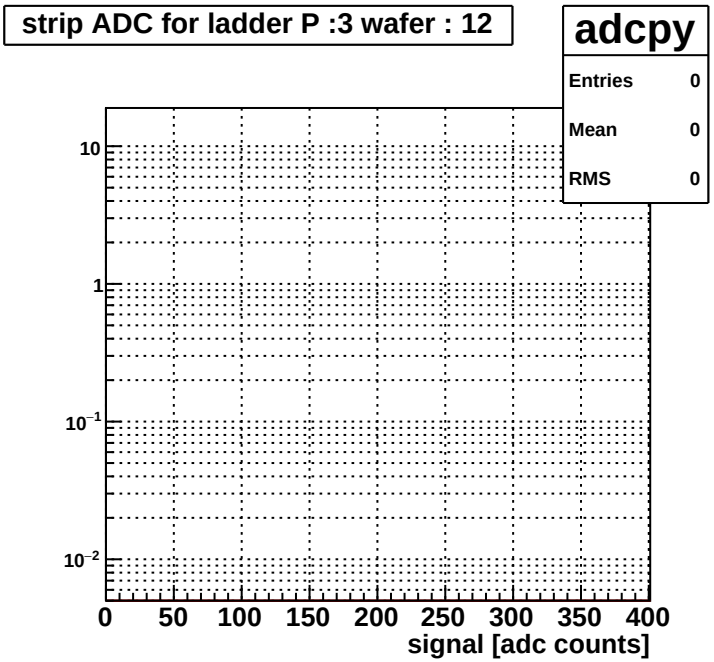

Strip adc vs. channels for ladder N : 3 wafer : 11
stripADCCHANsideN42
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

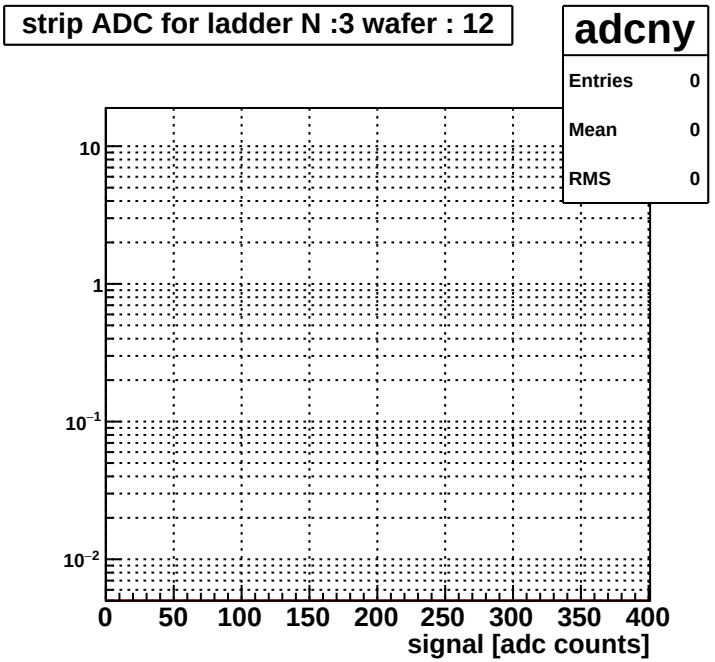

strip ADC for ladder P :3 wafer : 11
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

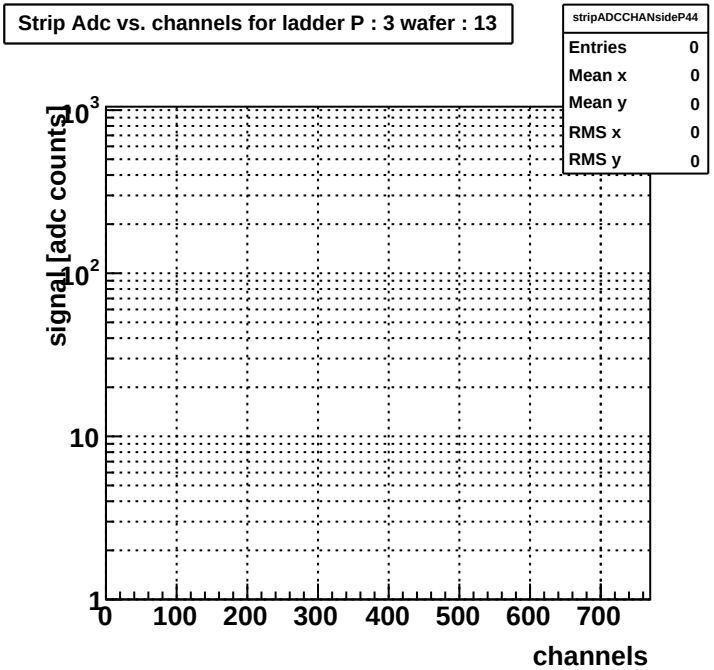

strip ADC for ladder N :3 wafer : 11
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

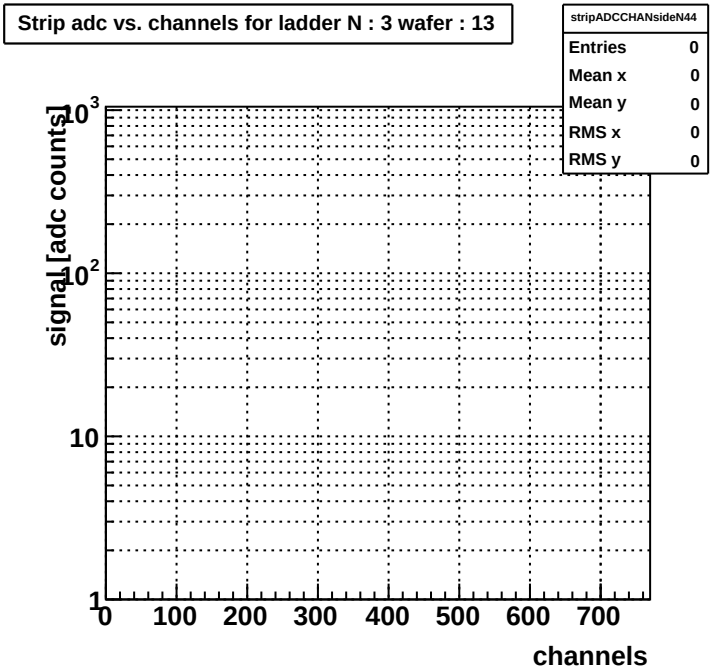

Strip Adc vs. channels for ladder P : 3 wafer : 12
stripADCCHANsideP43
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

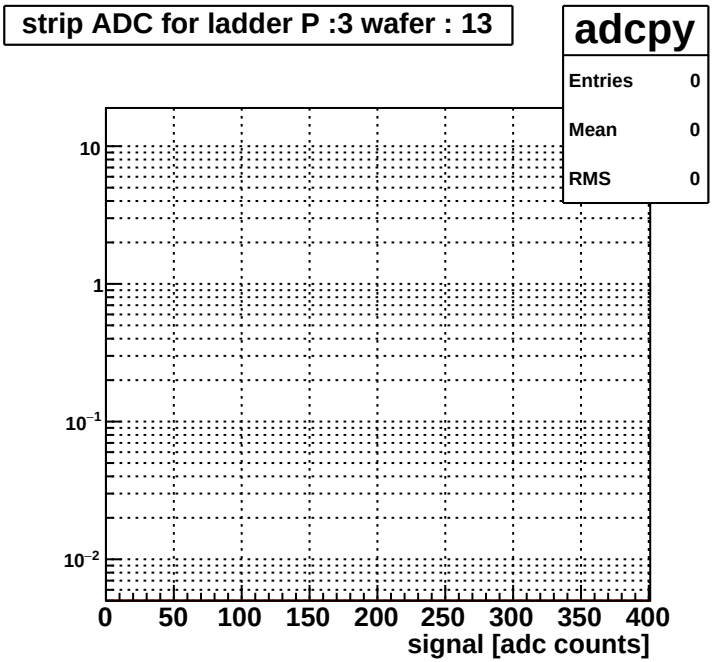

Strip adc vs. channels for ladder N : 3 wafer : 12
stripADCCHANsideN43
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

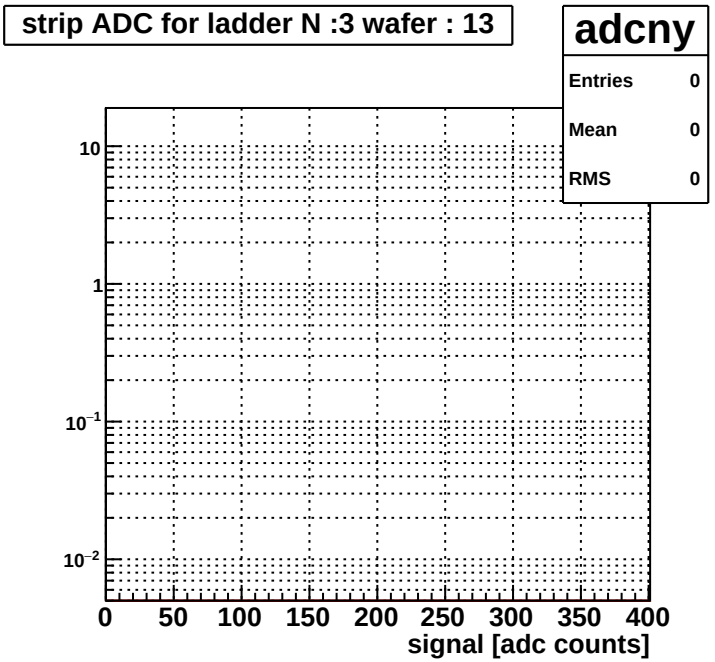

strip ADC for ladder P :3 wafer : 12
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

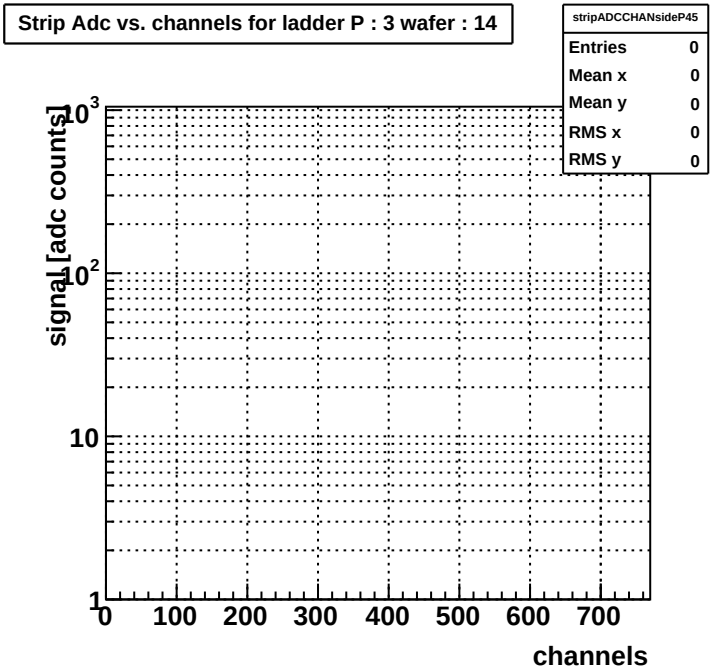

strip ADC for ladder N :3 wafer : 12
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

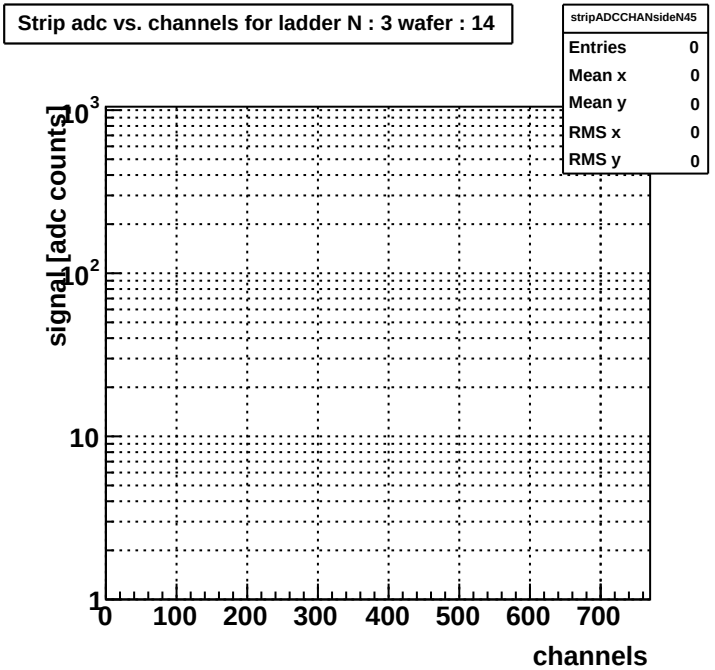

Strip Adc vs. channels for ladder P : 3 wafer : 13
stripADCCHANsideP44
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

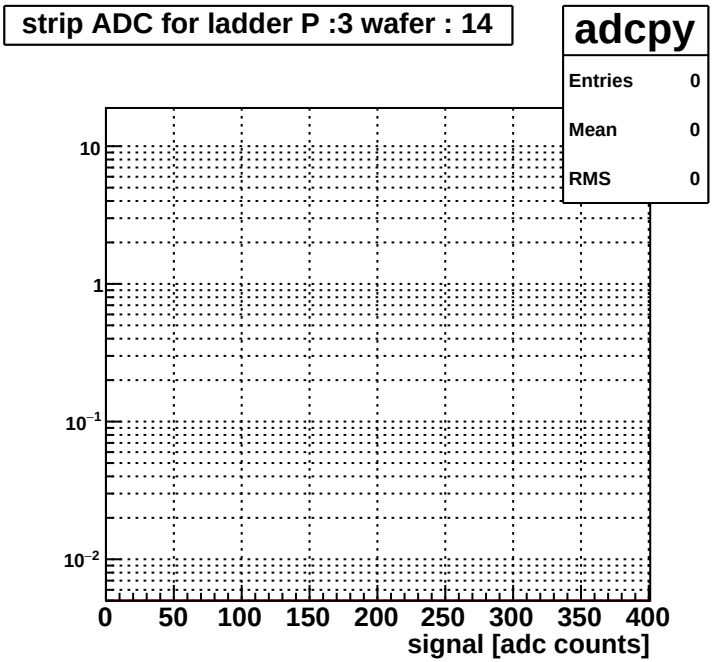

Strip adc vs. channels for ladder N : 3 wafer : 13
stripADCCHANsideN44
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

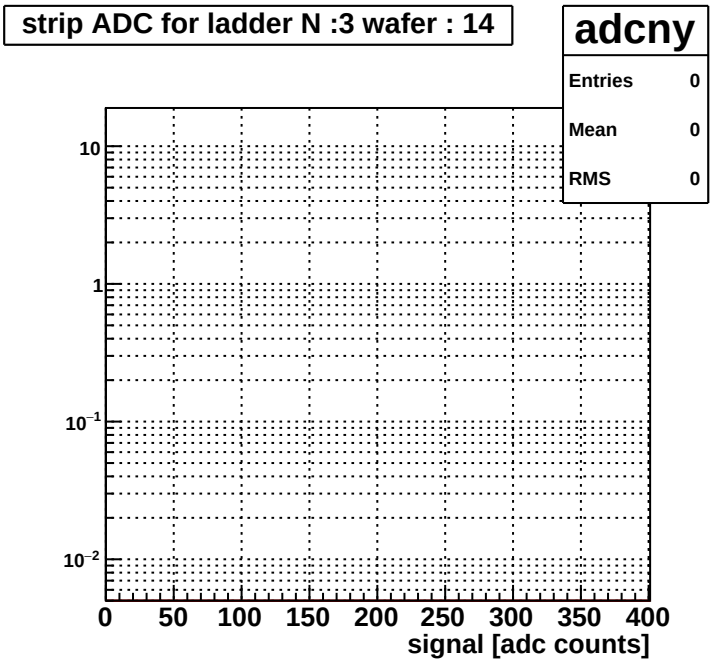

strip ADC for ladder P :3 wafer : 13
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

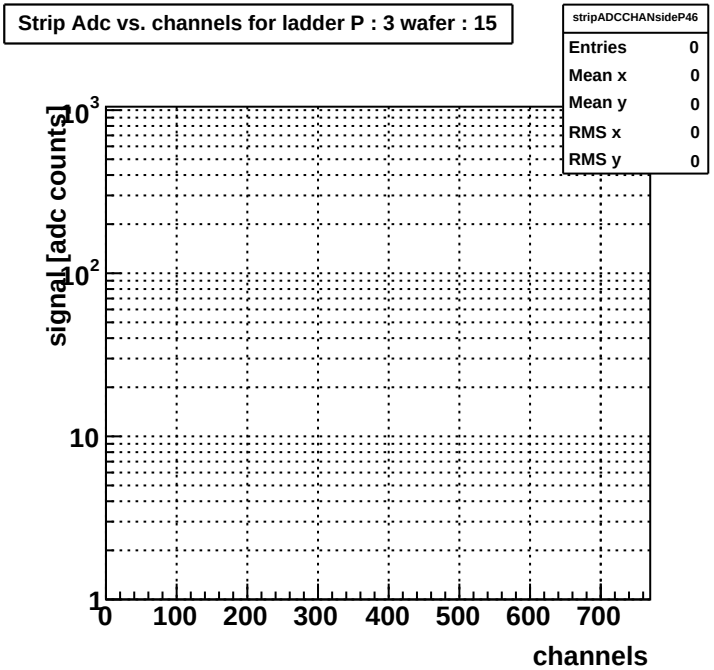

strip ADC for ladder N :3 wafer : 13
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

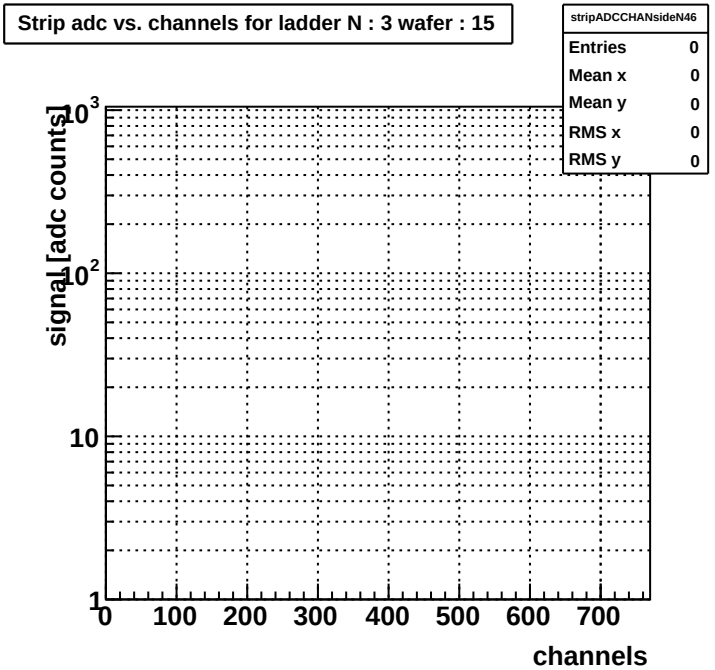

Strip Adc vs. channels for ladder P : 3 wafer : 14
stripADCCHANsideP45
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

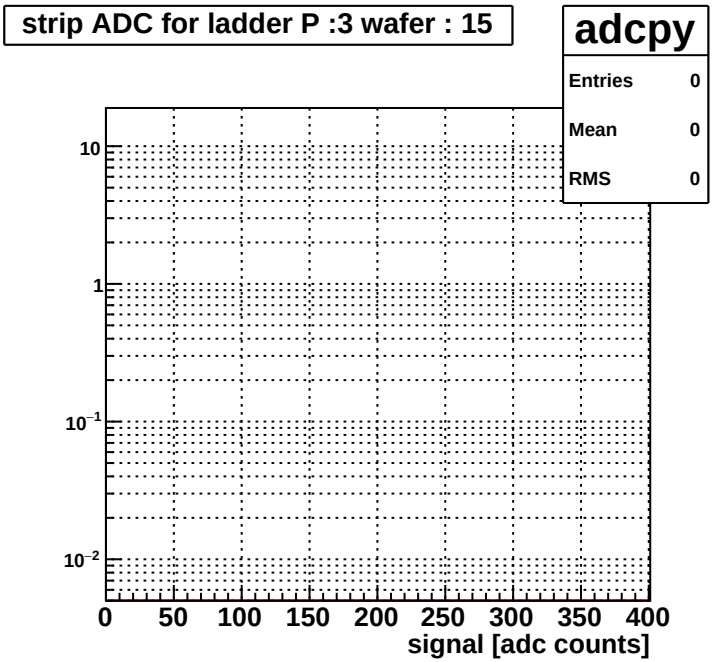

Strip adc vs. channels for ladder N : 3 wafer : 14
stripADCCHANsideN45
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

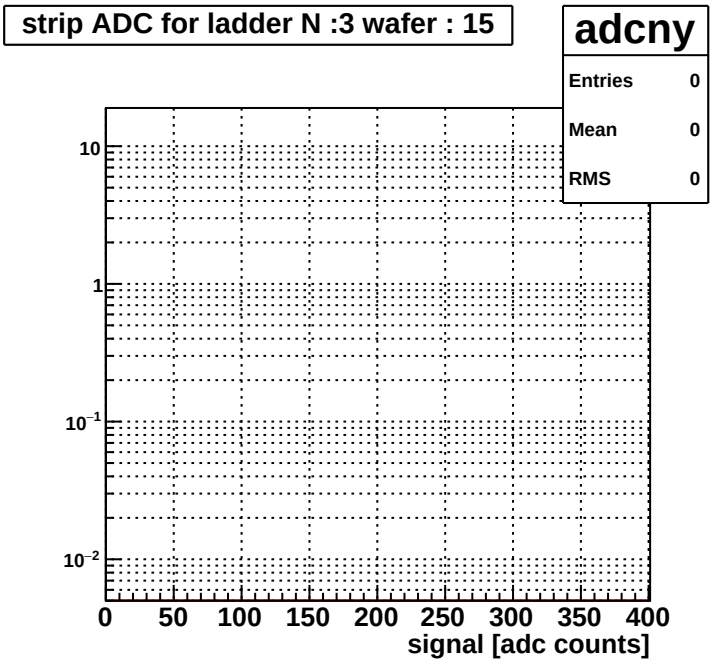

strip ADC for ladder P :3 wafer : 14
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

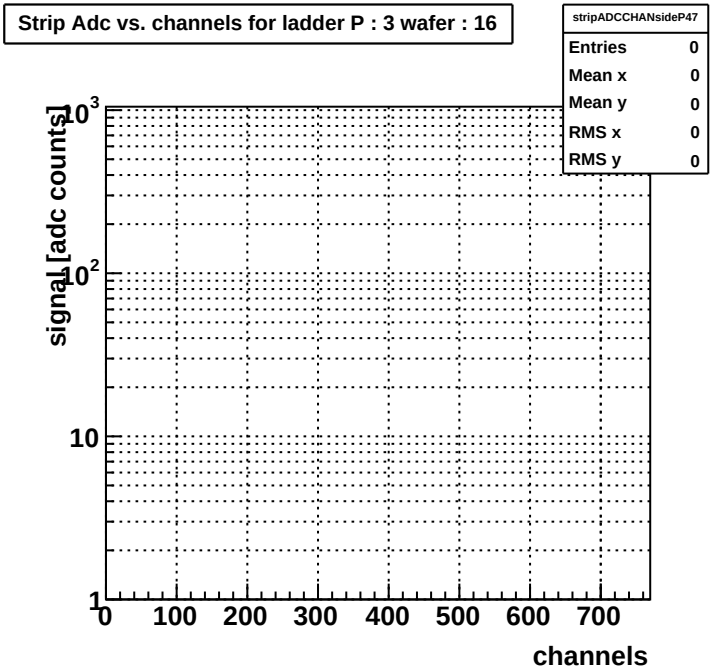

strip ADC for ladder N :3 wafer : 14
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

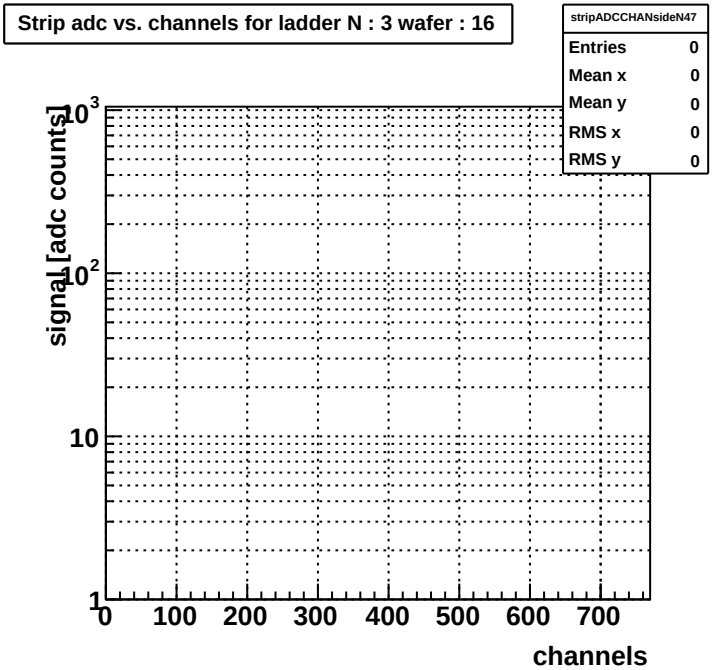

Strip Adc vs. channels for ladder P : 3 wafer : 15
stripADCCHANsideP46
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

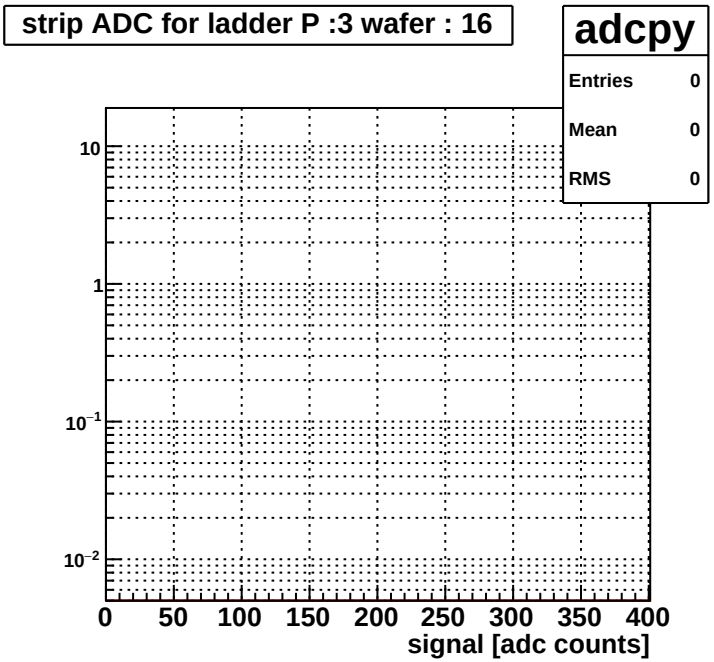

Strip adc vs. channels for ladder N : 3 wafer : 15
stripADCCHANsideN46
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

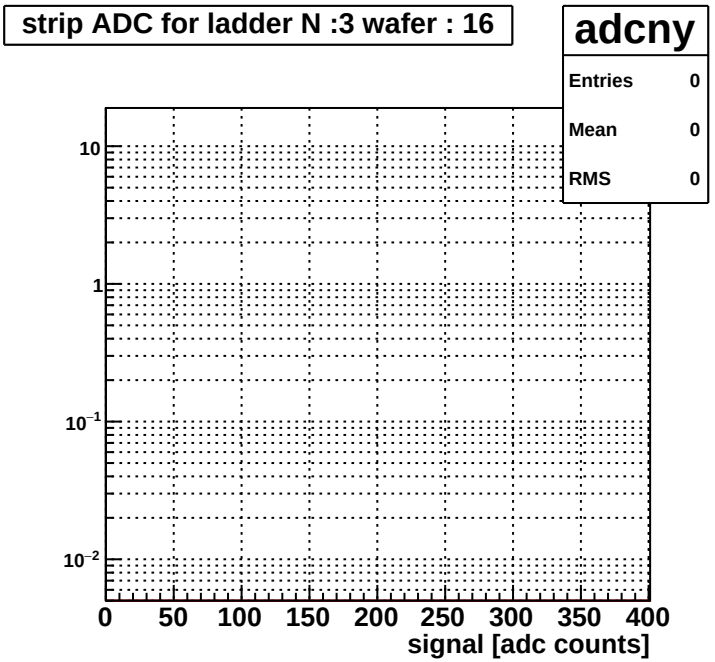

strip ADC for ladder P :3 wafer : 15
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

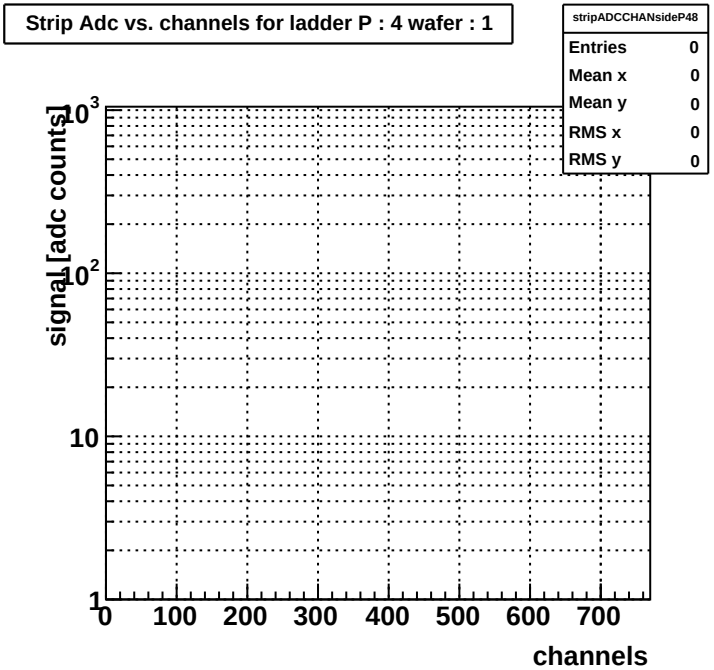

strip ADC for ladder N :3 wafer : 15
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

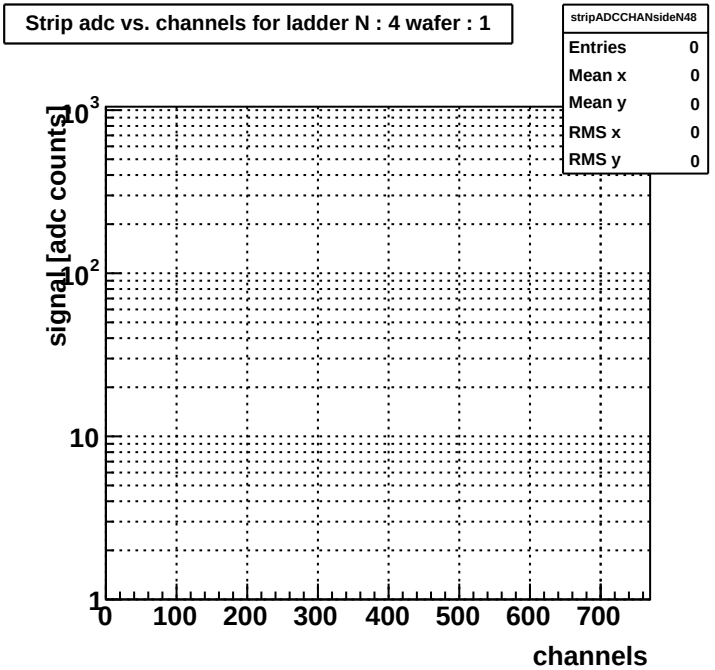

Strip Adc vs. channels for ladder P : 3 wafer : 16
stripADCCHANsideP47
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

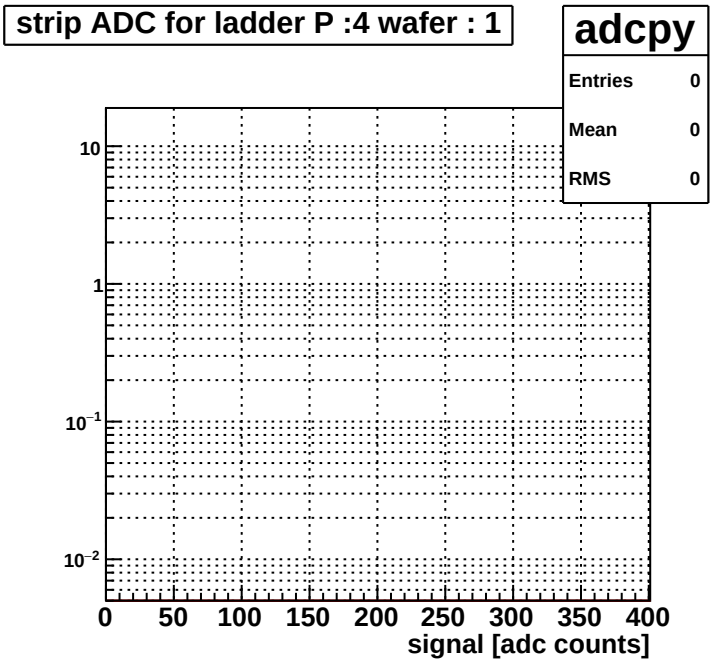

Strip adc vs. channels for ladder N : 3 wafer : 16
stripADCCHANsideN47
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

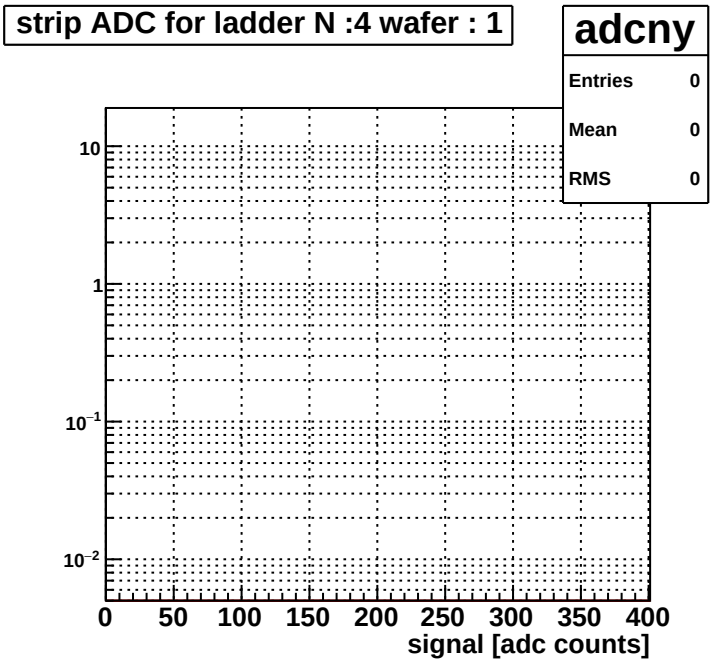

strip ADC for ladder P :3 wafer : 16
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

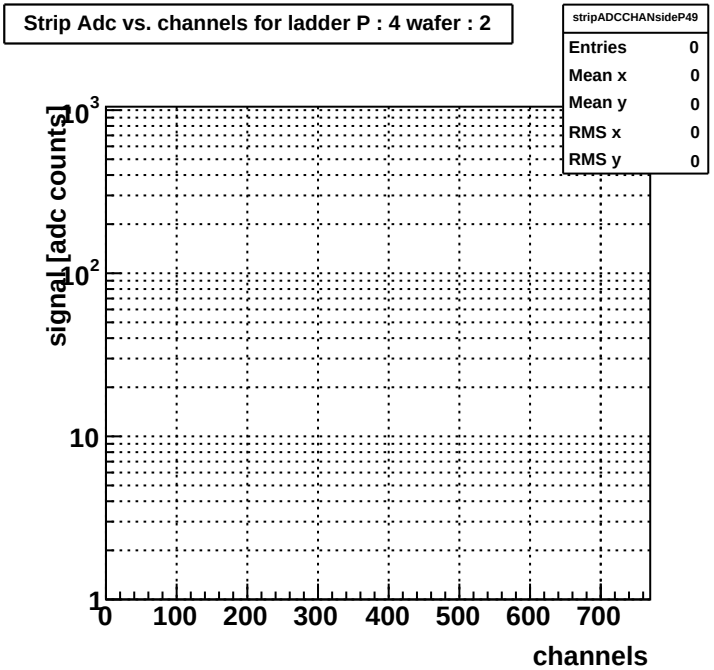

strip ADC for ladder N :3 wafer : 16
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

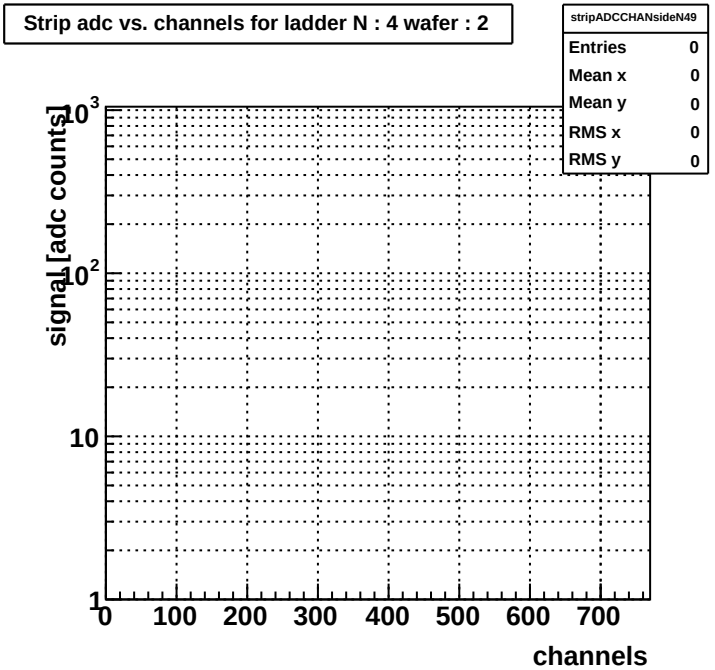

Strip Adc vs. channels for ladder P : 4 wafer : 1
stripADCCHANsideP48
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

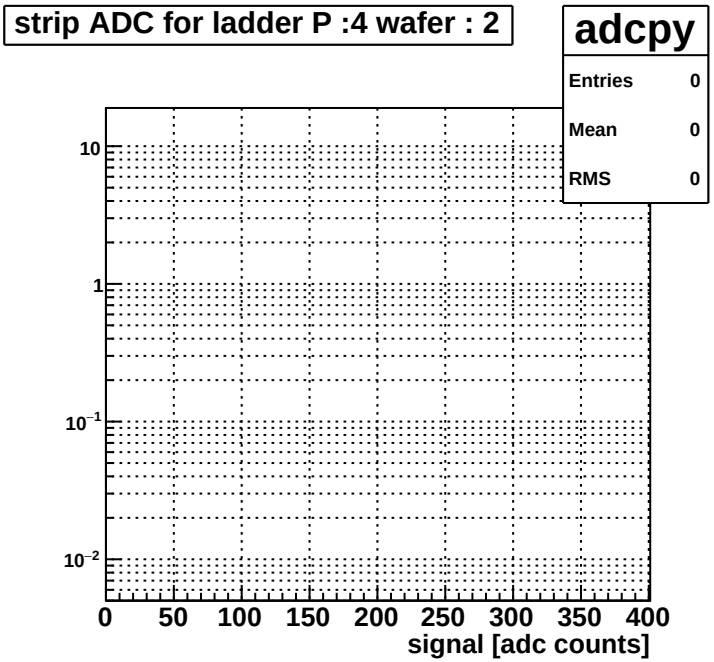

Strip adc vs. channels for ladder N : 4 wafer : 1
stripADCCHANsideN48
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

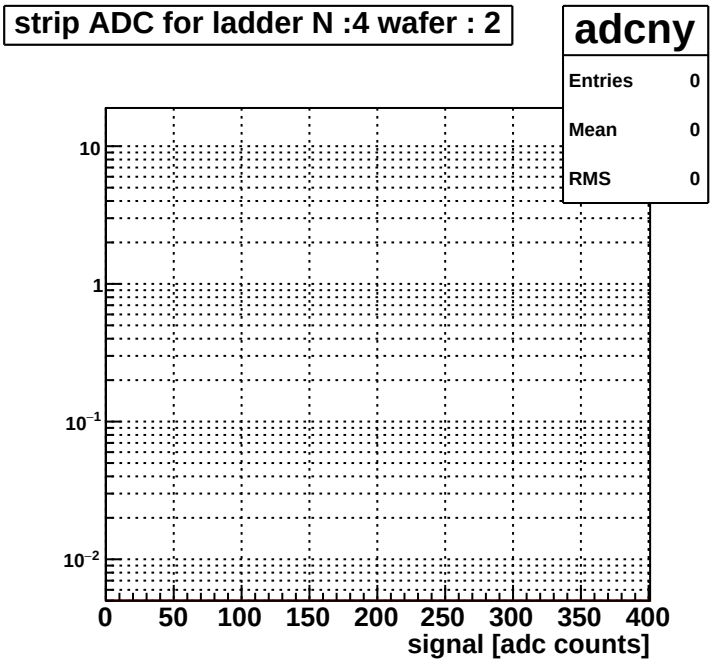

strip ADC for ladder P :4 wafer : 1
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

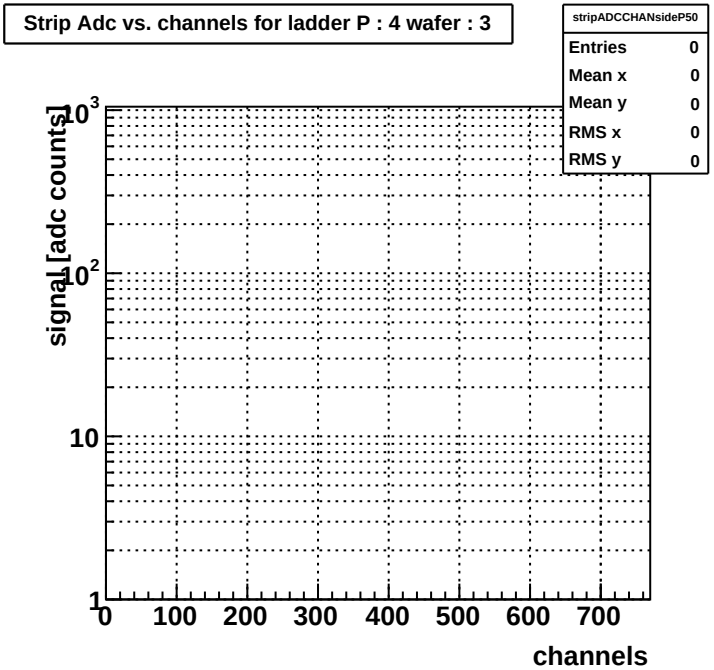

strip ADC for ladder N :4 wafer : 1
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

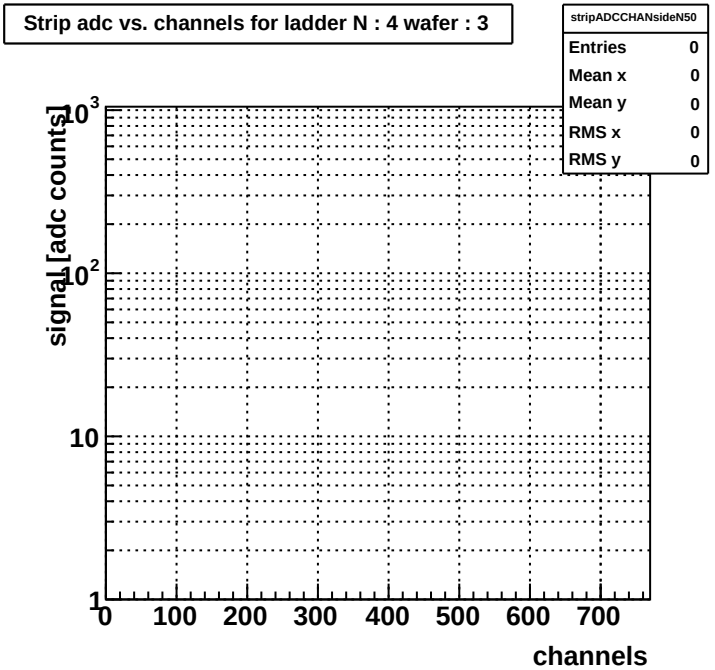

Strip Adc vs. channels for ladder P : 4 wafer : 2
stripADCCHANsideP49
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

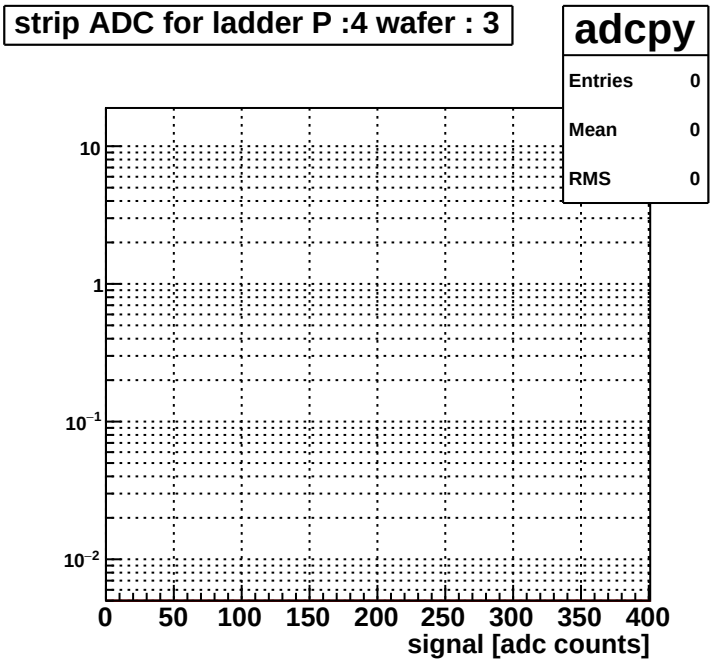

Strip adc vs. channels for ladder N : 4 wafer : 2
stripADCCHANsideN49
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

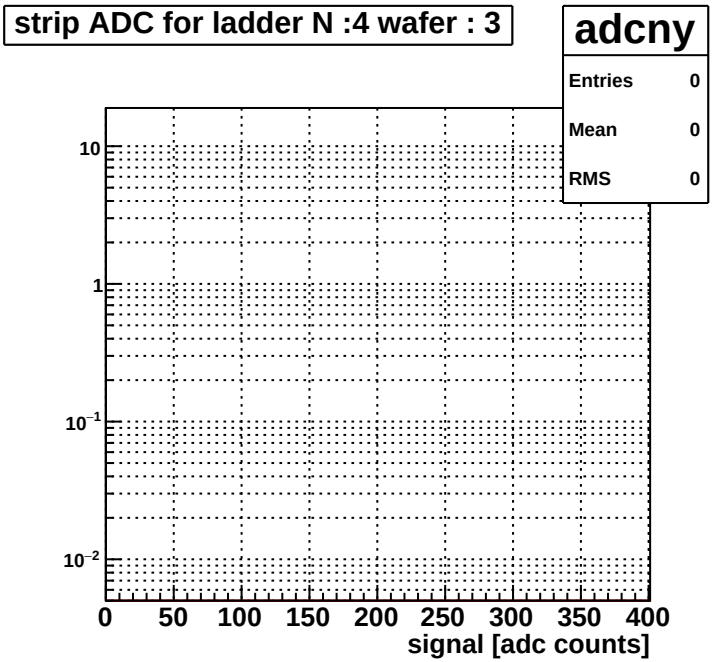

strip ADC for ladder P :4 wafer : 2
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

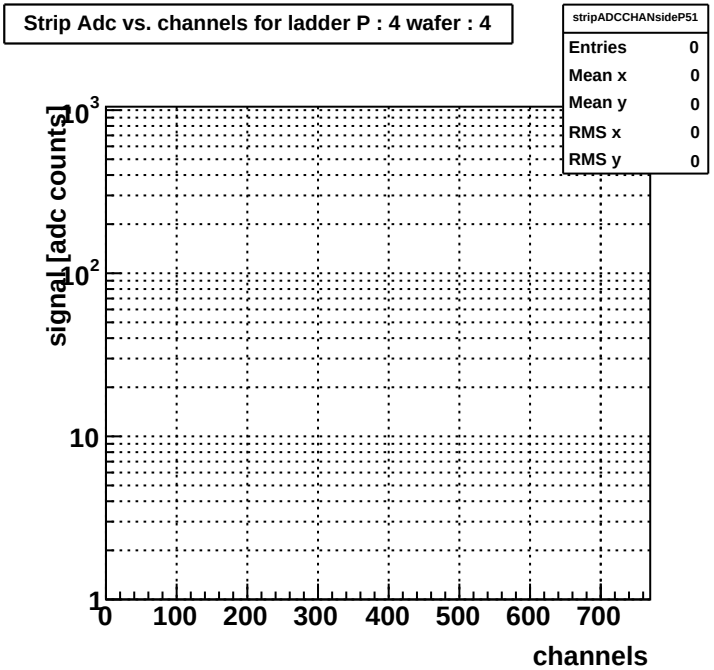

strip ADC for ladder N :4 wafer : 2
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

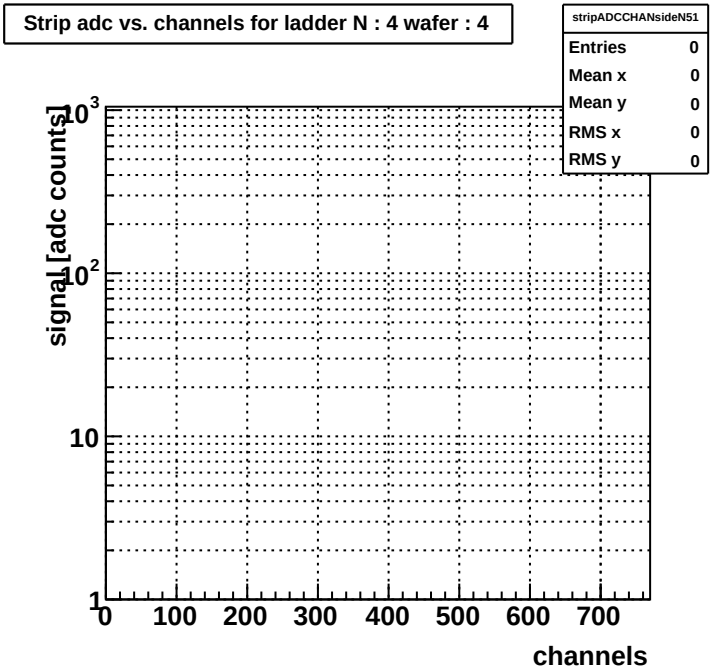

Strip Adc vs. channels for ladder P : 4 wafer : 3
stripADCCHANsideP50
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

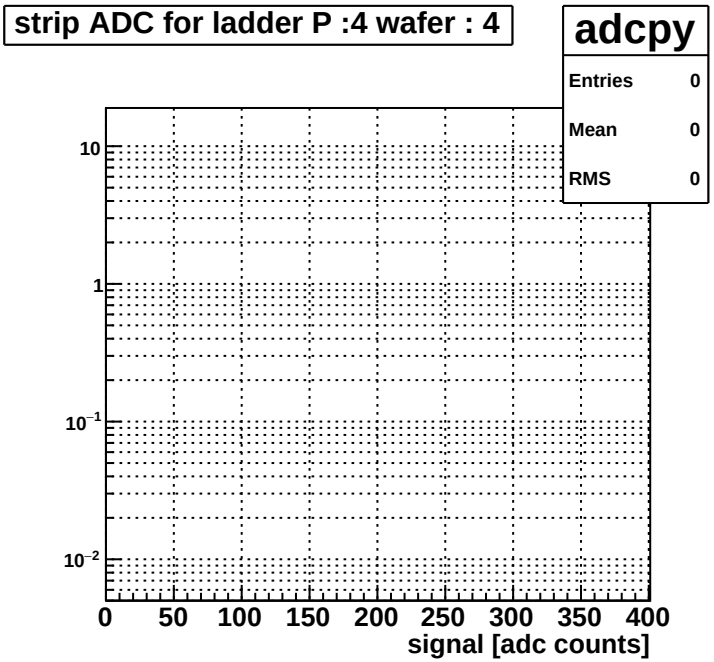

Strip adc vs. channels for ladder N : 4 wafer : 3
stripADCCHANsideN50
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

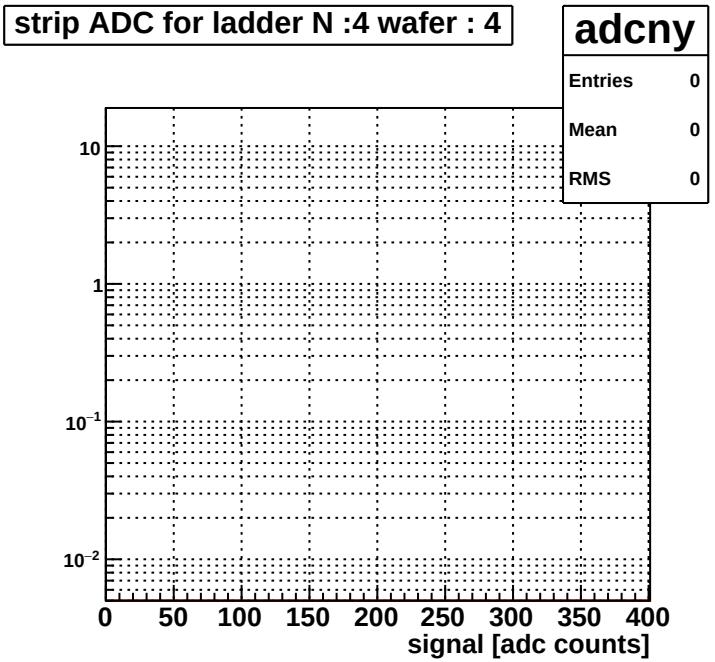

strip ADC for ladder P :4 wafer : 3
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

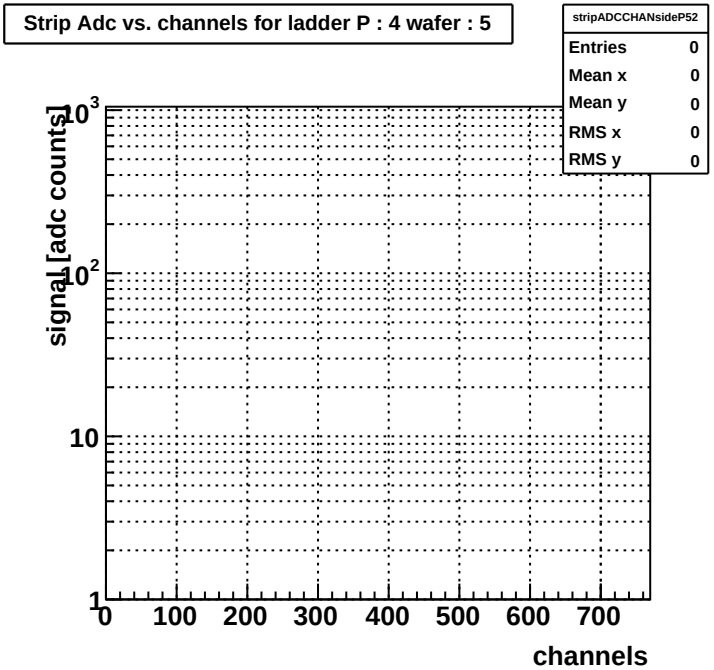

strip ADC for ladder N :4 wafer : 3
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

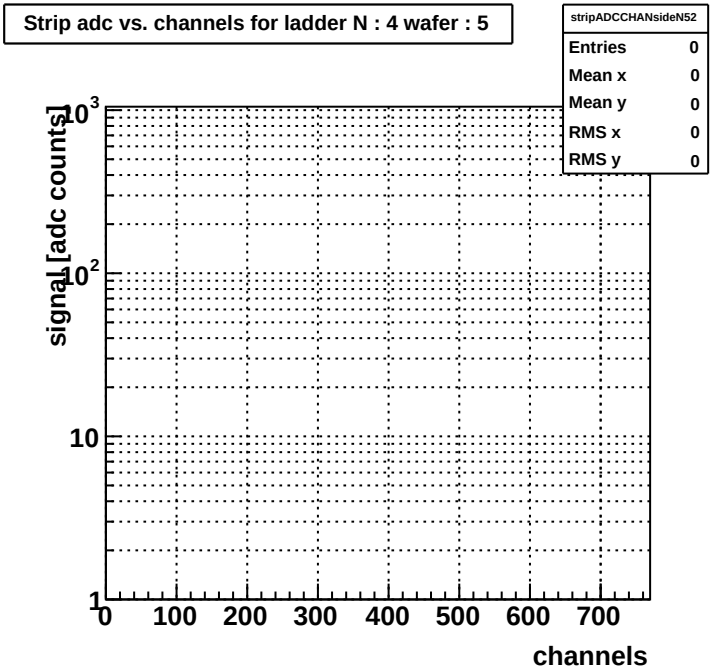

Strip Adc vs. channels for ladder P : 4 wafer : 4
stripADCCHANsideP51
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

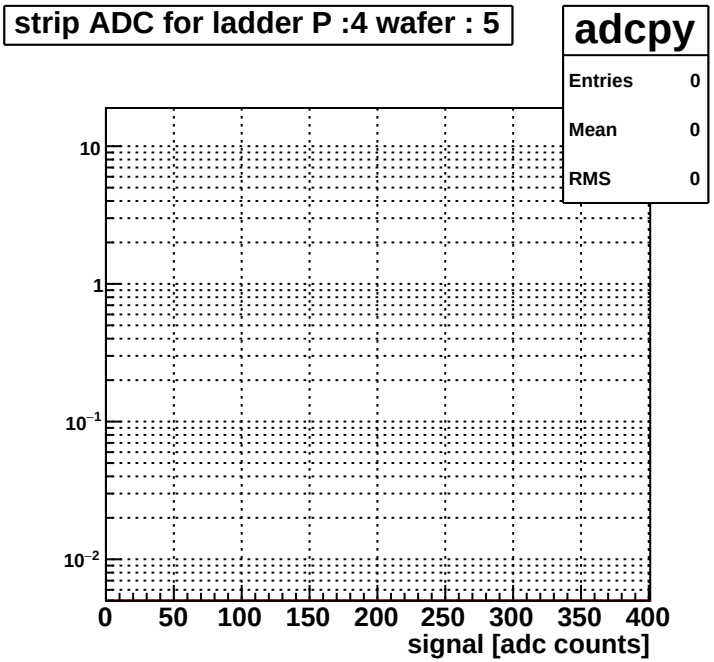

Strip adc vs. channels for ladder N : 4 wafer : 4
stripADCCHANsideN51
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

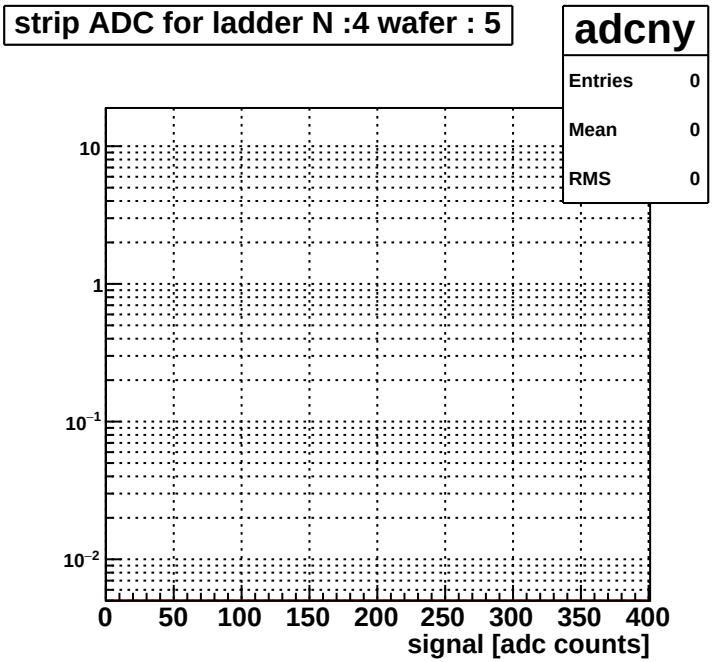

strip ADC for ladder P :4 wafer : 4
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

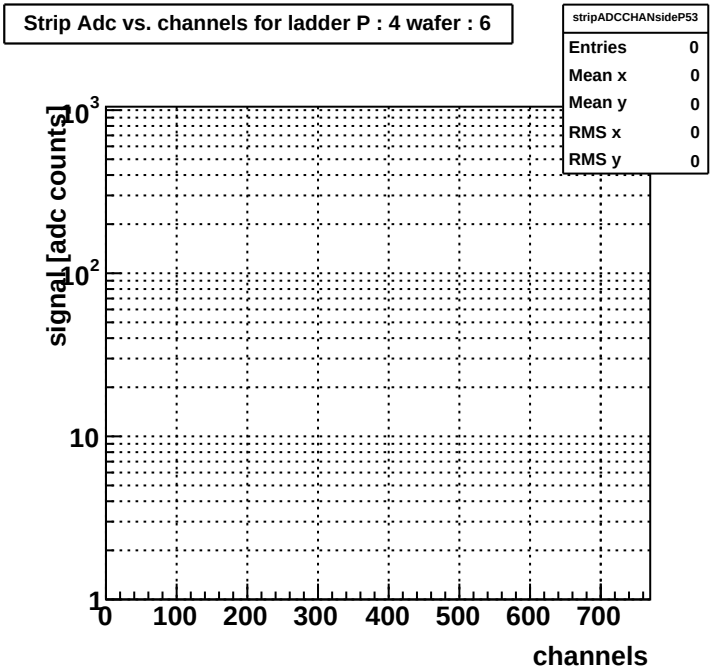

strip ADC for ladder N :4 wafer : 4
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

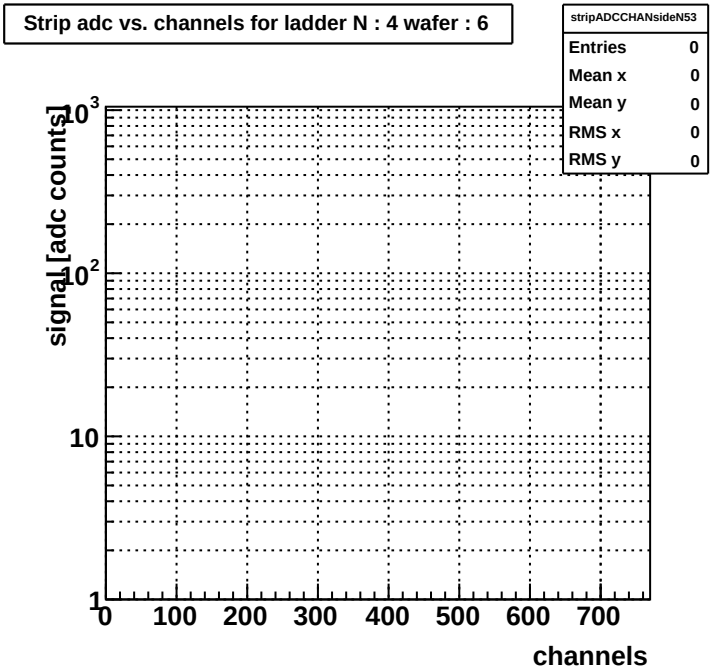

Strip Adc vs. channels for ladder P : 4 wafer : 5
stripADCCHANsideP52
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

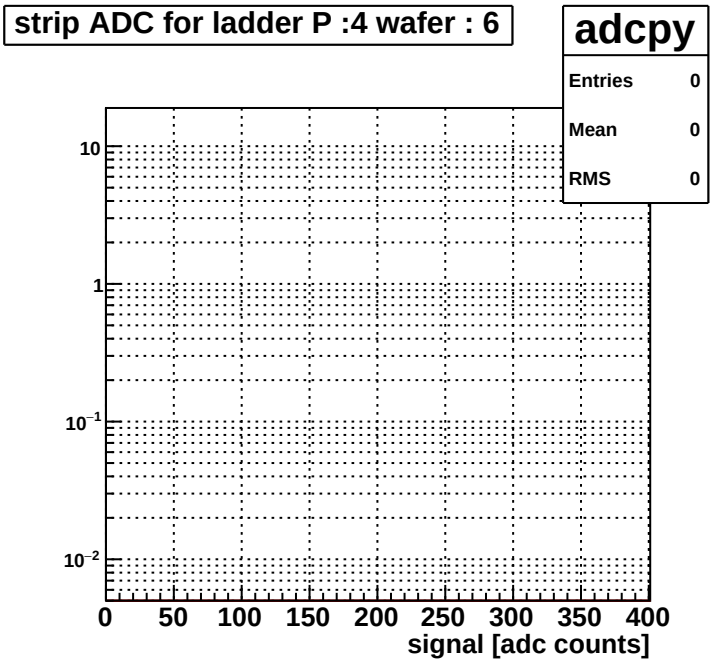

Strip adc vs. channels for ladder N : 4 wafer : 5
stripADCCHANsideN52
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

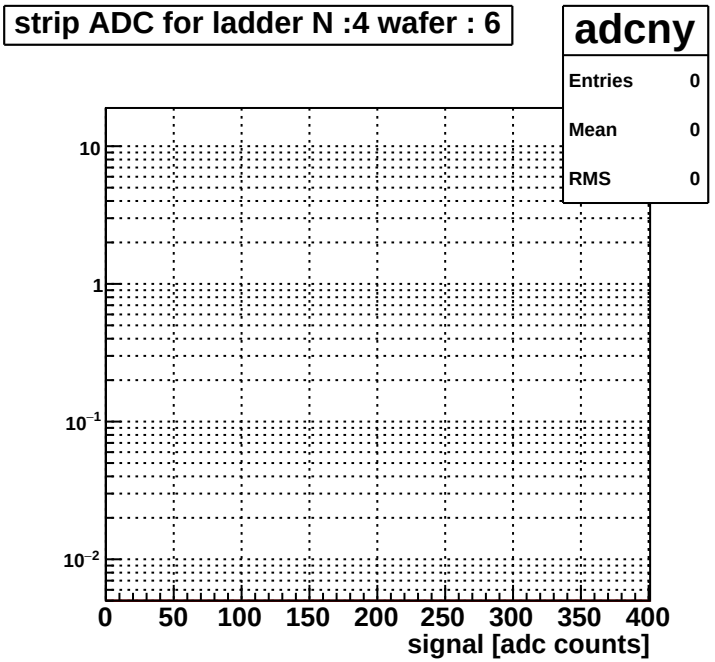

strip ADC for ladder P :4 wafer : 5
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

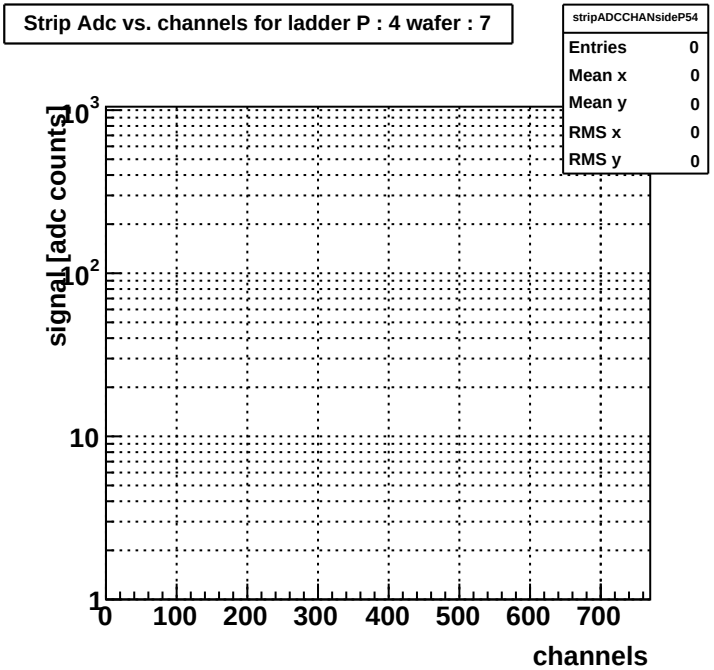

strip ADC for ladder N :4 wafer : 5
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

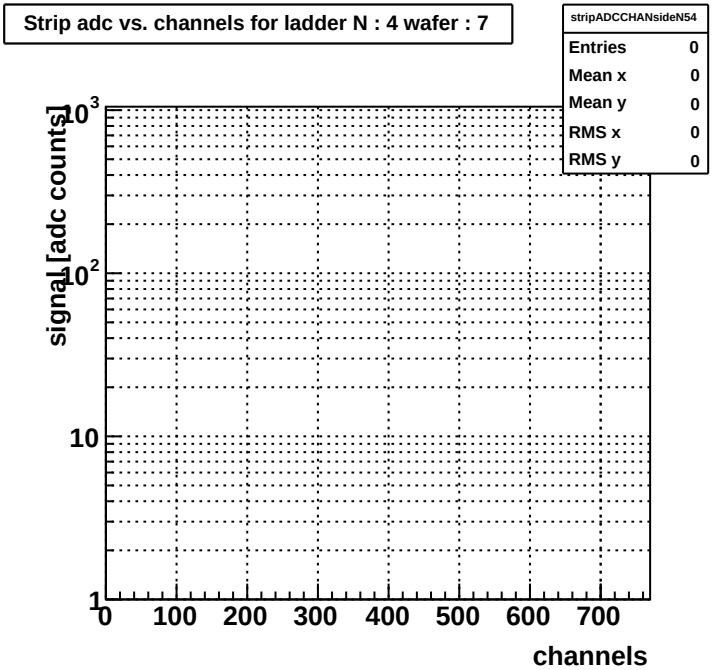

Strip Adc vs. channels for ladder P : 4 wafer : 6
stripADCCHANsideP53
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

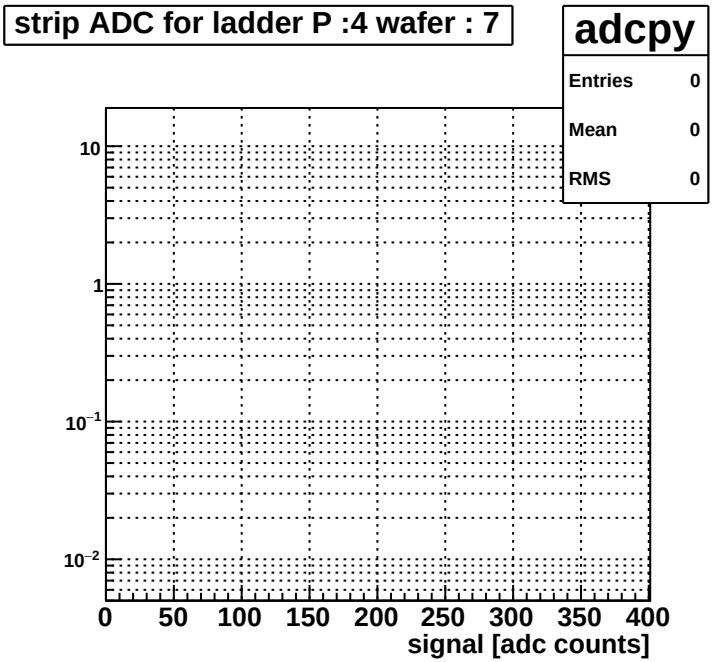

Strip adc vs. channels for ladder N : 4 wafer : 6
stripADCCHANsideN53
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

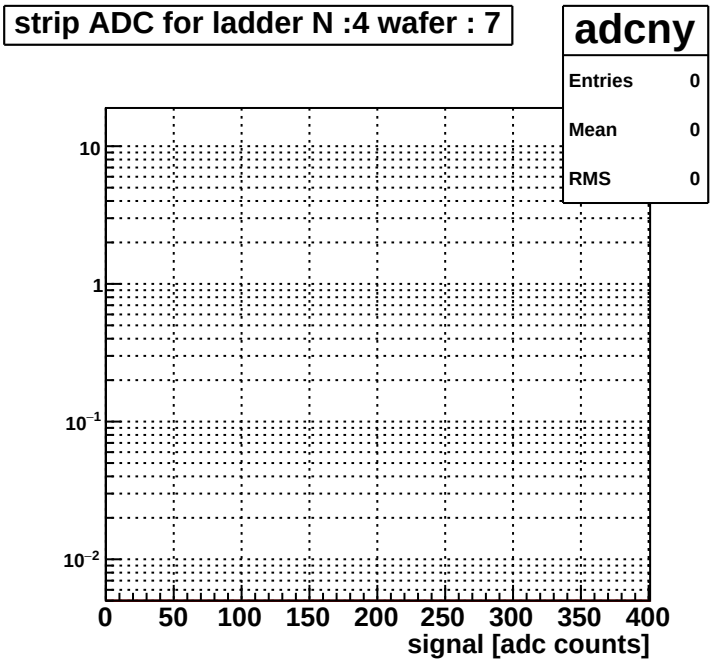

strip ADC for ladder P :4 wafer : 6
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

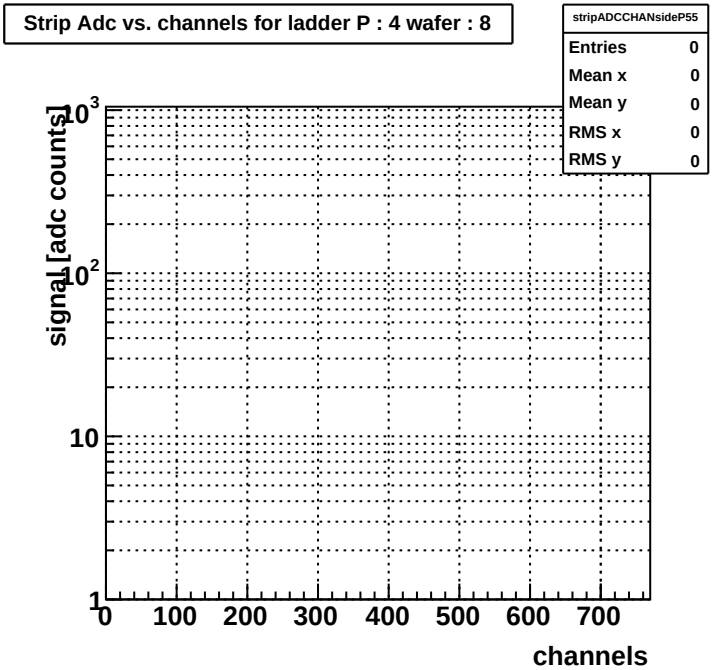

strip ADC for ladder N :4 wafer : 6
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

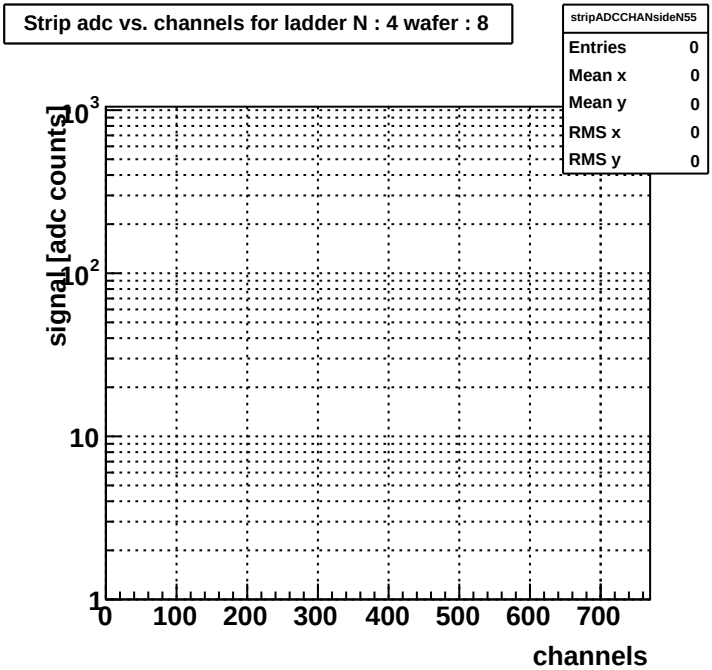

Strip Adc vs. channels for ladder P : 4 wafer : 7
stripADCCHANsideP54
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

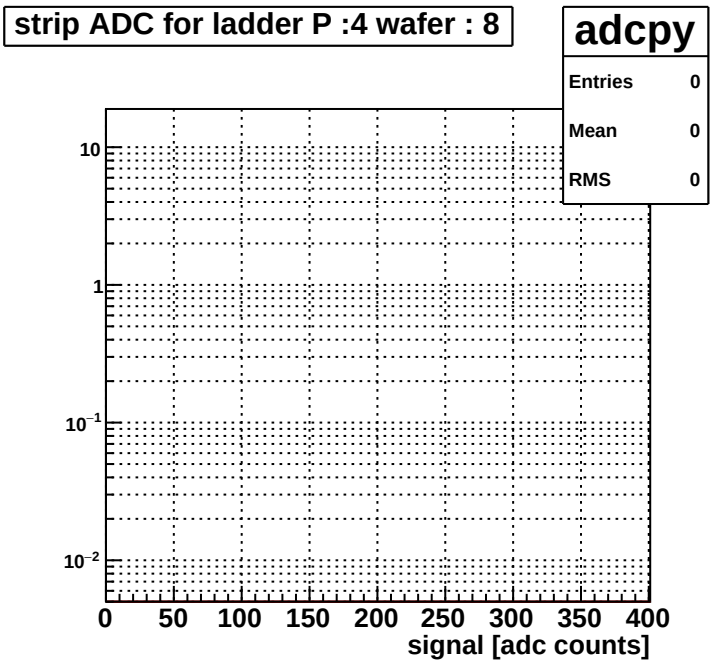

Strip adc vs. channels for ladder N : 4 wafer : 7
stripADCCHANsideN54
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

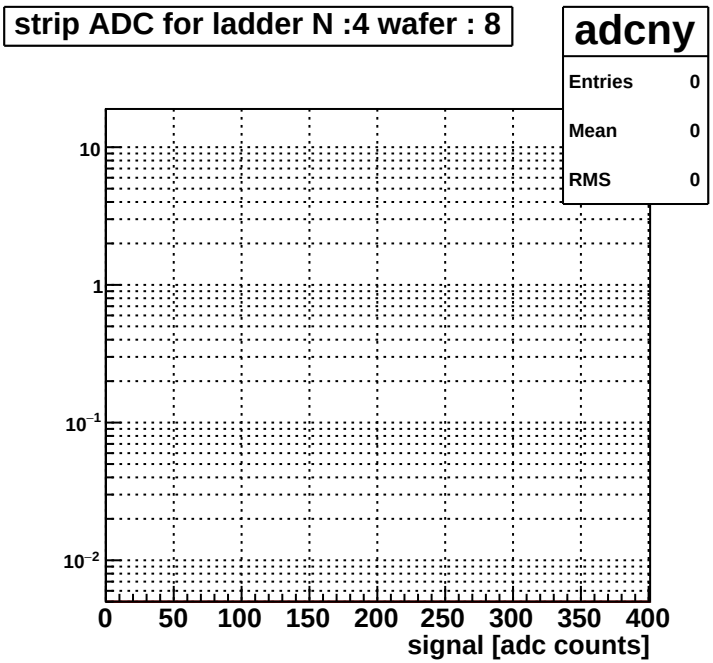

strip ADC for ladder P :4 wafer : 7
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

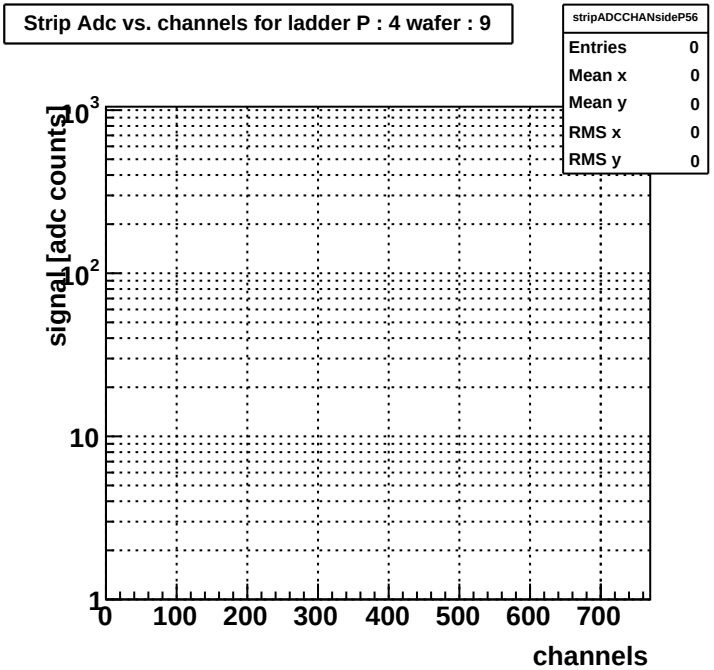

strip ADC for ladder N :4 wafer : 7
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

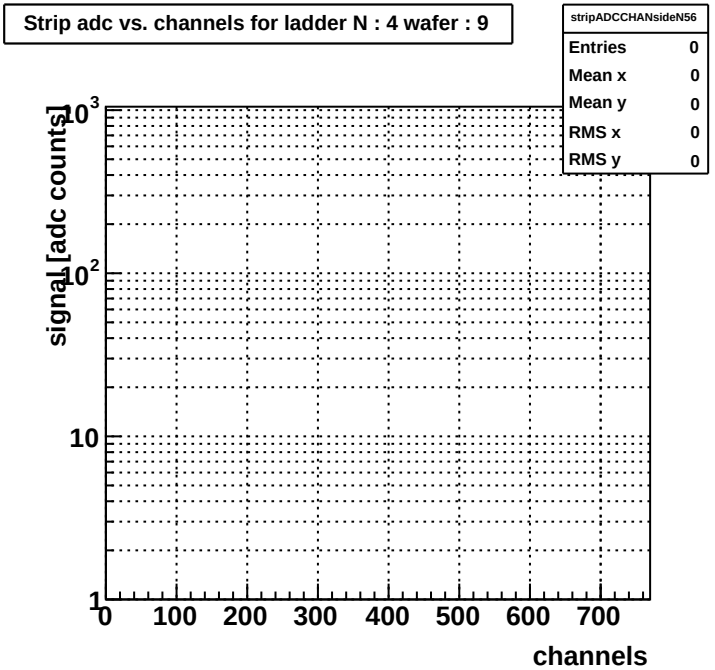

Strip Adc vs. channels for ladder P : 4 wafer : 8
stripADCCHANsideP55
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

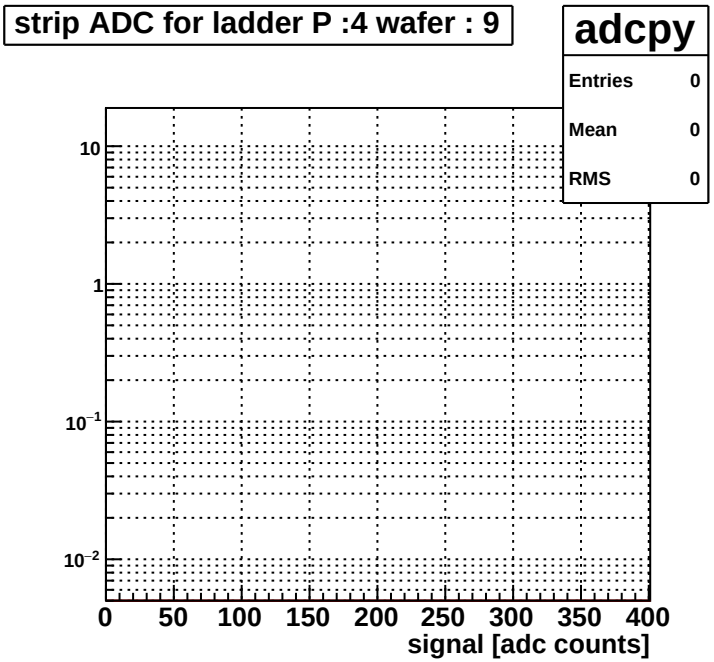

Strip adc vs. channels for ladder N : 4 wafer : 8
stripADCCHANsideN55
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

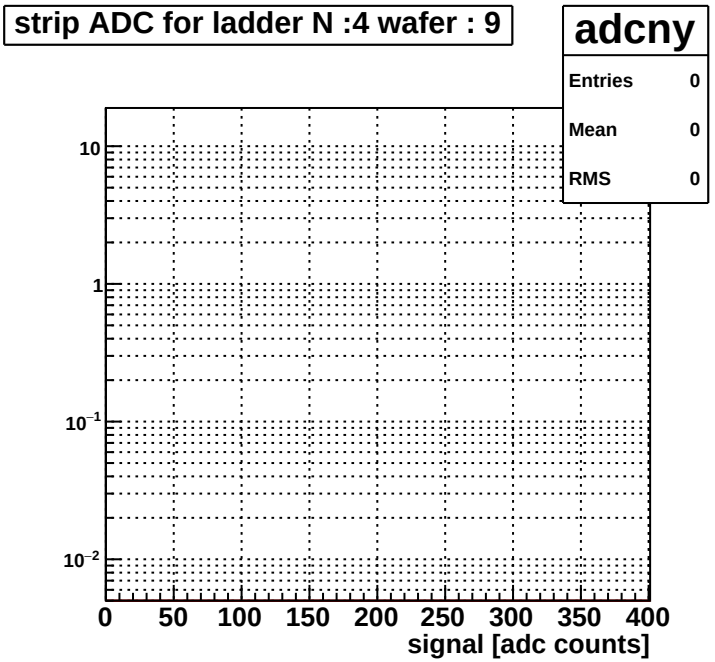

strip ADC for ladder P :4 wafer : 8
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

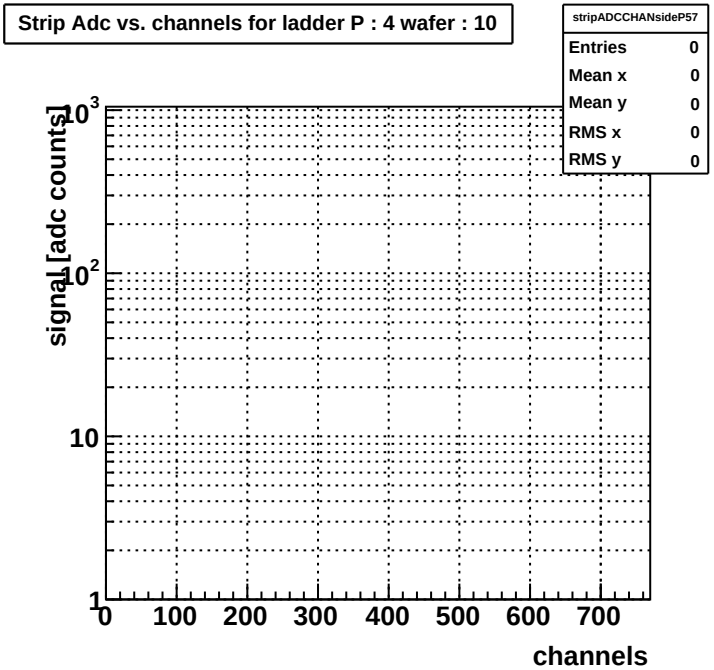

strip ADC for ladder N :4 wafer : 8
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

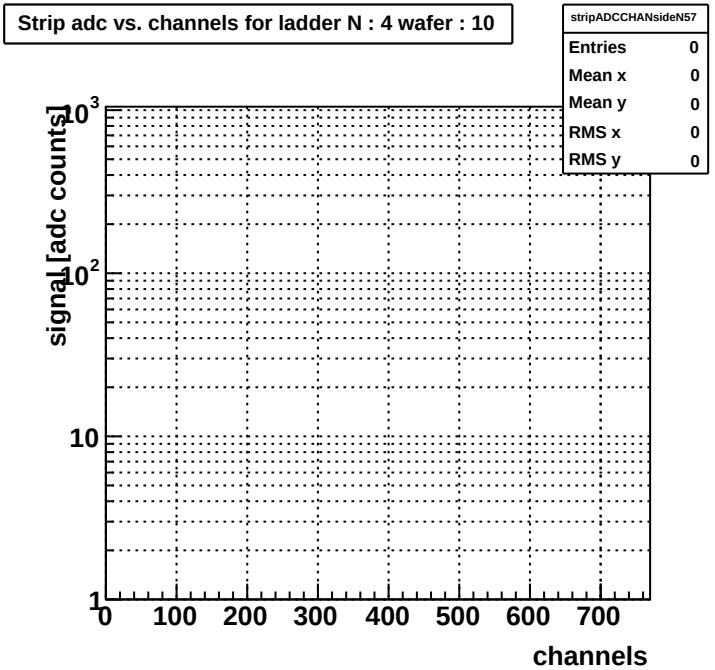

Strip Adc vs. channels for ladder P : 4 wafer : 9
stripADCCHANsideP56
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

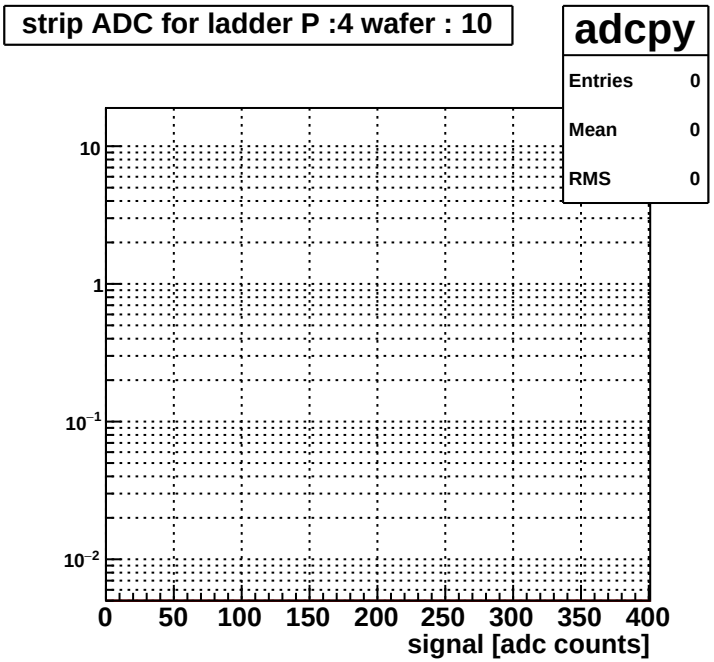

Strip adc vs. channels for ladder N : 4 wafer : 9
stripADCCHANsideN56
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

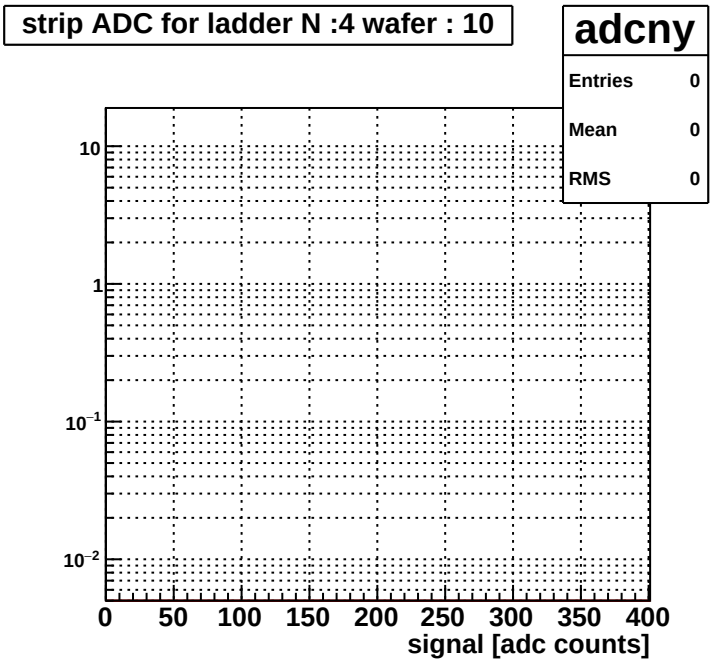

strip ADC for ladder P :4 wafer : 9
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

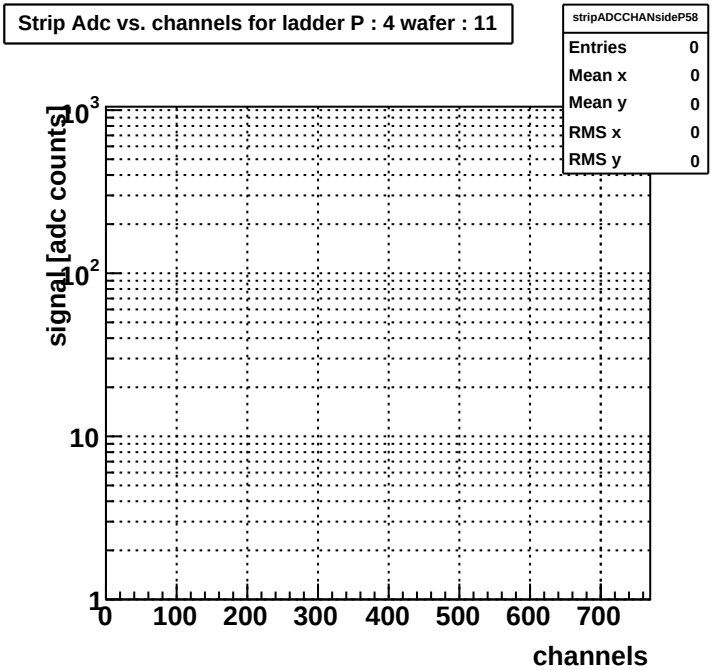

strip ADC for ladder N :4 wafer : 9
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

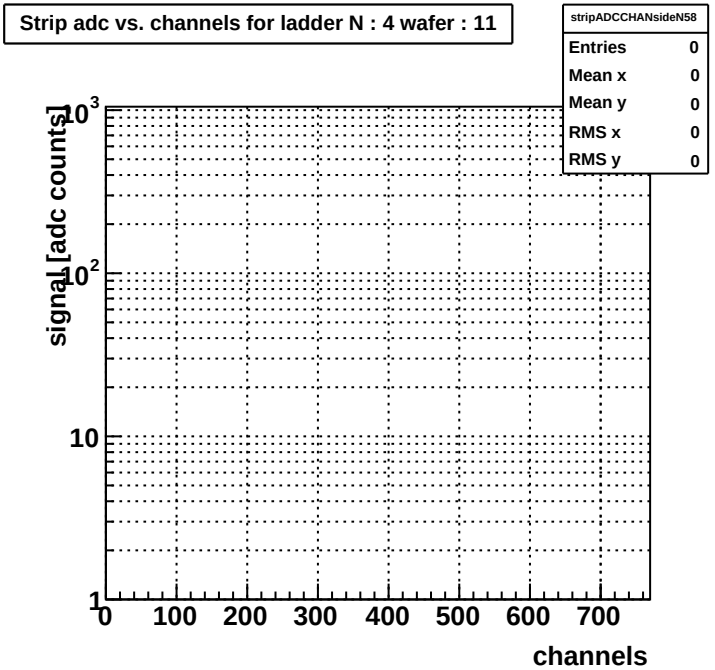

Strip Adc vs. channels for ladder P : 4 wafer : 10
stripADCCHANsideP57
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

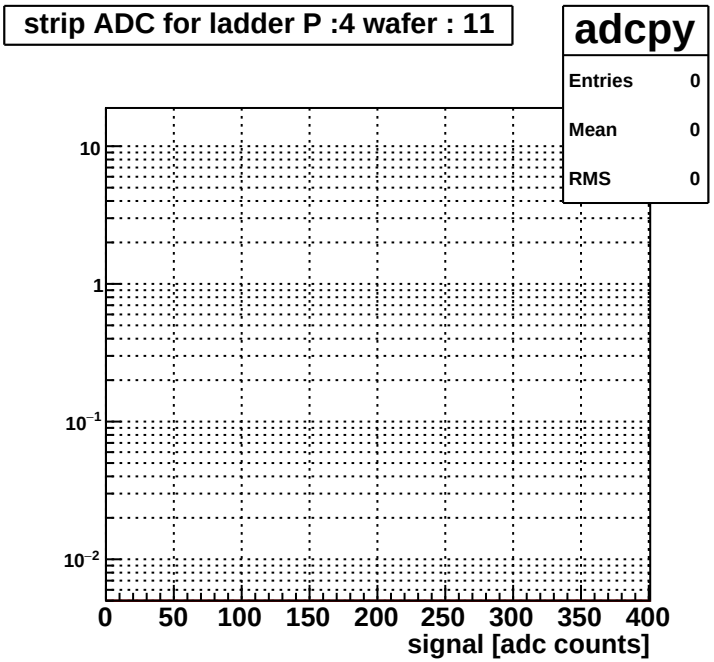

Strip adc vs. channels for ladder N : 4 wafer : 10
stripADCCHANsideN57
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

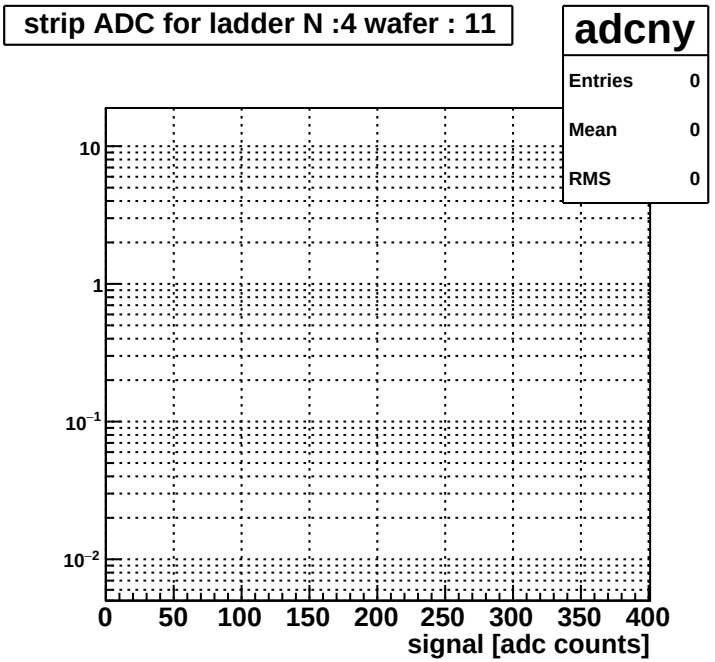

strip ADC for ladder P :4 wafer : 10
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

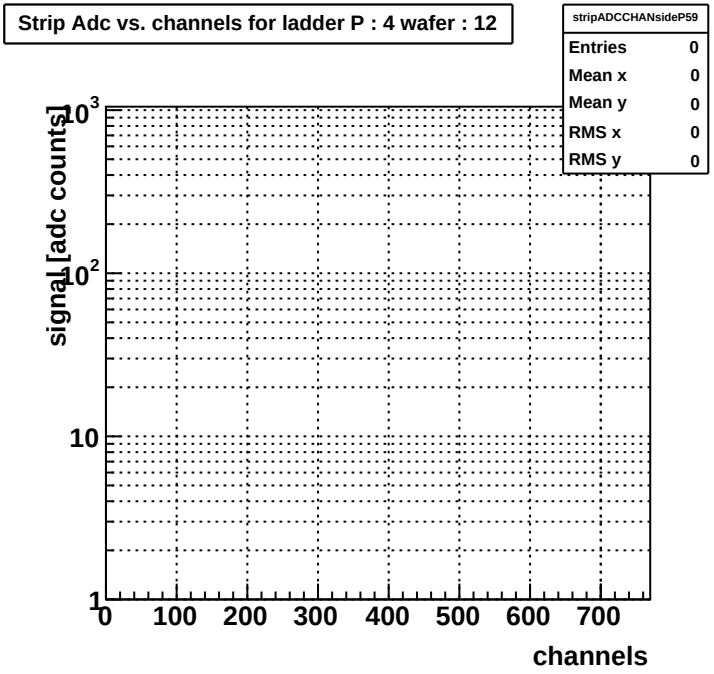

strip ADC for ladder N :4 wafer : 10
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

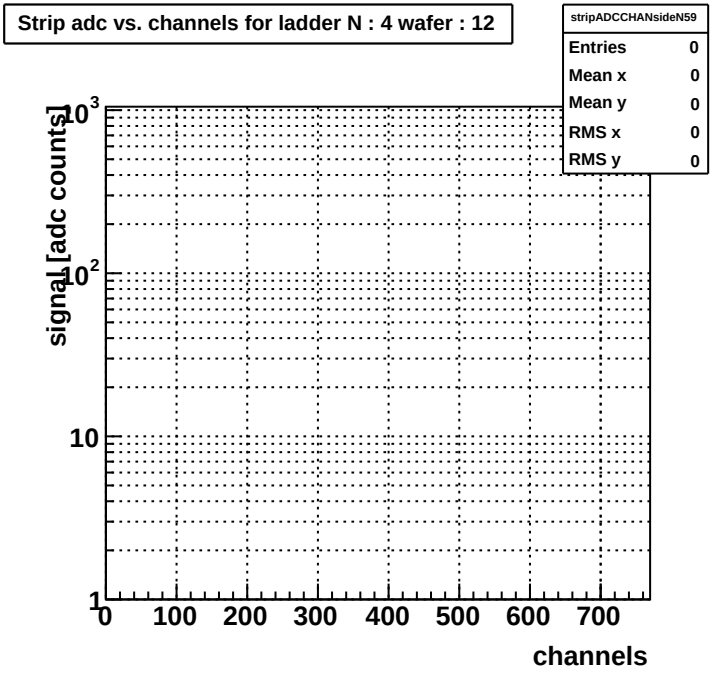

Strip Adc vs. channels for ladder P : 4 wafer : 11
stripADCCHANsideP58
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

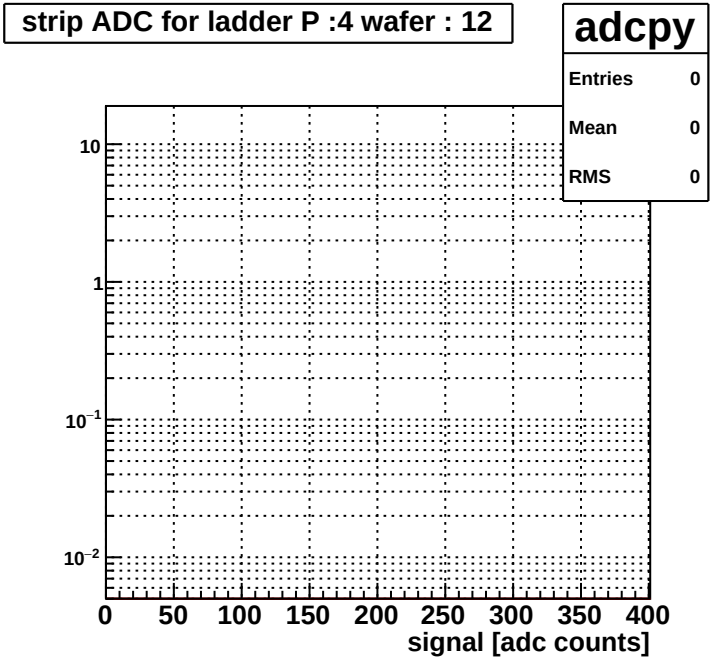

Strip adc vs. channels for ladder N : 4 wafer : 11
stripADCCHANsideN58
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

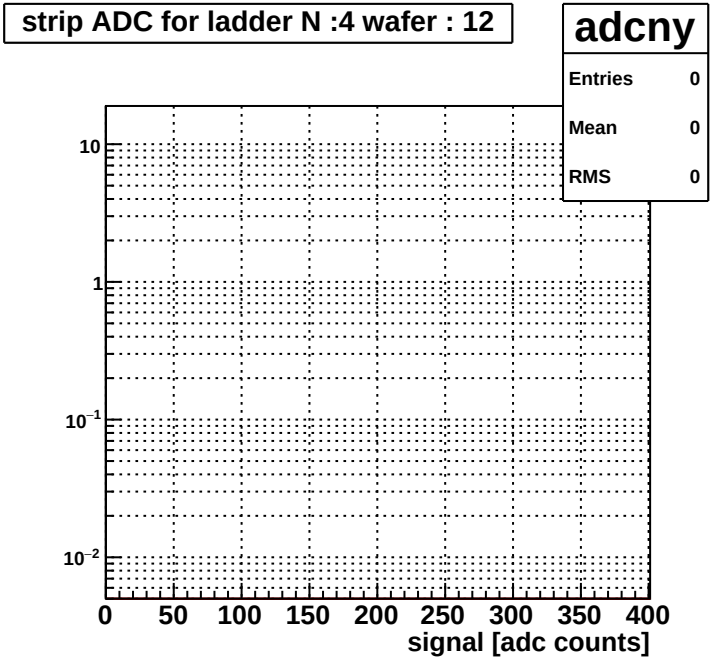

strip ADC for ladder P :4 wafer : 11
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

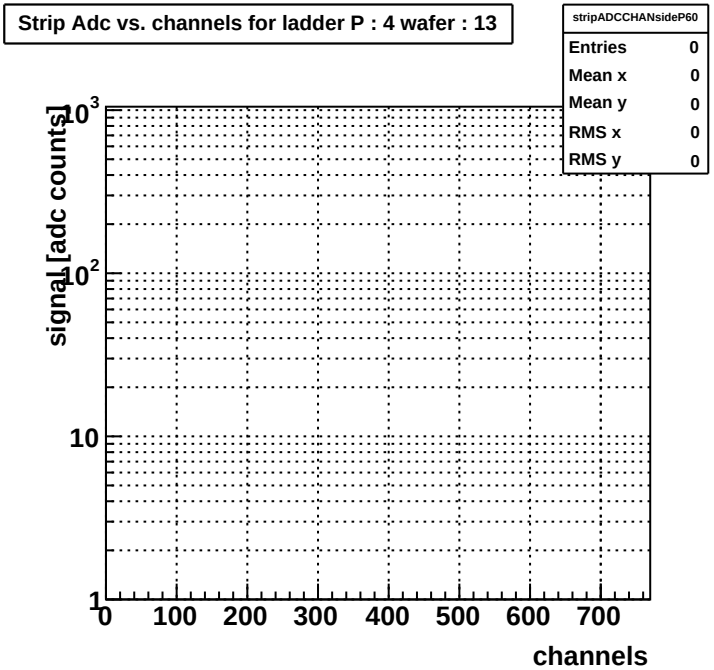

strip ADC for ladder N :4 wafer : 11
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

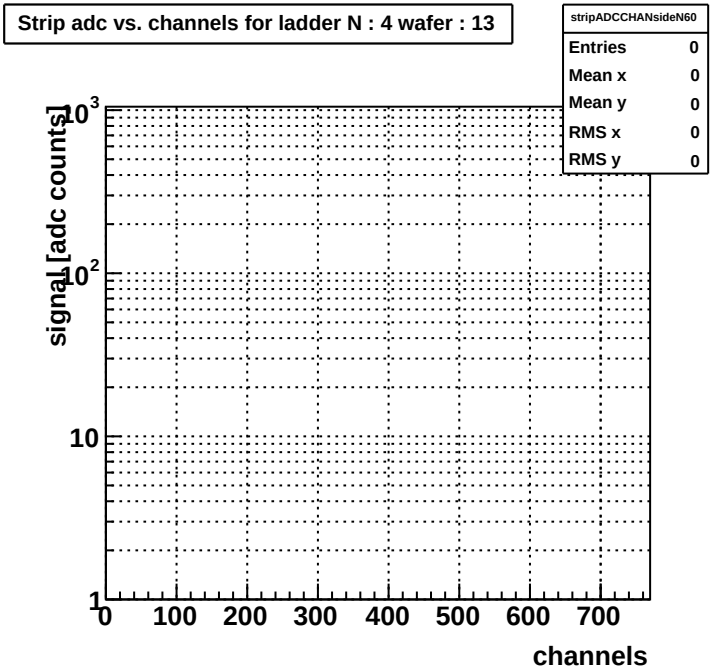

Strip Adc vs. channels for ladder P : 4 wafer : 12
stripADCCHANsideP59
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

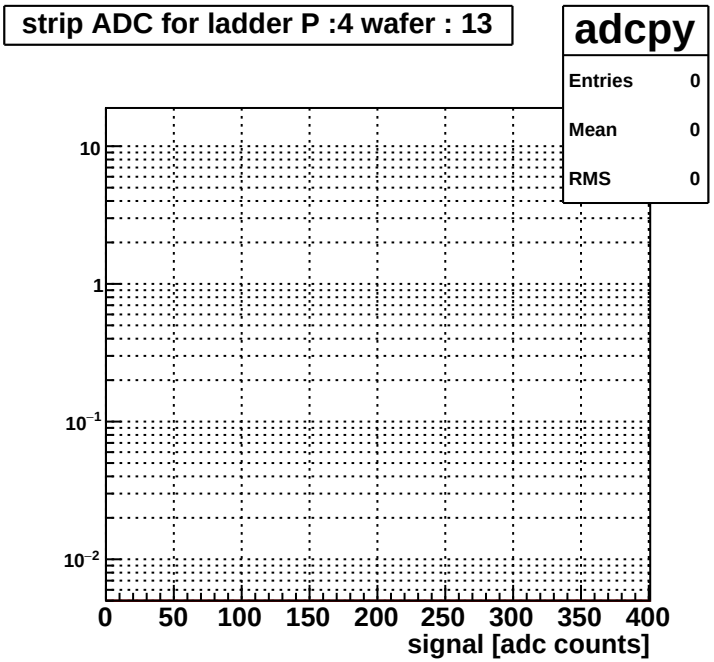

Strip adc vs. channels for ladder N : 4 wafer : 12
stripADCCHANsideN59
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

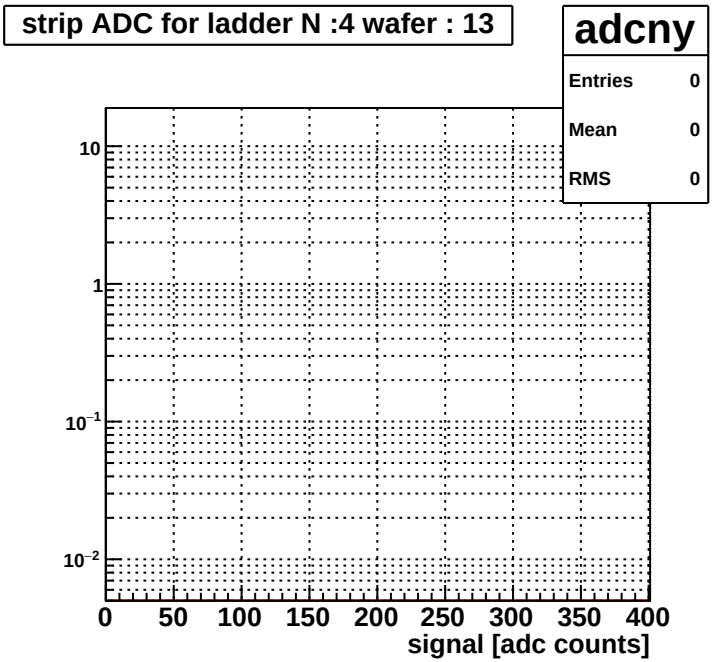

strip ADC for ladder P :4 wafer : 12
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

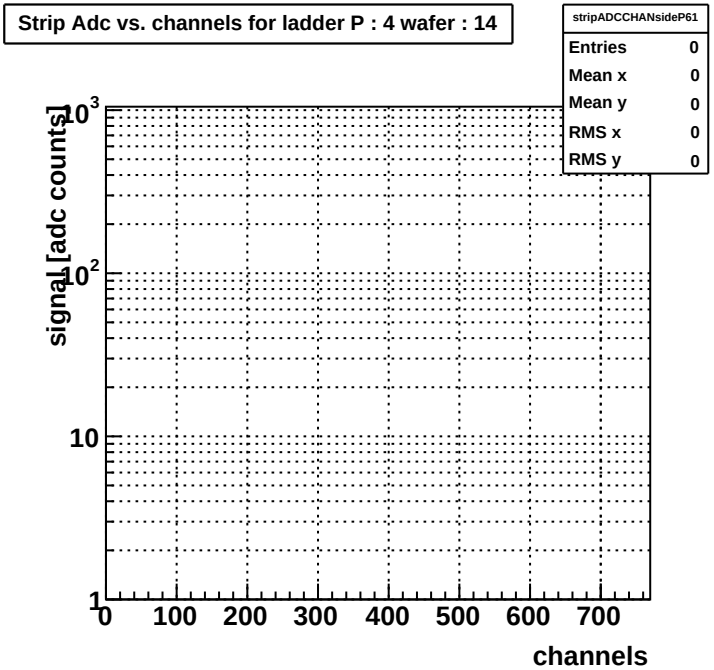

strip ADC for ladder N :4 wafer : 12
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

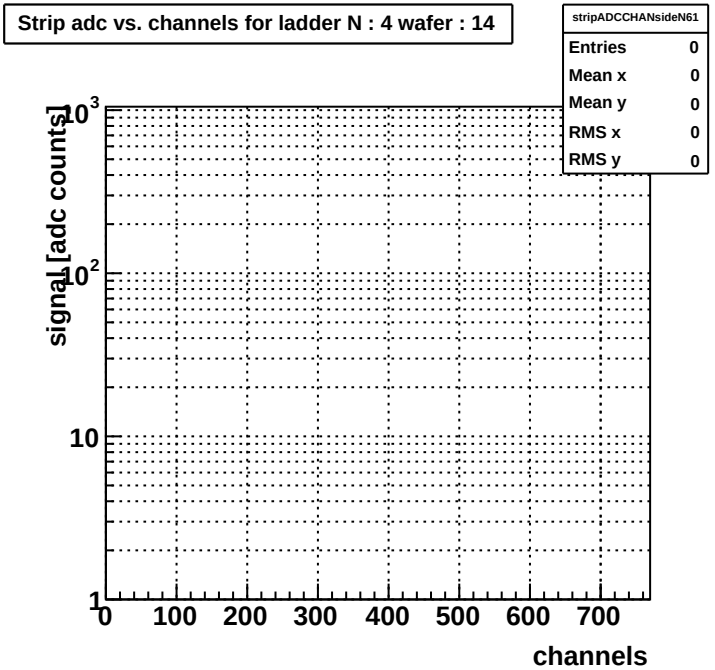

Strip Adc vs. channels for ladder P : 4 wafer : 13
stripADCCHANsideP60
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

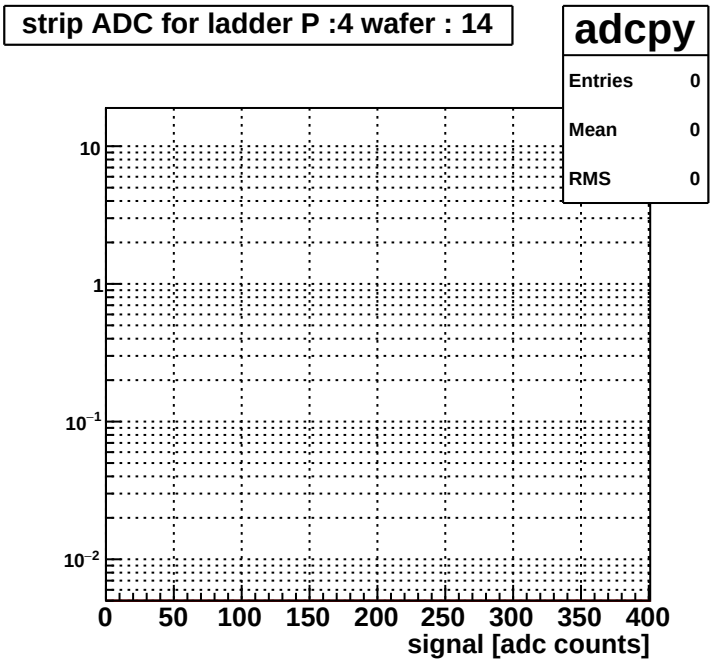

Strip adc vs. channels for ladder N : 4 wafer : 13
stripADCCHANsideN60
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

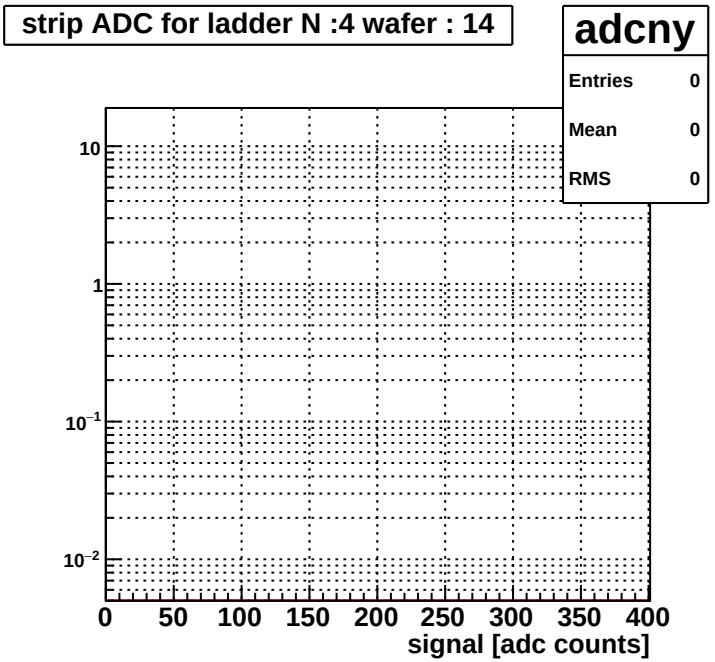

strip ADC for ladder P :4 wafer : 13
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

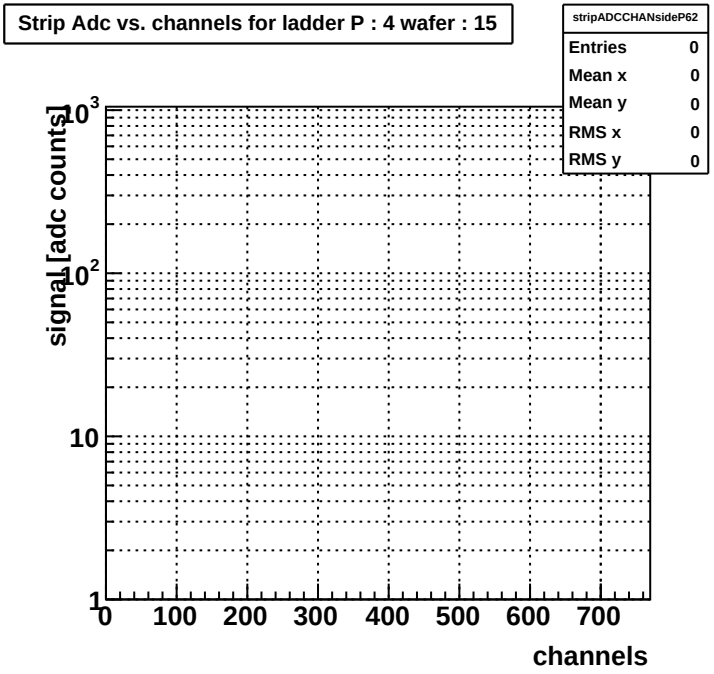

strip ADC for ladder N :4 wafer : 13
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

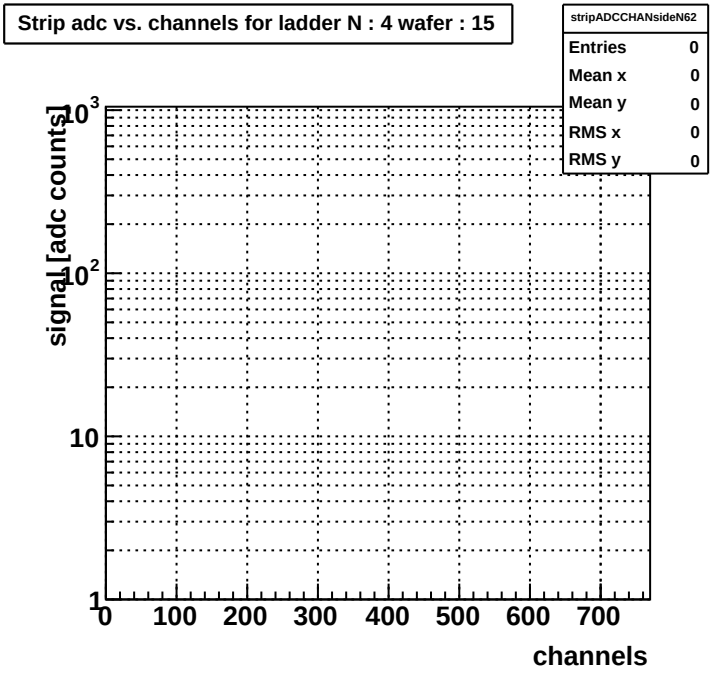

Strip Adc vs. channels for ladder P : 4 wafer : 14
stripADCCHANsideP61
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

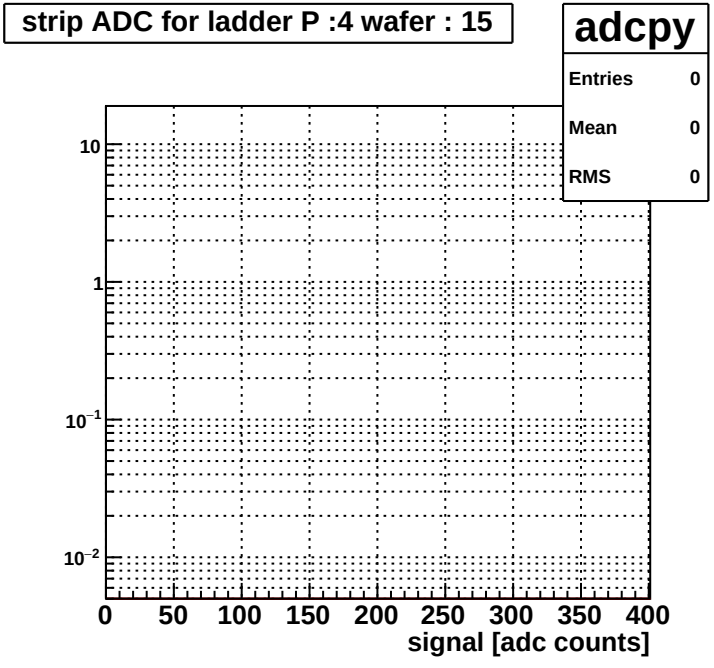

Strip adc vs. channels for ladder N : 4 wafer : 14
stripADCCHANsideN61
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

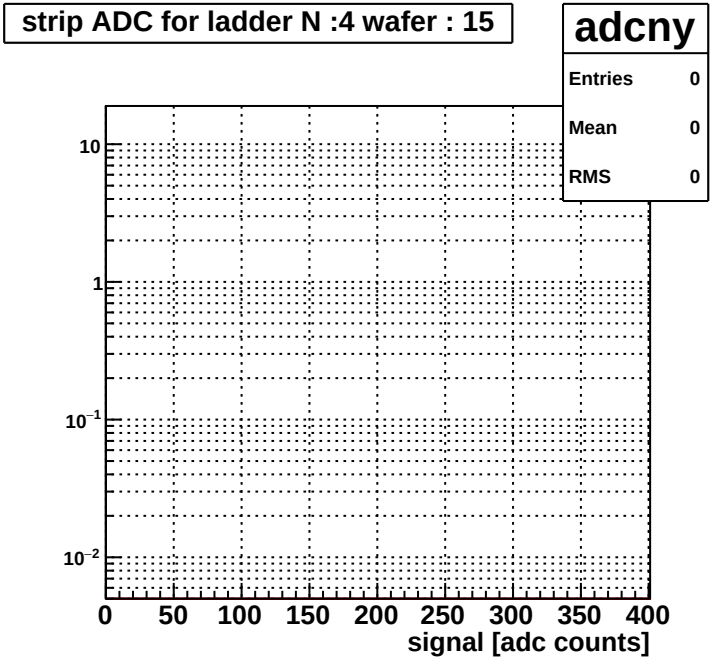

strip ADC for ladder P :4 wafer : 14
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

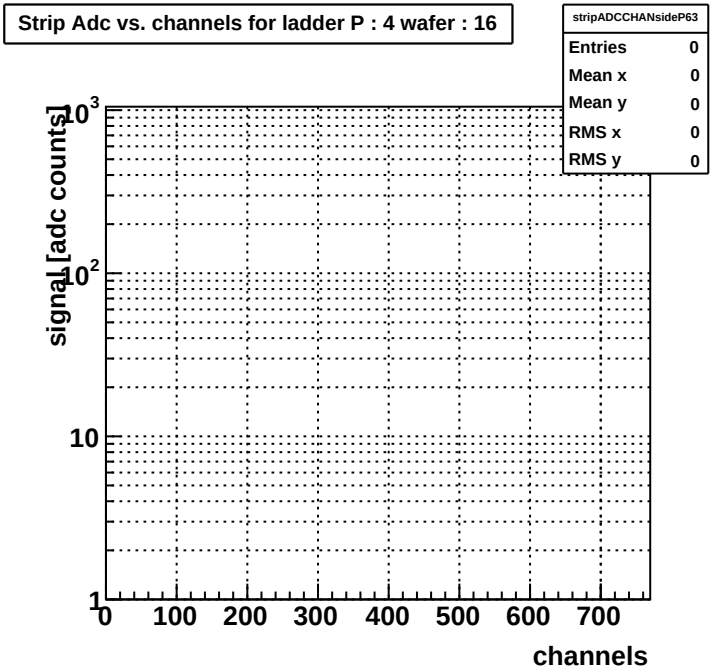

strip ADC for ladder N :4 wafer : 14
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

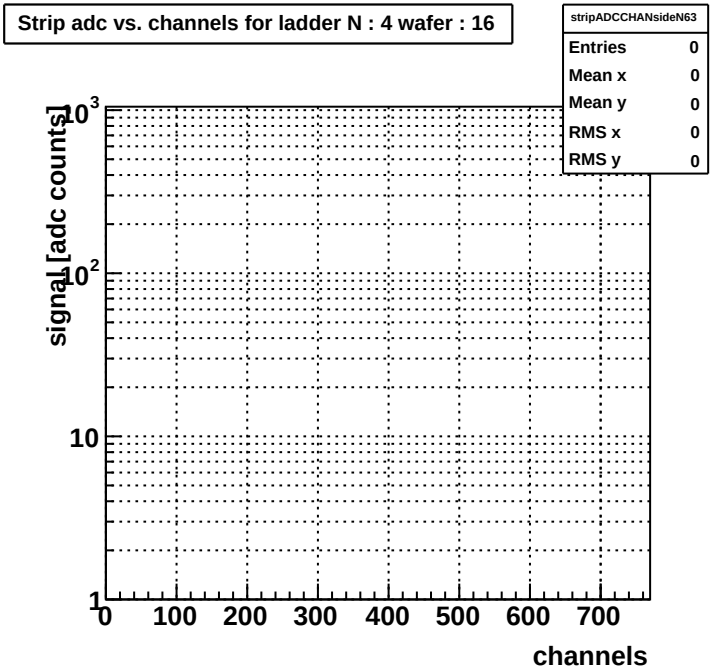

Strip Adc vs. channels for ladder P : 4 wafer : 15
stripADCCHANsideP62
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

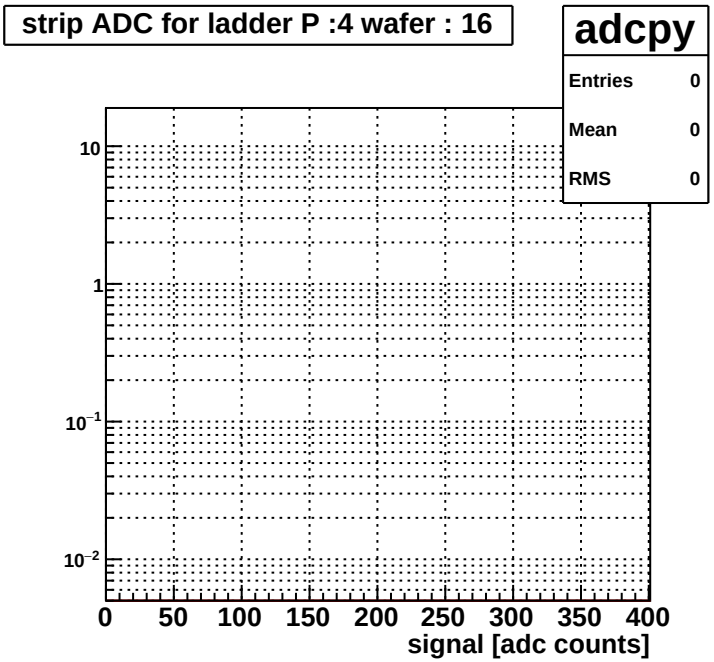

Strip adc vs. channels for ladder N : 4 wafer : 15
stripADCCHANsideN62
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

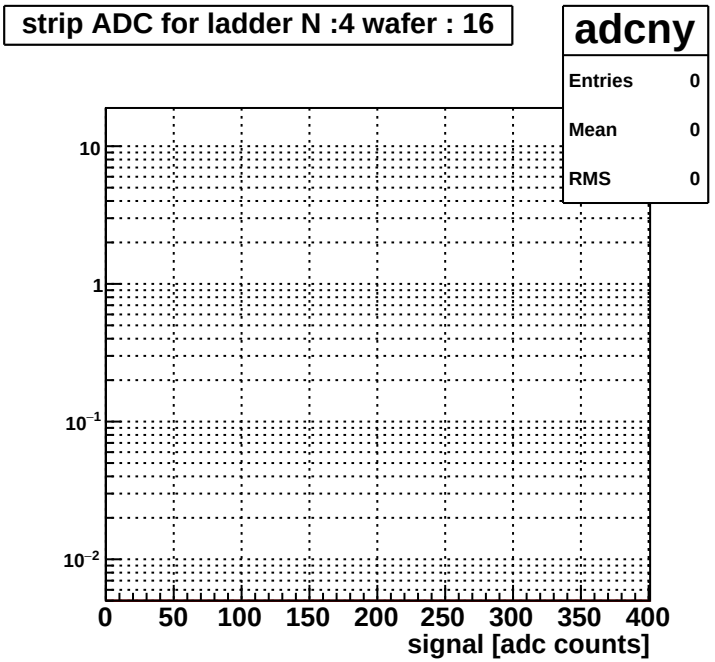

strip ADC for ladder P :4 wafer : 15
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

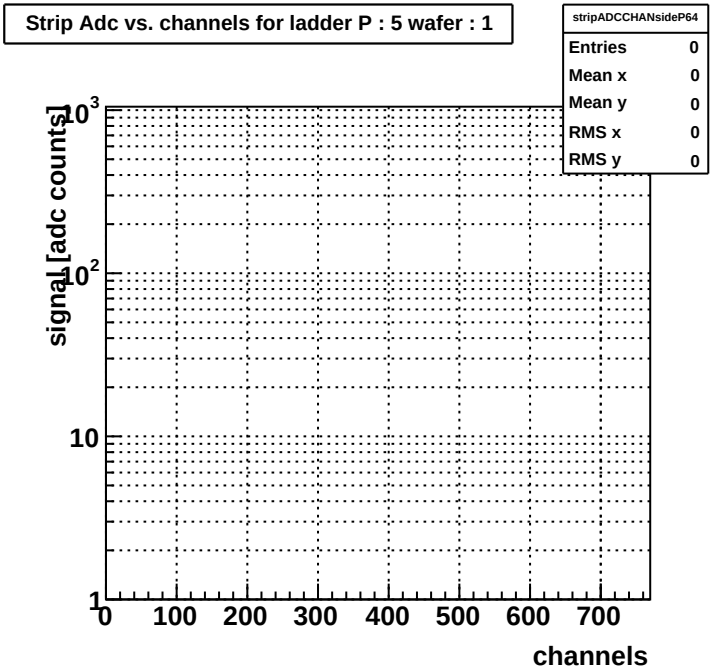

strip ADC for ladder N :4 wafer : 15
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

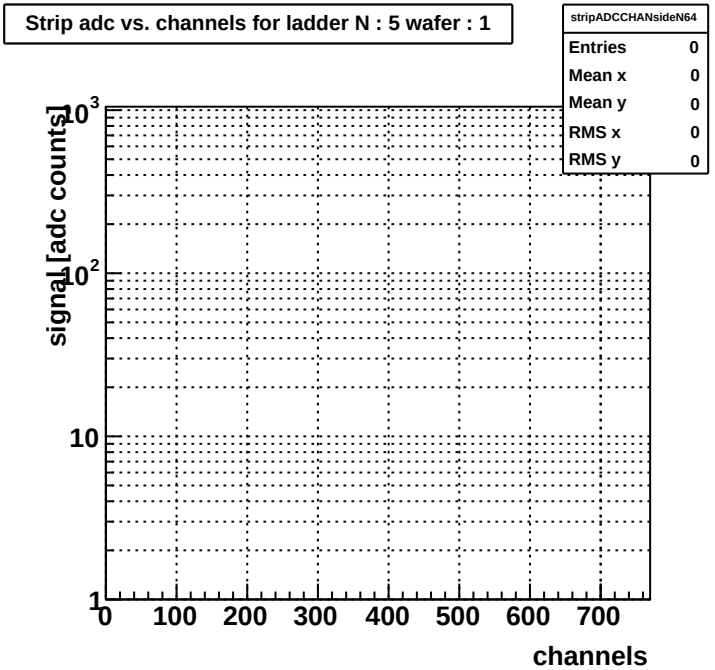

Strip Adc vs. channels for ladder P : 4 wafer : 16
stripADCCHANsideP63
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

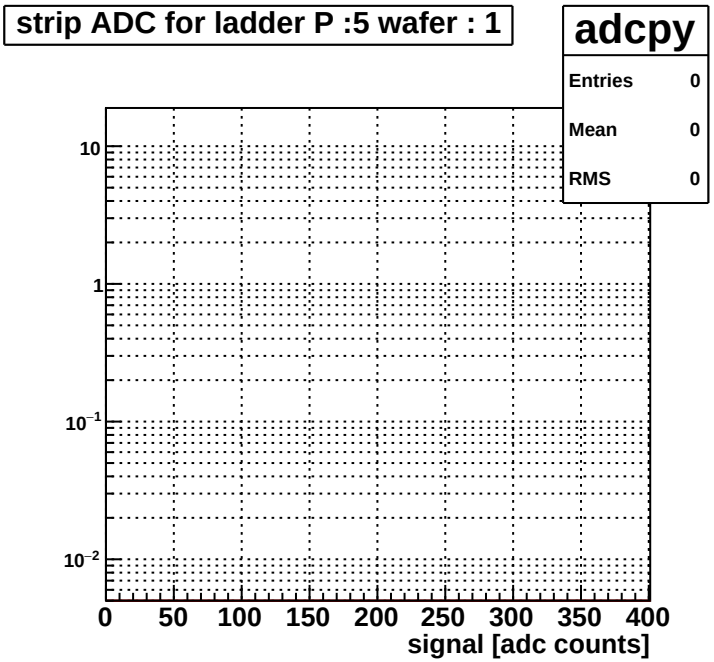

Strip adc vs. channels for ladder N : 4 wafer : 16
stripADCCHANsideN63
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

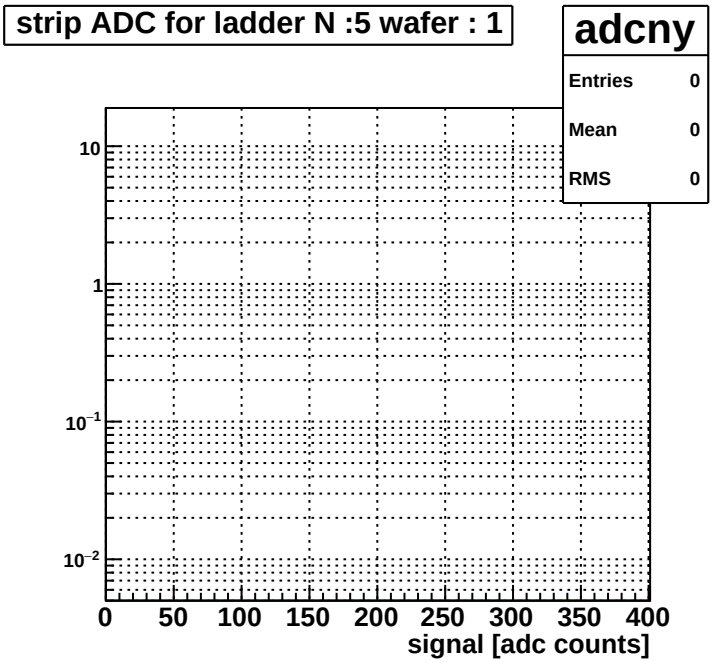

strip ADC for ladder P :4 wafer : 16
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

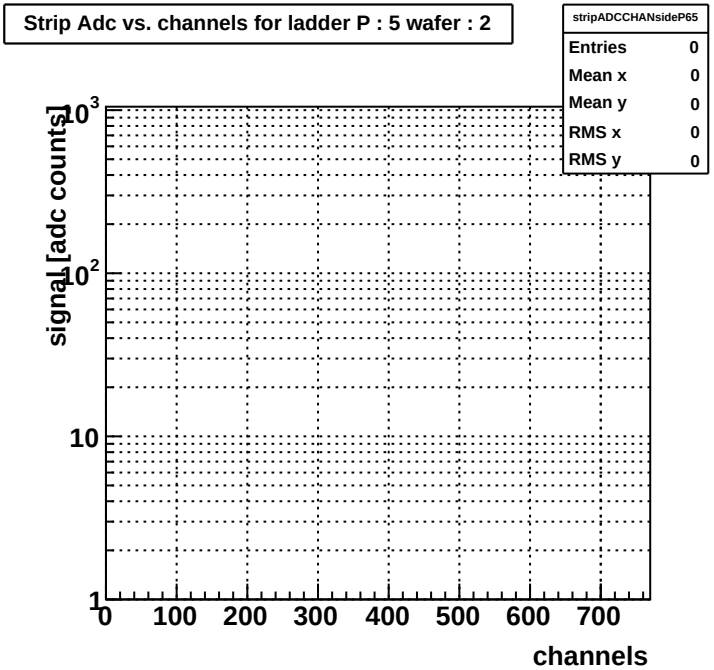

strip ADC for ladder N :4 wafer : 16
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

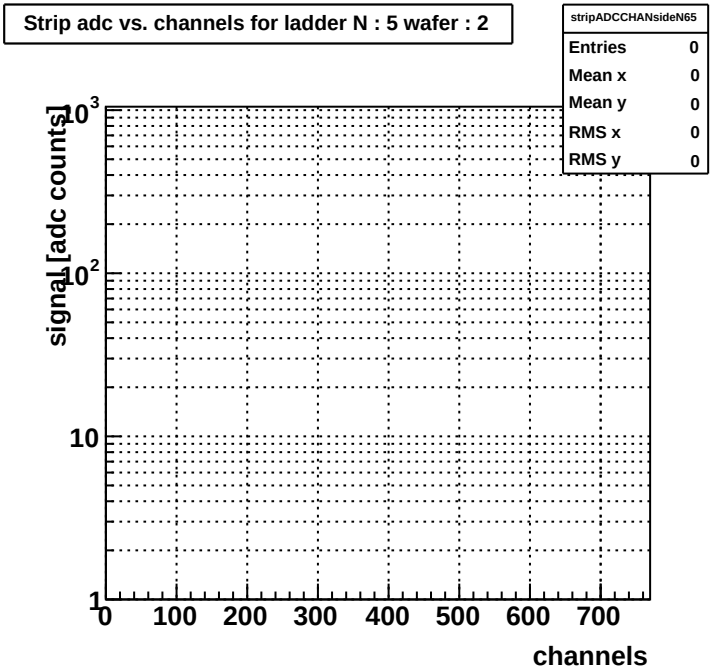

Strip Adc vs. channels for ladder P : 5 wafer : 1
stripADCCHANsideP64
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

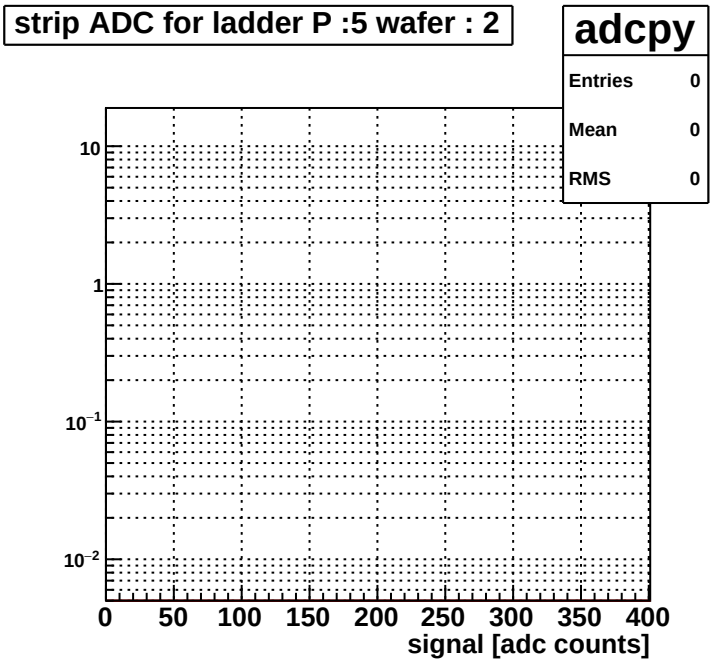

Strip adc vs. channels for ladder N : 5 wafer : 1
stripADCCHANsideN64
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

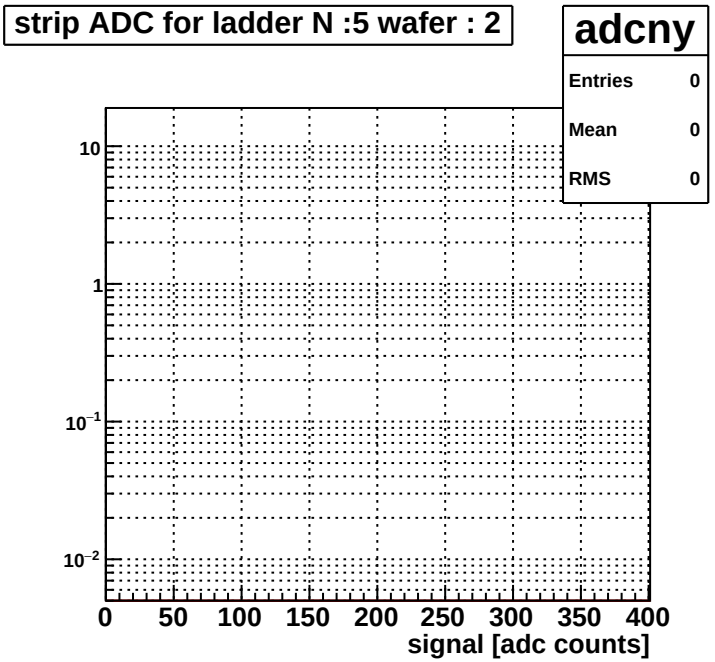

strip ADC for ladder P :5 wafer : 1
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

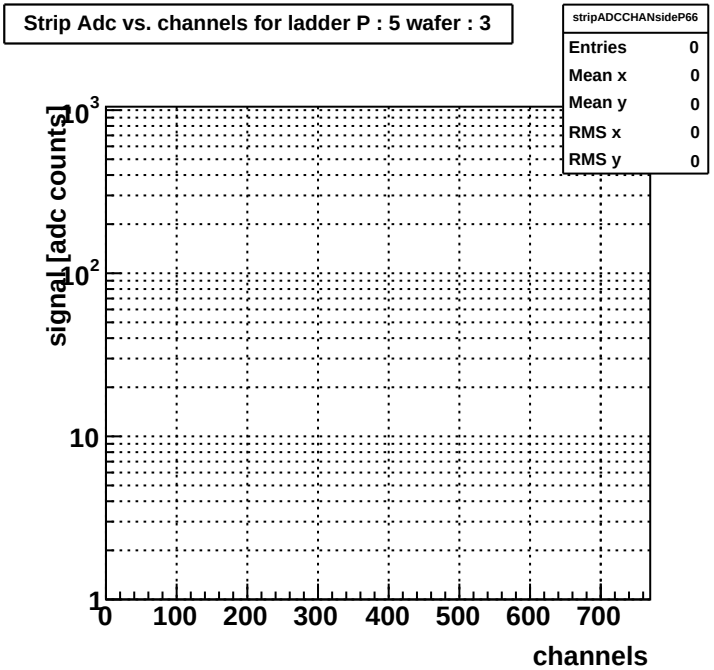

strip ADC for ladder N :5 wafer : 1
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

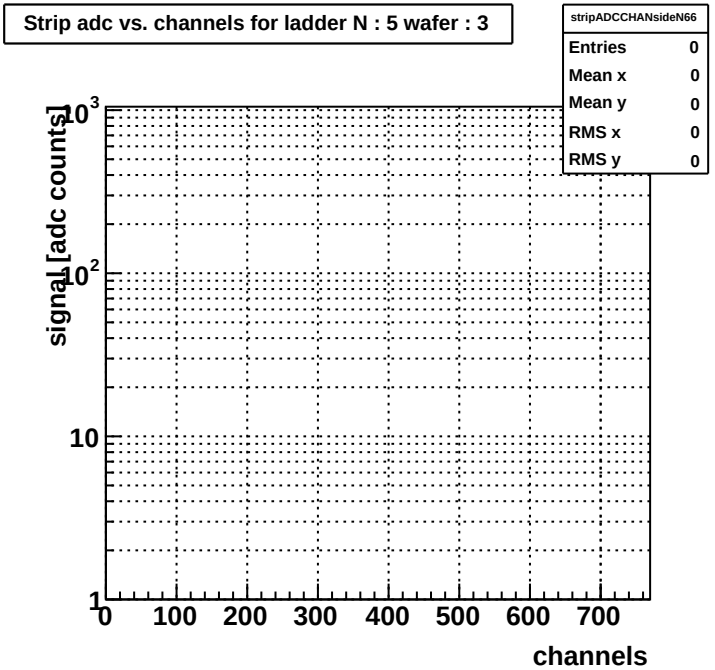

Strip Adc vs. channels for ladder P : 5 wafer : 2
stripADCCHANsideP65
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

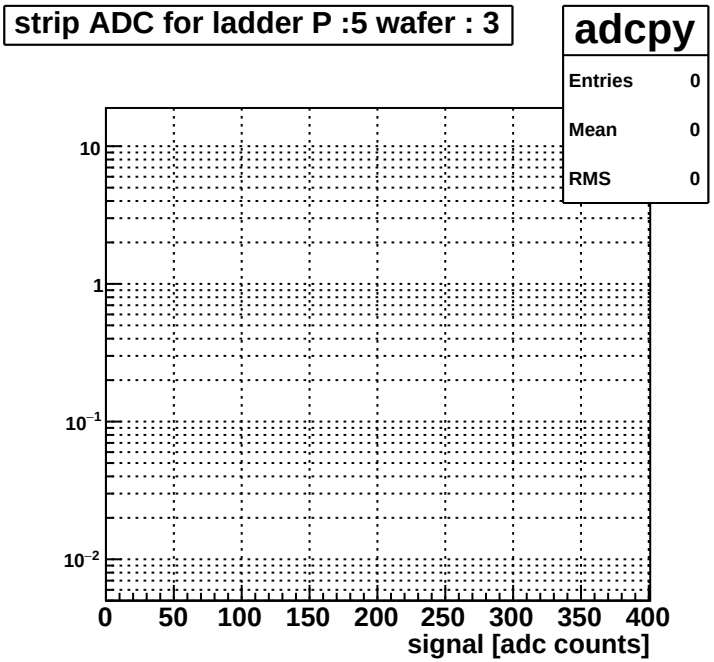

Strip adc vs. channels for ladder N : 5 wafer : 2
stripADCCHANsideN65
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

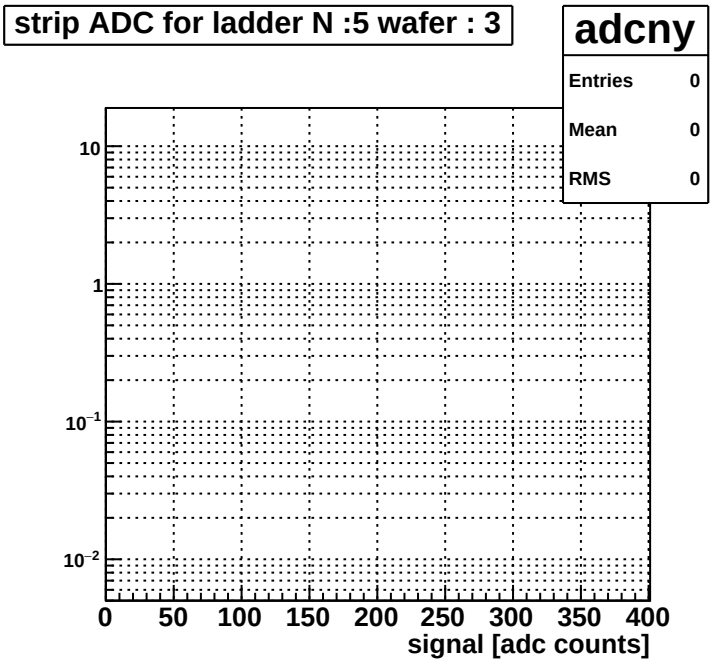

strip ADC for ladder P :5 wafer : 2
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

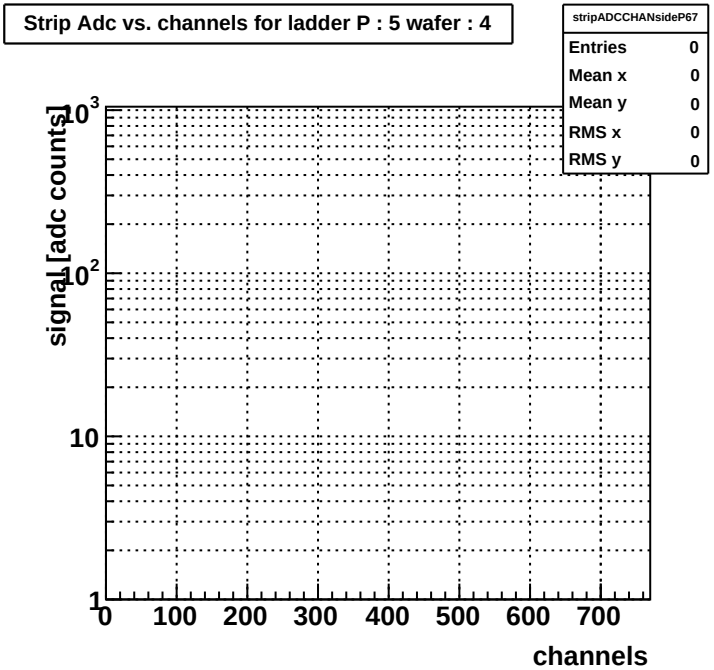

strip ADC for ladder N :5 wafer : 2
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

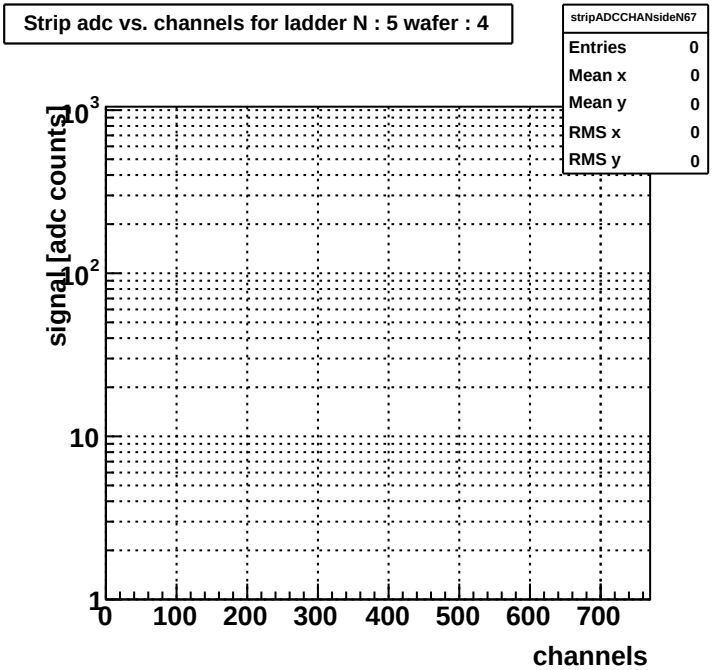

Strip Adc vs. channels for ladder P : 5 wafer : 3
stripADCCHANsideP66
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

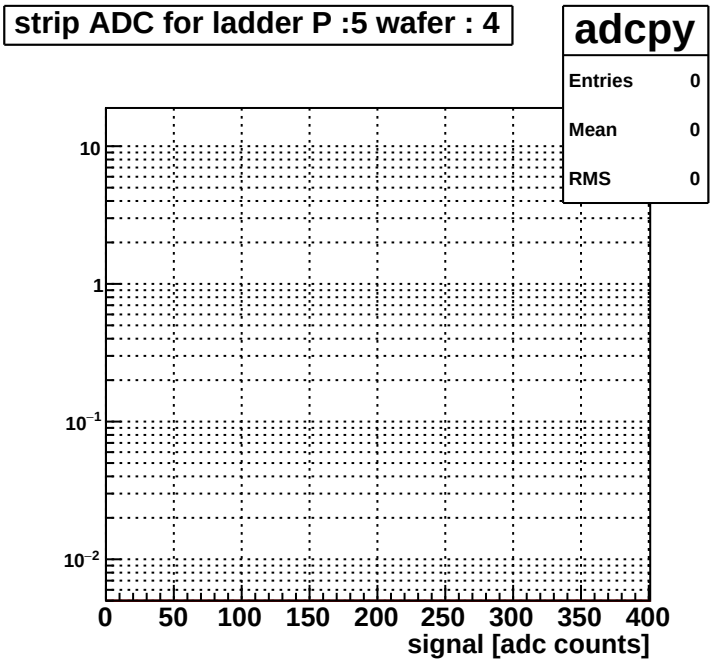

Strip adc vs. channels for ladder N : 5 wafer : 3
stripADCCHANsideN66
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

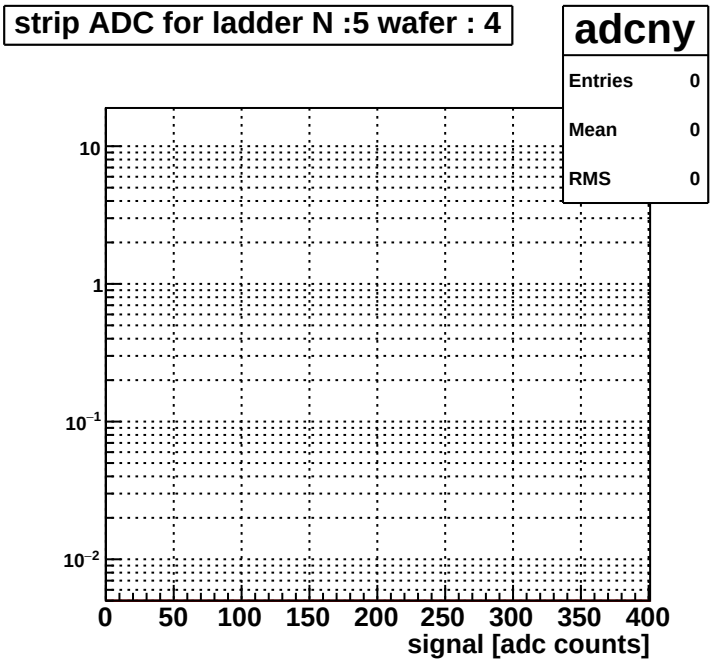

strip ADC for ladder P :5 wafer : 3
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

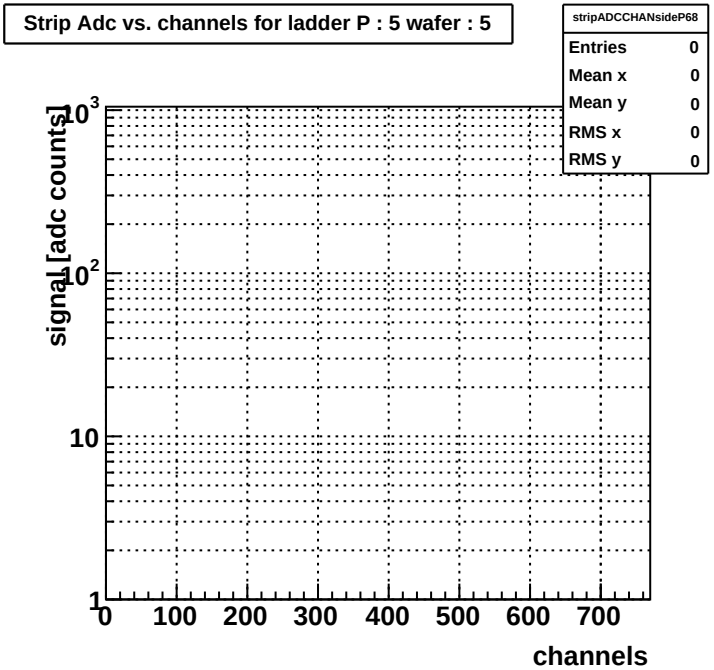

strip ADC for ladder N :5 wafer : 3
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

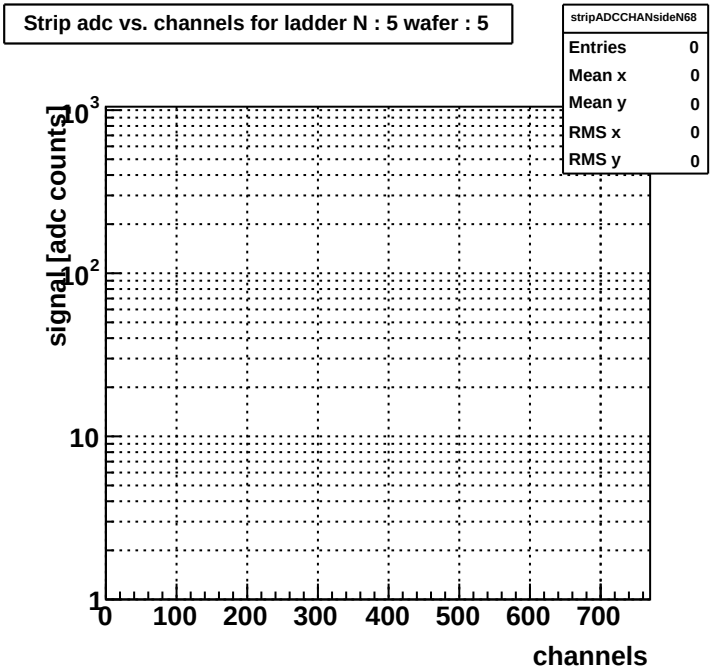

Strip Adc vs. channels for ladder P : 5 wafer : 4
stripADCCHANsideP67
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

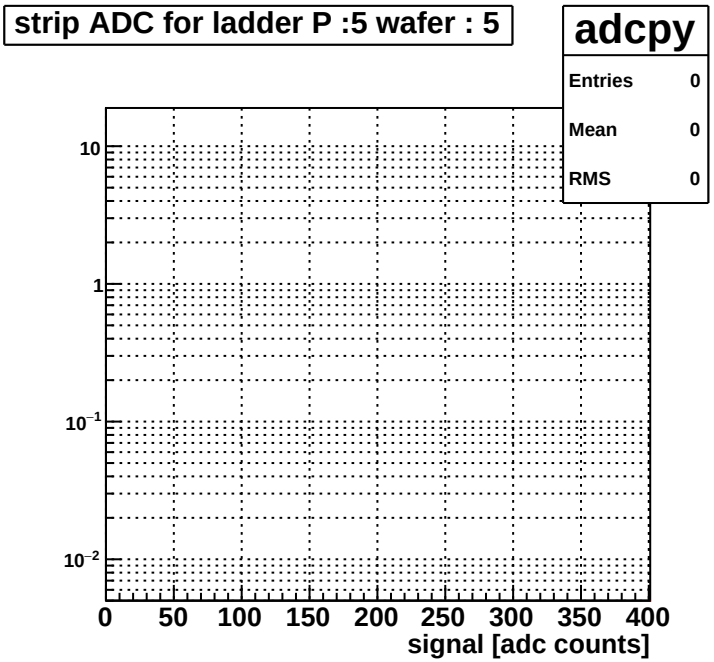

Strip adc vs. channels for ladder N : 5 wafer : 4
stripADCCHANsideN67
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

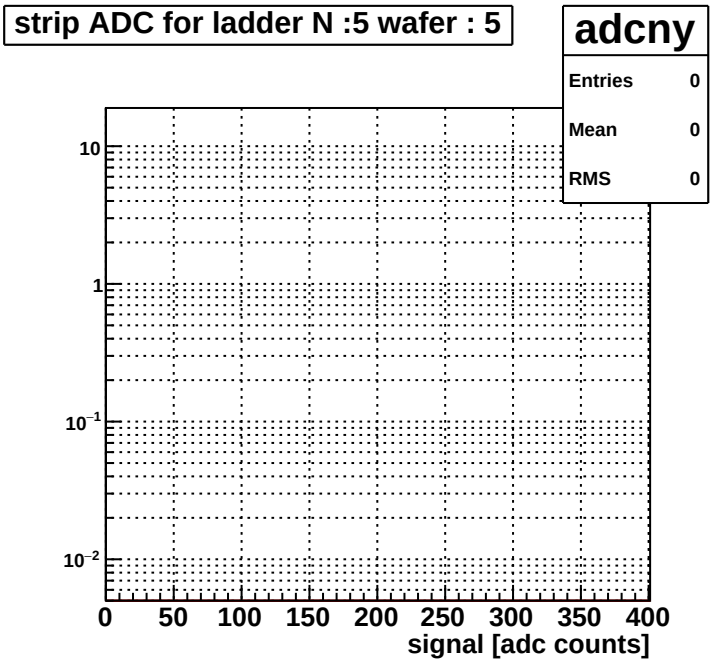

strip ADC for ladder P :5 wafer : 4
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

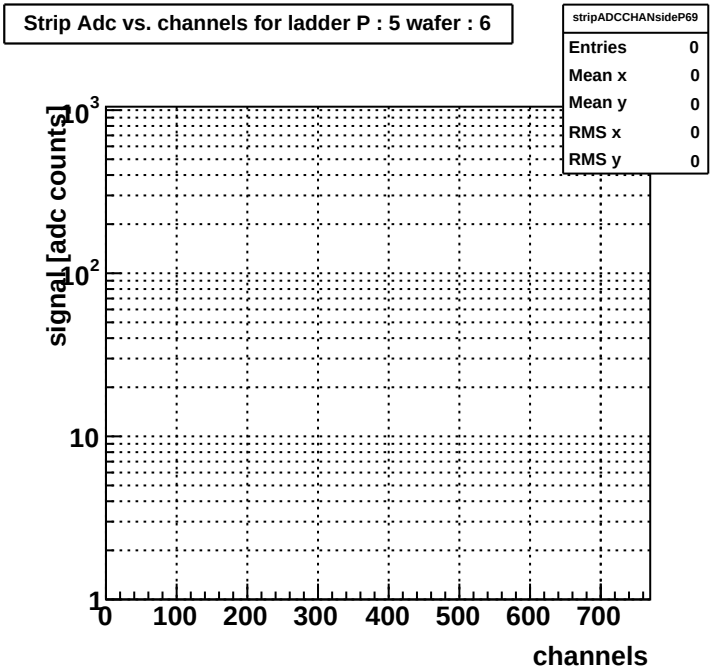

strip ADC for ladder N :5 wafer : 4
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

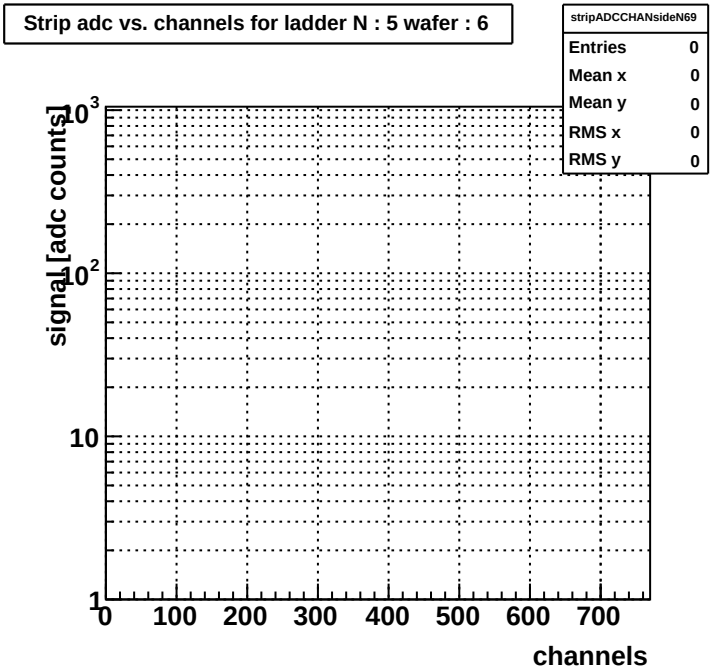

Strip Adc vs. channels for ladder P : 5 wafer : 5
stripADCCHANsideP68
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

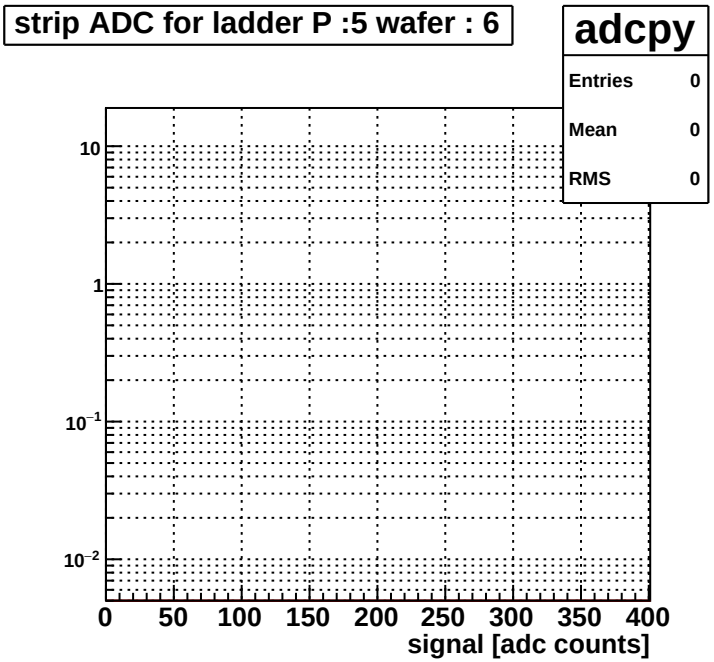

Strip adc vs. channels for ladder N : 5 wafer : 5
stripADCCHANsideN68
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

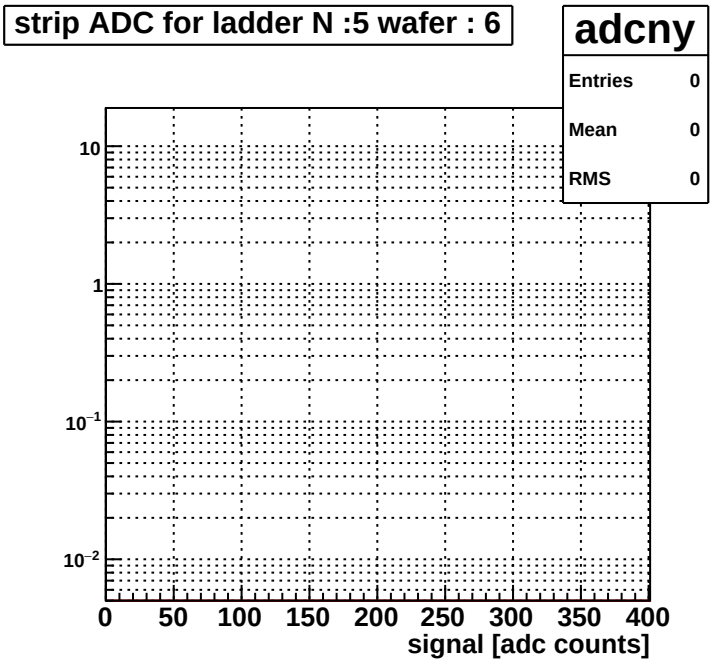

strip ADC for ladder P :5 wafer : 5
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

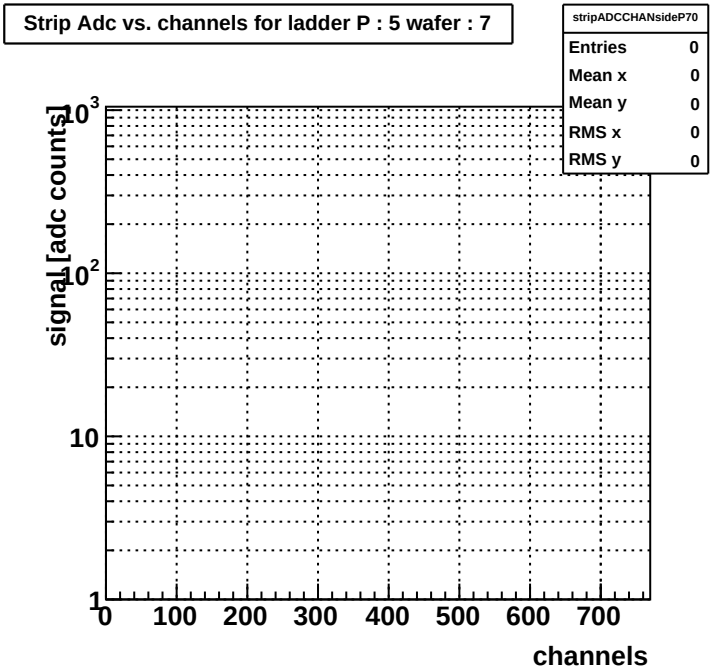

strip ADC for ladder N :5 wafer : 5
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

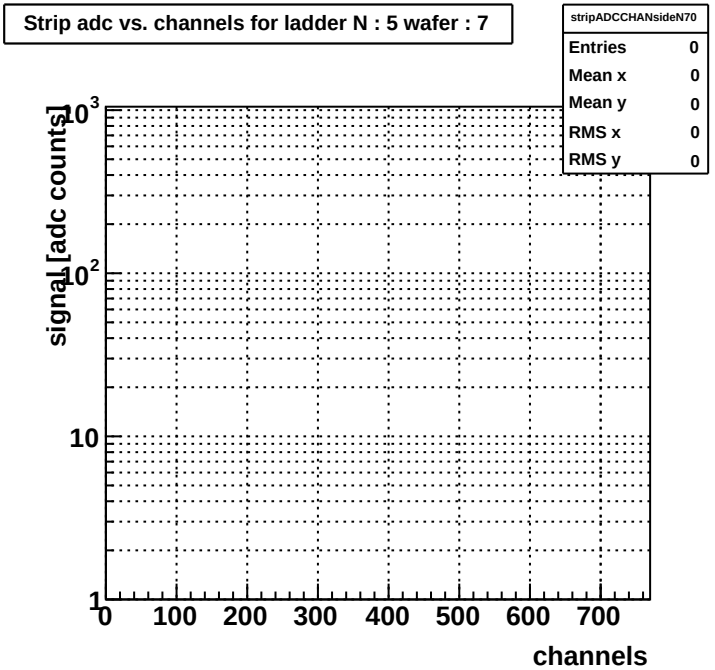

Strip Adc vs. channels for ladder P : 5 wafer : 6
stripADCCHANsideP69
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

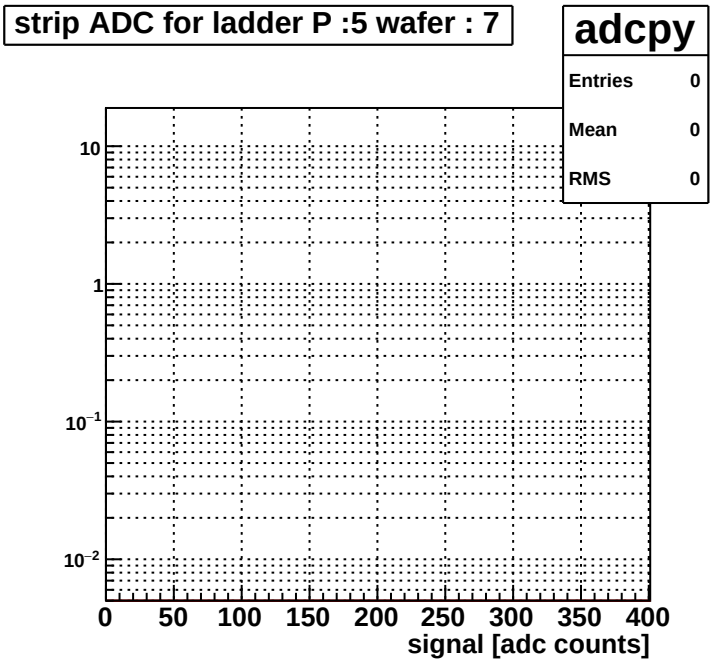

Strip adc vs. channels for ladder N : 5 wafer : 6
stripADCCHANsideN69
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

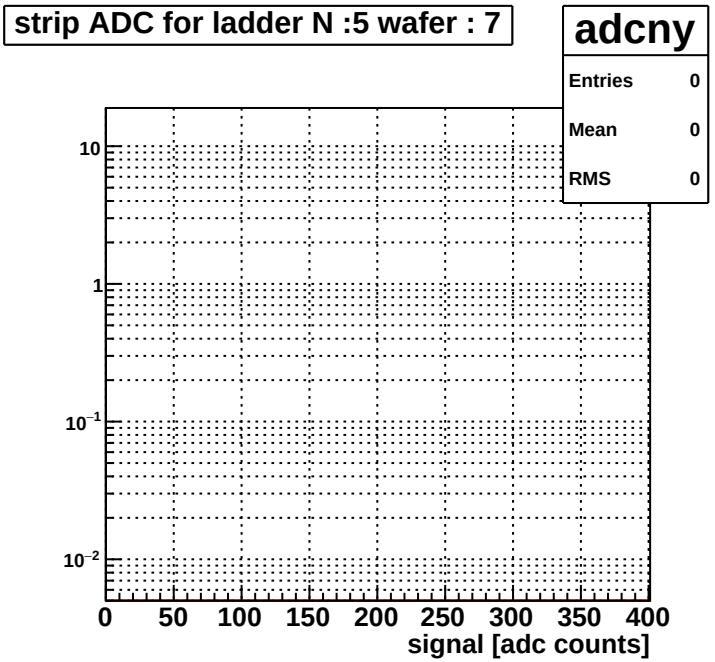

strip ADC for ladder P :5 wafer : 6
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

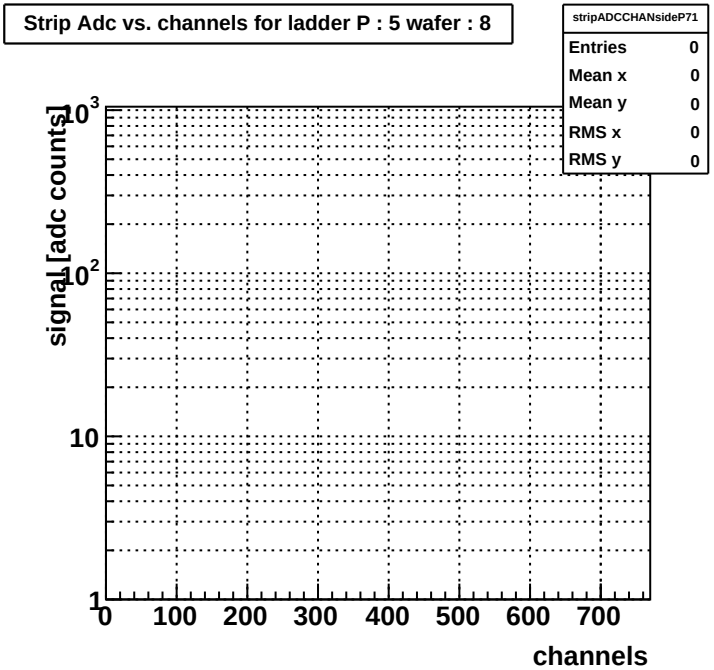

strip ADC for ladder N :5 wafer : 6
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

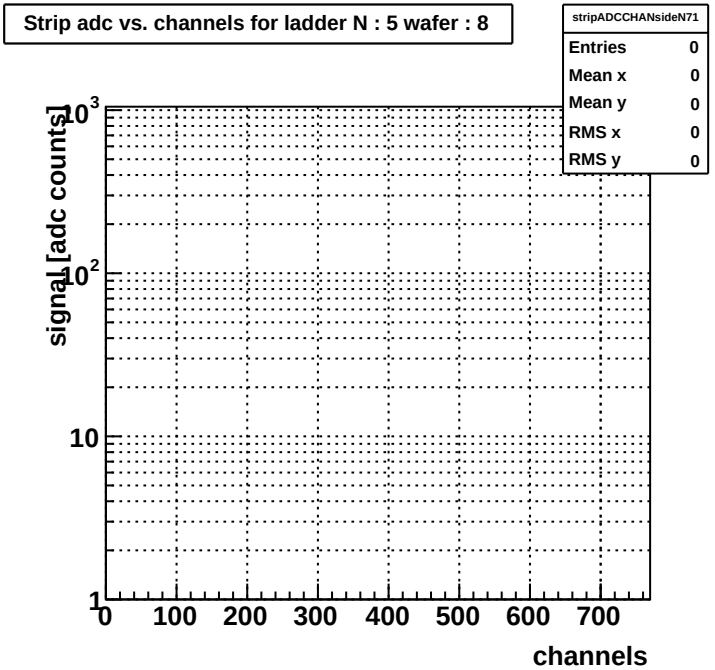

Strip Adc vs. channels for ladder P : 5 wafer : 7
stripADCCHANsideP70
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

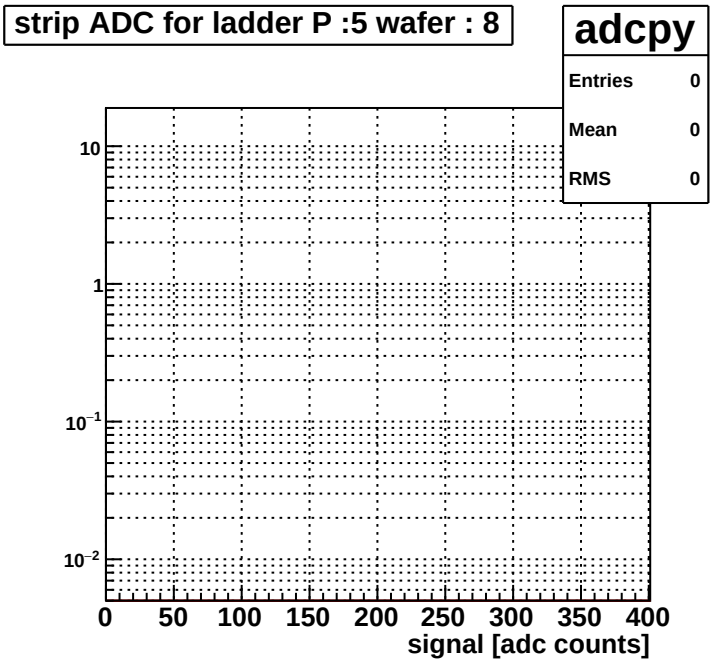

Strip adc vs. channels for ladder N : 5 wafer : 7
stripADCCHANsideN70
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

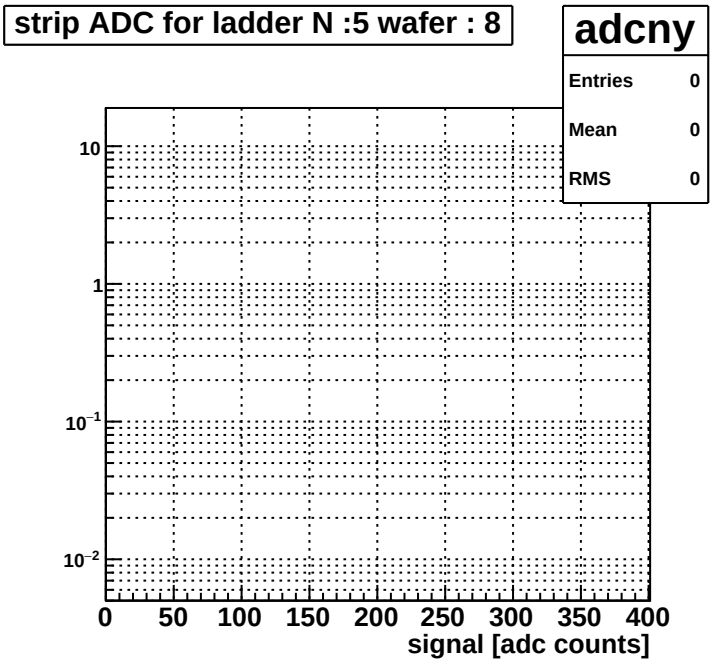

strip ADC for ladder P :5 wafer : 7
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

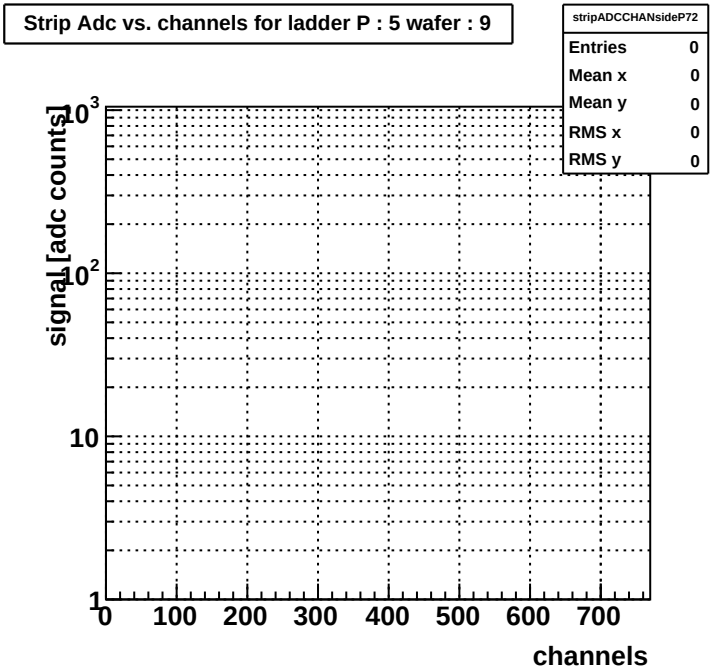

strip ADC for ladder N :5 wafer : 7
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

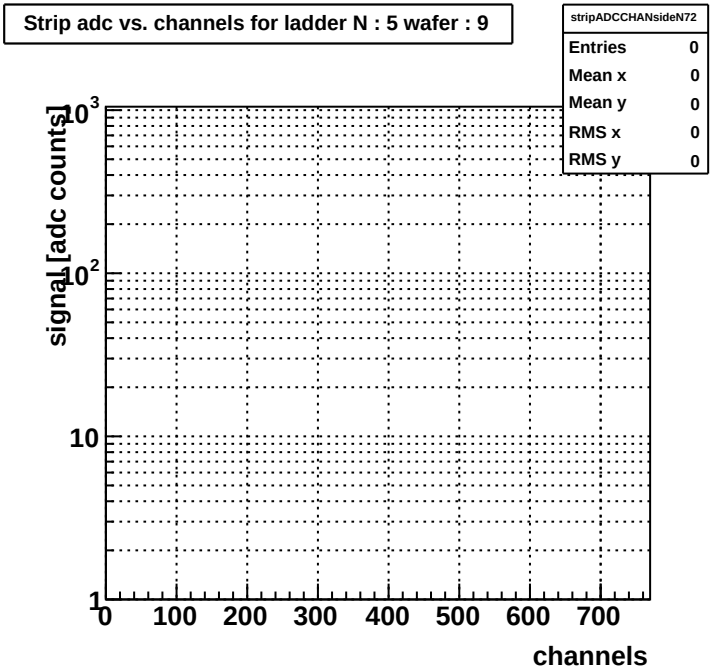

Strip Adc vs. channels for ladder P : 5 wafer : 8
stripADCCHANsideP71
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

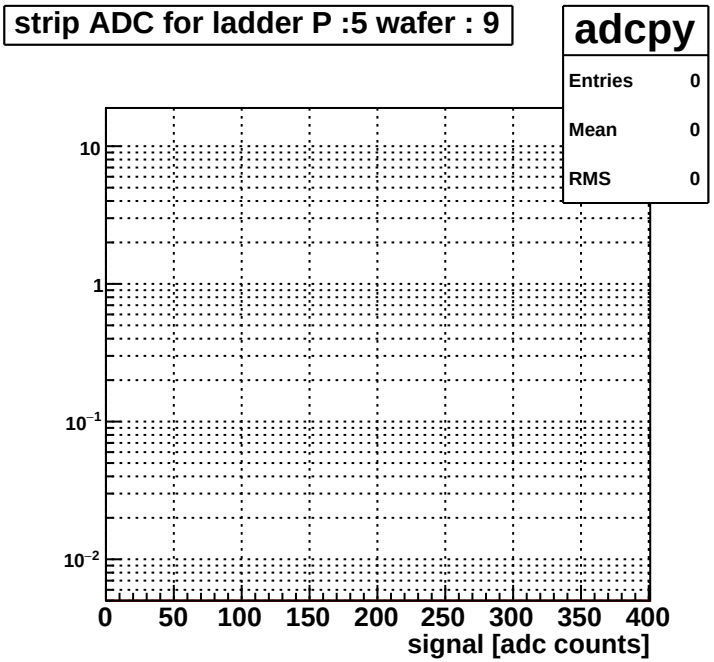

Strip adc vs. channels for ladder N : 5 wafer : 8
stripADCCHANsideN71
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

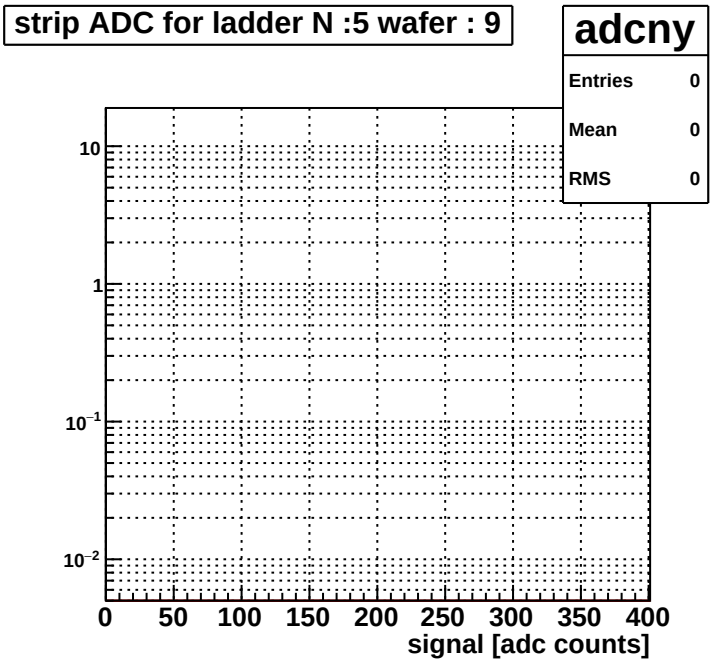

strip ADC for ladder P :5 wafer : 8
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

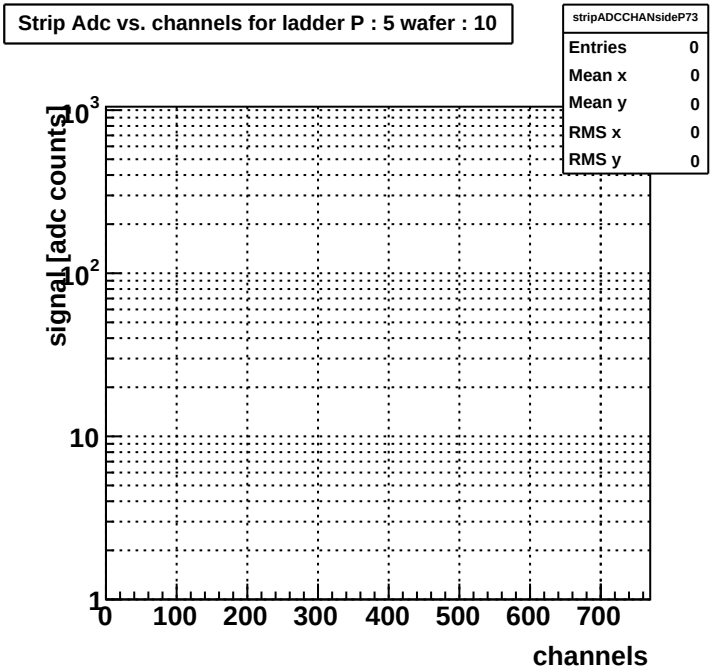

strip ADC for ladder N :5 wafer : 8
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

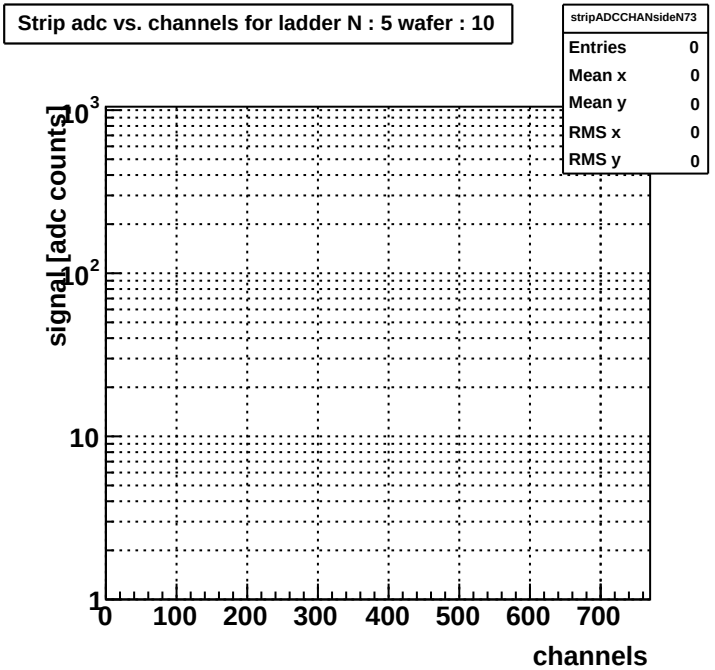

Strip Adc vs. channels for ladder P : 5 wafer : 9
stripADCCHANsideP72
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

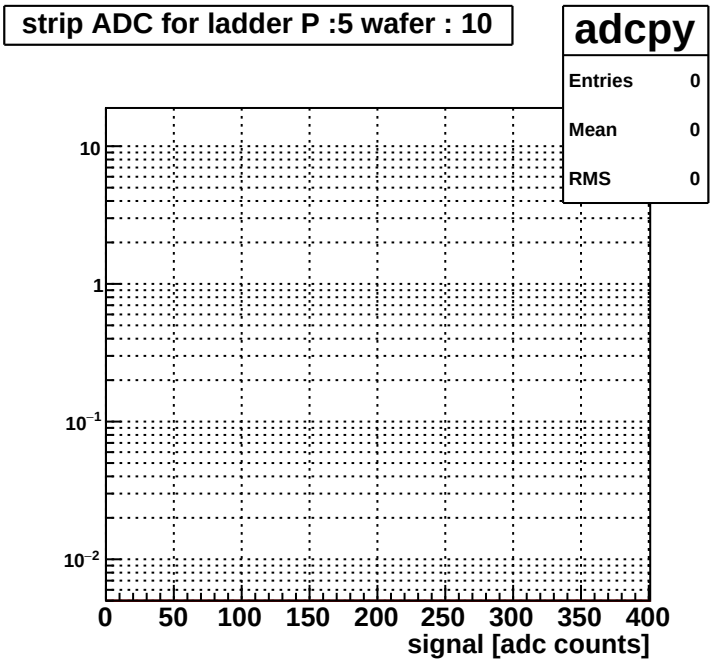

Strip adc vs. channels for ladder N : 5 wafer : 9
stripADCCHANsideN72
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

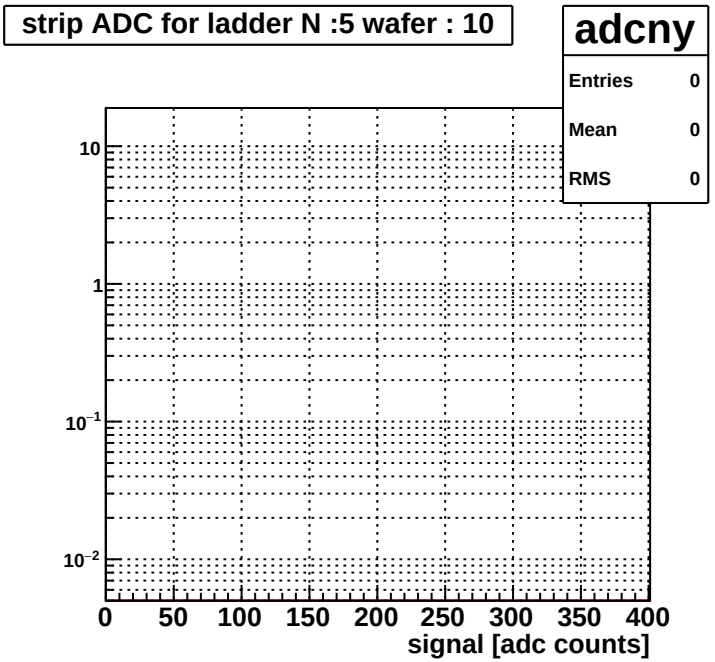

strip ADC for ladder P :5 wafer : 9
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

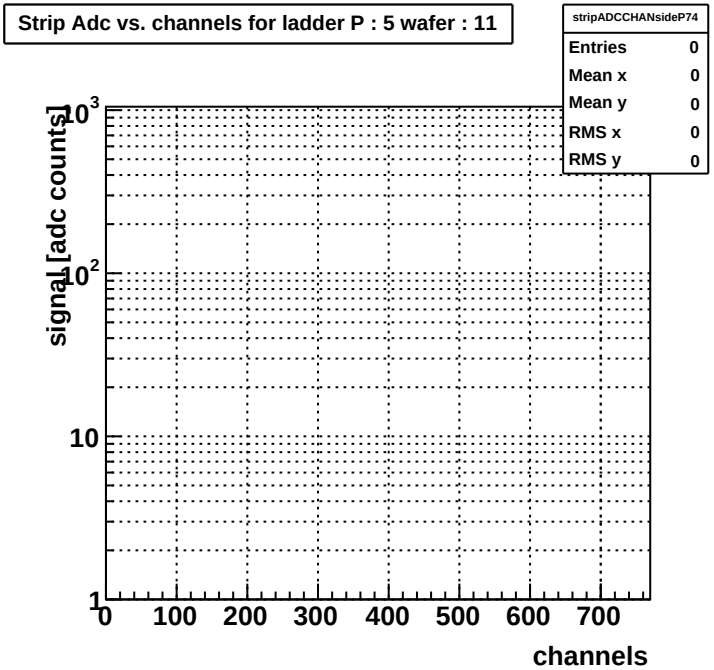

strip ADC for ladder N :5 wafer : 9
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

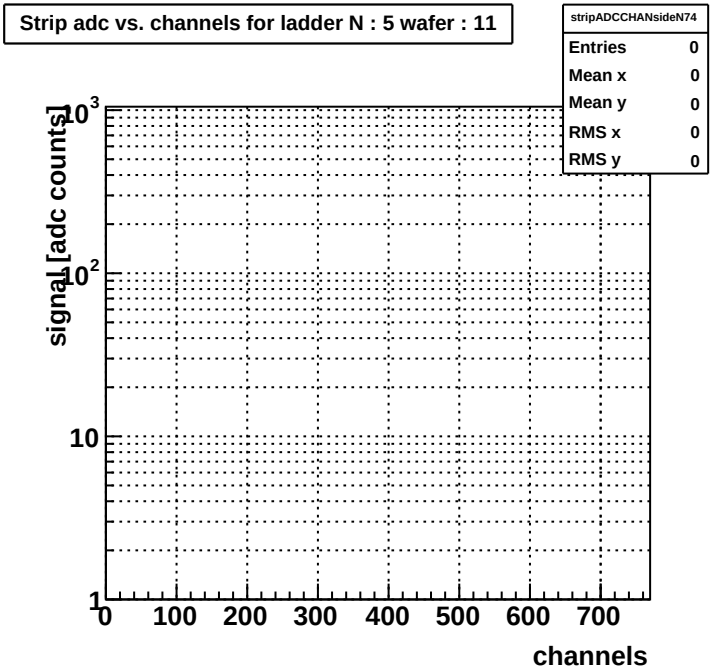

Strip Adc vs. channels for ladder P : 5 wafer : 10
stripADCCHANsideP73
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

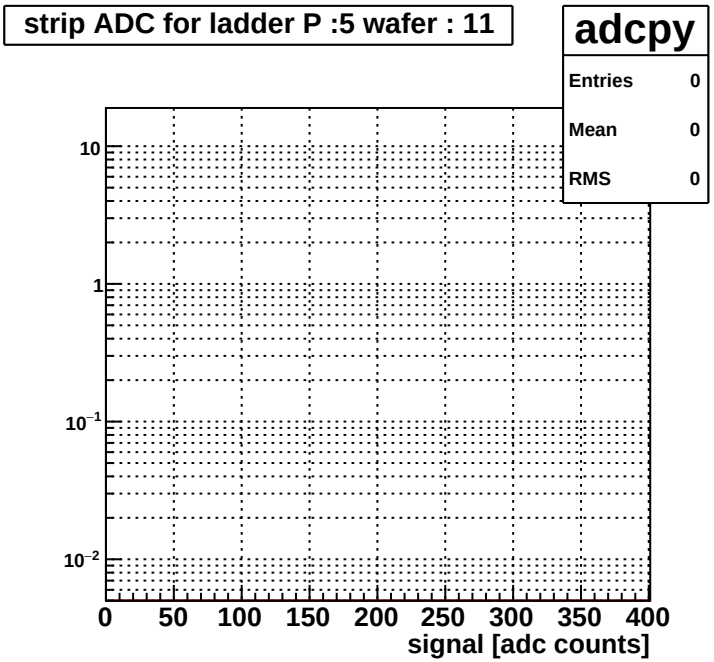

Strip adc vs. channels for ladder N : 5 wafer : 10
stripADCCHANsideN73
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

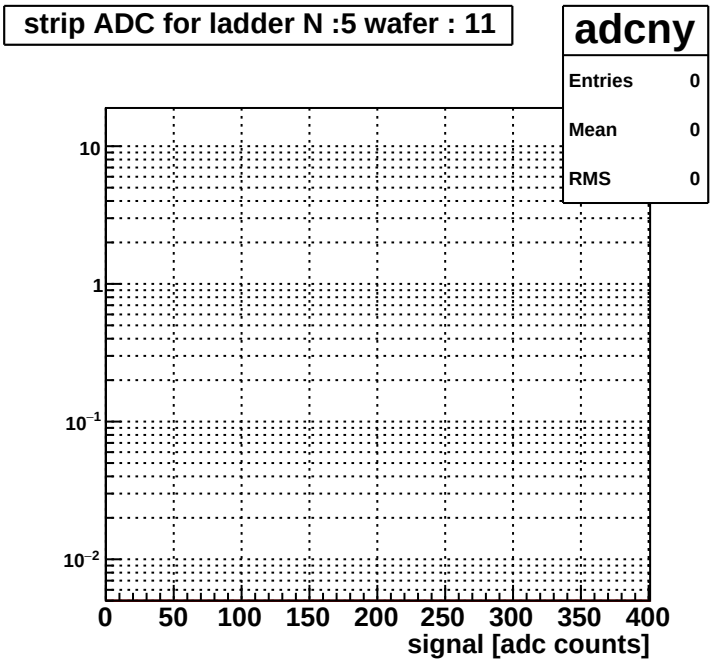

strip ADC for ladder P :5 wafer : 10
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

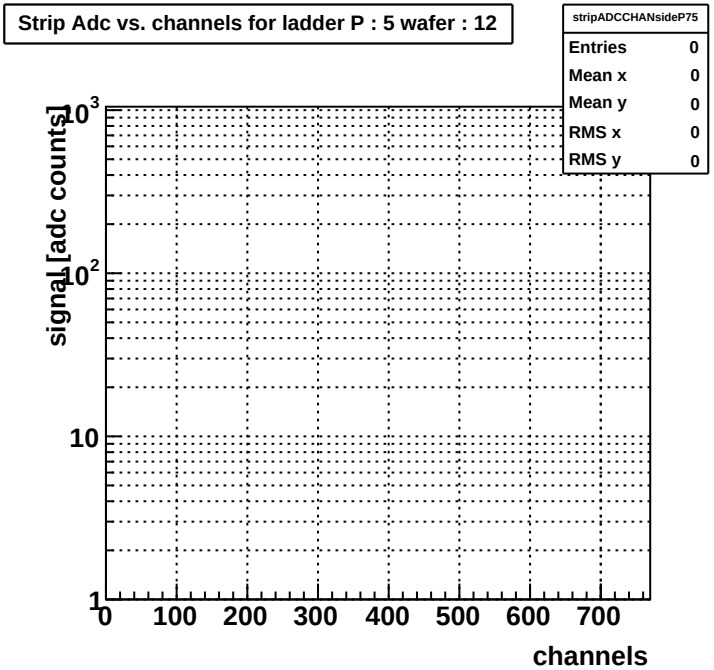

strip ADC for ladder N :5 wafer : 10
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

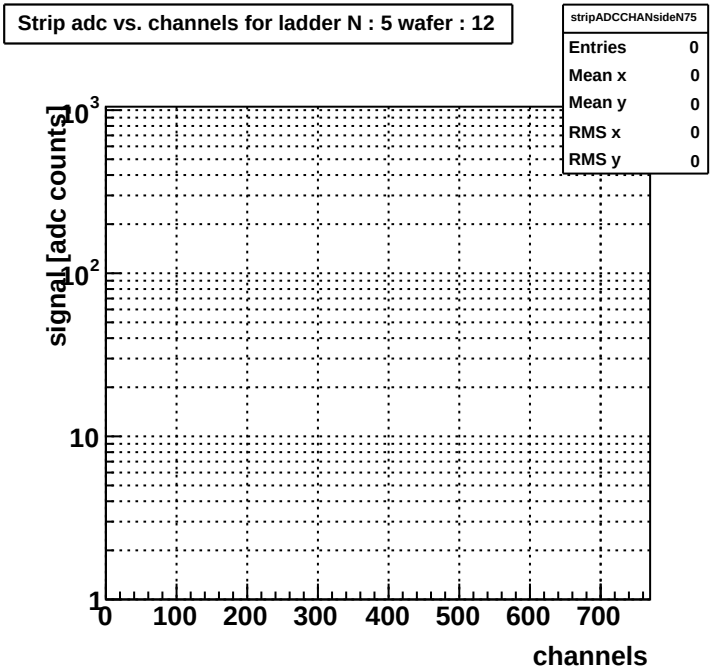

Strip Adc vs. channels for ladder P : 5 wafer : 11
stripADCCHANsideP74
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

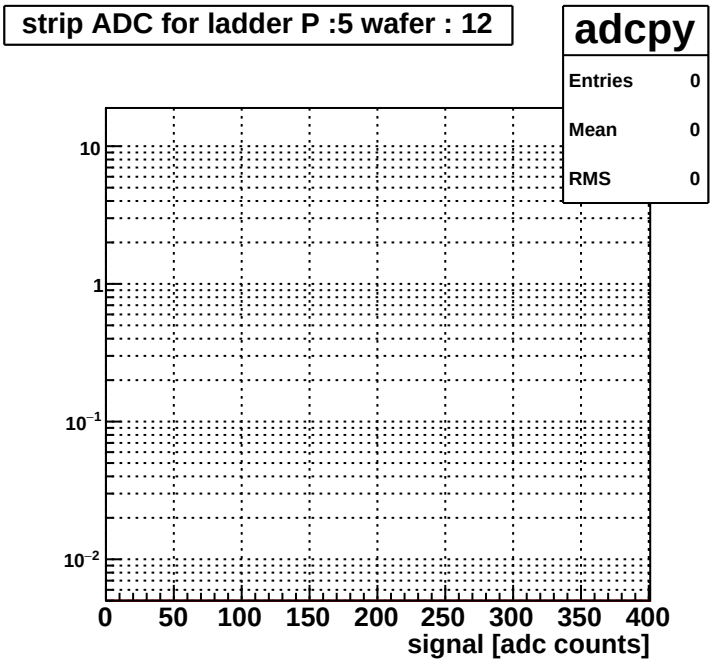

Strip adc vs. channels for ladder N : 5 wafer : 11
stripADCCHANsideN74
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

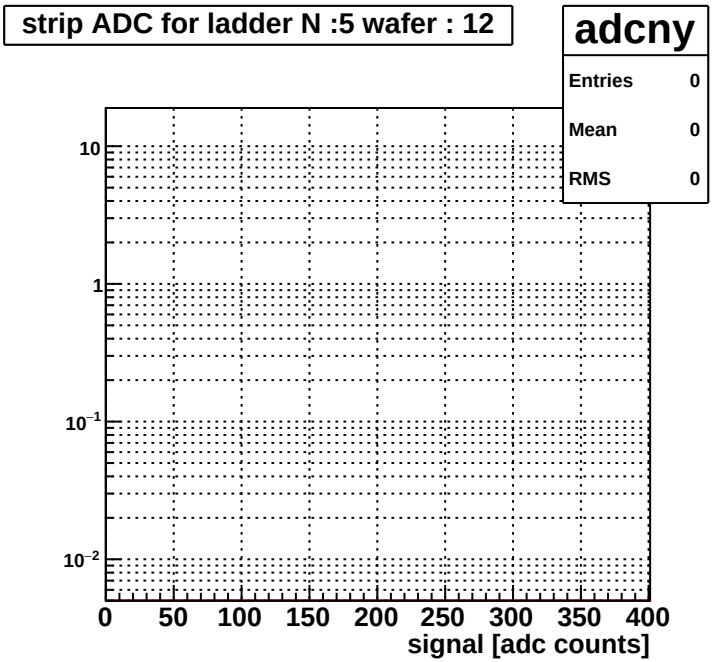

strip ADC for ladder P :5 wafer : 11
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

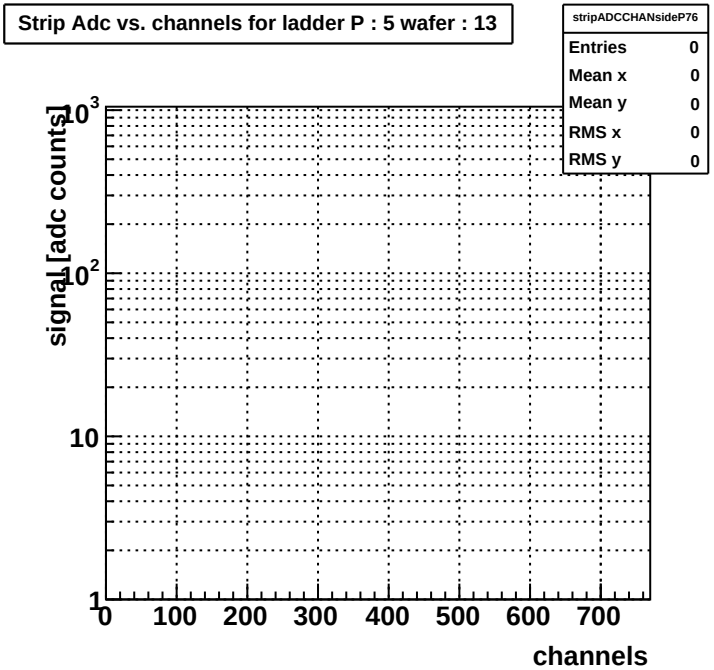

strip ADC for ladder N :5 wafer : 11
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

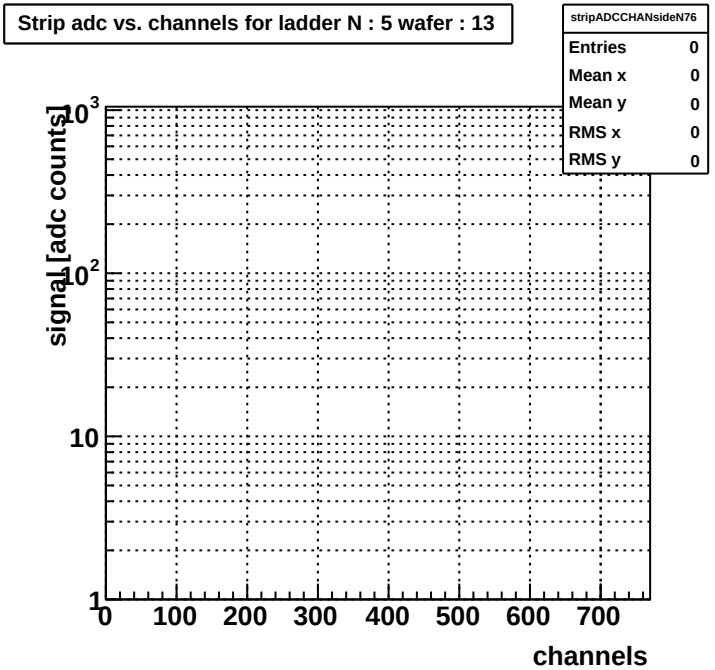

Strip Adc vs. channels for ladder P : 5 wafer : 12
stripADCCHANsideP75
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

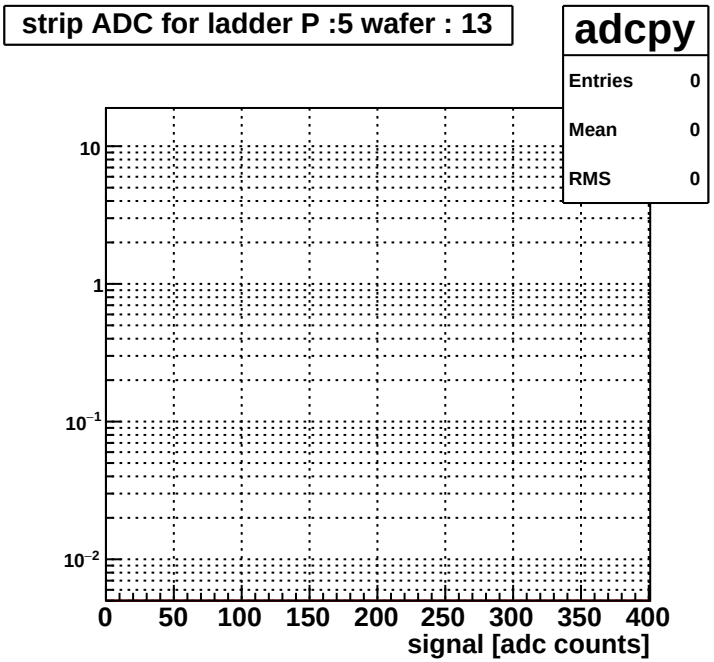

Strip adc vs. channels for ladder N : 5 wafer : 12
stripADCCHANsideN75
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

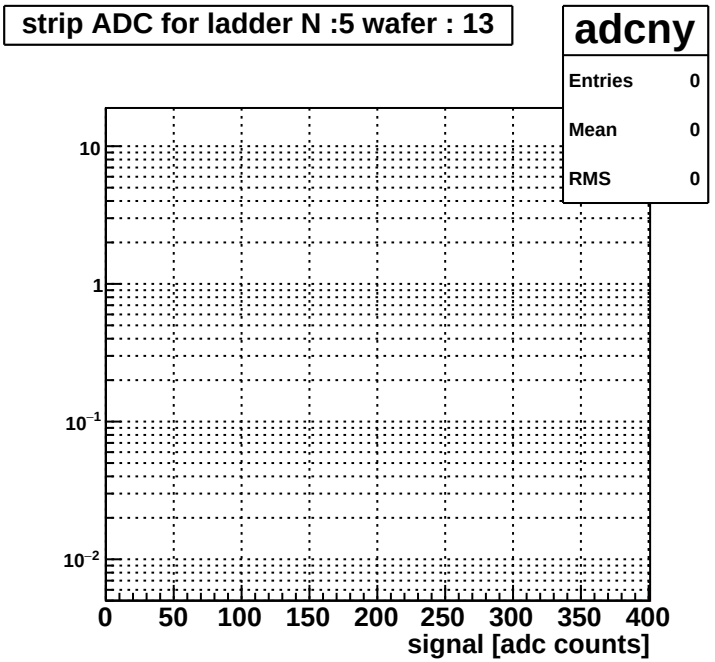

strip ADC for ladder P :5 wafer : 12
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

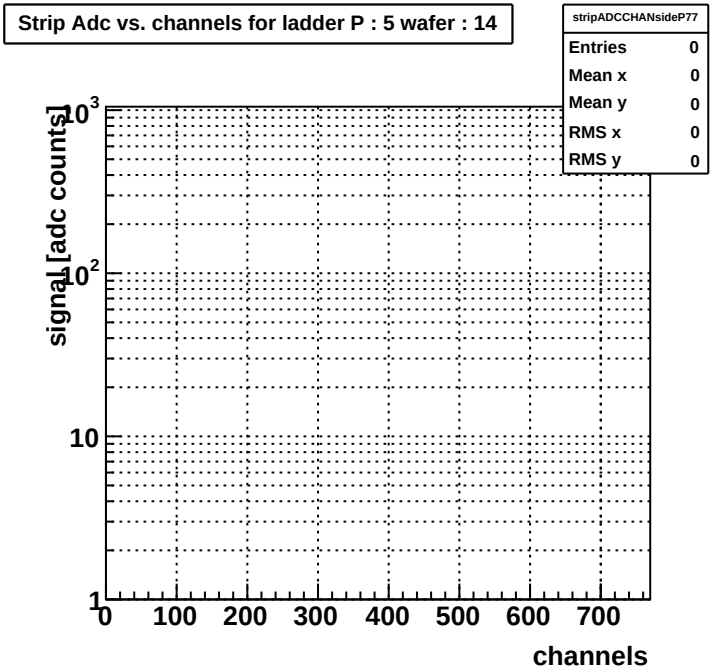

strip ADC for ladder N :5 wafer : 12
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

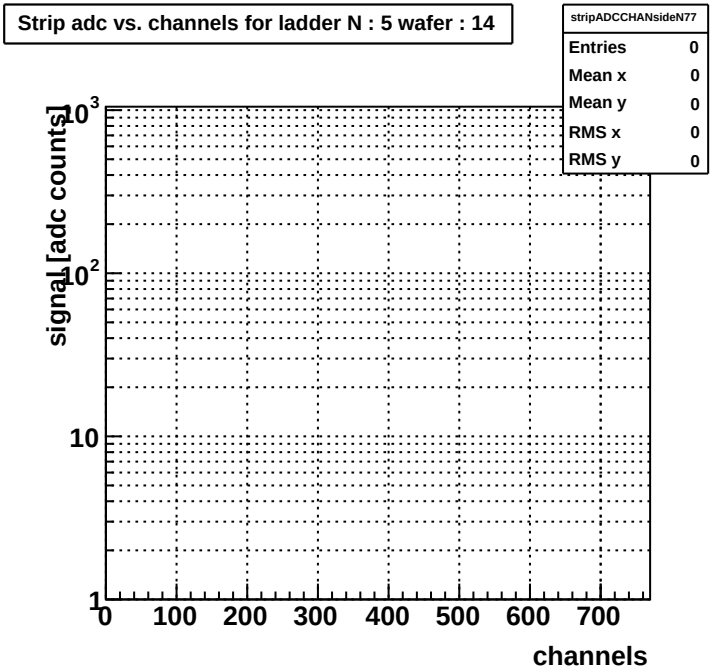

Strip Adc vs. channels for ladder P : 5 wafer : 13
stripADCCHANsideP76
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

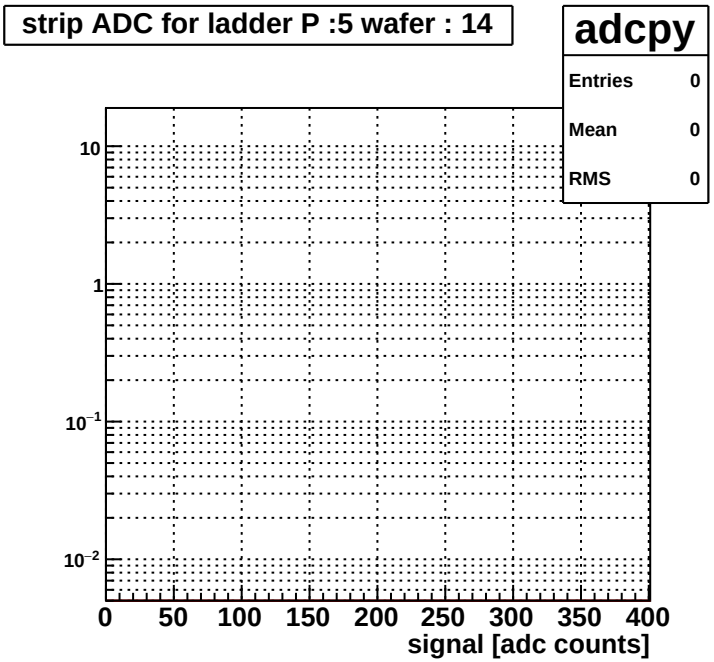

Strip adc vs. channels for ladder N : 5 wafer : 13
stripADCCHANsideN76
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

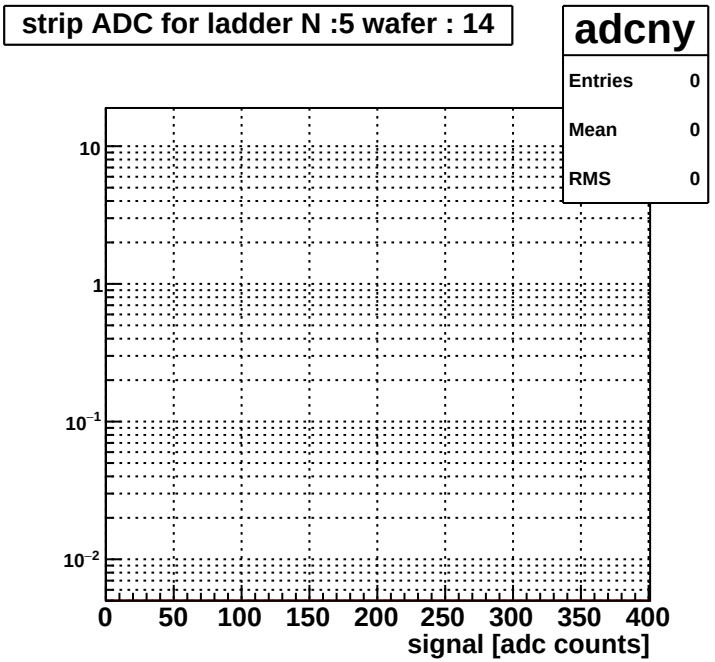

strip ADC for ladder P :5 wafer : 13
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

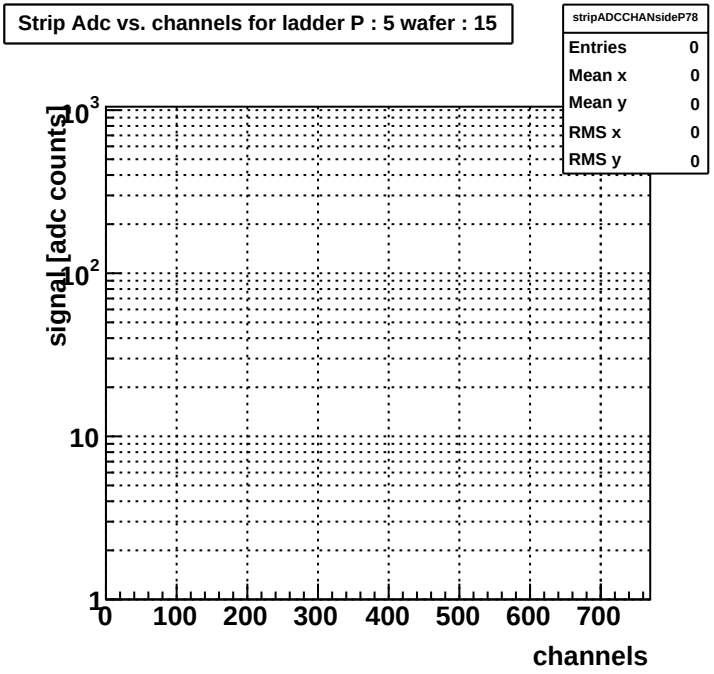

strip ADC for ladder N :5 wafer : 13
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

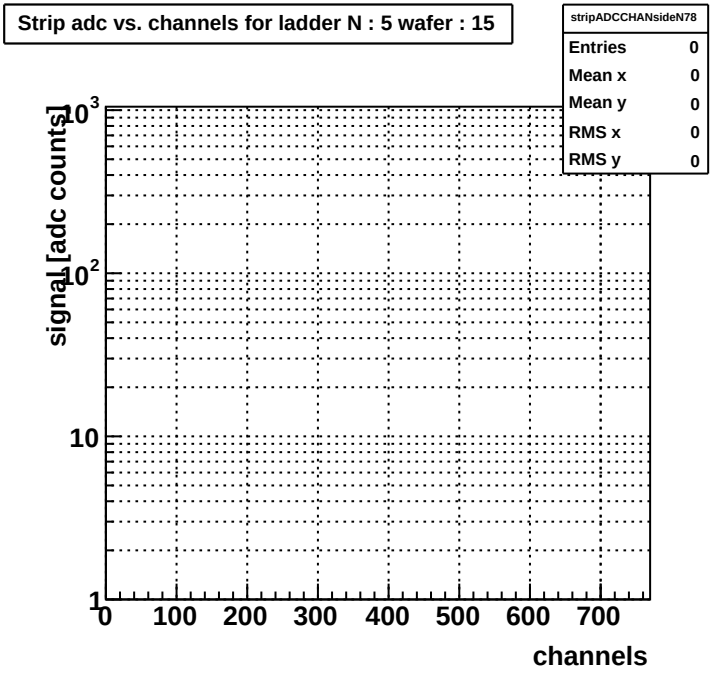

Strip Adc vs. channels for ladder P : 5 wafer : 14
stripADCCHANsideP77
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

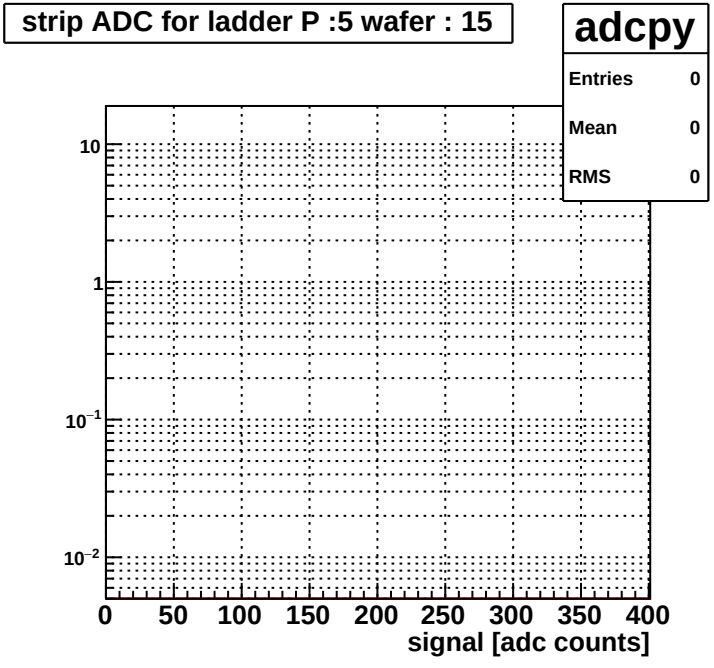

Strip adc vs. channels for ladder N : 5 wafer : 14
stripADCCHANsideN77
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

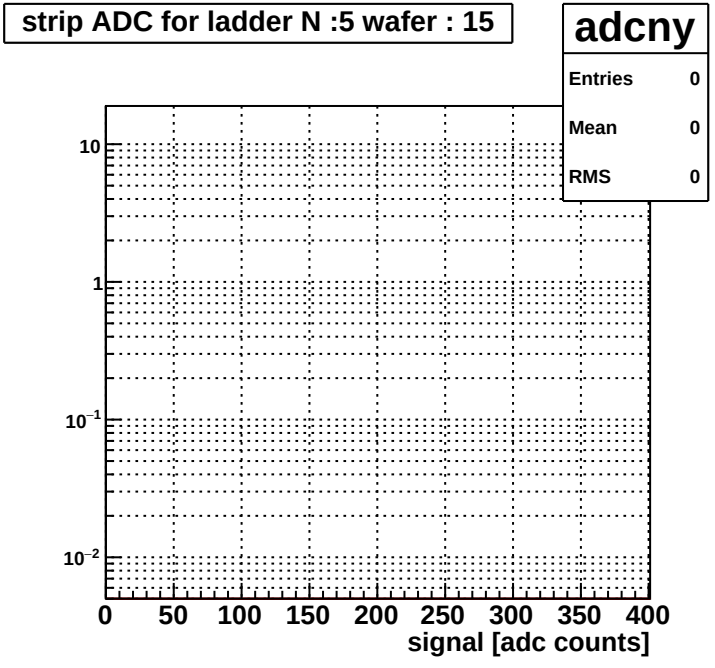

strip ADC for ladder P :5 wafer : 14
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

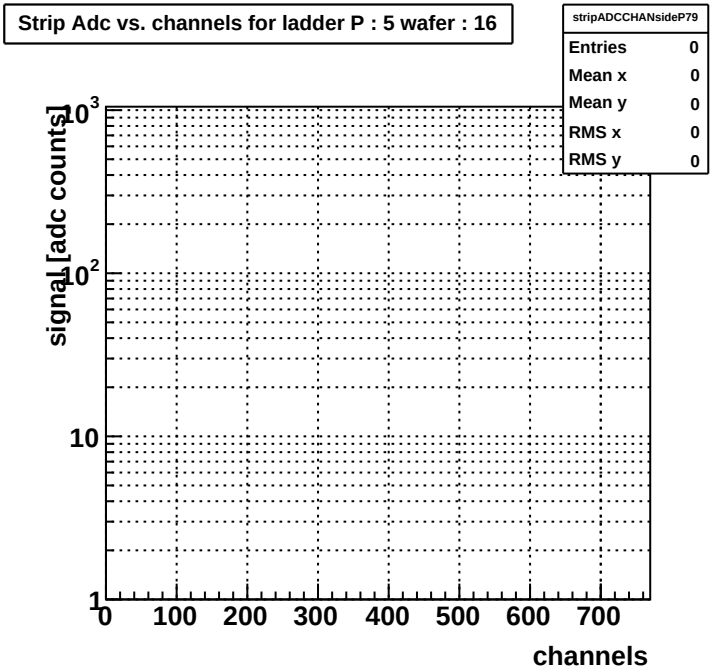

strip ADC for ladder N :5 wafer : 14
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

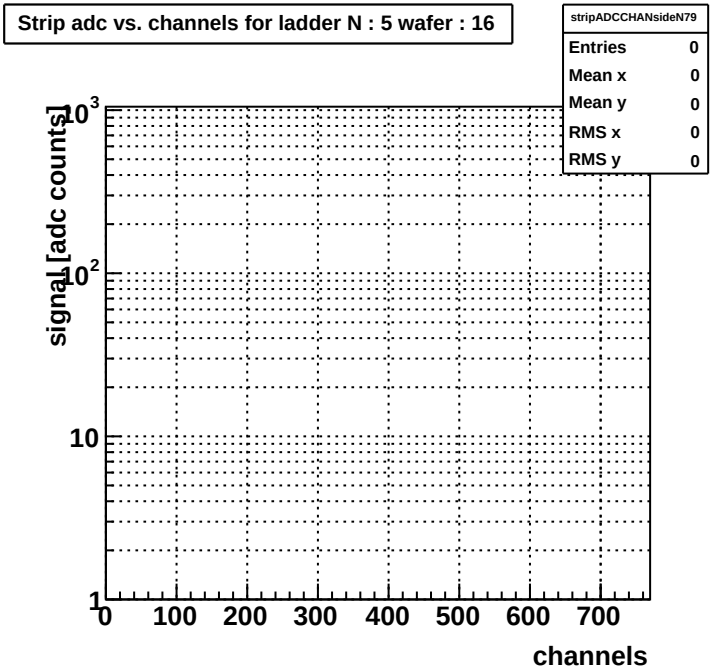

Strip Adc vs. channels for ladder P : 5 wafer : 15
stripADCCHANsideP78
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

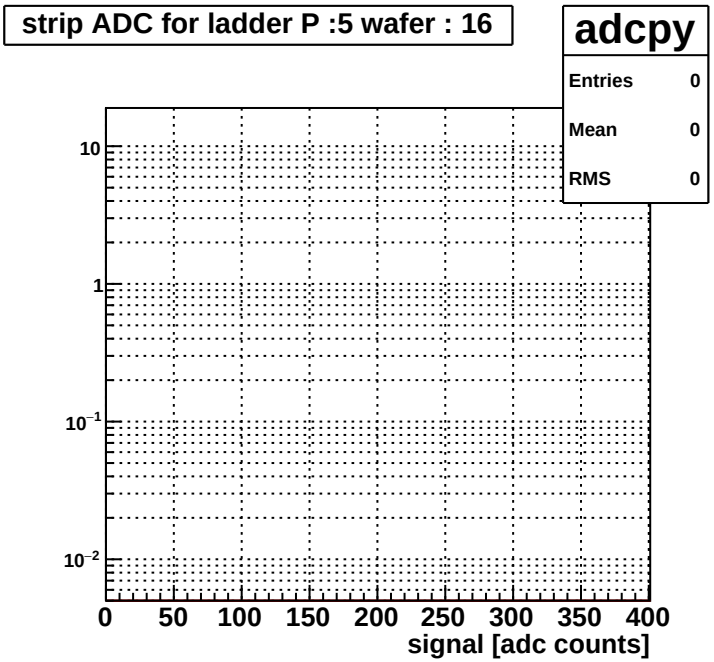

Strip adc vs. channels for ladder N : 5 wafer : 15
stripADCCHANsideN78
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

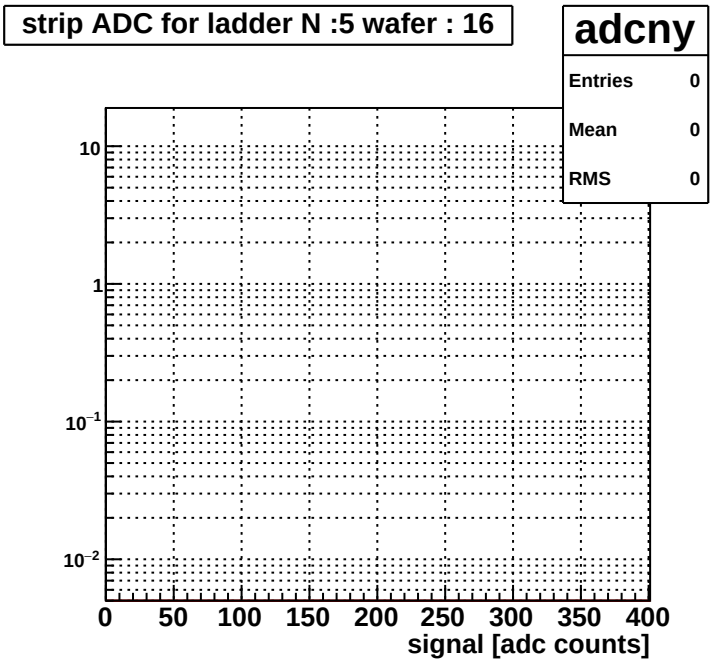

strip ADC for ladder P :5 wafer : 15
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

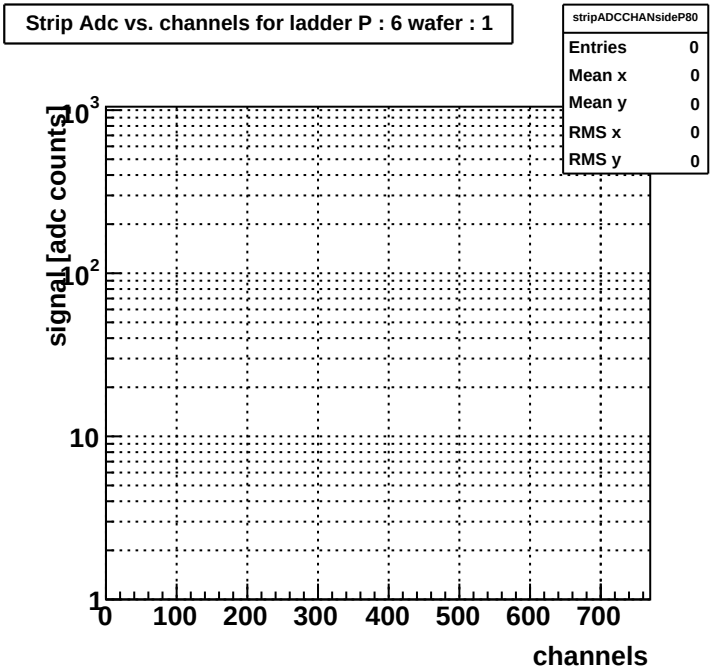

strip ADC for ladder N :5 wafer : 15
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

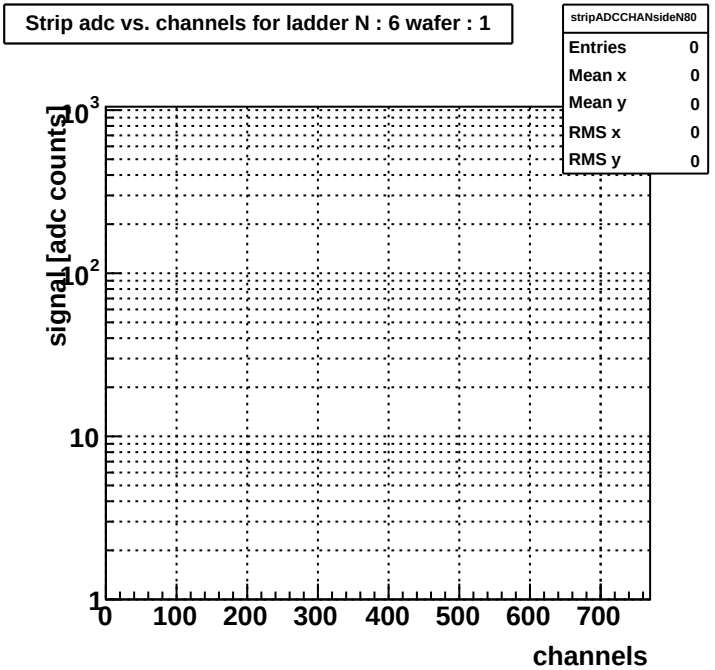

Strip Adc vs. channels for ladder P : 5 wafer : 16
stripADCCHANsideP79
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

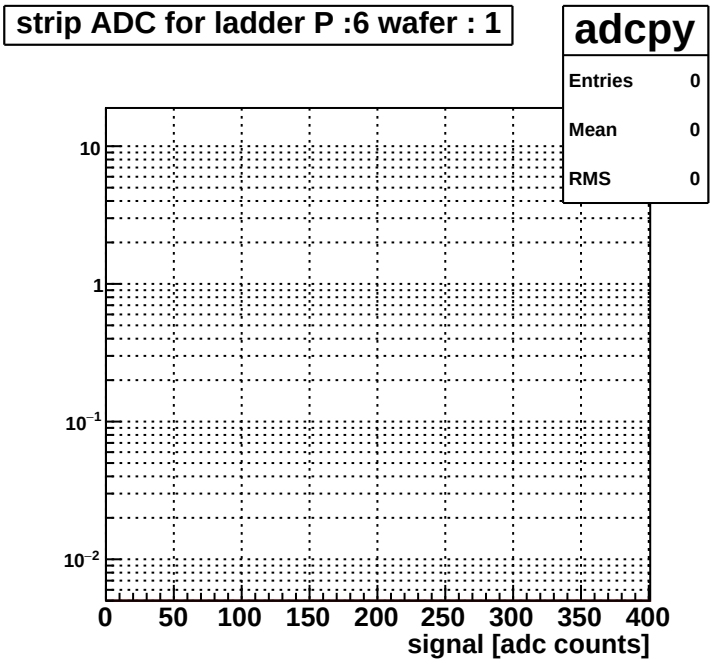

Strip adc vs. channels for ladder N : 5 wafer : 16
stripADCCHANsideN79
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

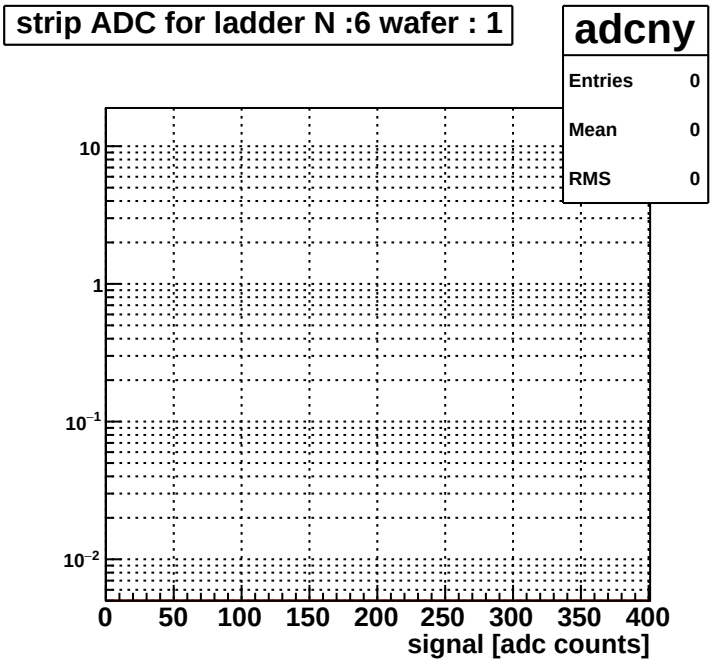

strip ADC for ladder P :5 wafer : 16
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

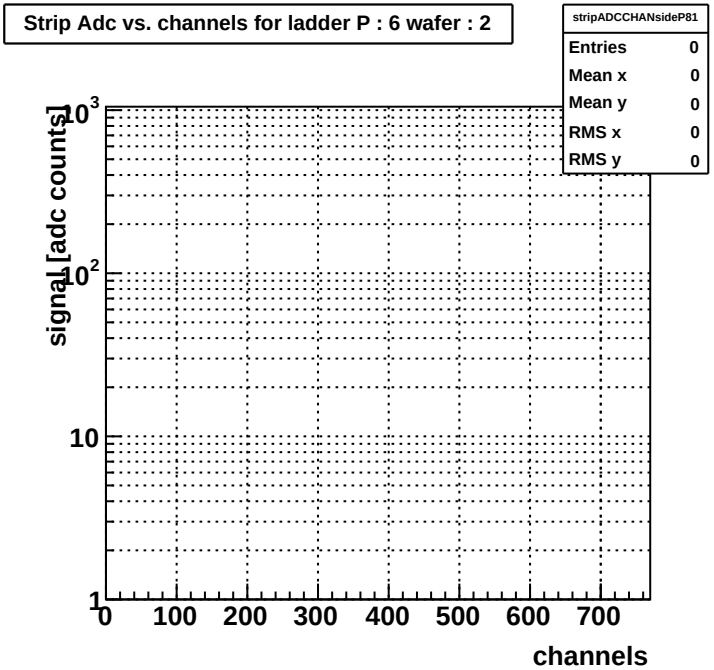

strip ADC for ladder N :5 wafer : 16
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

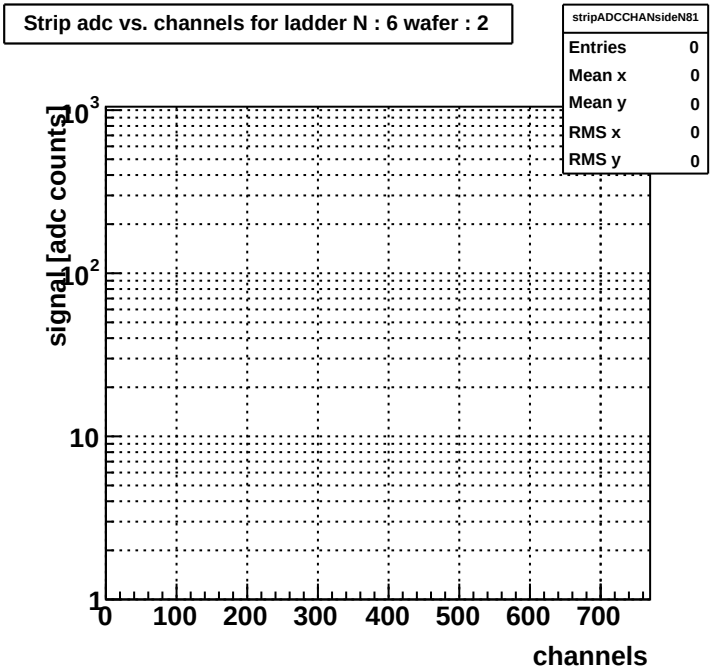

Strip Adc vs. channels for ladder P : 6 wafer : 1
stripADCCHANsideP80
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

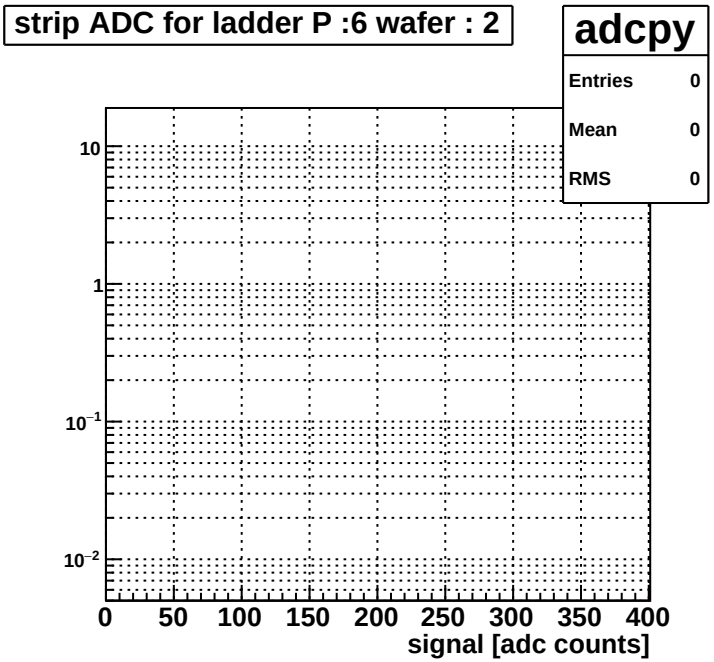

Strip adc vs. channels for ladder N : 6 wafer : 1
stripADCCHANsideN80
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

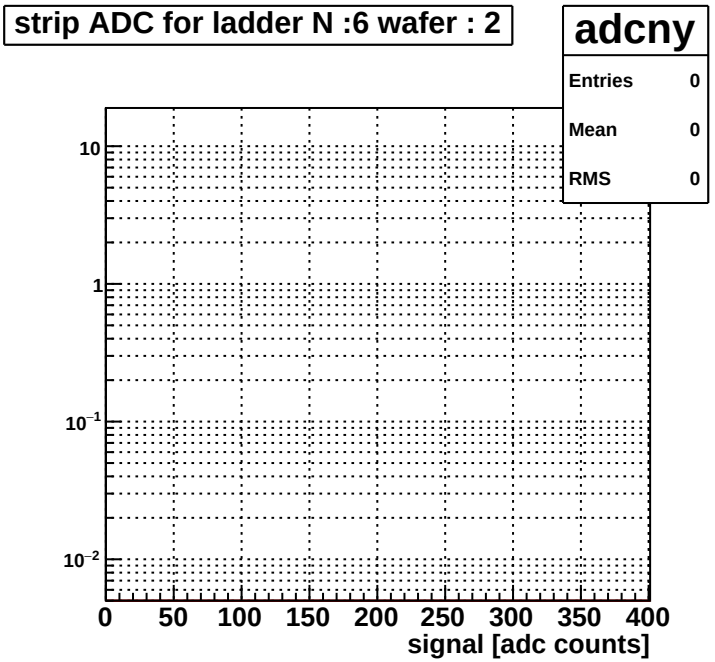

strip ADC for ladder P :6 wafer : 1
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

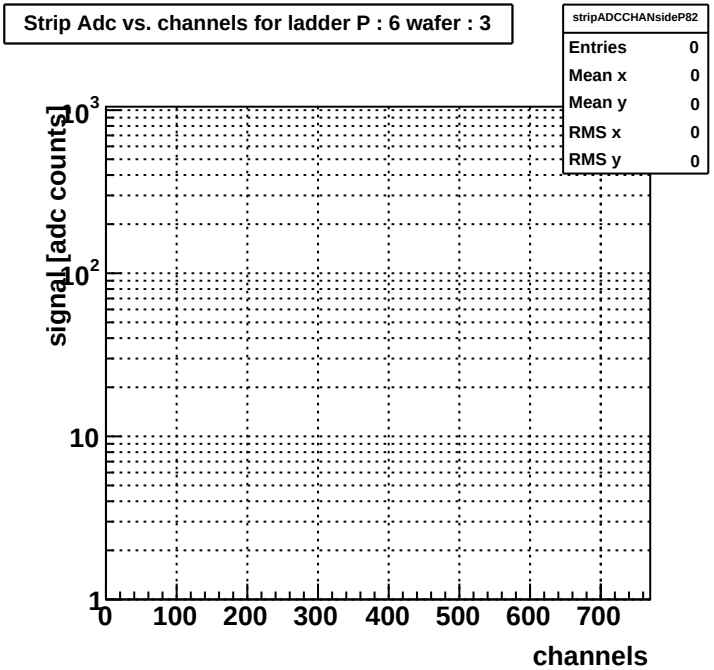

strip ADC for ladder N :6 wafer : 1
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

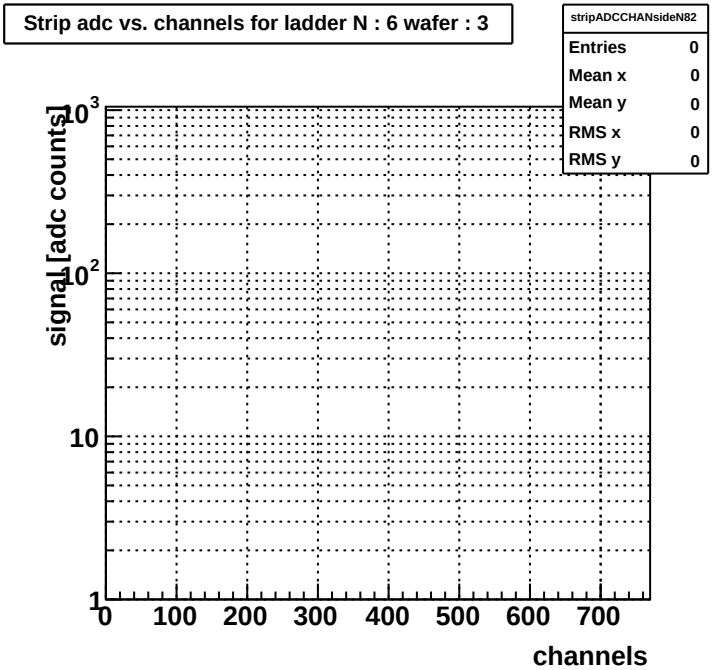

Strip Adc vs. channels for ladder P : 6 wafer : 2
stripADCCHANsideP81
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

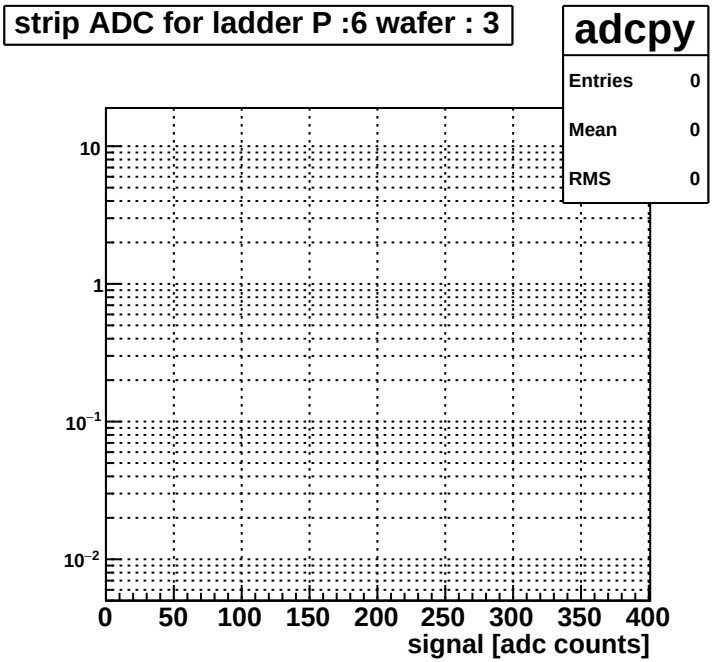

Strip adc vs. channels for ladder N : 6 wafer : 2
stripADCCHANsideN81
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

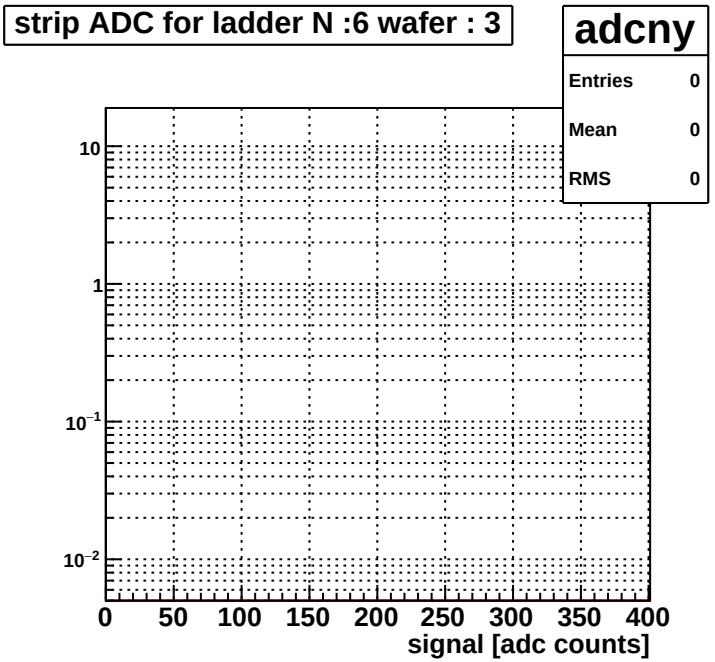

strip ADC for ladder P :6 wafer : 2
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

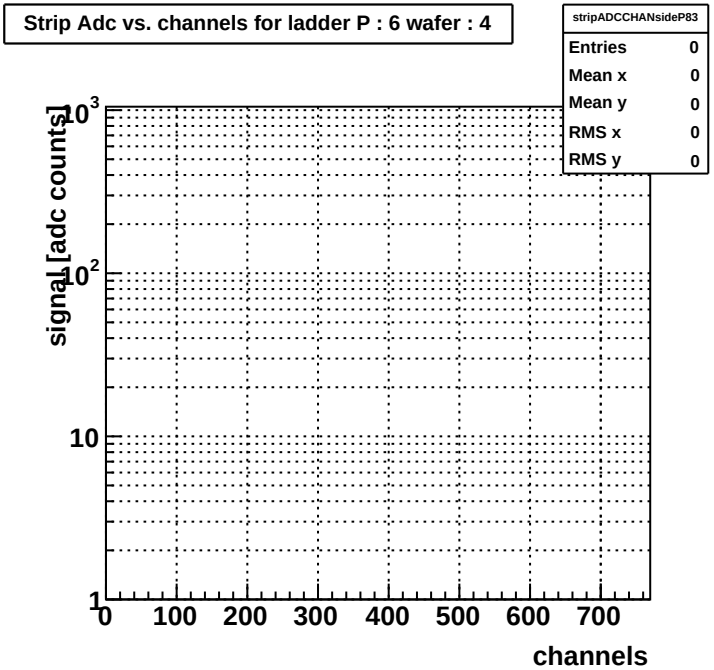

strip ADC for ladder N :6 wafer : 2
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

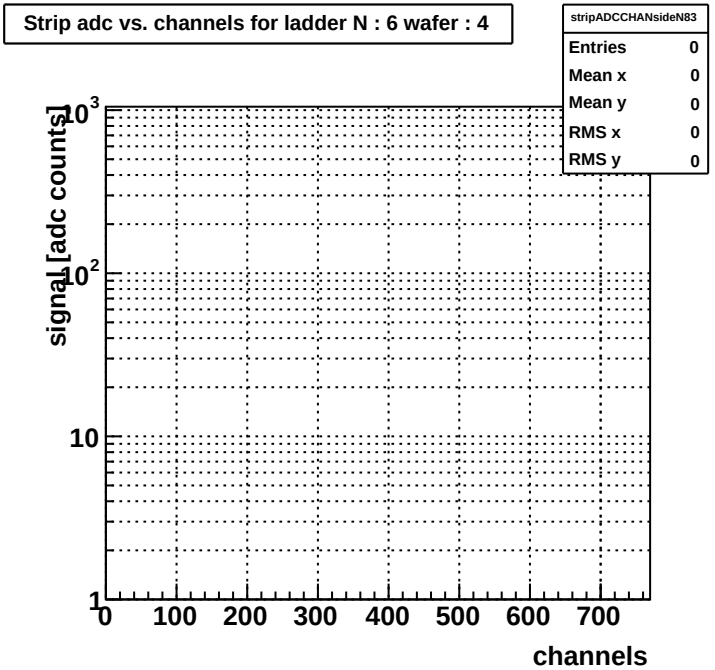

Strip Adc vs. channels for ladder P : 6 wafer : 3
stripADCCHANsideP82
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

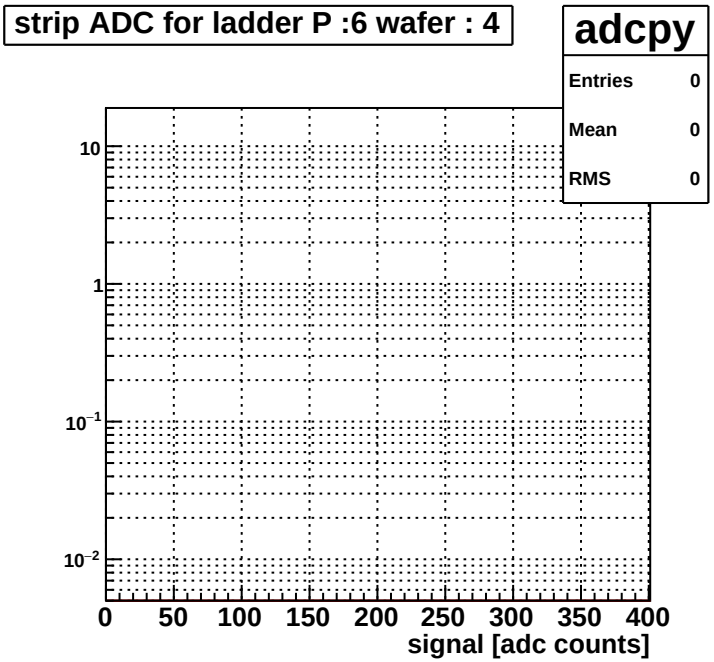

Strip adc vs. channels for ladder N : 6 wafer : 3
stripADCCHANsideN82
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

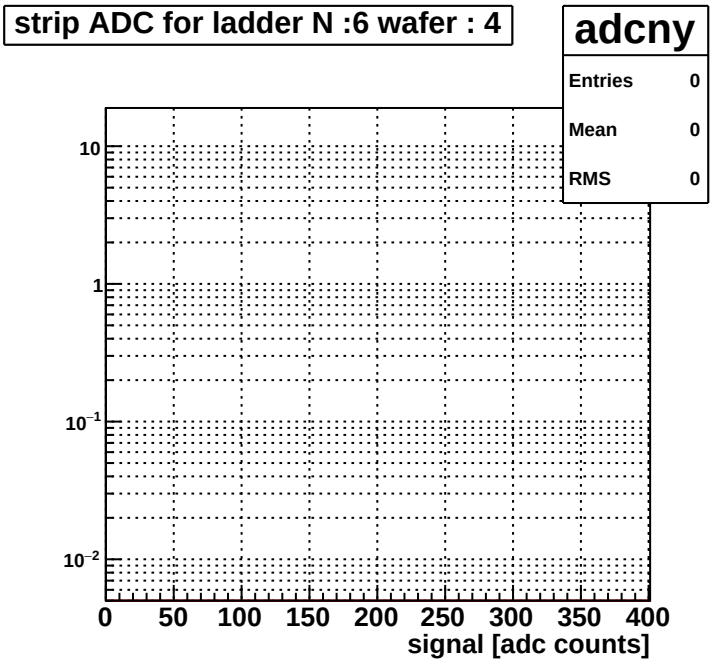

strip ADC for ladder P :6 wafer : 3
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

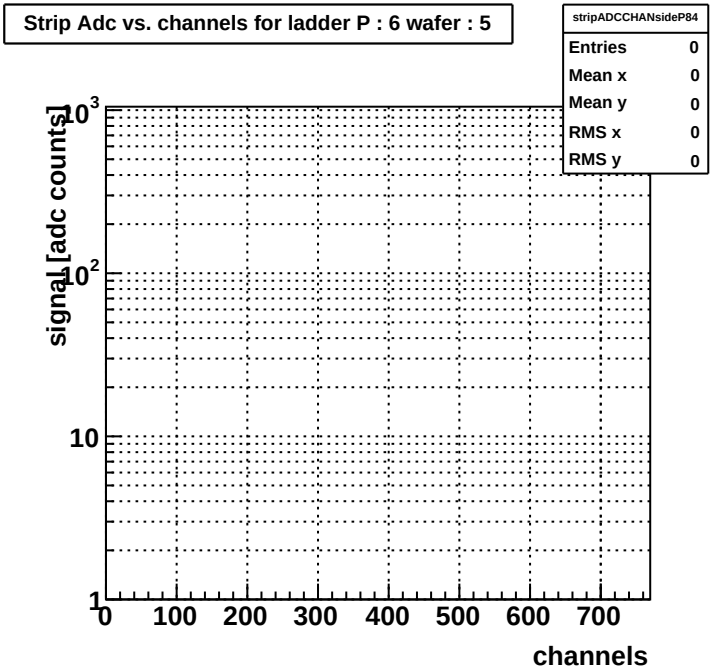

strip ADC for ladder N :6 wafer : 3
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

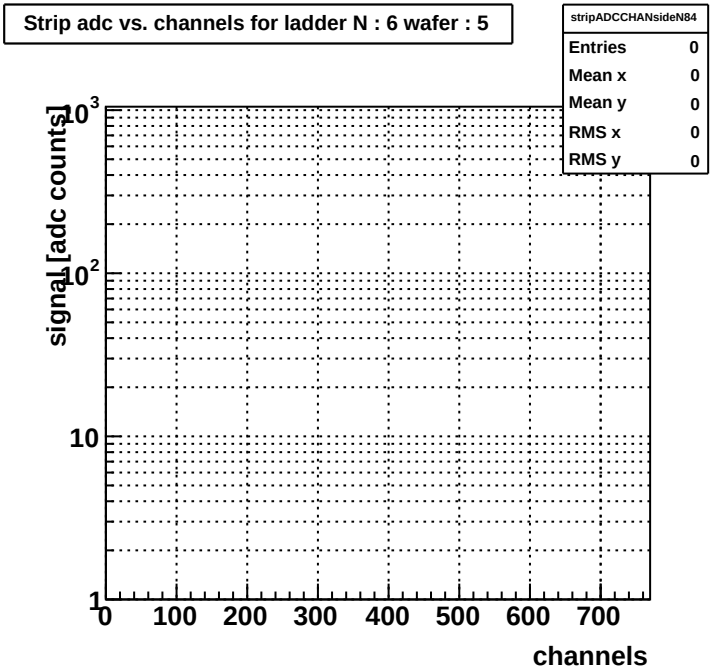

Strip Adc vs. channels for ladder P : 6 wafer : 4
stripADCCHANsideP83
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

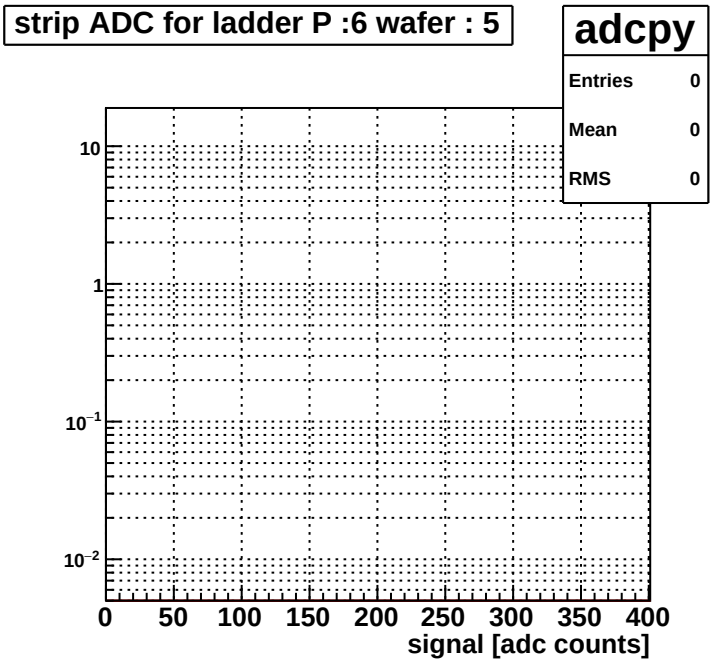

Strip adc vs. channels for ladder N : 6 wafer : 4
stripADCCHANsideN83
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

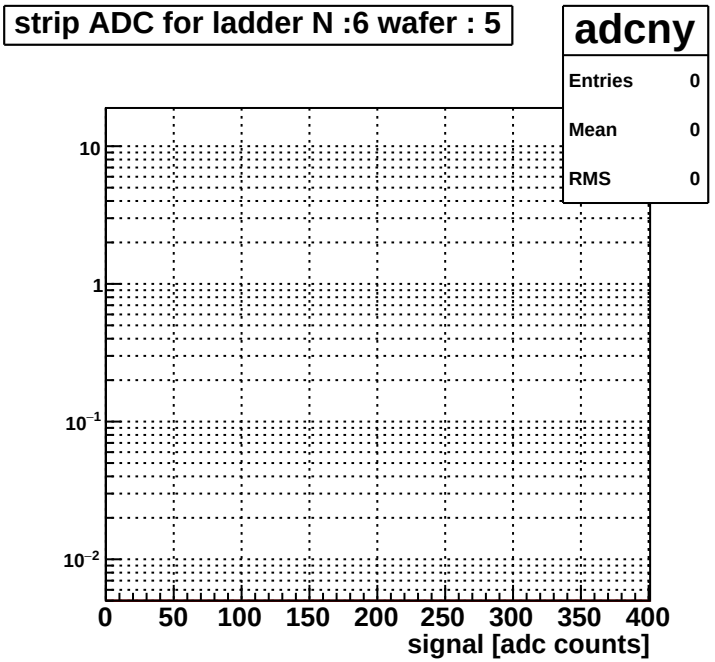

strip ADC for ladder P :6 wafer : 4
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

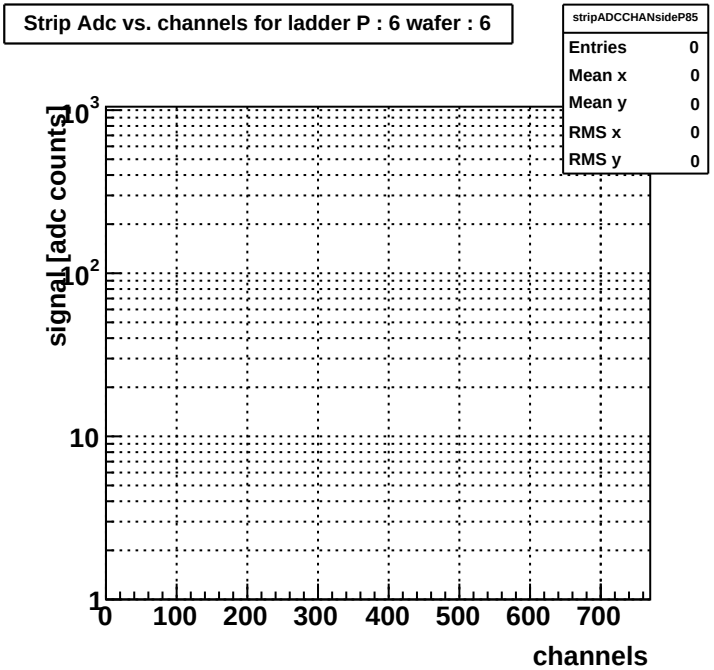

strip ADC for ladder N :6 wafer : 4
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

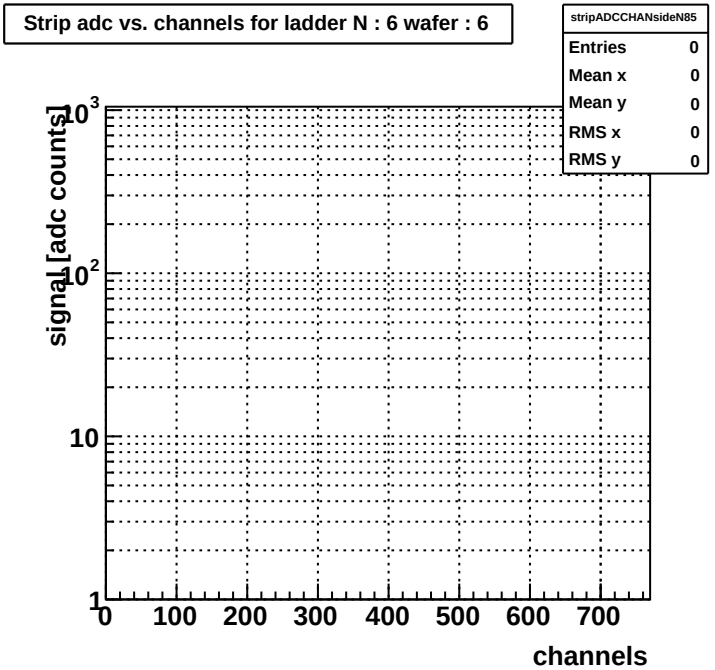

Strip Adc vs. channels for ladder P : 6 wafer : 5
stripADCCHANsideP84
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

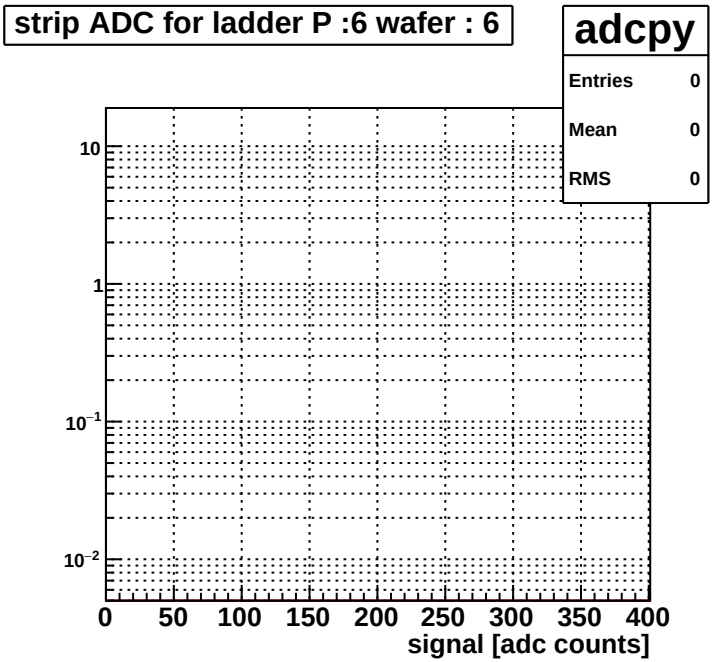

Strip adc vs. channels for ladder N : 6 wafer : 5
stripADCCHANsideN84
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

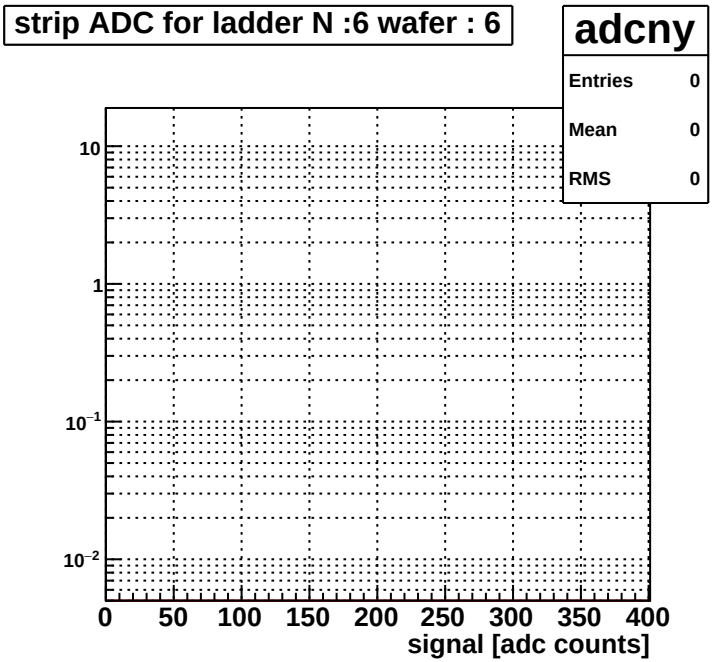

strip ADC for ladder P :6 wafer : 5
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

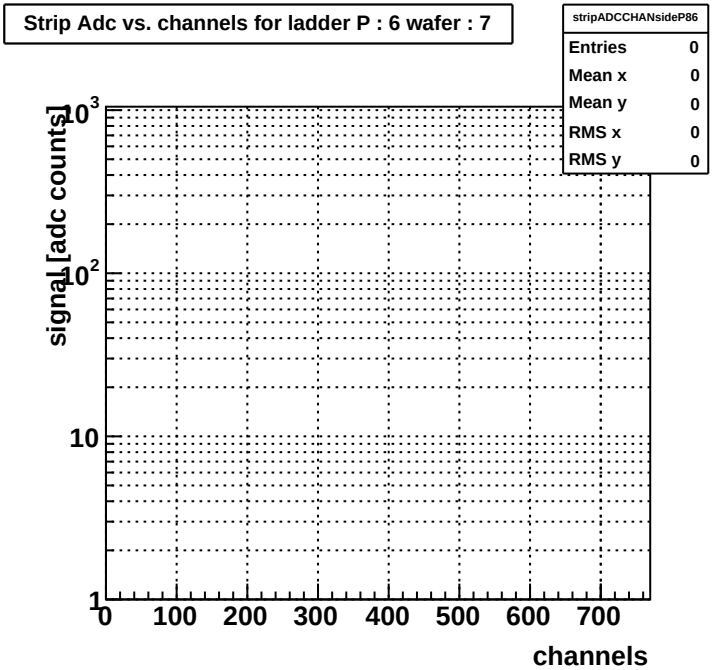

strip ADC for ladder N :6 wafer : 5
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

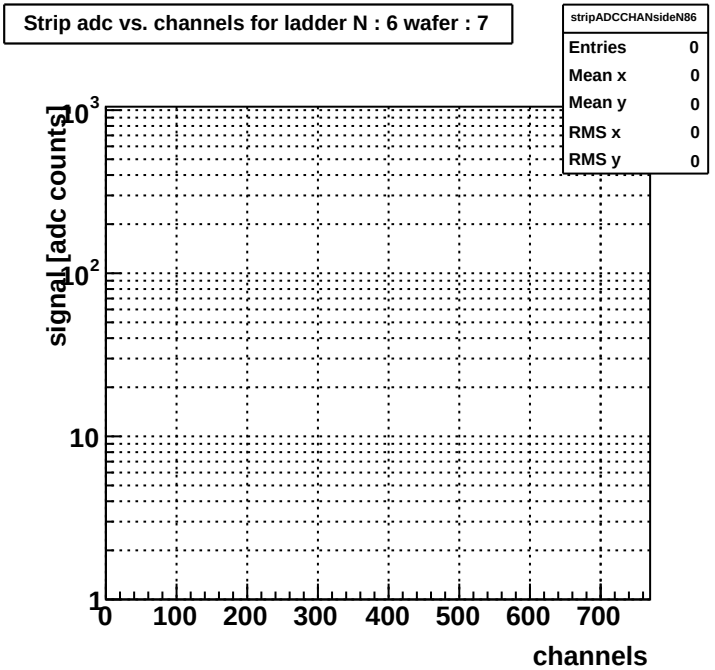

Strip Adc vs. channels for ladder P : 6 wafer : 6
stripADCCHANsideP85
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

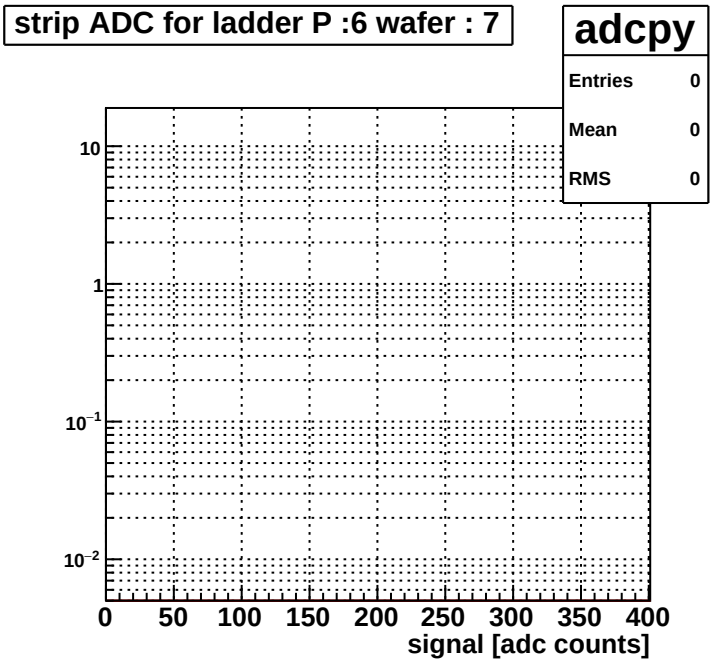

Strip adc vs. channels for ladder N : 6 wafer : 6
stripADCCHANsideN85
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

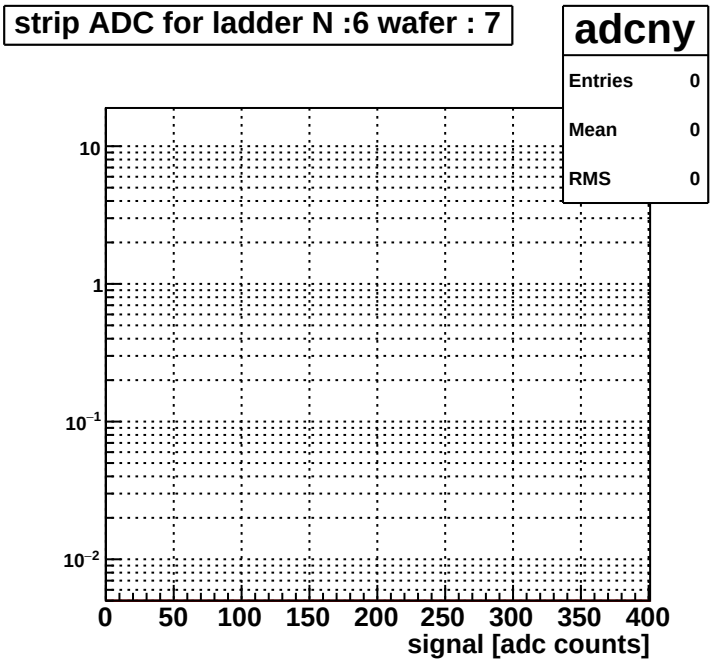

strip ADC for ladder P :6 wafer : 6
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

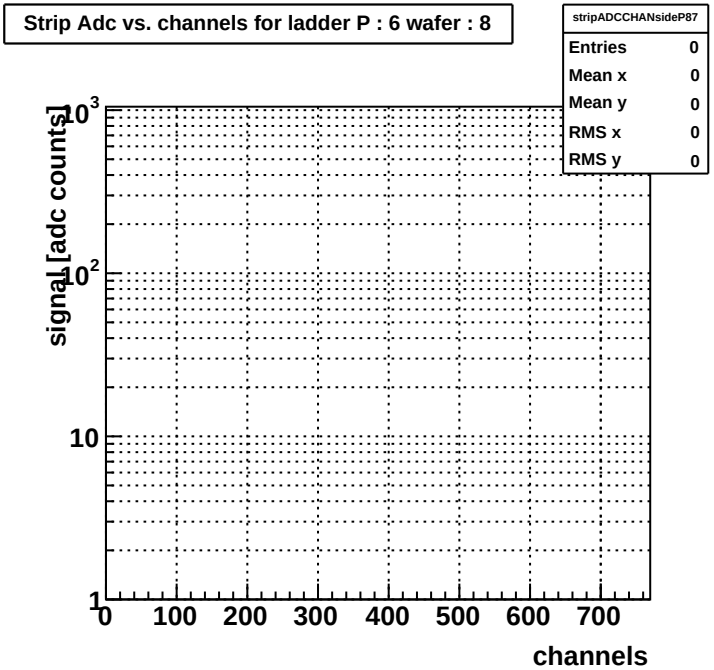

strip ADC for ladder N :6 wafer : 6
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

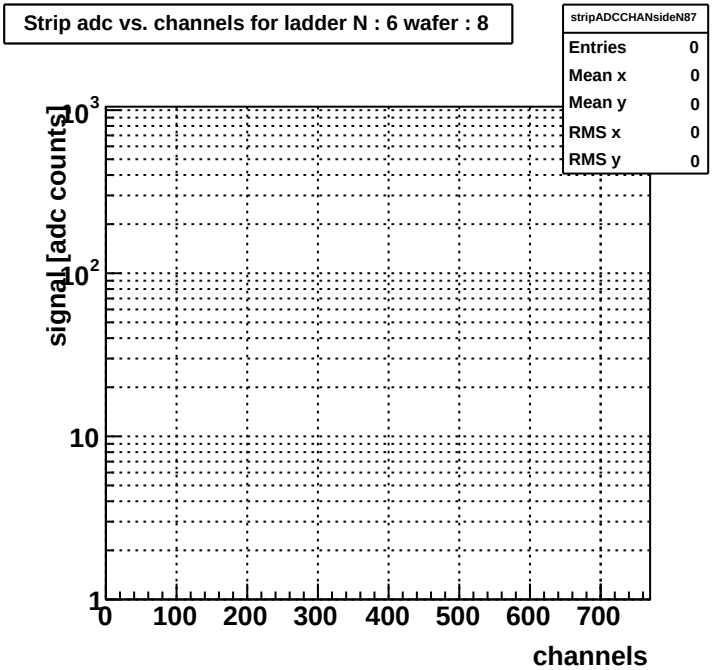

Strip Adc vs. channels for ladder P : 6 wafer : 7
stripADCCHANsideP86
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

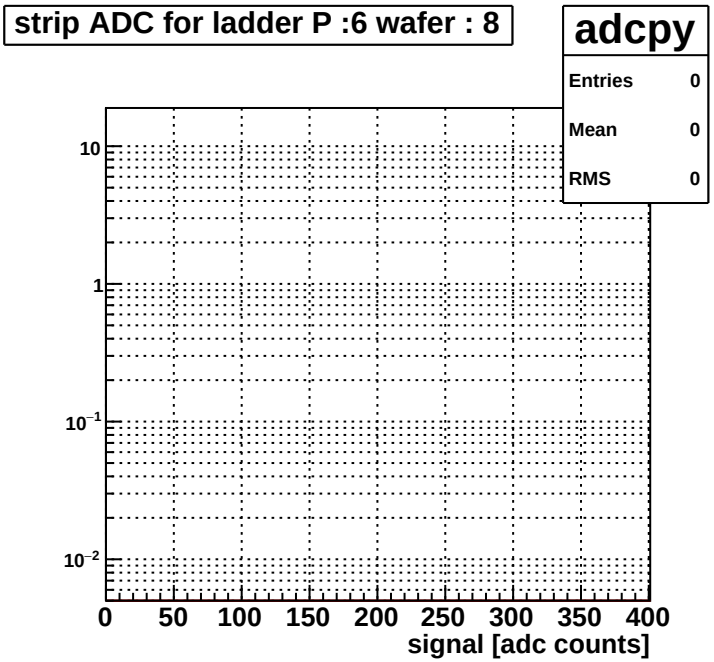

Strip adc vs. channels for ladder N : 6 wafer : 7
stripADCCHANsideN86
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

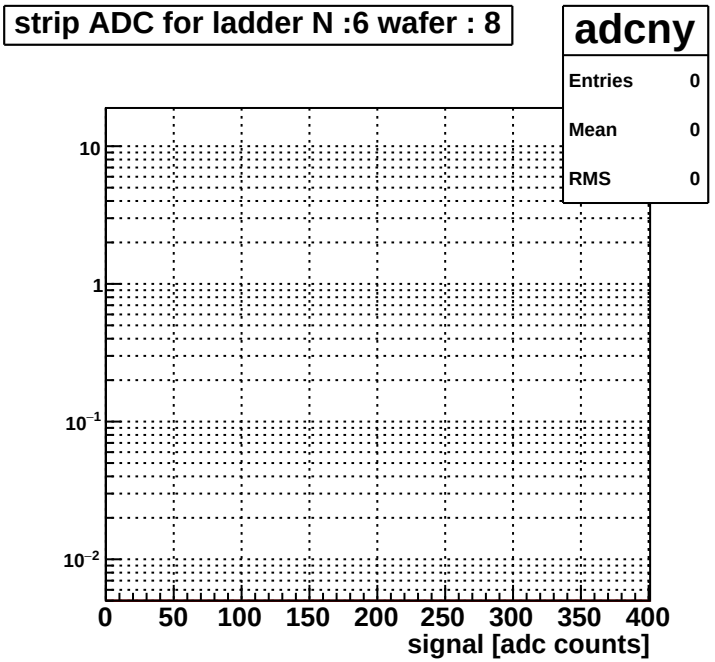

strip ADC for ladder P :6 wafer : 7
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

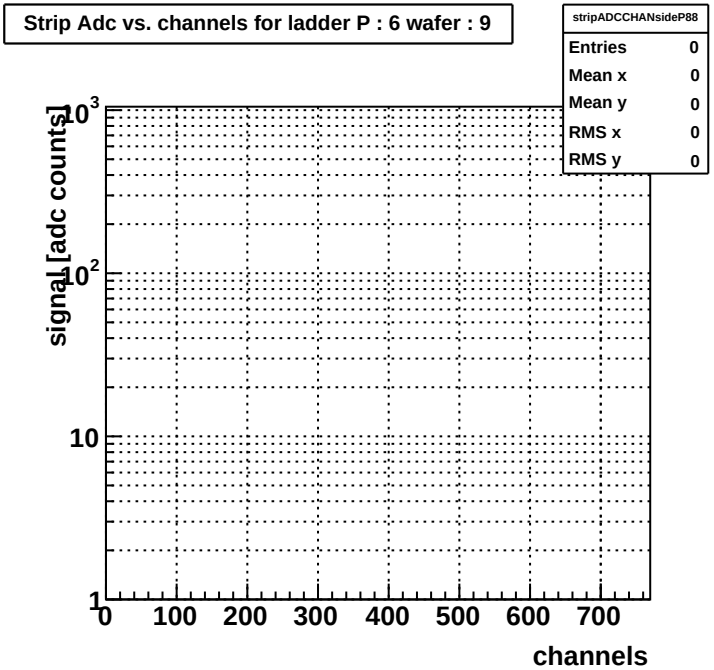

strip ADC for ladder N :6 wafer : 7
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

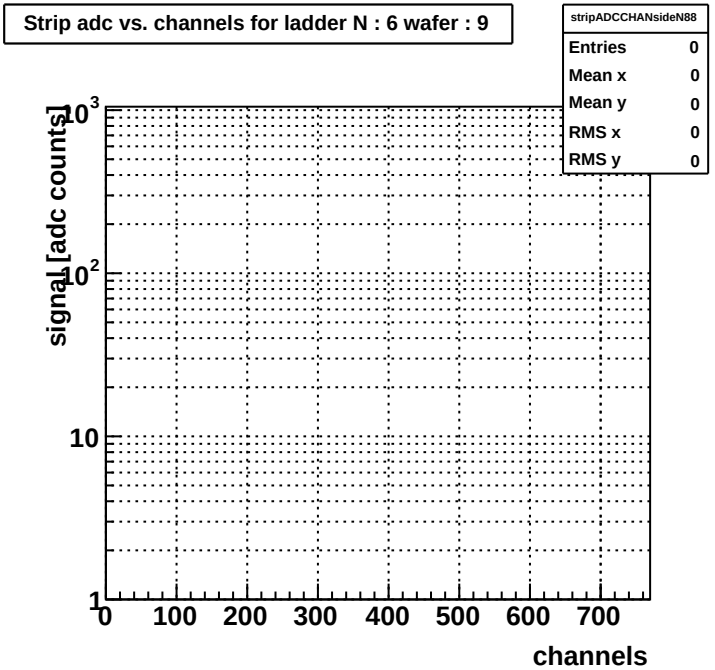

Strip Adc vs. channels for ladder P : 6 wafer : 8
stripADCCHANsideP87
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

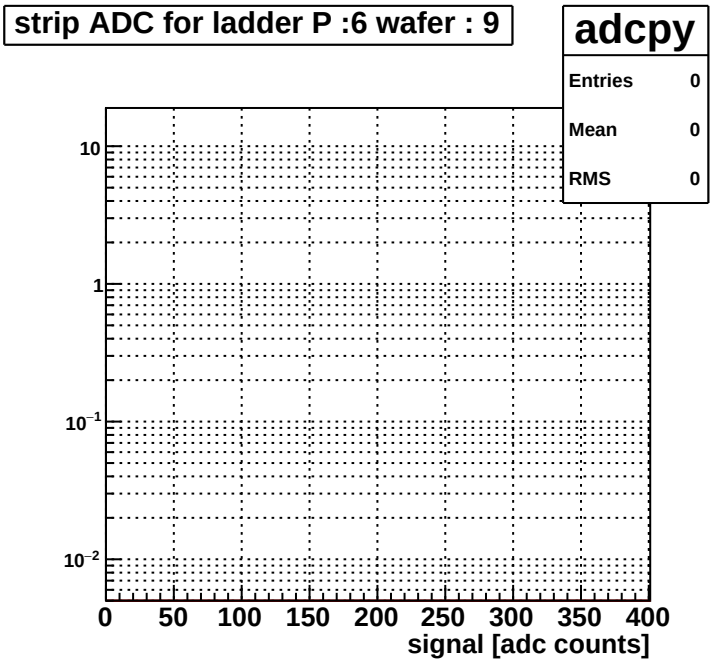

Strip adc vs. channels for ladder N : 6 wafer : 8
stripADCCHANsideN87
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

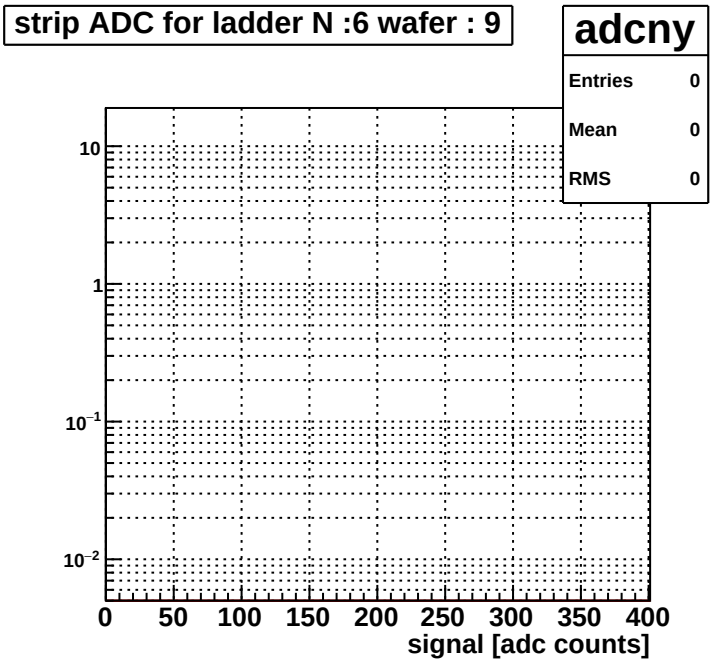

strip ADC for ladder P :6 wafer : 8
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

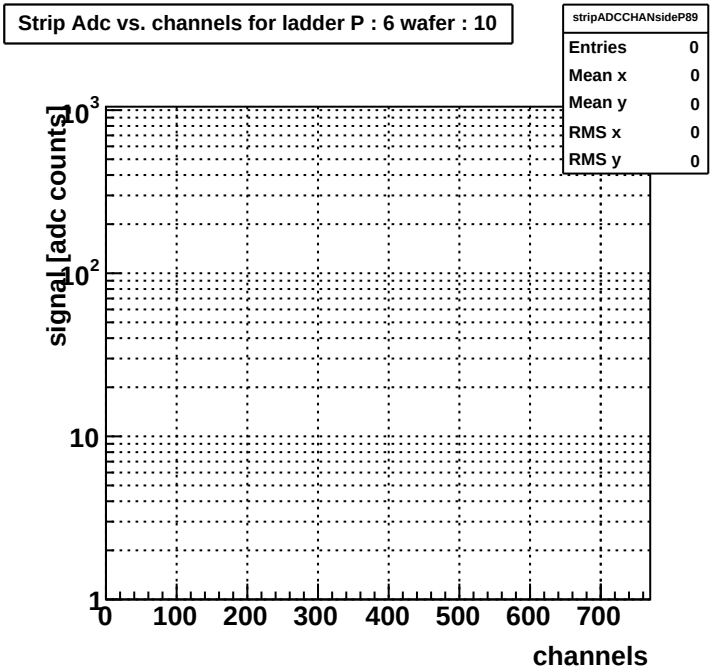

strip ADC for ladder N :6 wafer : 8
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

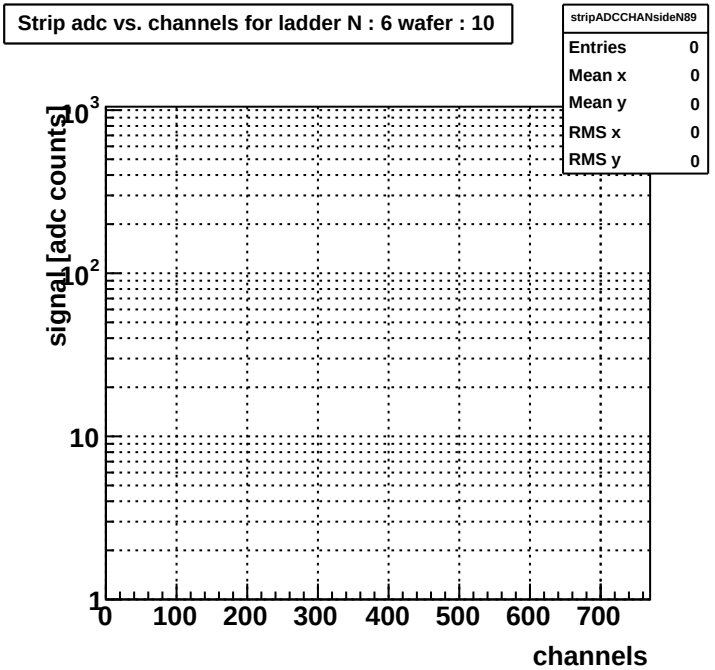

Strip Adc vs. channels for ladder P : 6 wafer : 9
stripADCCHANsideP88
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

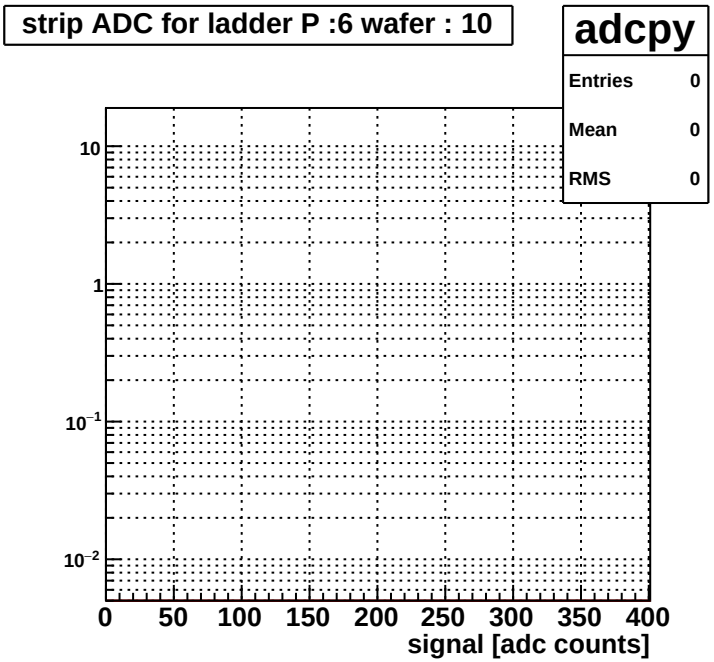

Strip adc vs. channels for ladder N : 6 wafer : 9
stripADCCHANsideN88
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

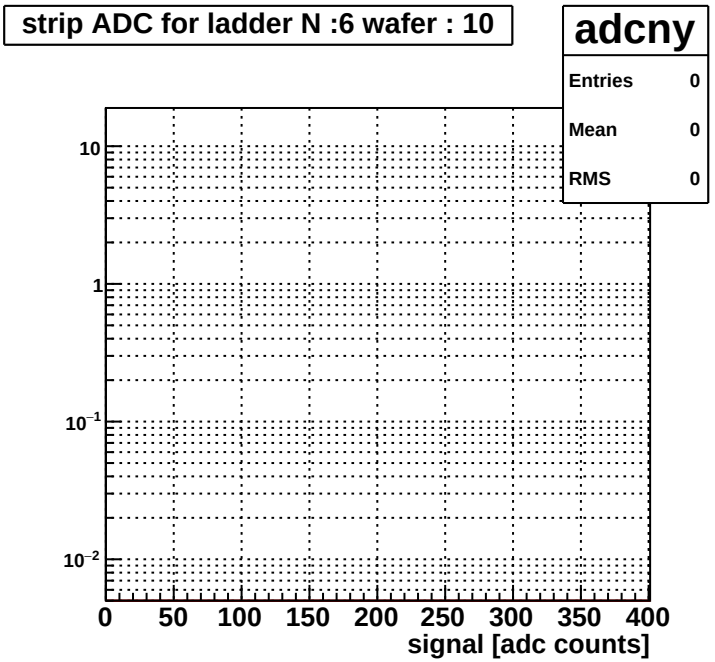

strip ADC for ladder P :6 wafer : 9
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

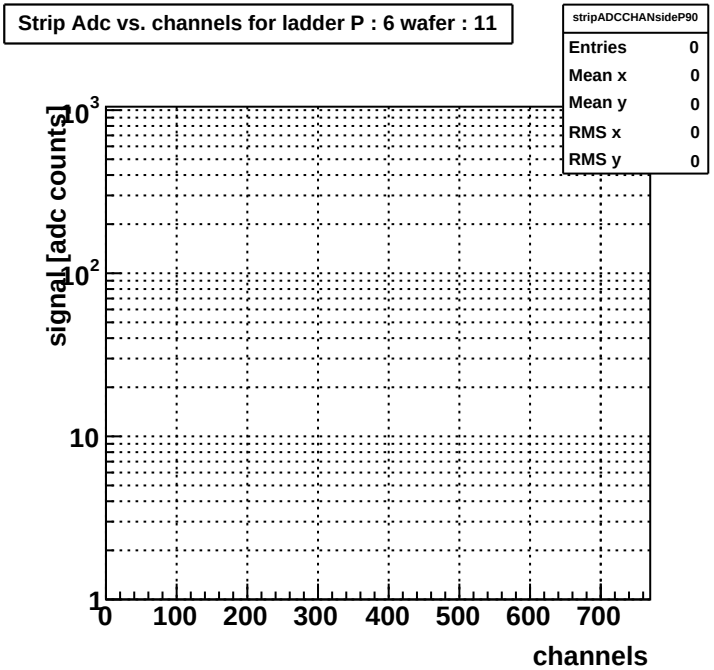

strip ADC for ladder N :6 wafer : 9
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

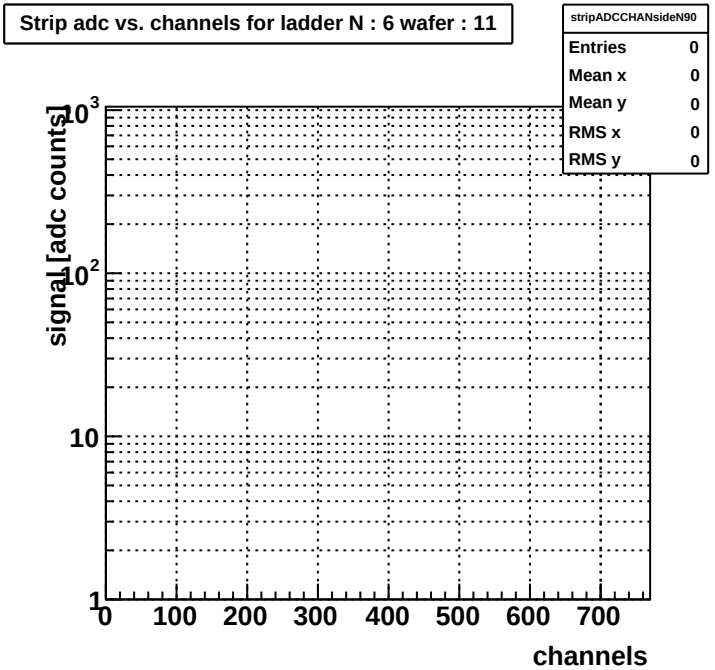

Strip Adc vs. channels for ladder P : 6 wafer : 10
stripADCCHANsideP89
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

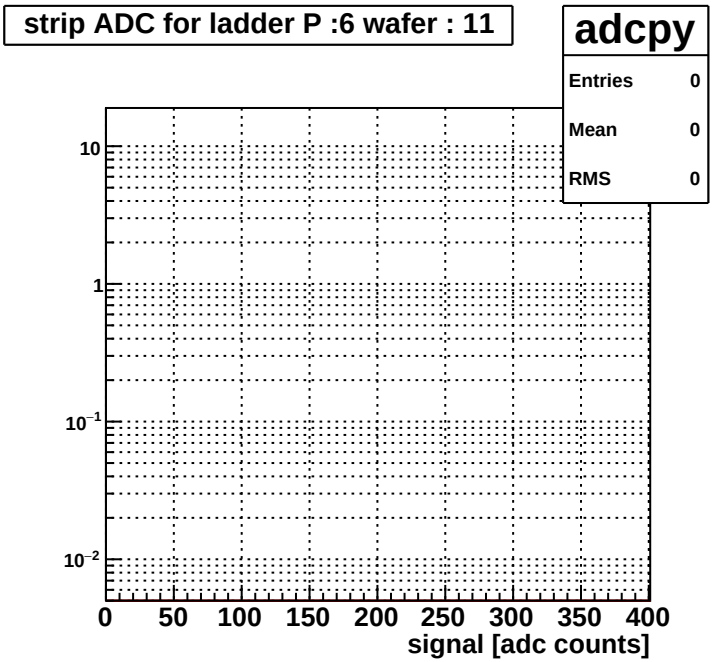

Strip adc vs. channels for ladder N : 6 wafer : 10
stripADCCHANsideN89
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

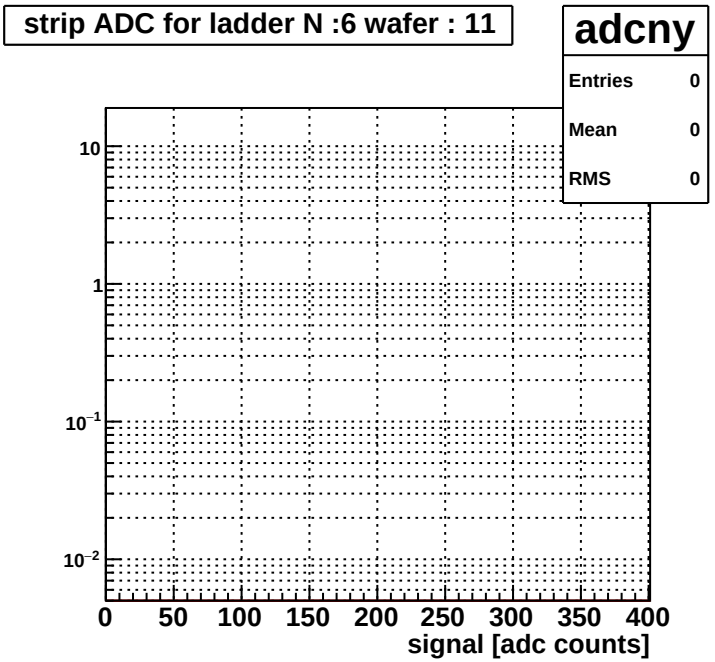

strip ADC for ladder P :6 wafer : 10
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

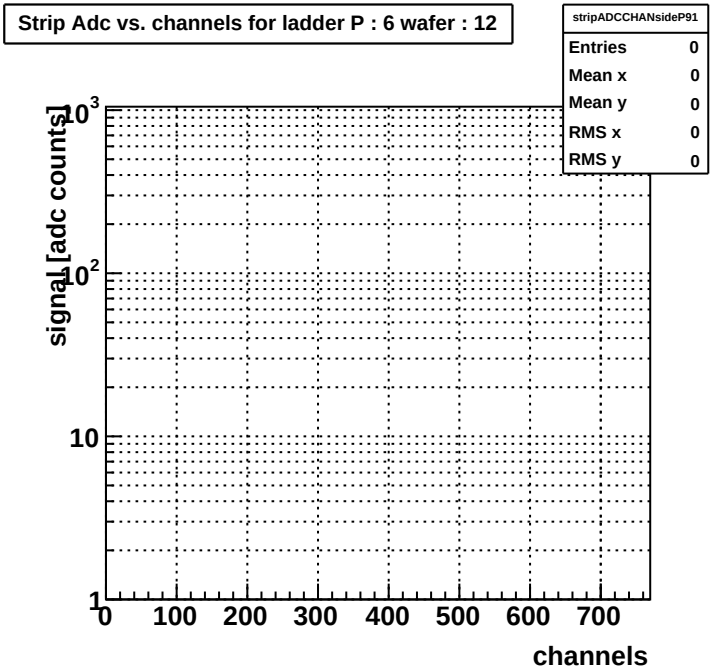

strip ADC for ladder N :6 wafer : 10
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

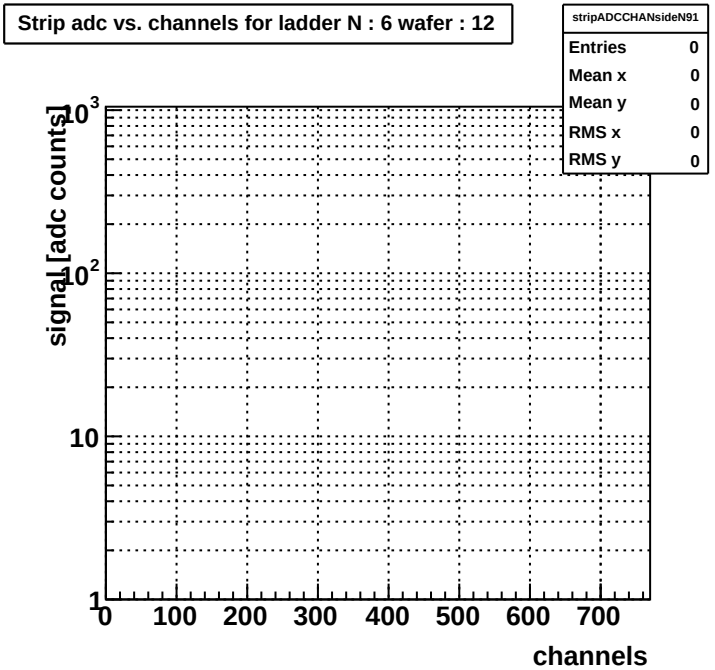

Strip Adc vs. channels for ladder P : 6 wafer : 11
stripADCCHANsideP90
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

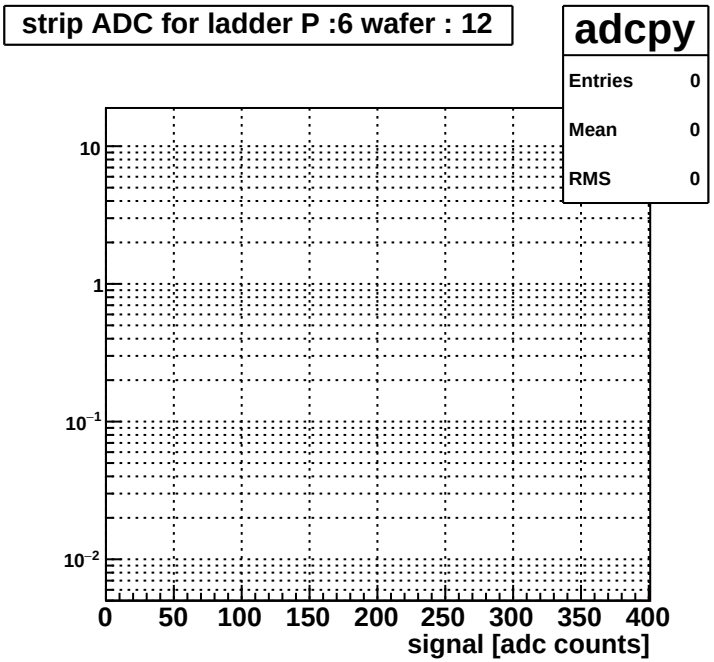

Strip adc vs. channels for ladder N : 6 wafer : 11
stripADCCHANsideN90
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

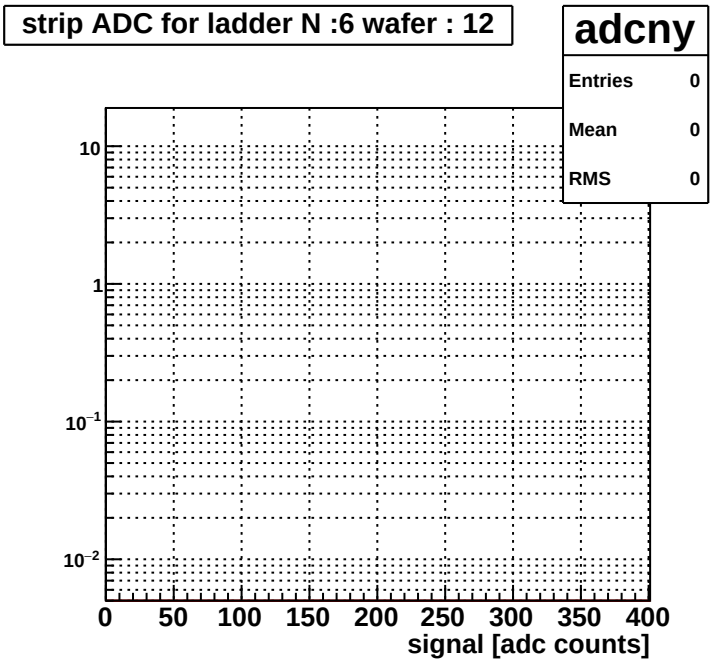

strip ADC for ladder P :6 wafer : 11
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

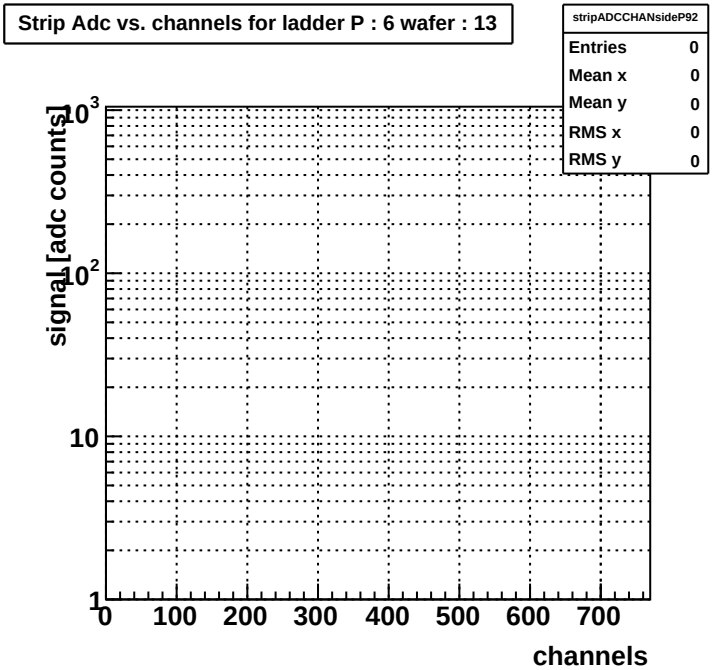

strip ADC for ladder N :6 wafer : 11
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

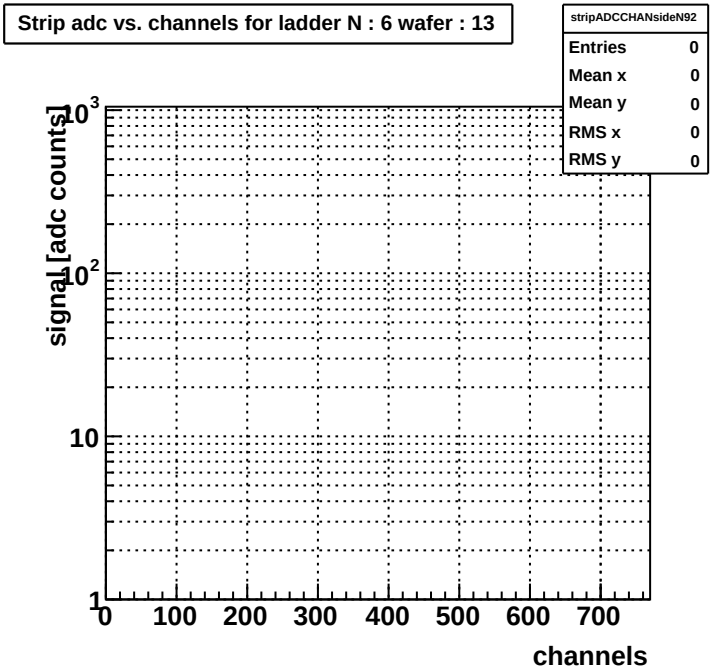

Strip Adc vs. channels for ladder P : 6 wafer : 12
stripADCCHANsideP91
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

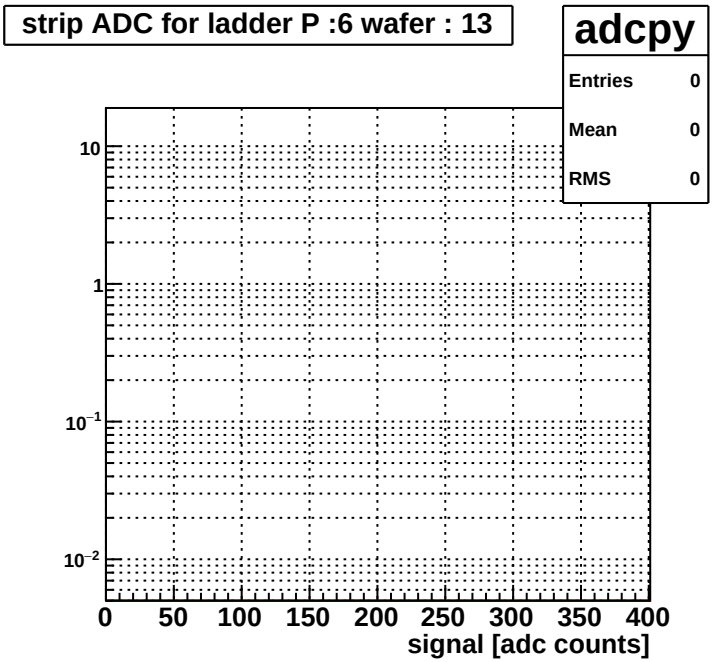

Strip adc vs. channels for ladder N : 6 wafer : 12
stripADCCHANsideN91
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

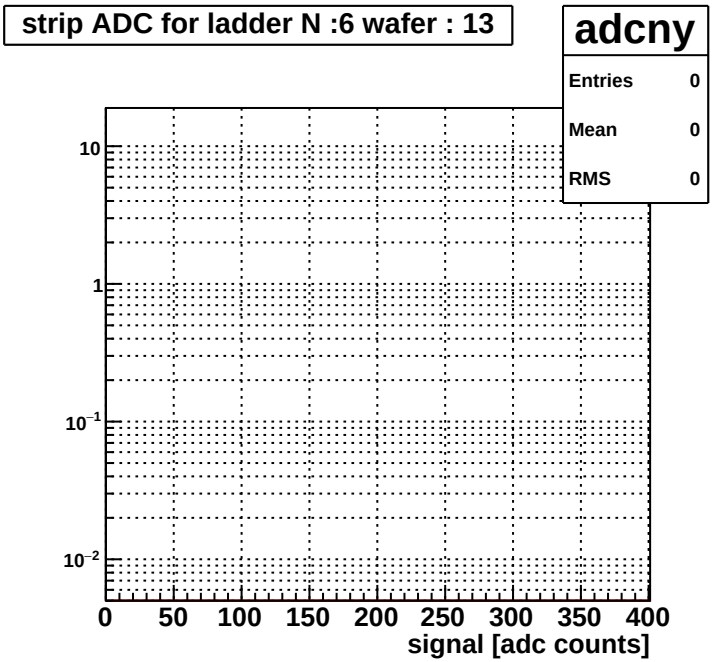

strip ADC for ladder P :6 wafer : 12
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

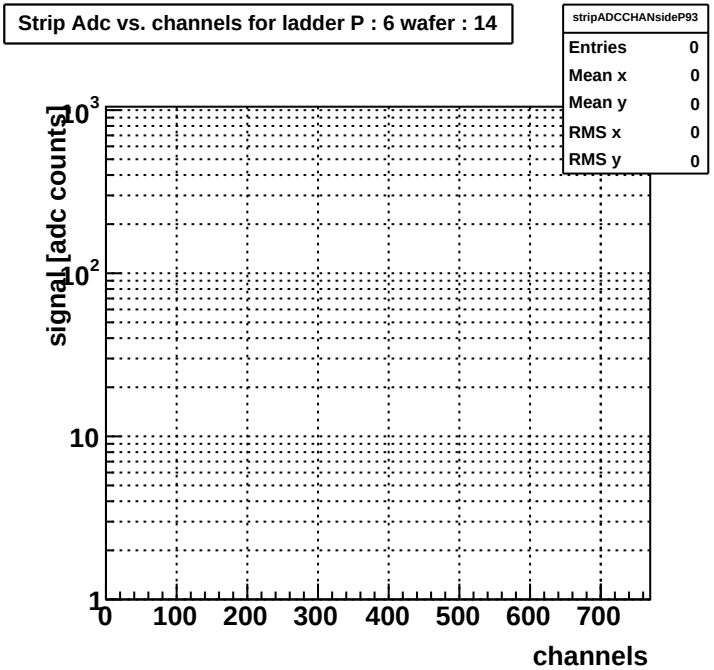

strip ADC for ladder N :6 wafer : 12
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

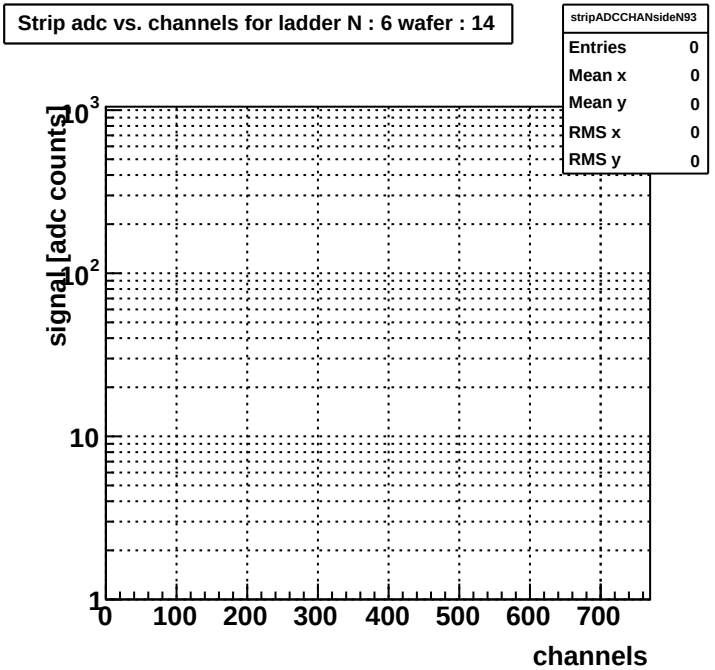

Strip Adc vs. channels for ladder P : 6 wafer : 13
stripADCCHANsideP92
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

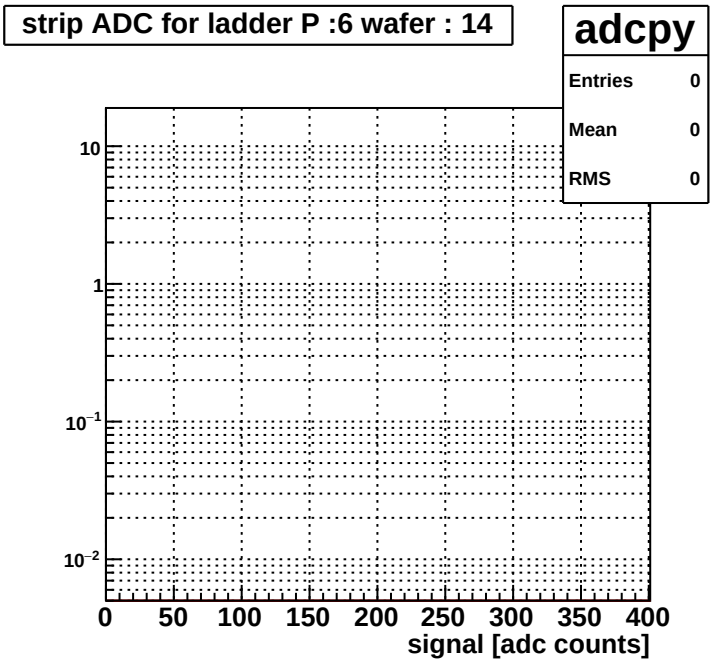

Strip adc vs. channels for ladder N : 6 wafer : 13
stripADCCHANsideN92
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

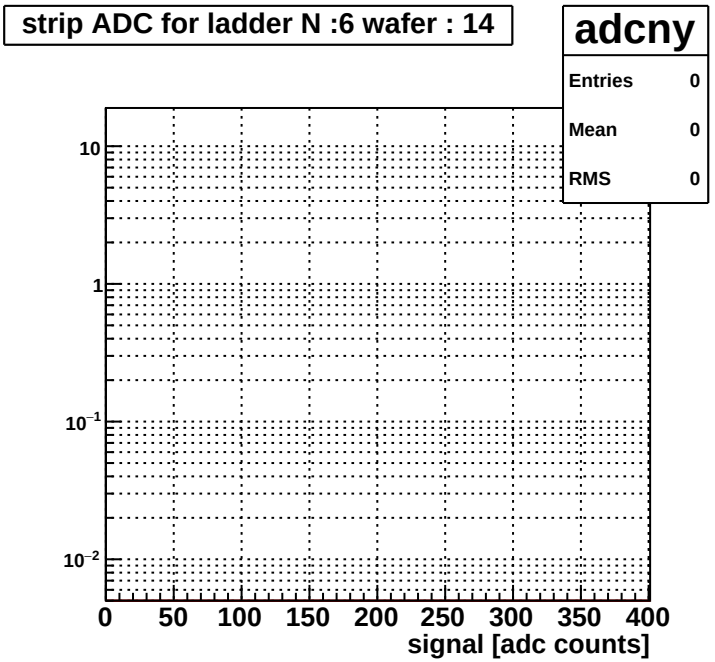

strip ADC for ladder P :6 wafer : 13
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

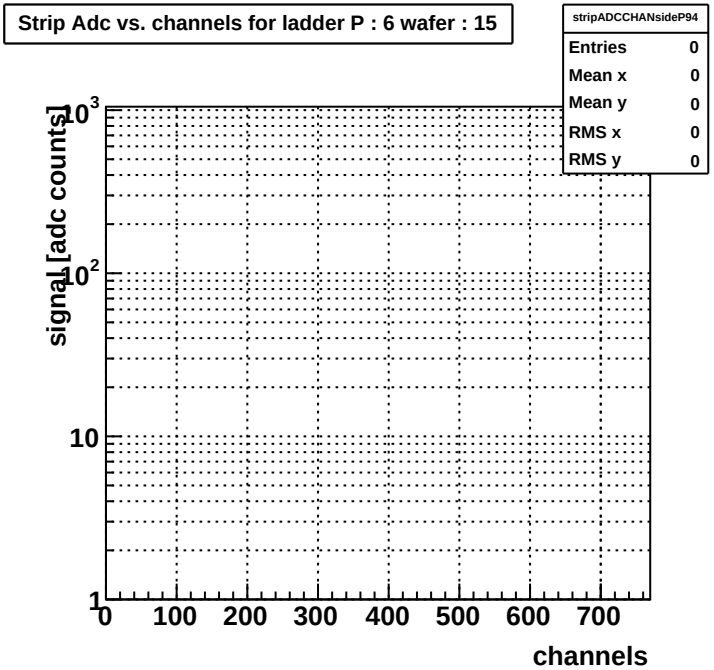

strip ADC for ladder N :6 wafer : 13
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

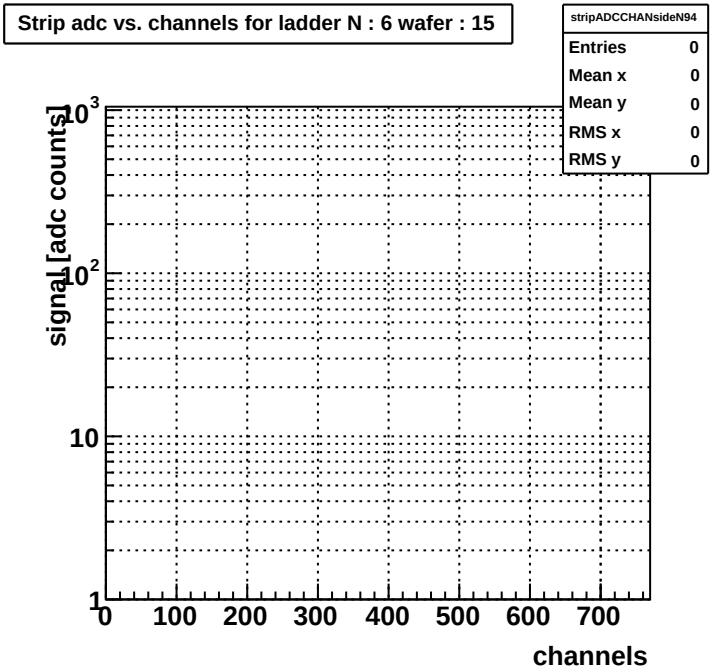

Strip Adc vs. channels for ladder P : 6 wafer : 14
stripADCCHANsideP93
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

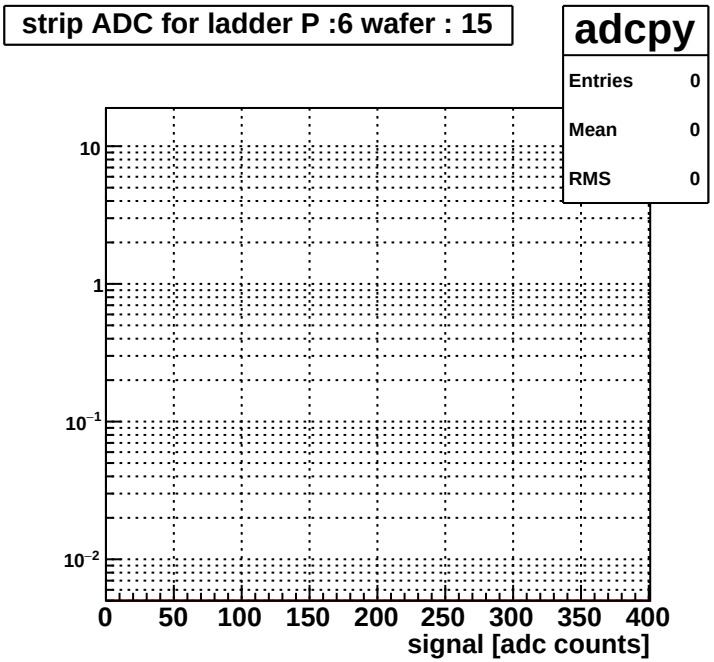

Strip adc vs. channels for ladder N : 6 wafer : 14
stripADCCHANsideN93
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

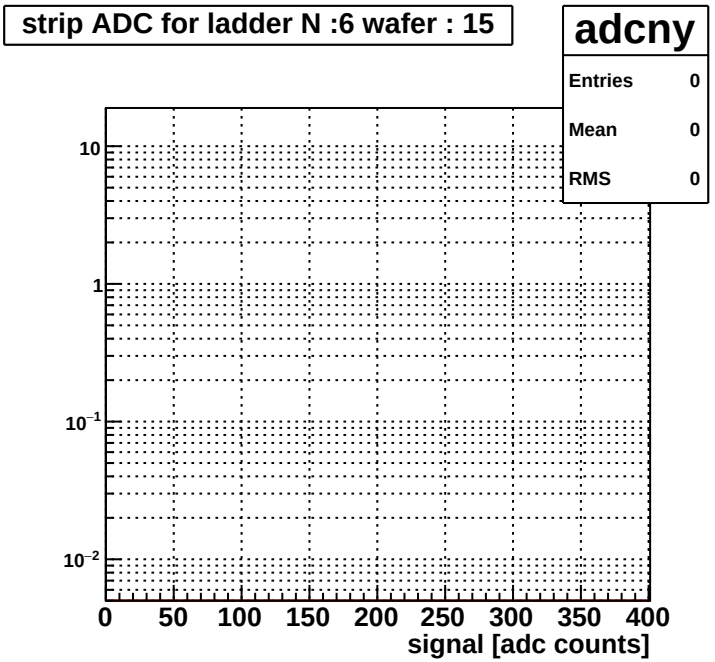

strip ADC for ladder P :6 wafer : 14
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

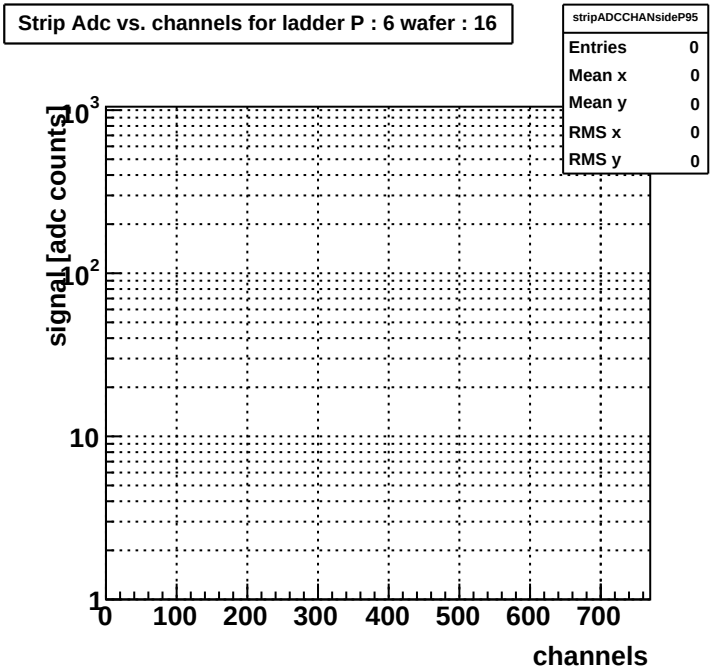

strip ADC for ladder N :6 wafer : 14
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

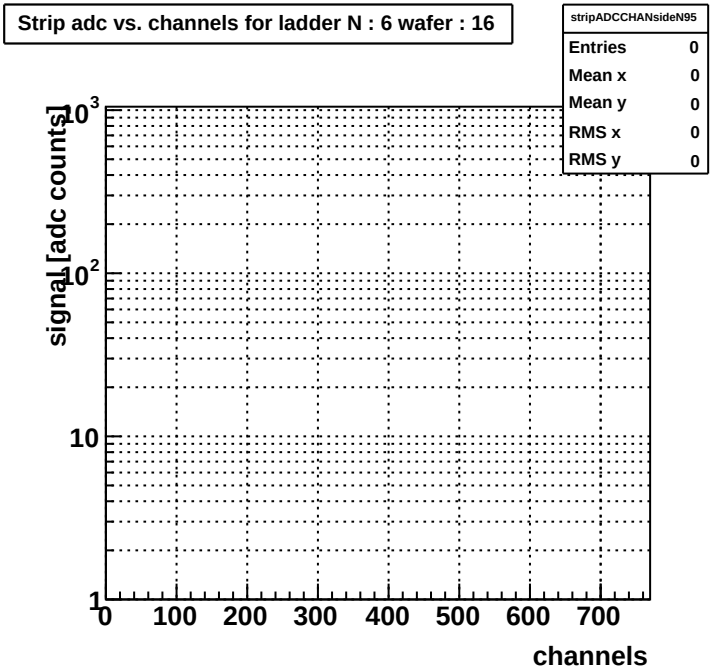

Strip Adc vs. channels for ladder P : 6 wafer : 15
stripADCCHANsideP94
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

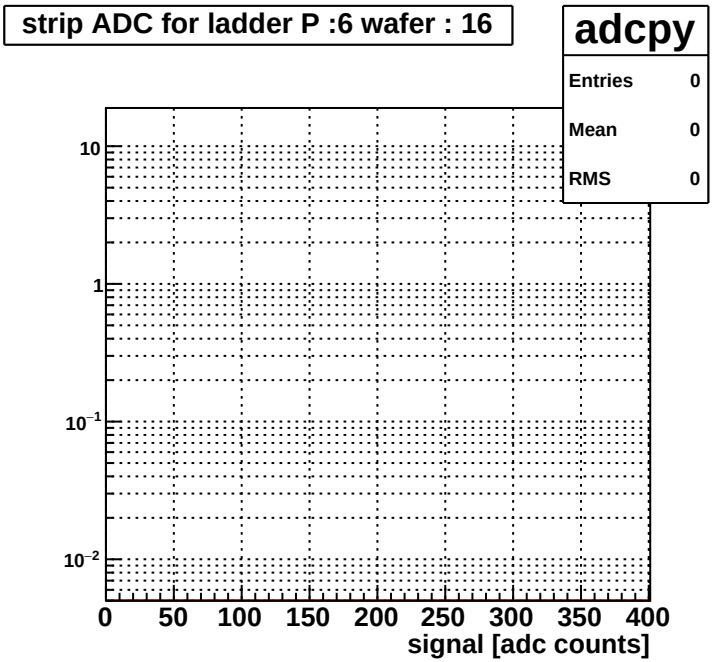

Strip adc vs. channels for ladder N : 6 wafer : 15
stripADCCHANsideN94
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

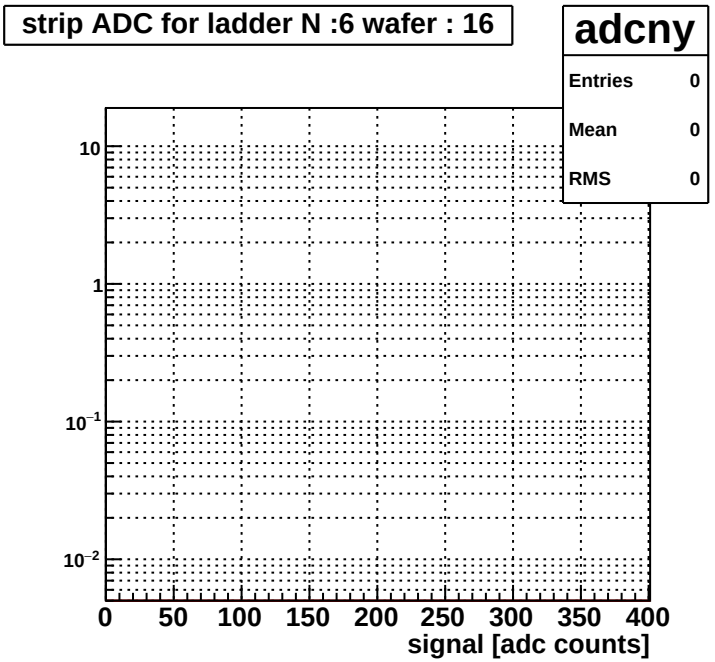

strip ADC for ladder P :6 wafer : 15
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

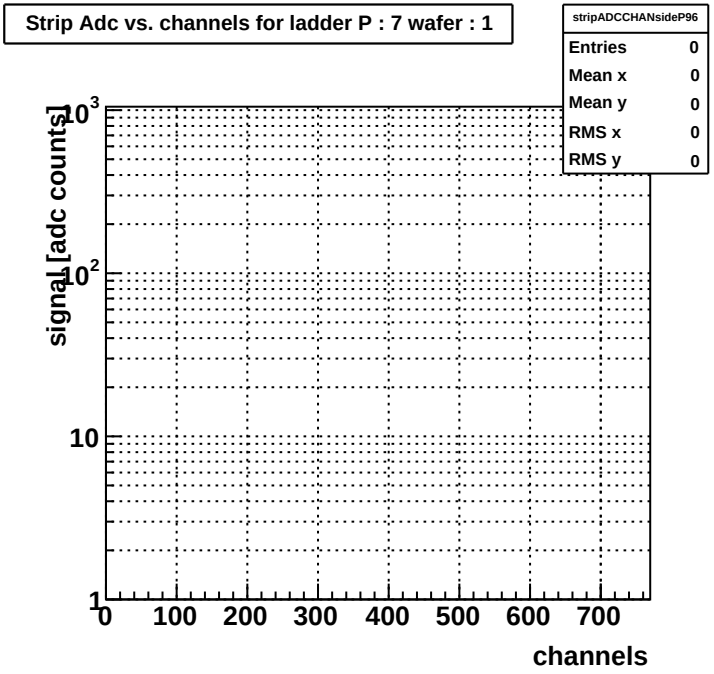

strip ADC for ladder N :6 wafer : 15
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

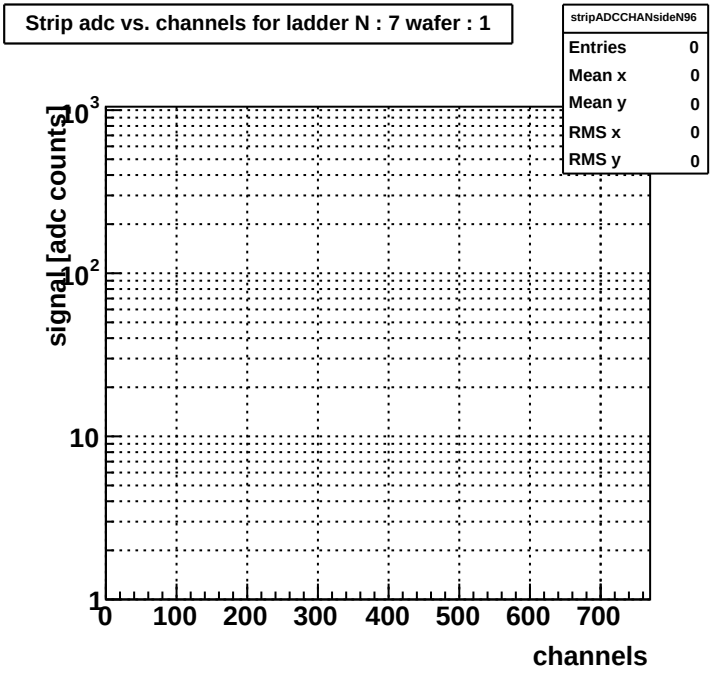

Strip Adc vs. channels for ladder P : 6 wafer : 16
stripADCCHANsideP95
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

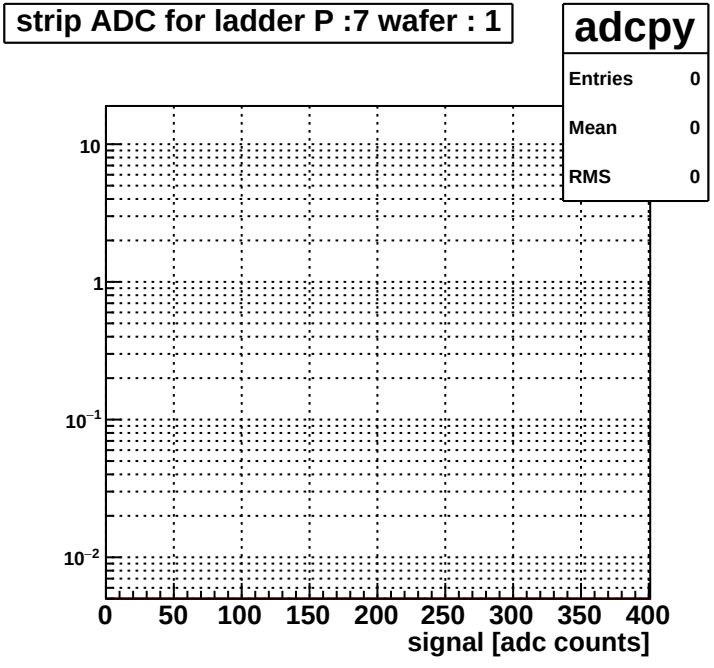

Strip adc vs. channels for ladder N : 6 wafer : 16
stripADCCHANsideN95
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

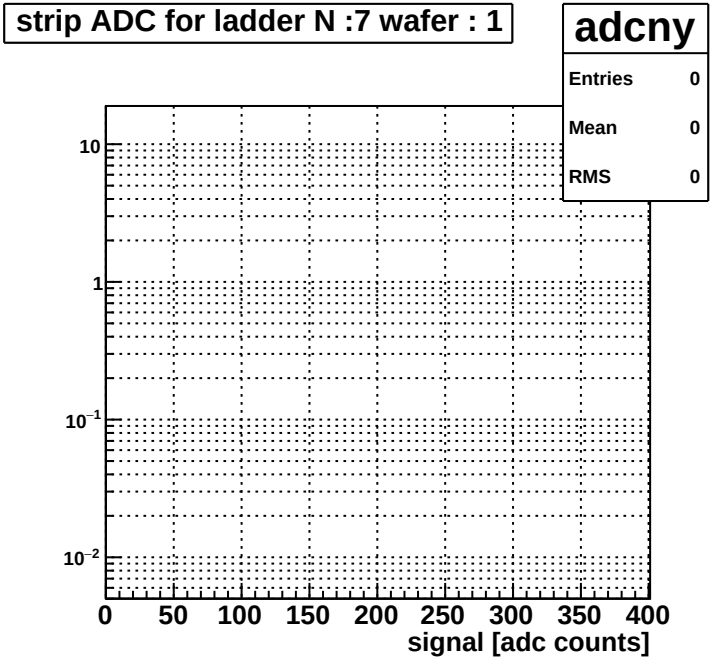

strip ADC for ladder P :6 wafer : 16
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

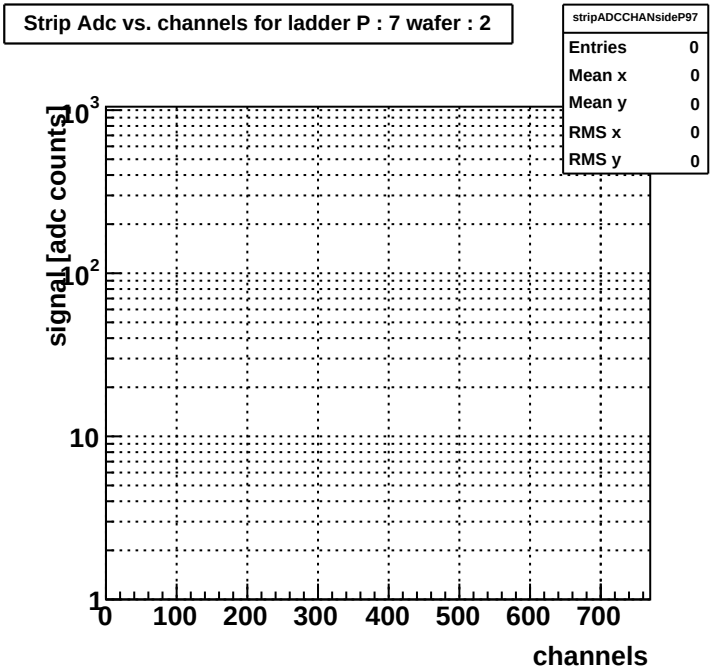

strip ADC for ladder N :6 wafer : 16
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

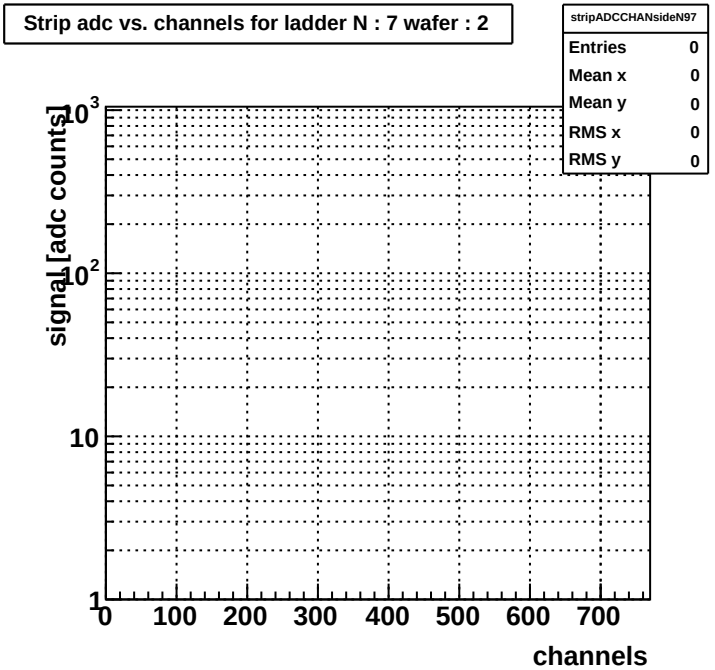

Strip Adc vs. channels for ladder P : 7 wafer : 1
stripADCCHANsideP96
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

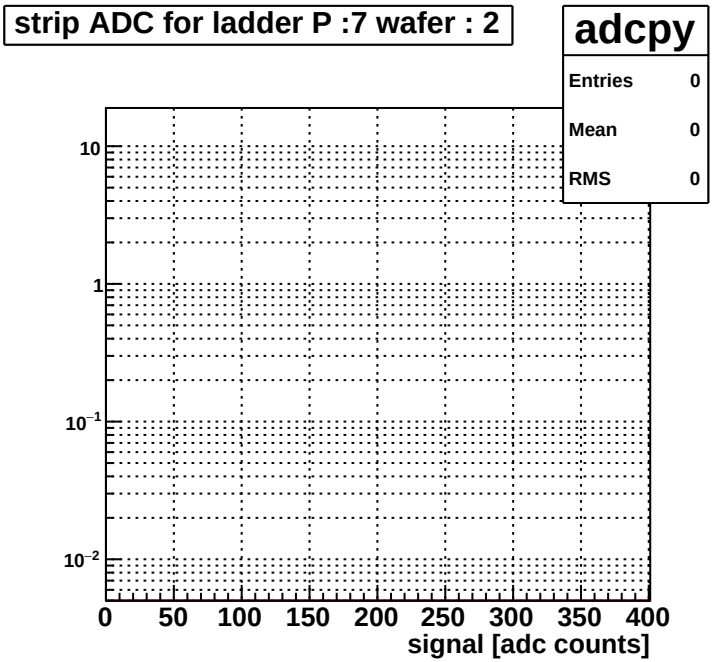

Strip adc vs. channels for ladder N : 7 wafer : 1
stripADCCHANsideN96
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

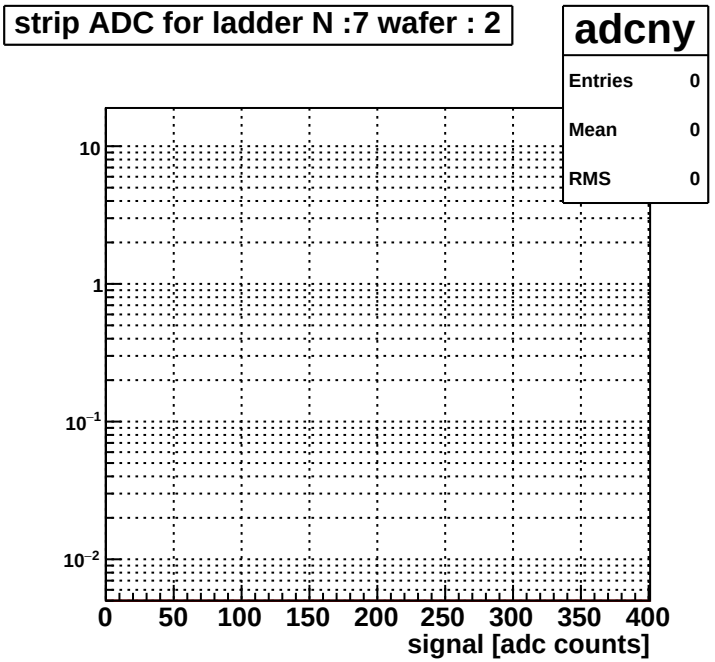

strip ADC for ladder P :7 wafer : 1
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

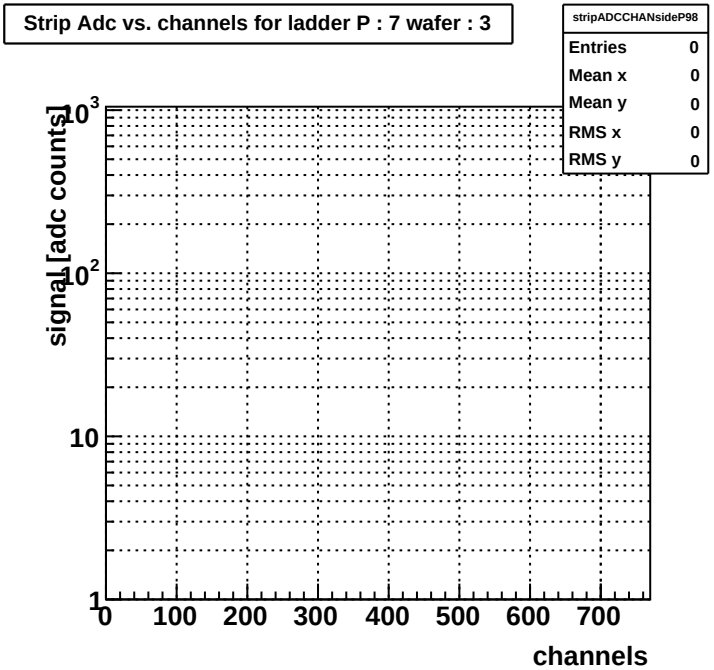

strip ADC for ladder N :7 wafer : 1
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

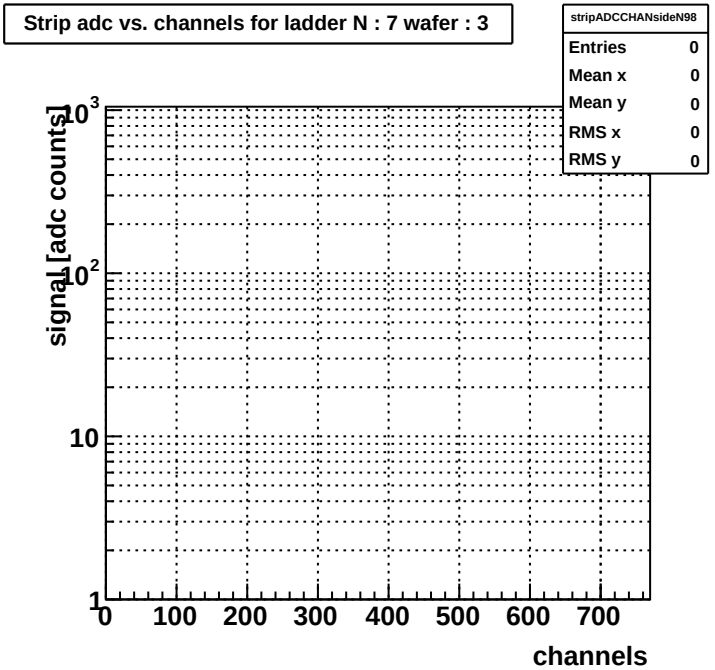

Strip Adc vs. channels for ladder P : 7 wafer : 2
stripADCCHANsideP97
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

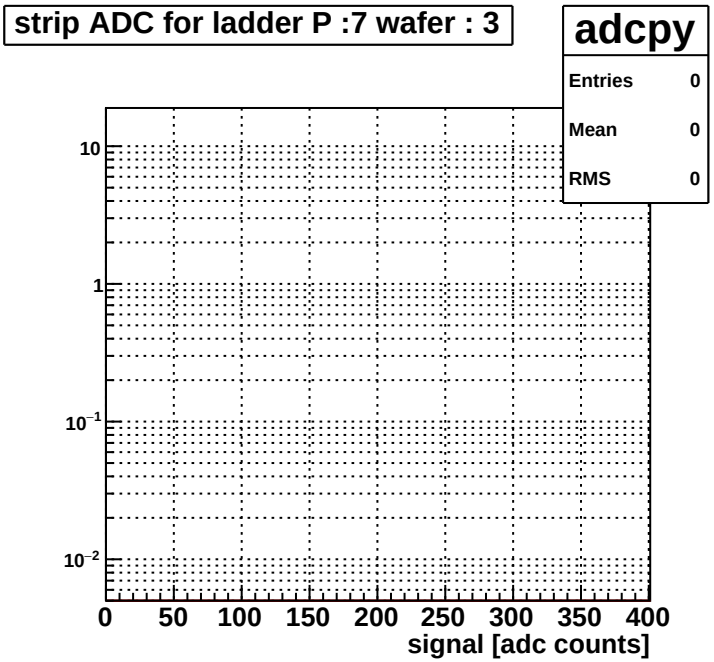

Strip adc vs. channels for ladder N : 7 wafer : 2
stripADCCHANsideN97
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

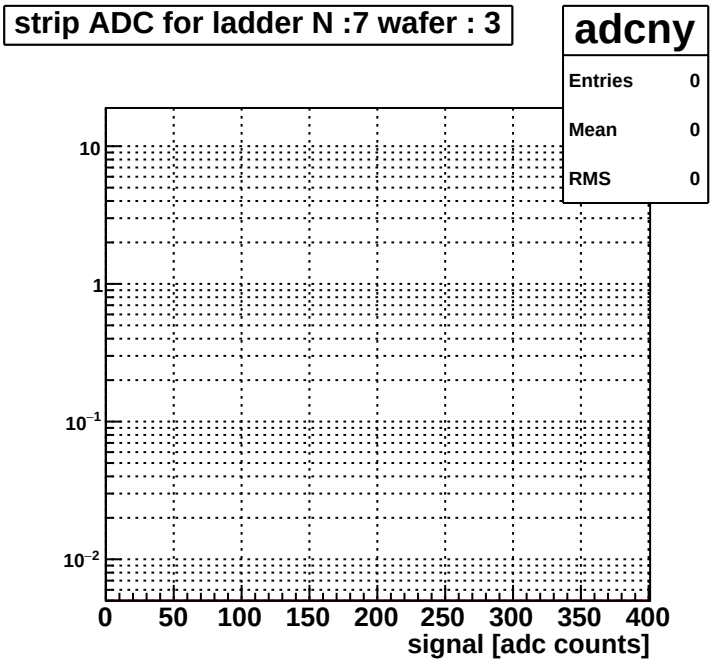

strip ADC for ladder P :7 wafer : 2
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

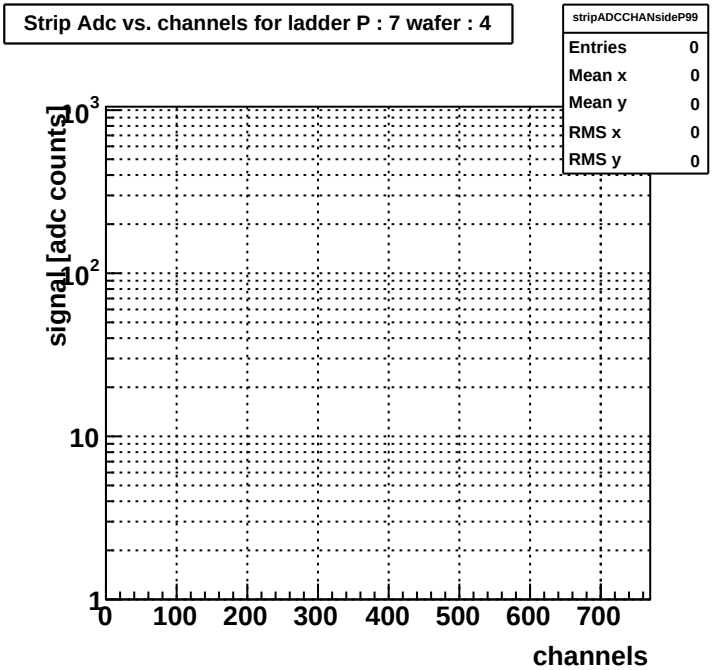

strip ADC for ladder N :7 wafer : 2
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

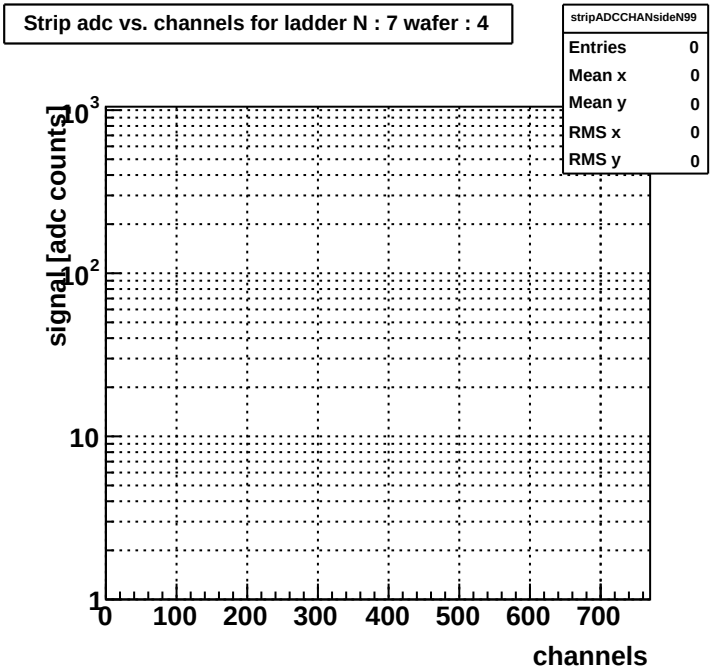

Strip Adc vs. channels for ladder P : 7 wafer : 3
stripADCCHANsideP98
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

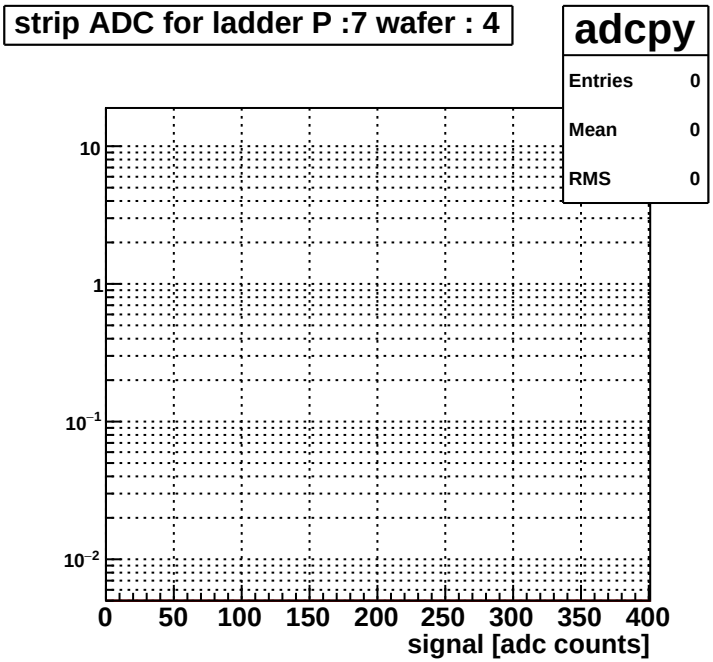

Strip adc vs. channels for ladder N : 7 wafer : 3
stripADCCHANsideN98
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

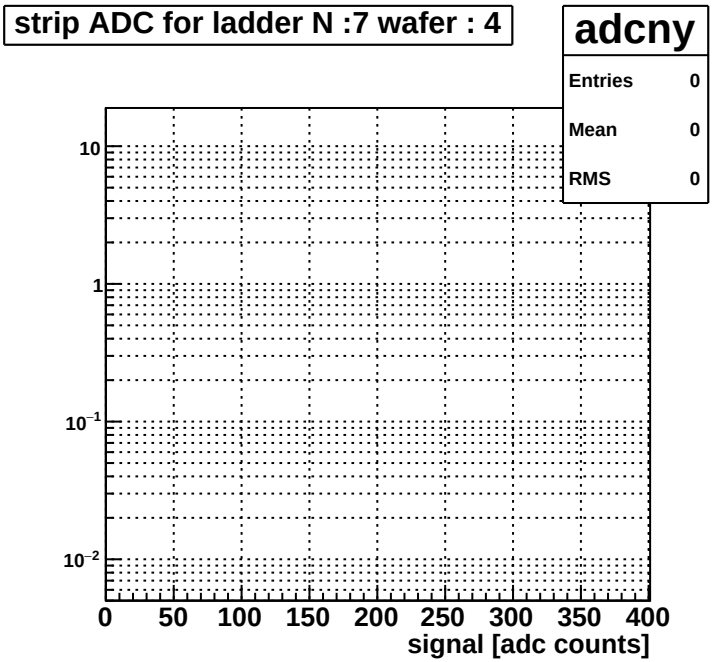

strip ADC for ladder P :7 wafer : 3
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

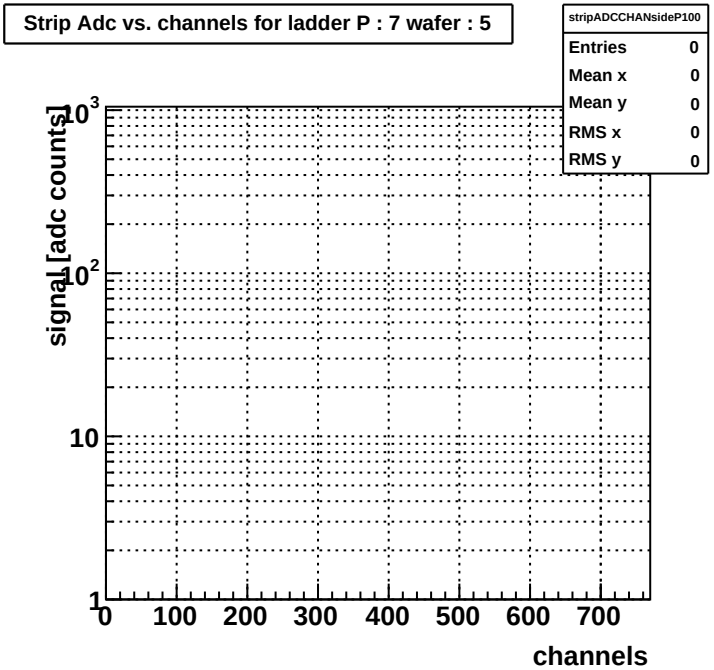

strip ADC for ladder N :7 wafer : 3
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

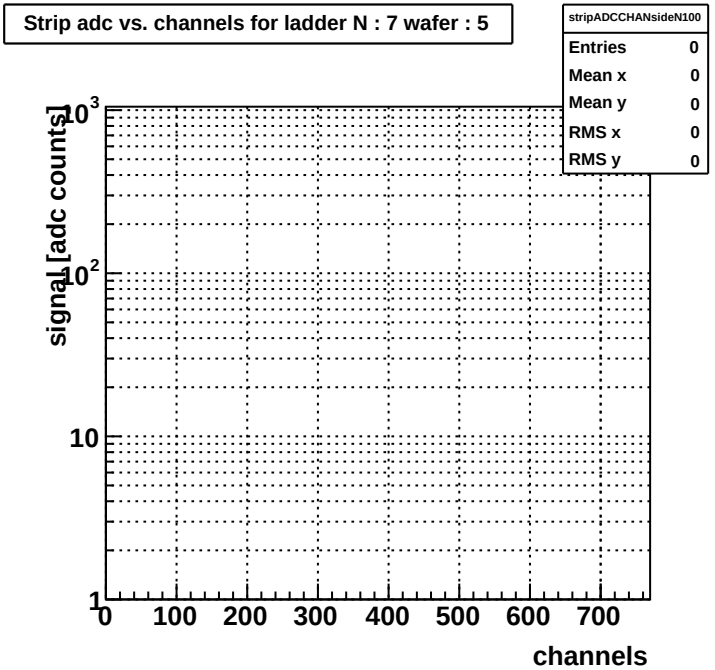

Strip Adc vs. channels for ladder P : 7 wafer : 4
stripADCCHANsideP99
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

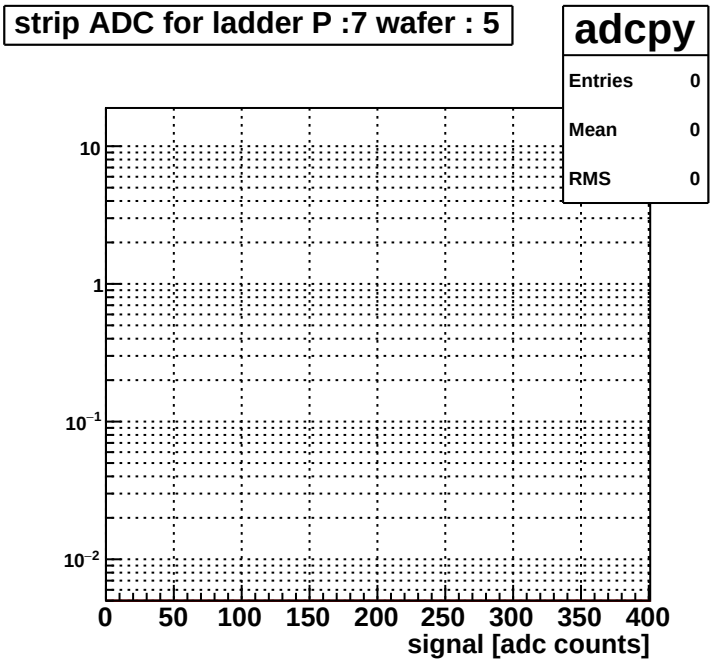

Strip adc vs. channels for ladder N : 7 wafer : 4
stripADCCHANsideN99
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

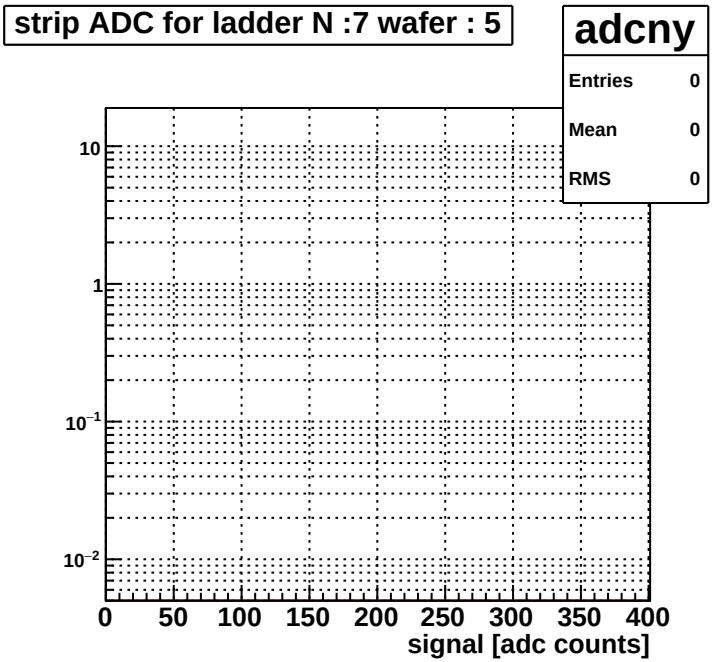

strip ADC for ladder P :7 wafer : 4
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

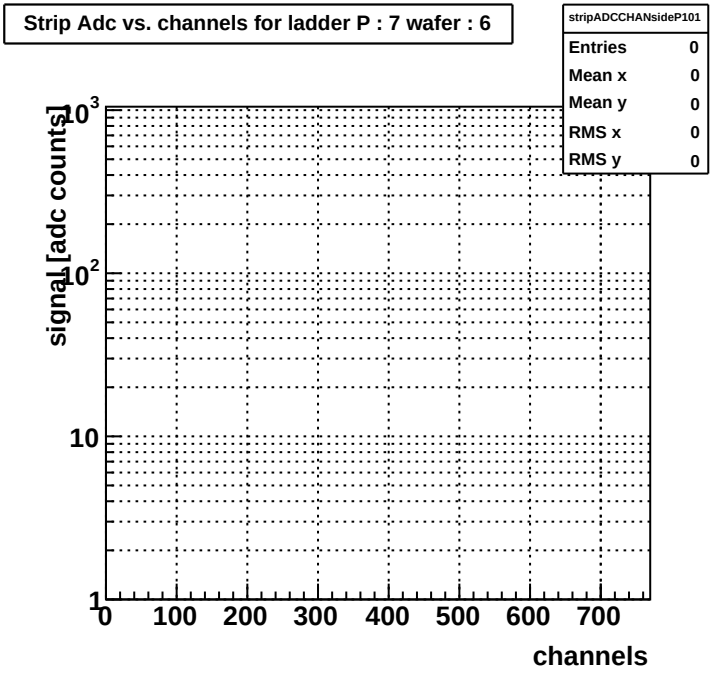

strip ADC for ladder N :7 wafer : 4
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

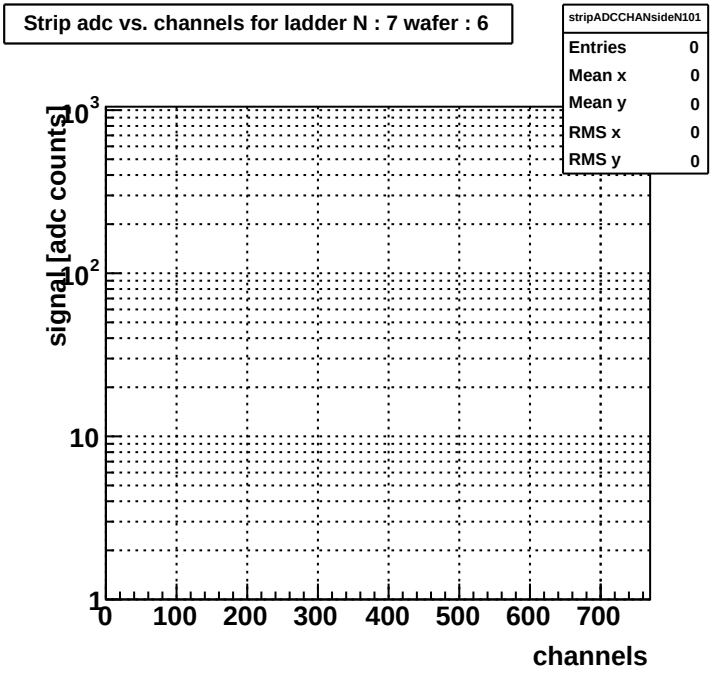

Strip Adc vs. channels for ladder P : 7 wafer : 5
stripADCCHANsideP100
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

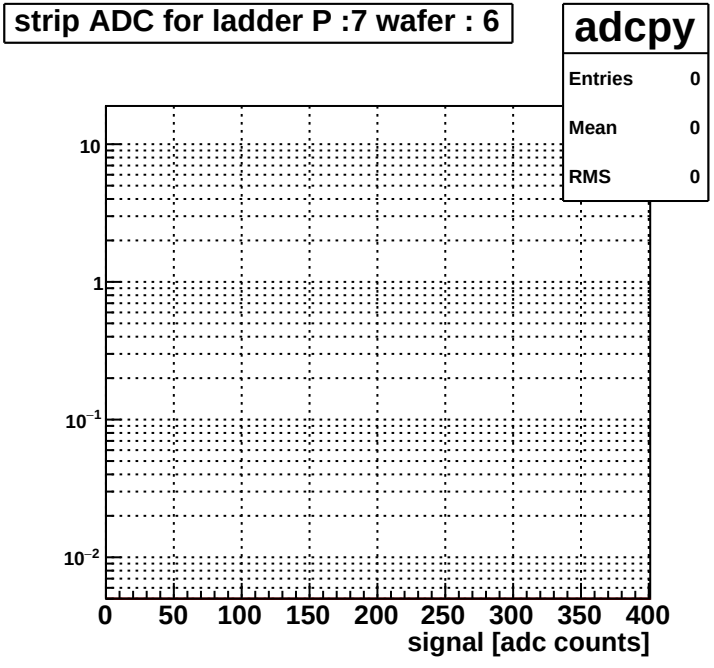

Strip adc vs. channels for ladder N : 7 wafer : 5
stripADCCHANsideN100
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

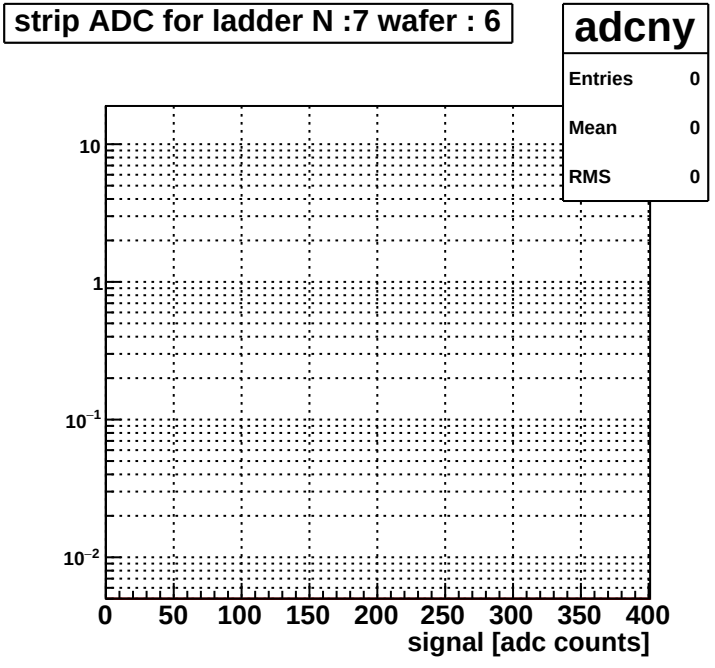

strip ADC for ladder P :7 wafer : 5
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

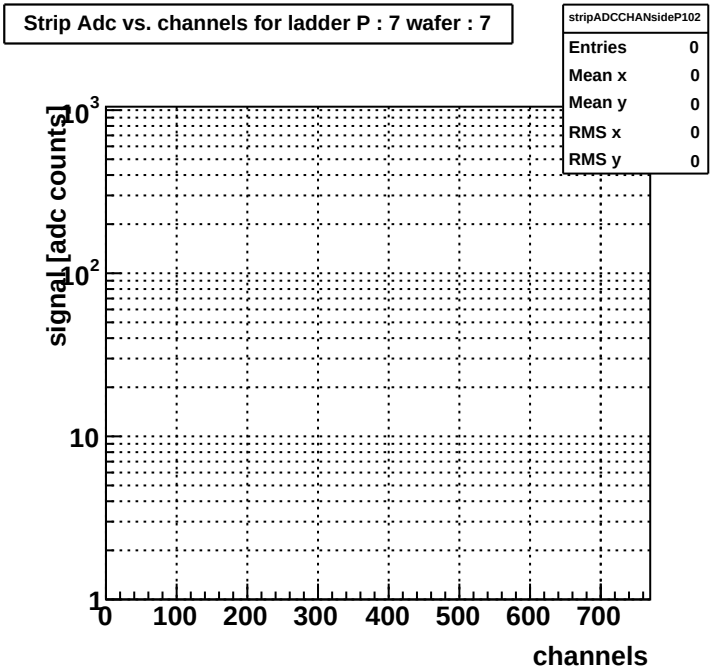

strip ADC for ladder N :7 wafer : 5
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

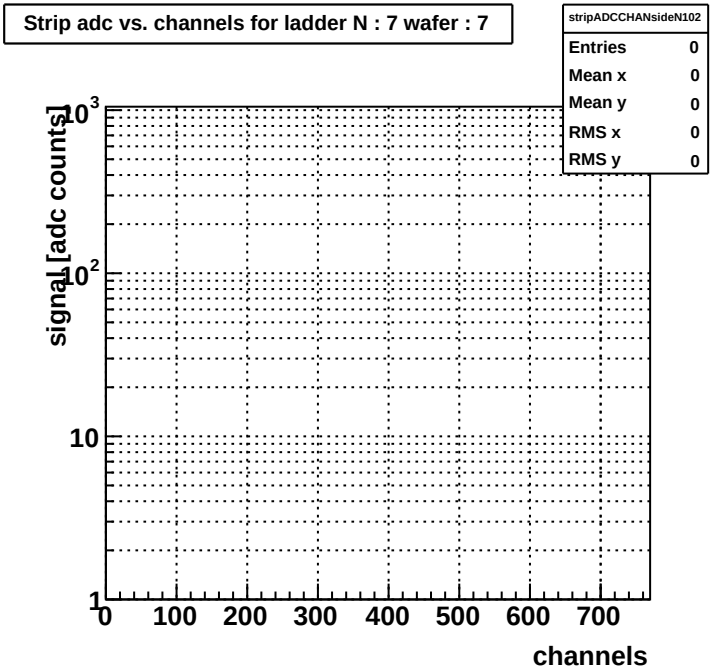

Strip Adc vs. channels for ladder P : 7 wafer : 6
stripADCCHANsideP101
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

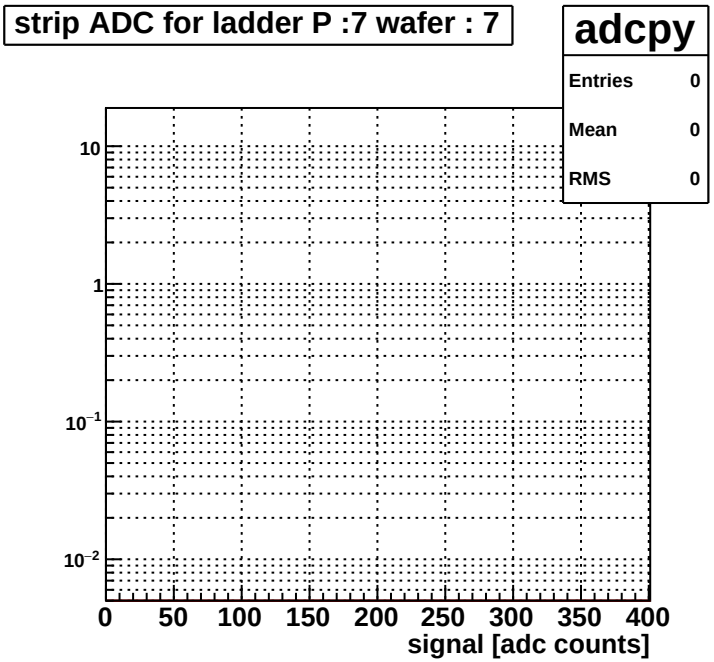

Strip adc vs. channels for ladder N : 7 wafer : 6
stripADCCHANsideN101
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

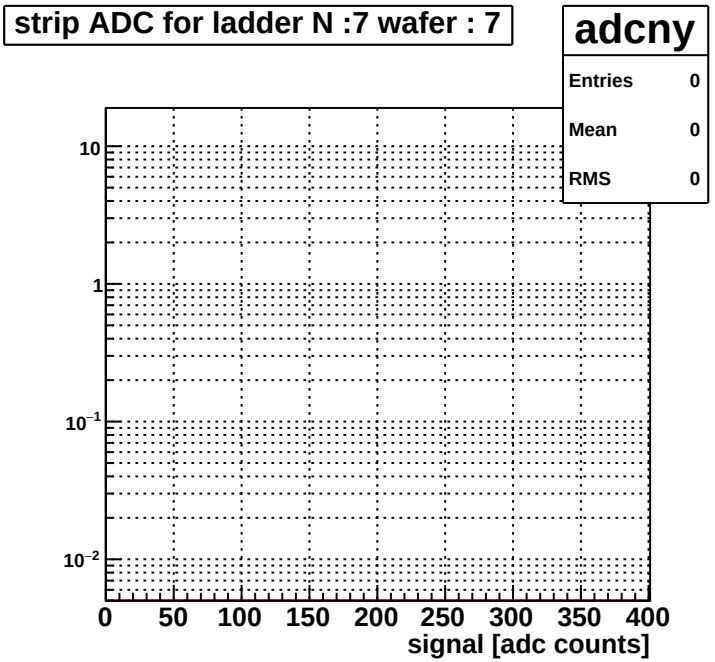

strip ADC for ladder P :7 wafer : 6
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

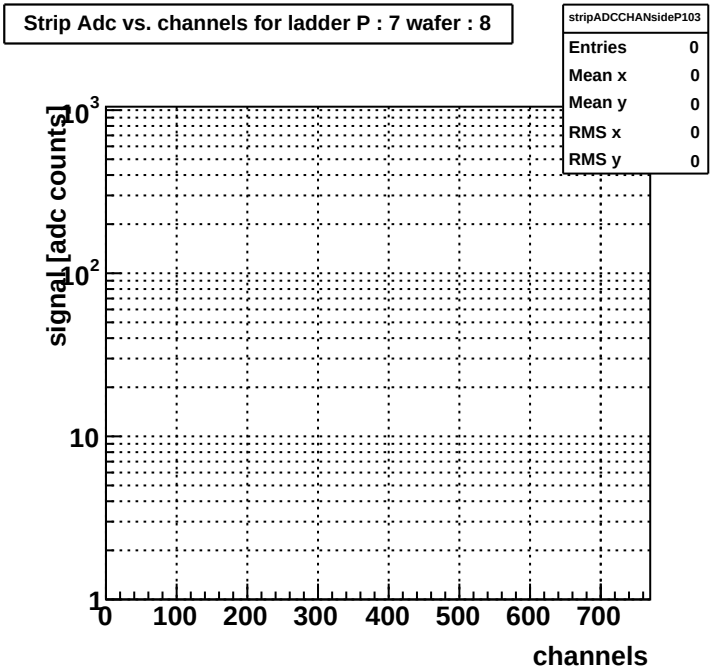

strip ADC for ladder N :7 wafer : 6
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

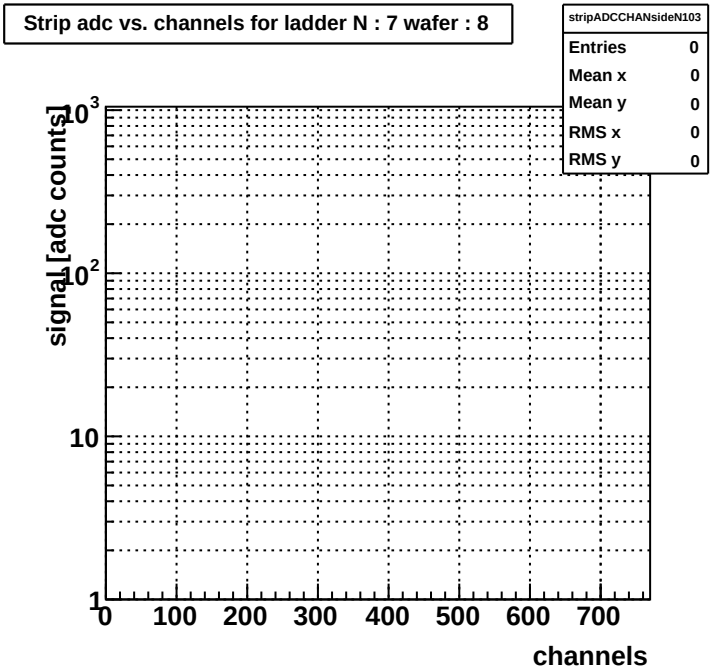

Strip Adc vs. channels for ladder P : 7 wafer : 7
stripADCCHANsideP102
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

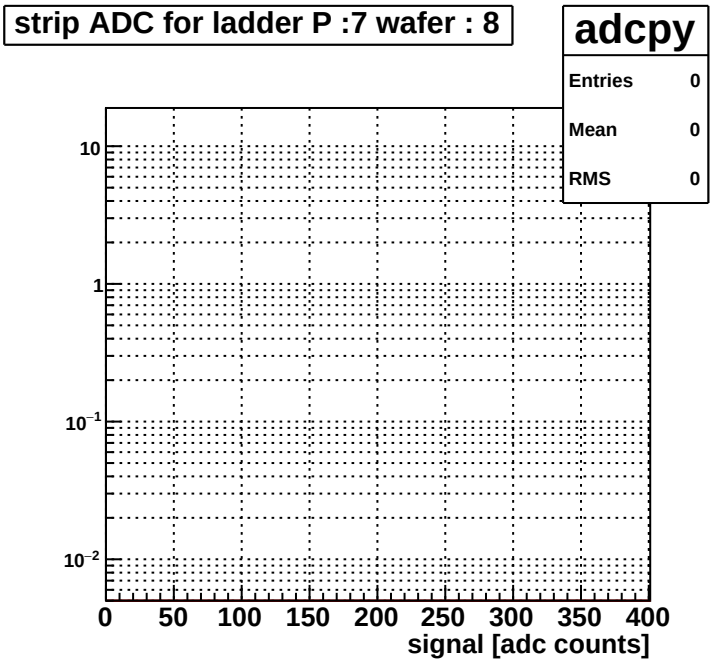

Strip adc vs. channels for ladder N : 7 wafer : 7
stripADCCHANsideN102
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

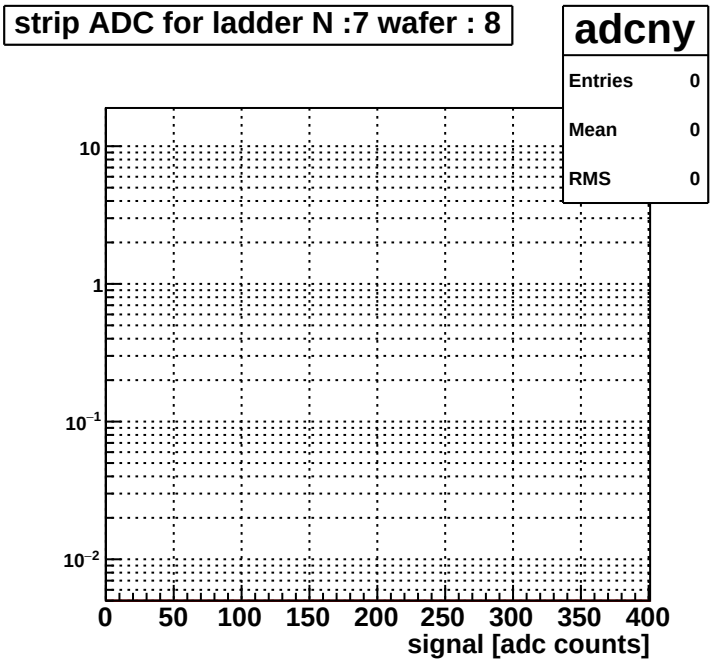

strip ADC for ladder P :7 wafer : 7
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

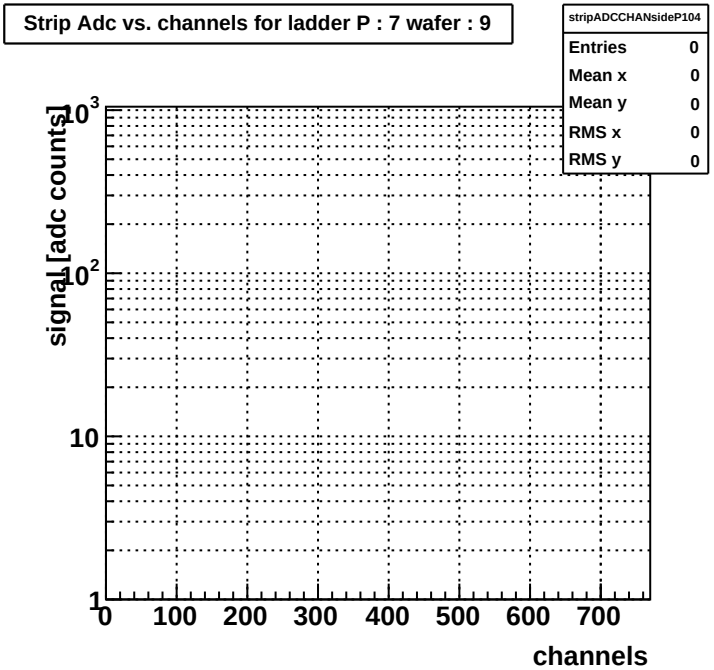

strip ADC for ladder N :7 wafer : 7
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

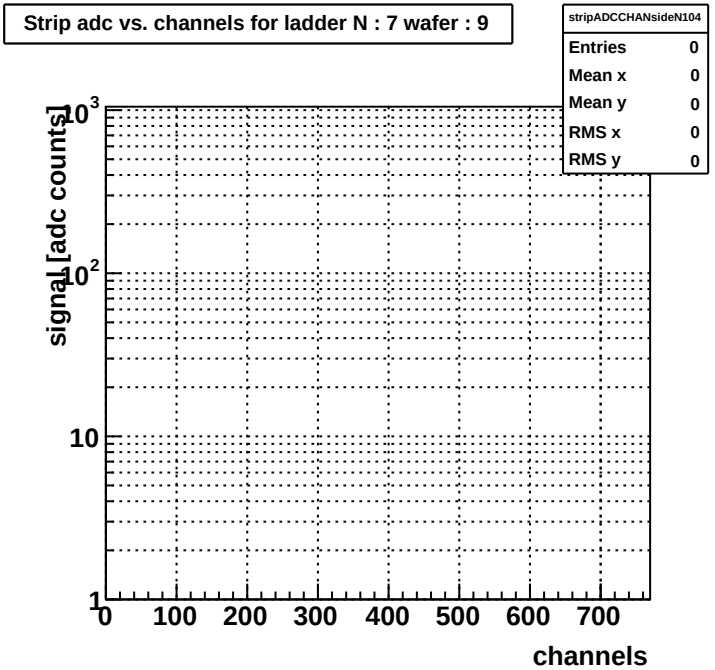

Strip Adc vs. channels for ladder P : 7 wafer : 8
stripADCCHANsideP103
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

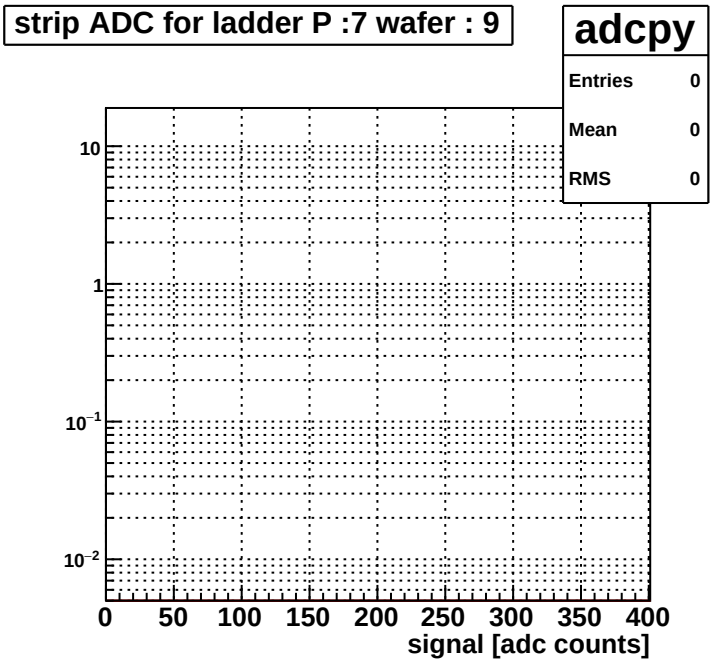

Strip adc vs. channels for ladder N : 7 wafer : 8
stripADCCHANsideN103
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

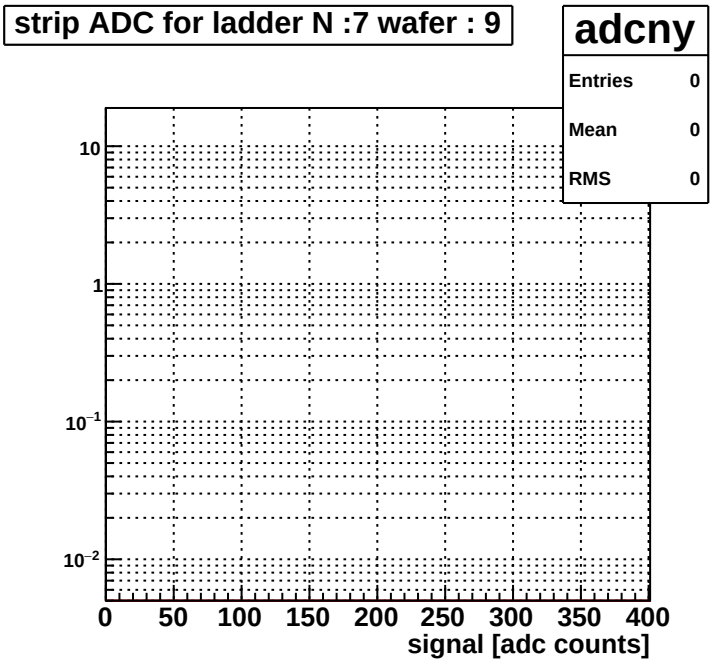

strip ADC for ladder P :7 wafer : 8
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

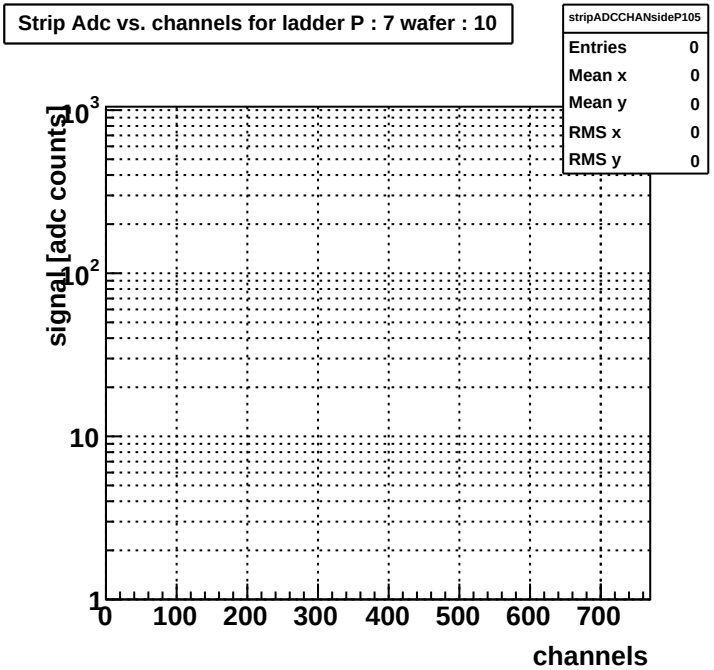

strip ADC for ladder N :7 wafer : 8
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

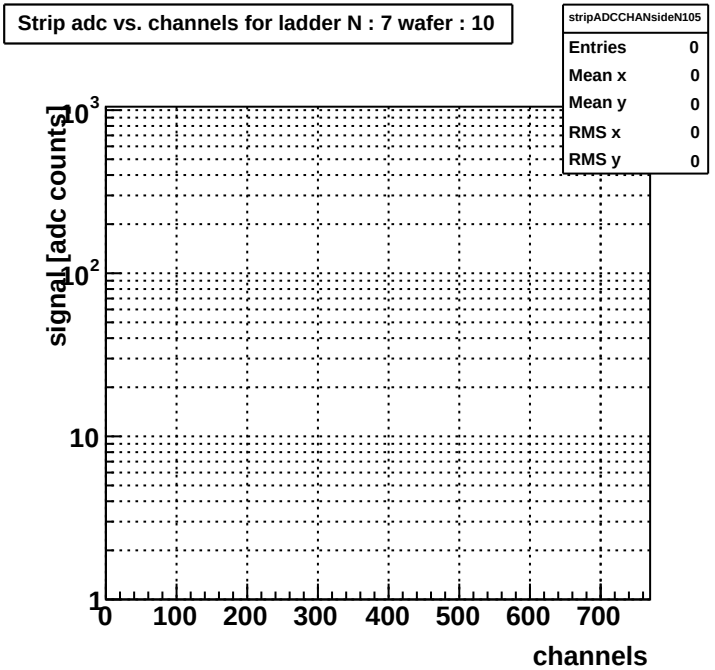

Strip Adc vs. channels for ladder P : 7 wafer : 9
stripADCCHANsideP104
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

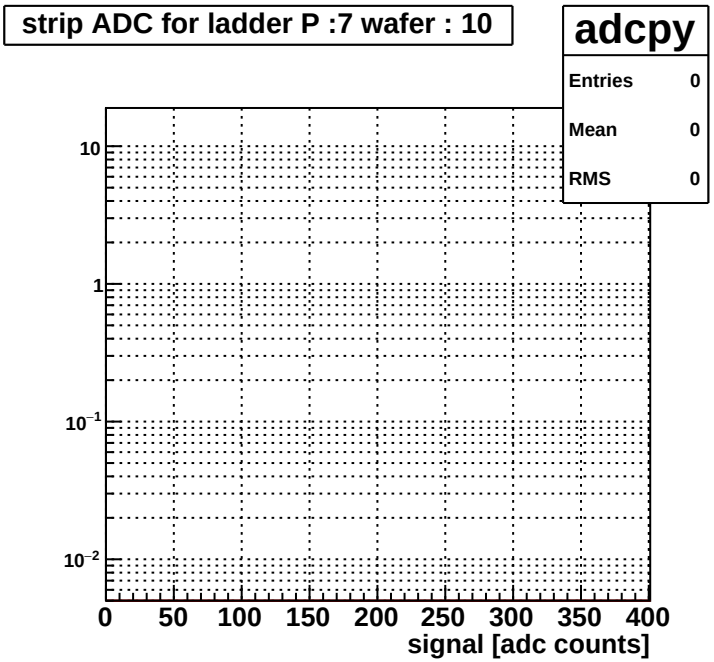

Strip adc vs. channels for ladder N : 7 wafer : 9
stripADCCHANsideN104
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

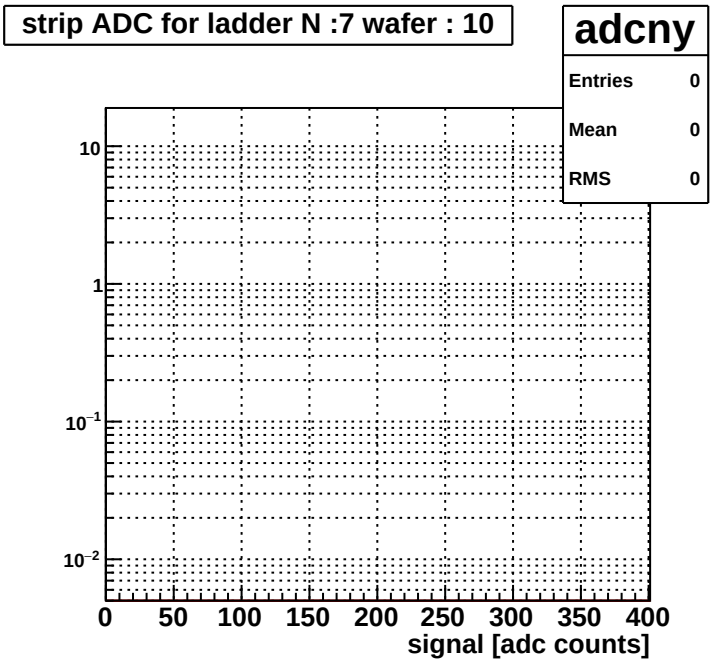

strip ADC for ladder P :7 wafer : 9
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

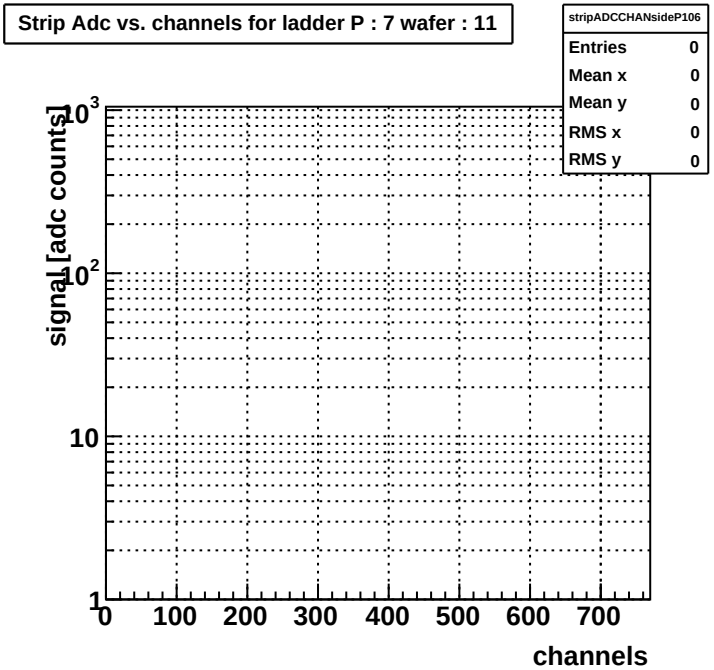

strip ADC for ladder N :7 wafer : 9
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

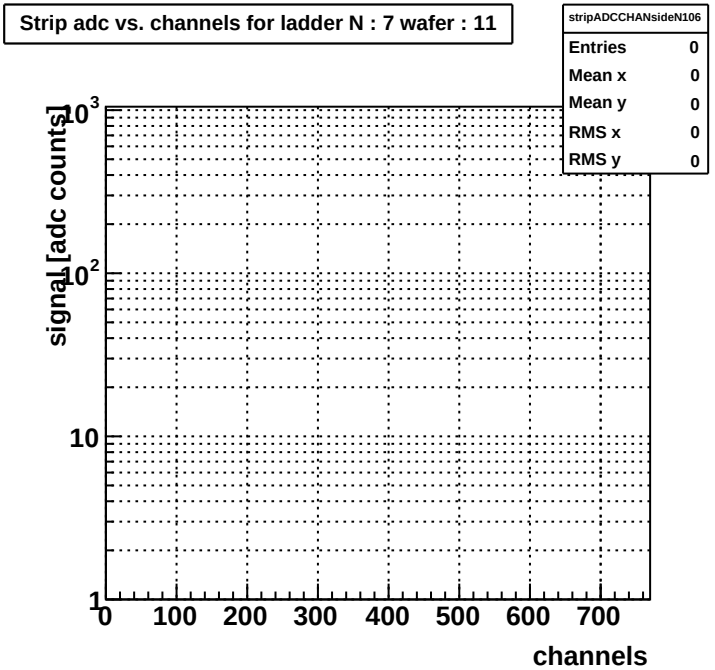

Strip Adc vs. channels for ladder P : 7 wafer : 10
stripADCCHANsideP105
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

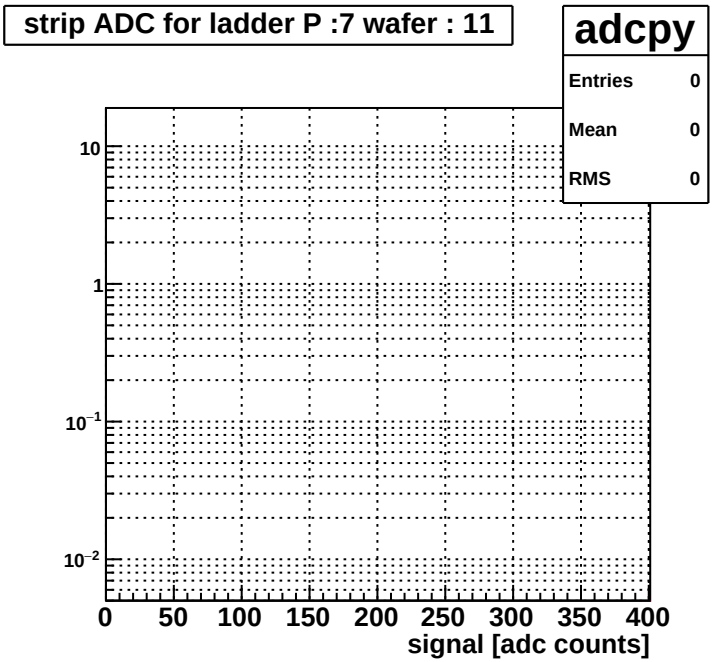

Strip adc vs. channels for ladder N : 7 wafer : 10
stripADCCHANsideN105
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

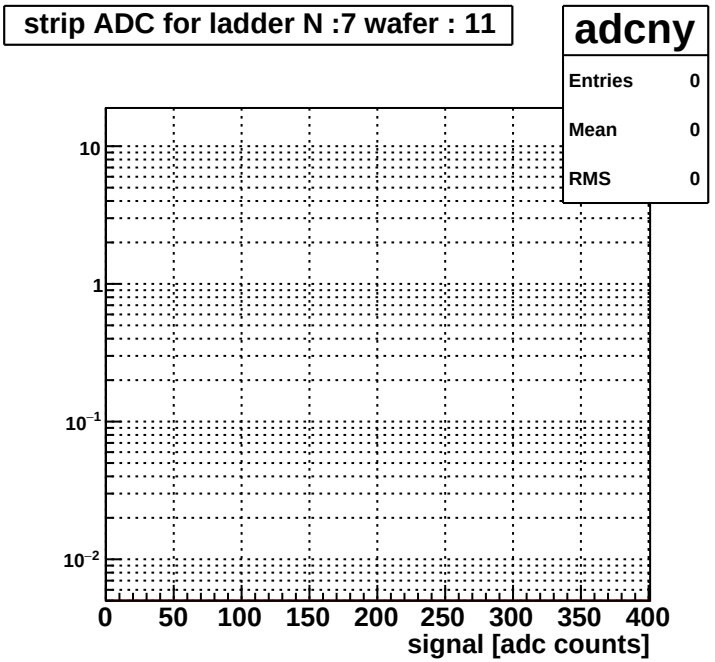

strip ADC for ladder P :7 wafer : 10
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

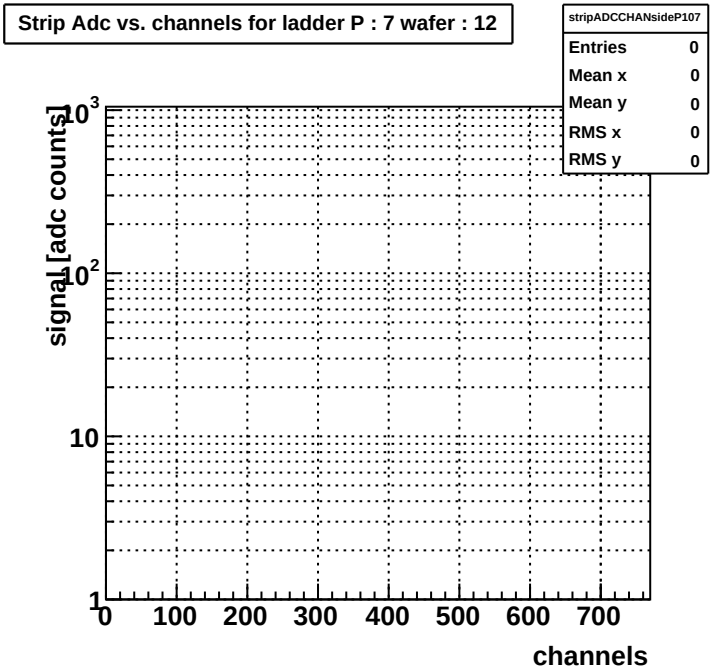

strip ADC for ladder N :7 wafer : 10
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

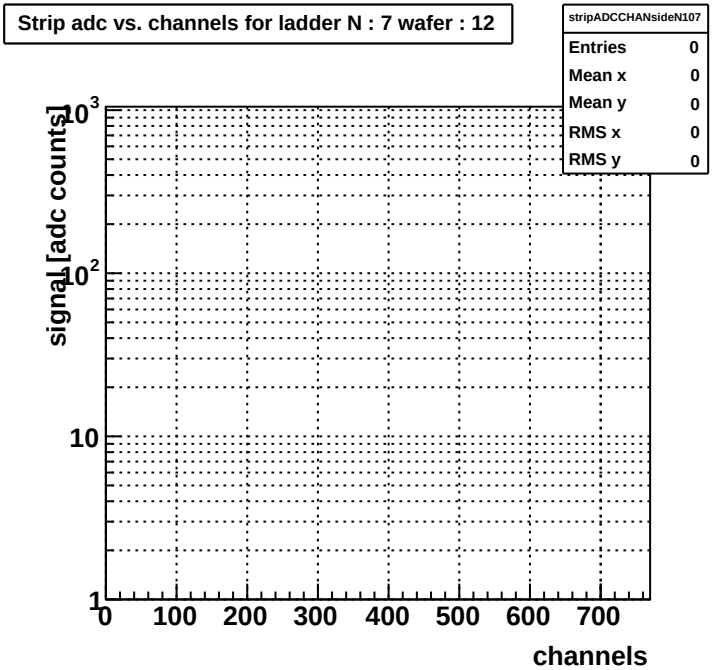

Strip Adc vs. channels for ladder P : 7 wafer : 11
stripADCCHANsideP106
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

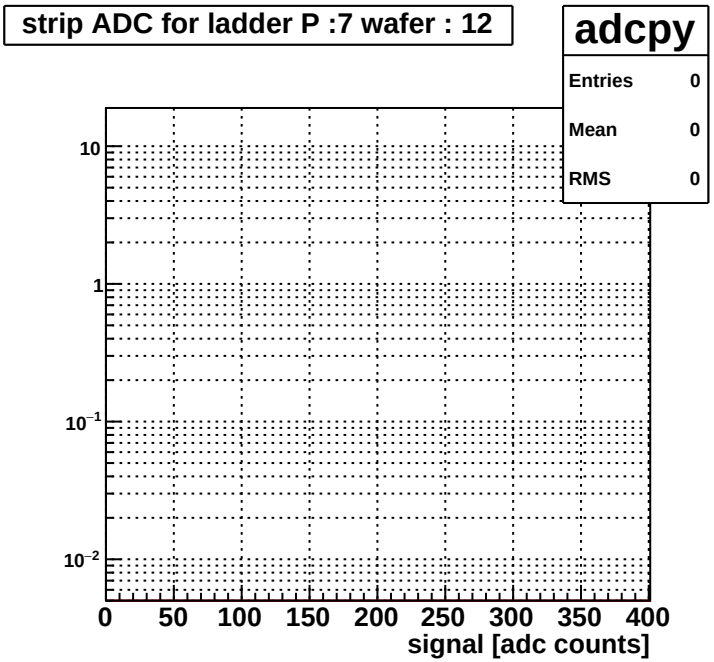

Strip adc vs. channels for ladder N : 7 wafer : 11
stripADCCHANsideN106
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

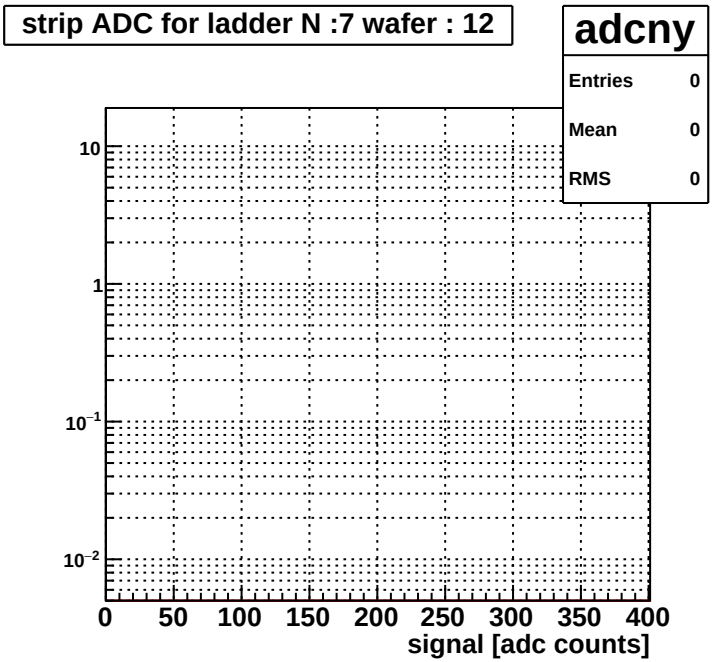

strip ADC for ladder P :7 wafer : 11
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

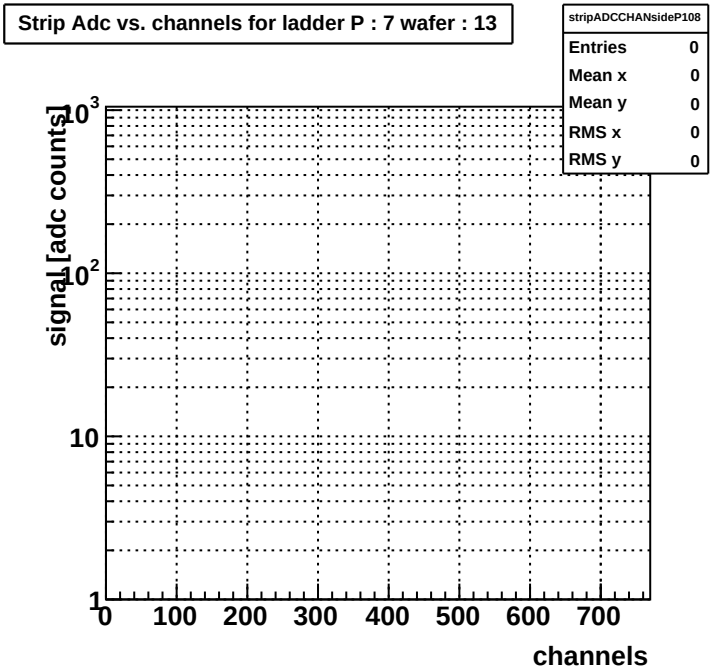

strip ADC for ladder N :7 wafer : 11
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

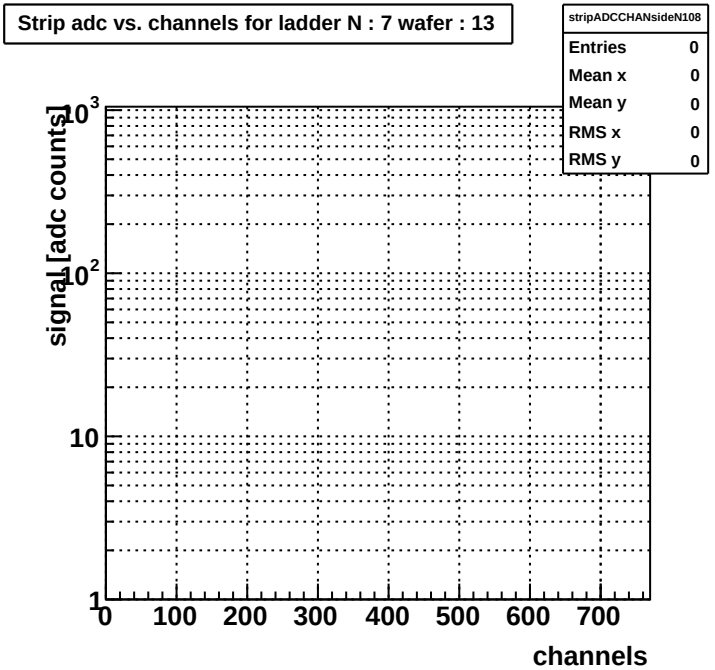

Strip Adc vs. channels for ladder P : 7 wafer : 12
stripADCCHANsideP107
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

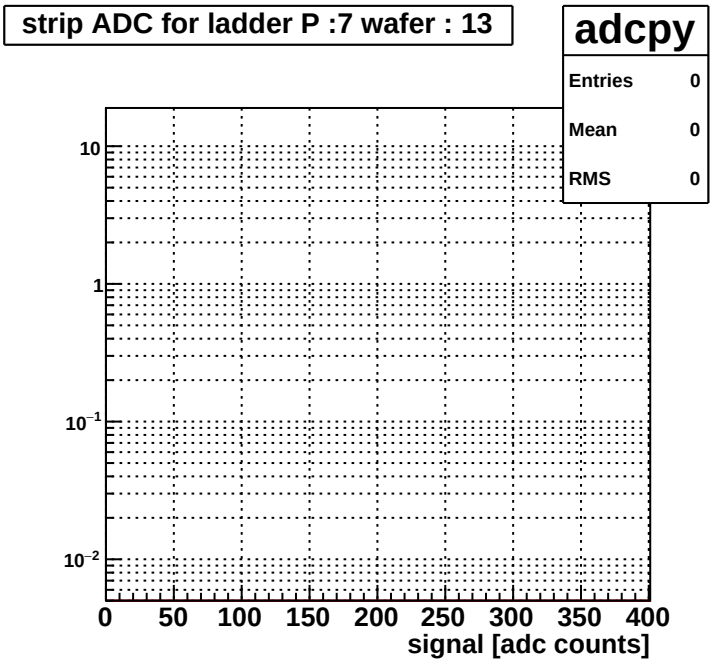

Strip adc vs. channels for ladder N : 7 wafer : 12
stripADCCHANsideN107
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

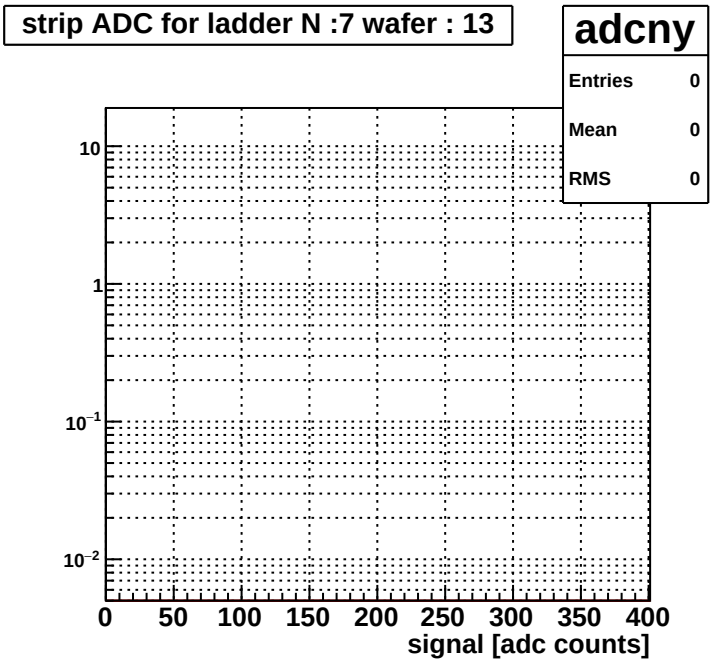

strip ADC for ladder P :7 wafer : 12
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

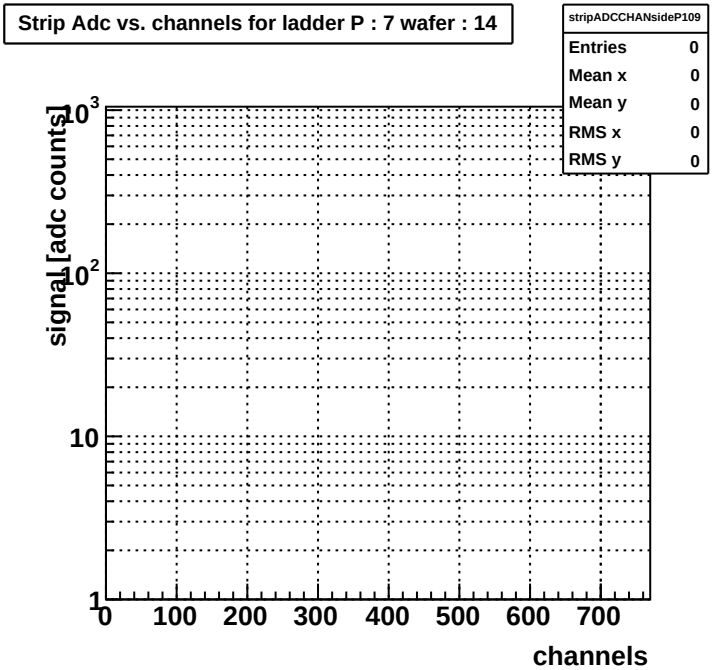

strip ADC for ladder N :7 wafer : 12
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

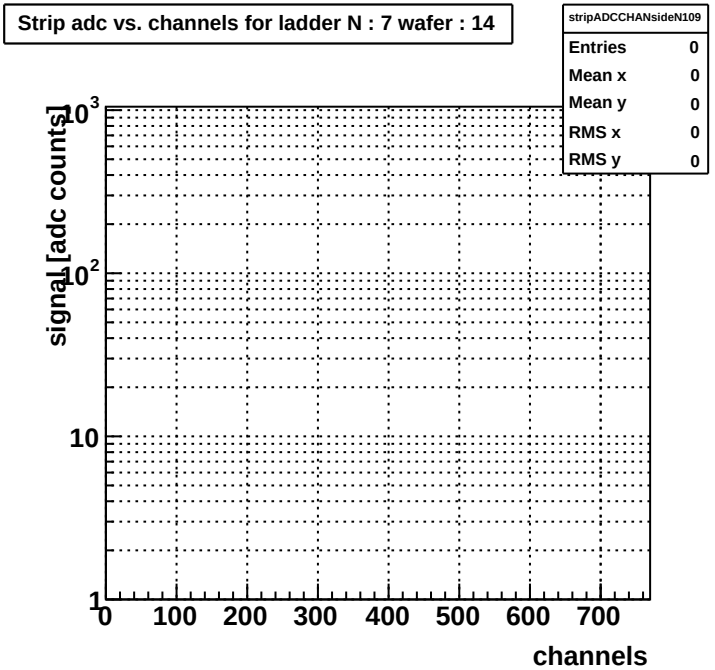

Strip Adc vs. channels for ladder P : 7 wafer : 13
stripADCCHANsideP108
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

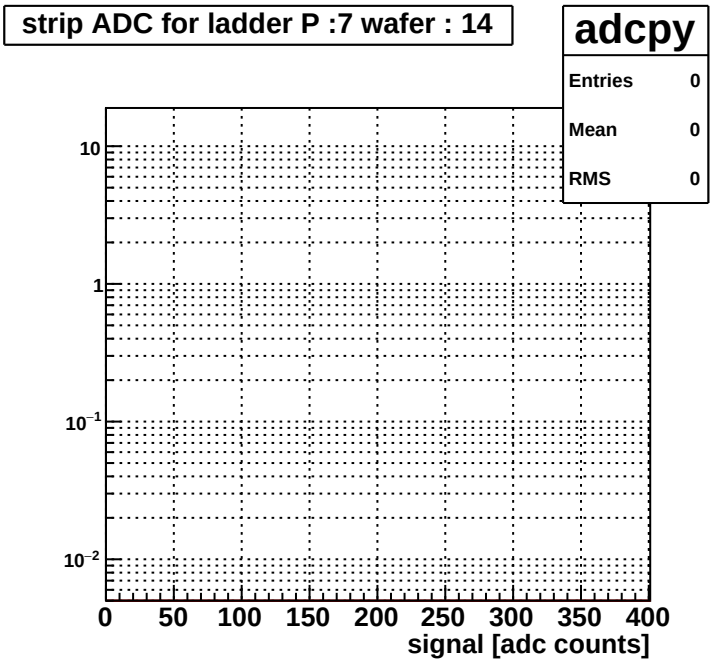

Strip adc vs. channels for ladder N : 7 wafer : 13
stripADCCHANsideN108
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

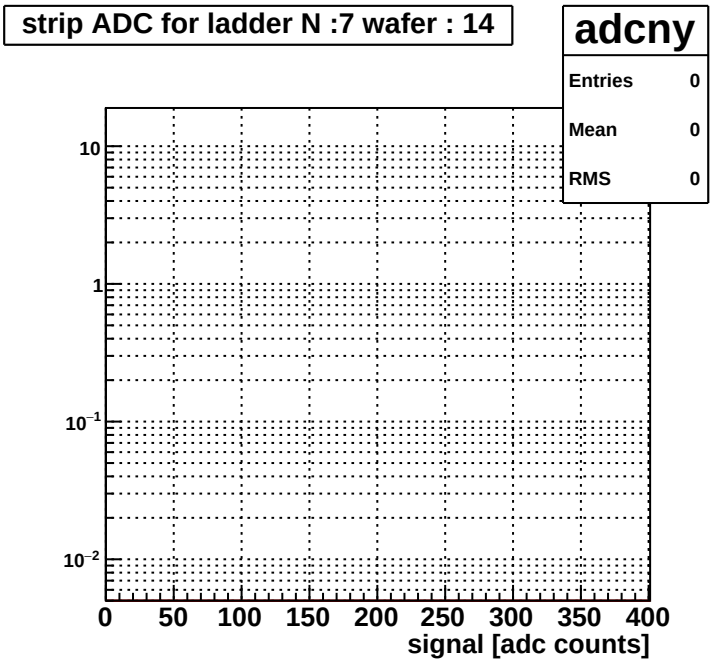

strip ADC for ladder P :7 wafer : 13
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

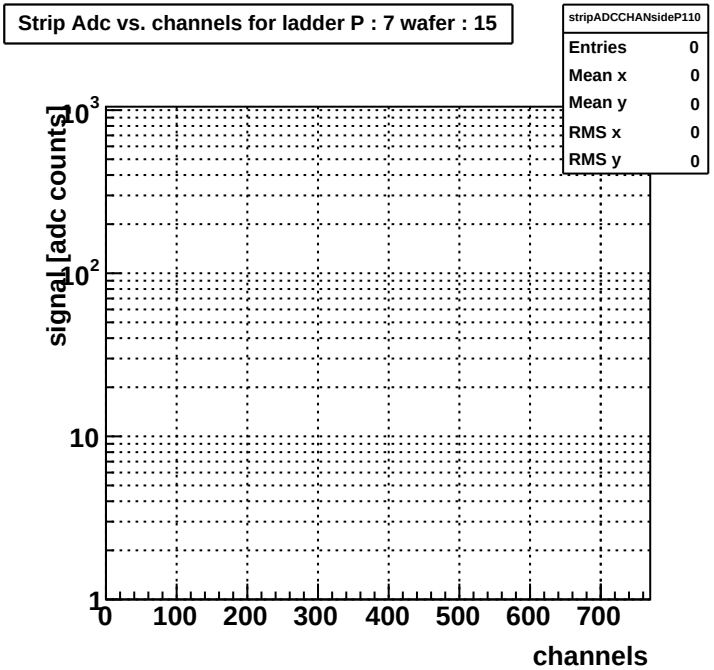

strip ADC for ladder N :7 wafer : 13
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

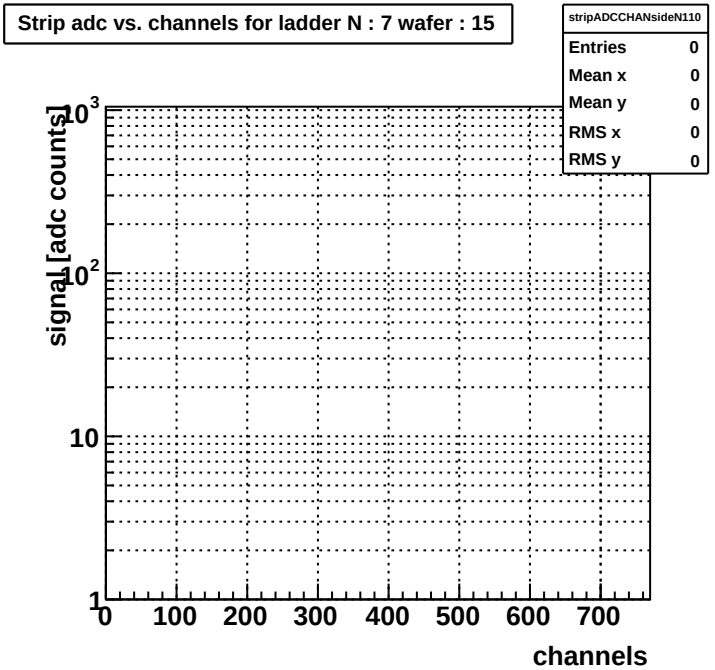

Strip Adc vs. channels for ladder P : 7 wafer : 14
stripADCCHANsideP109
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

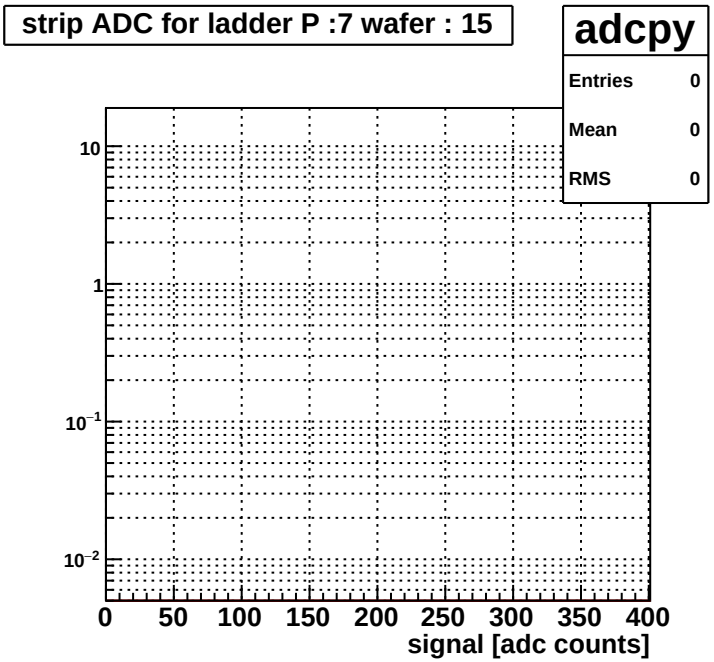

Strip adc vs. channels for ladder N : 7 wafer : 14
stripADCCHANsideN109
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

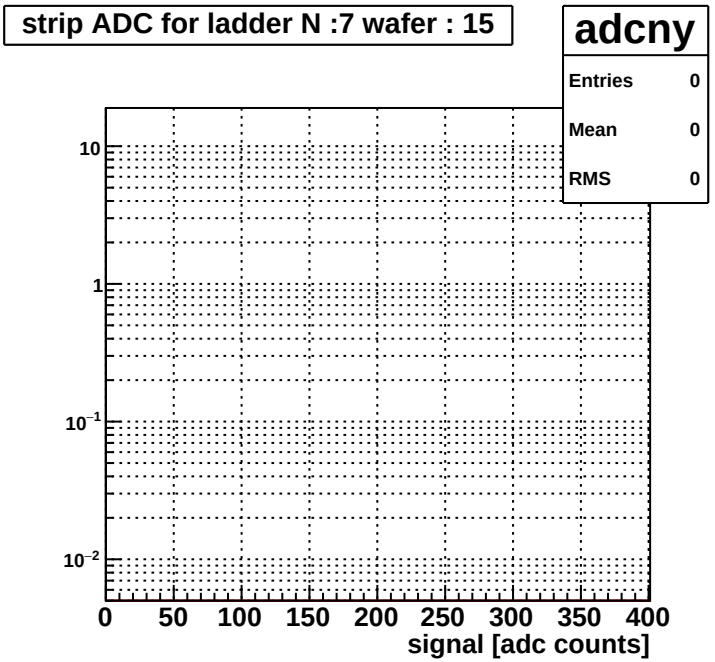

strip ADC for ladder P :7 wafer : 14
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

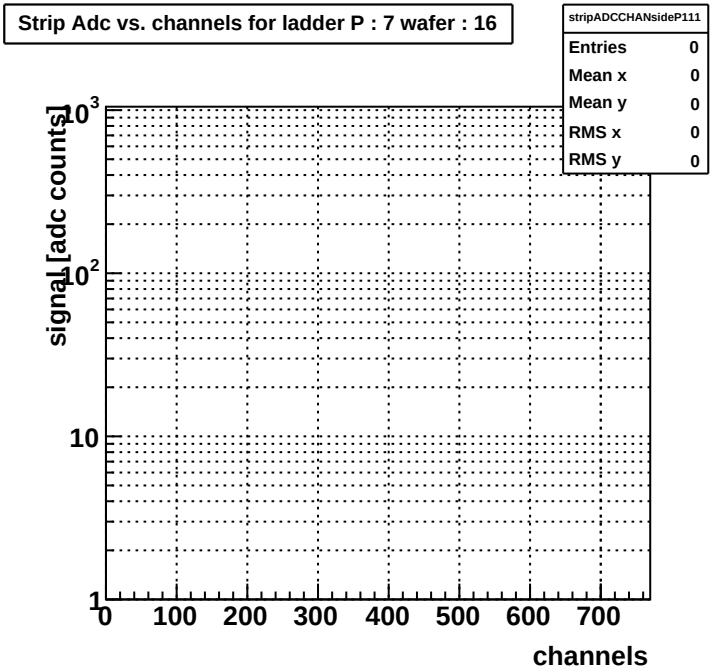

strip ADC for ladder N :7 wafer : 14
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

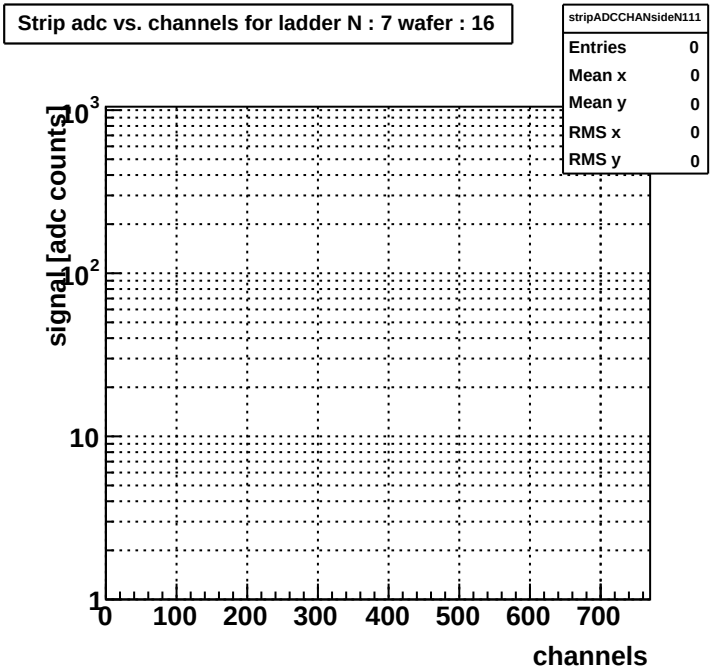

Strip Adc vs. channels for ladder P : 7 wafer : 15
stripADCCHANsideP110
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

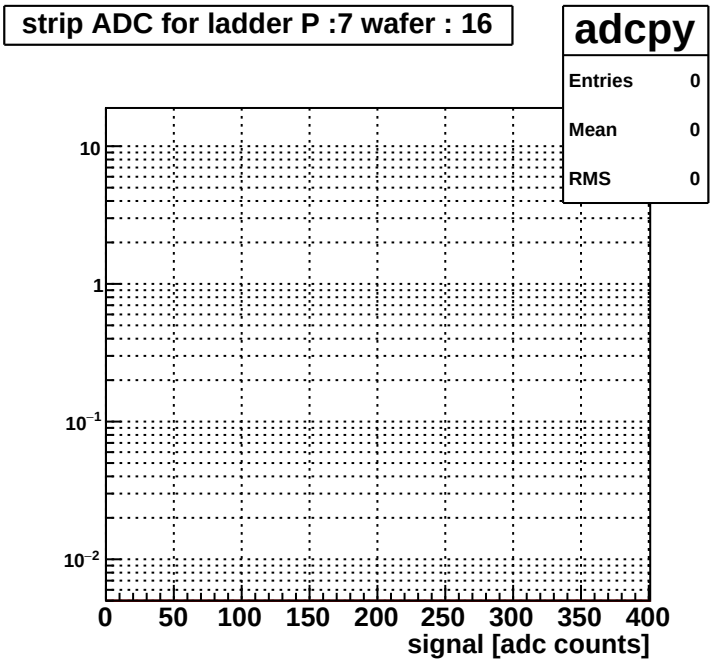

Strip adc vs. channels for ladder N : 7 wafer : 15
stripADCCHANsideN110
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

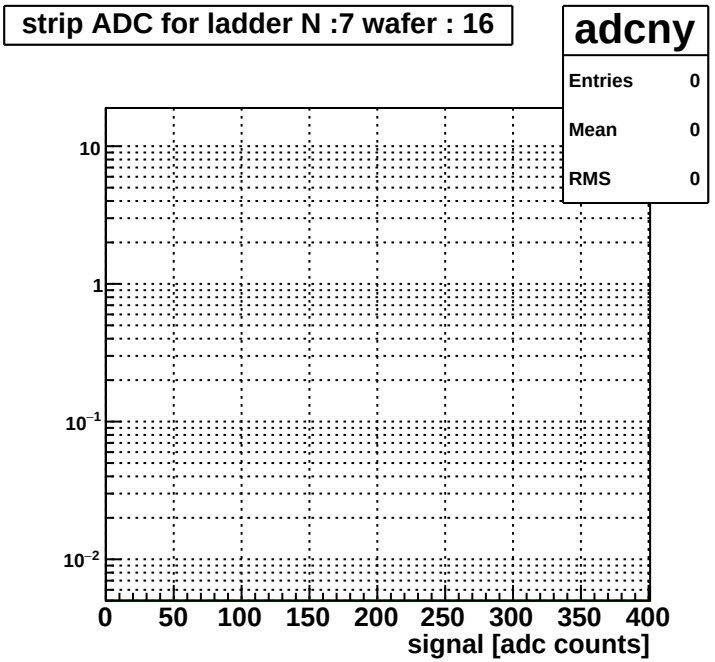

strip ADC for ladder P :7 wafer : 15
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

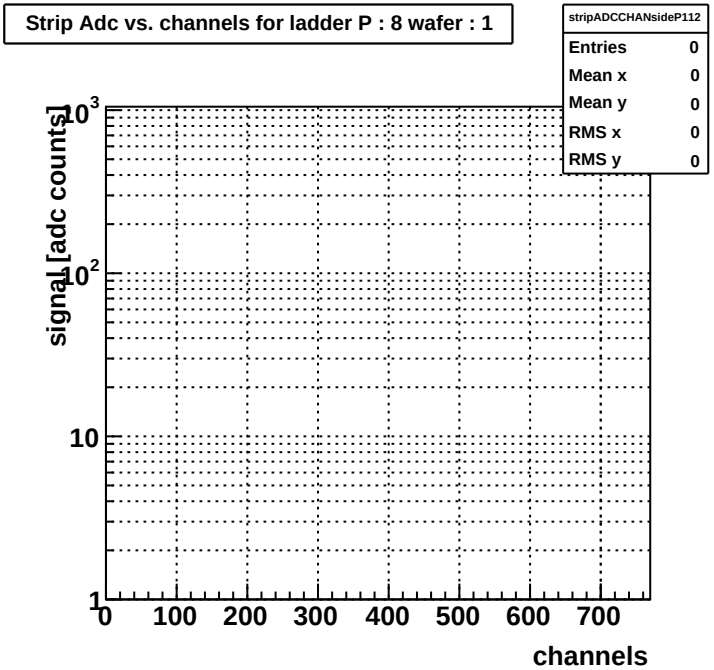

strip ADC for ladder N :7 wafer : 15
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

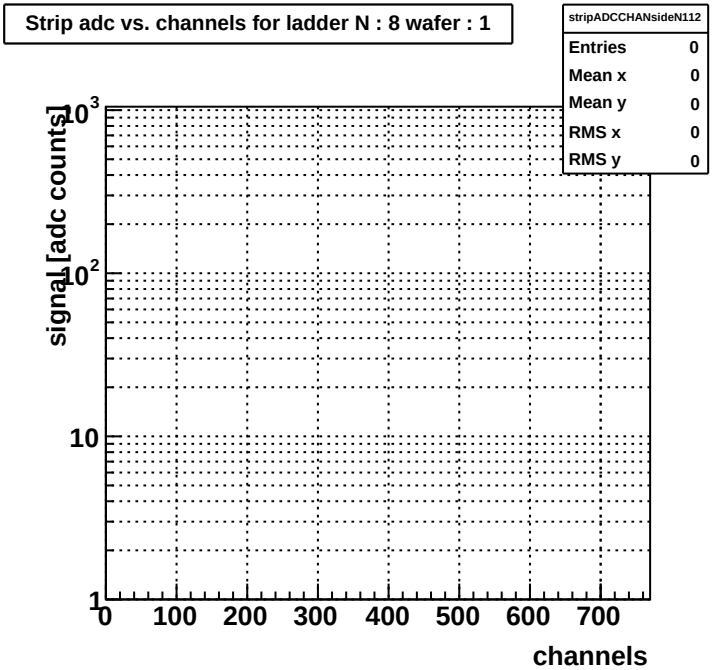

Strip Adc vs. channels for ladder P : 7 wafer : 16
stripADCCHANsideP111
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

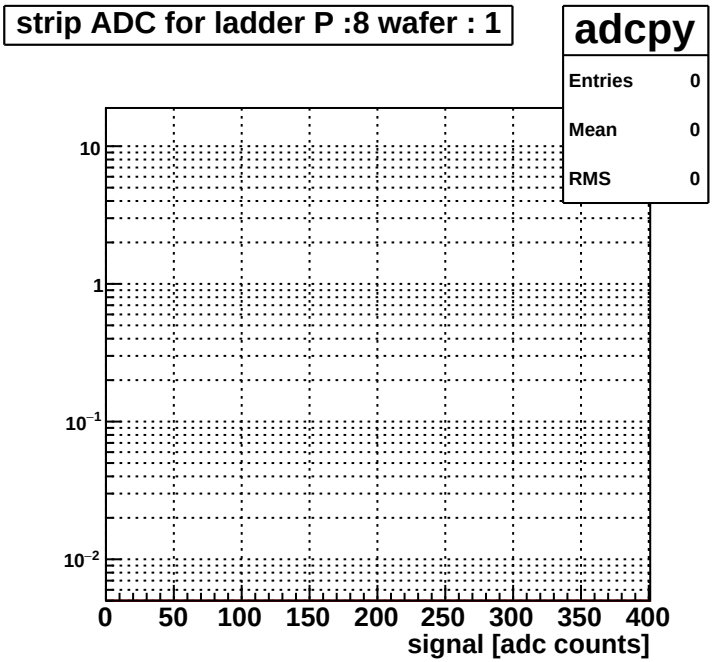

Strip adc vs. channels for ladder N : 7 wafer : 16
stripADCCHANsideN111
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

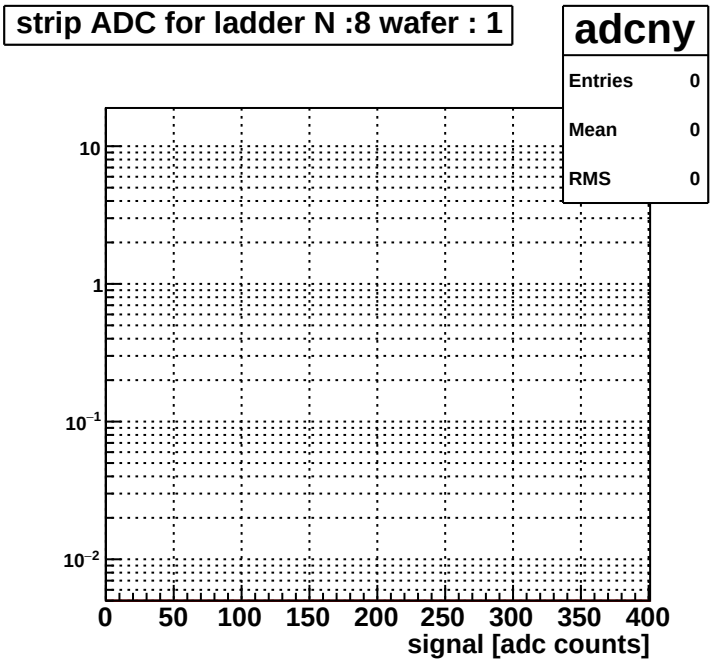

strip ADC for ladder P :7 wafer : 16
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

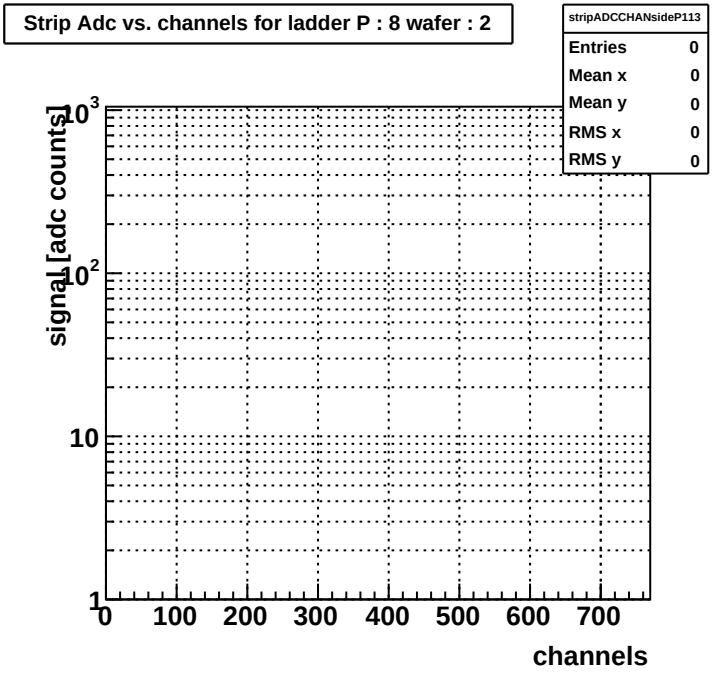

strip ADC for ladder N :7 wafer : 16
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

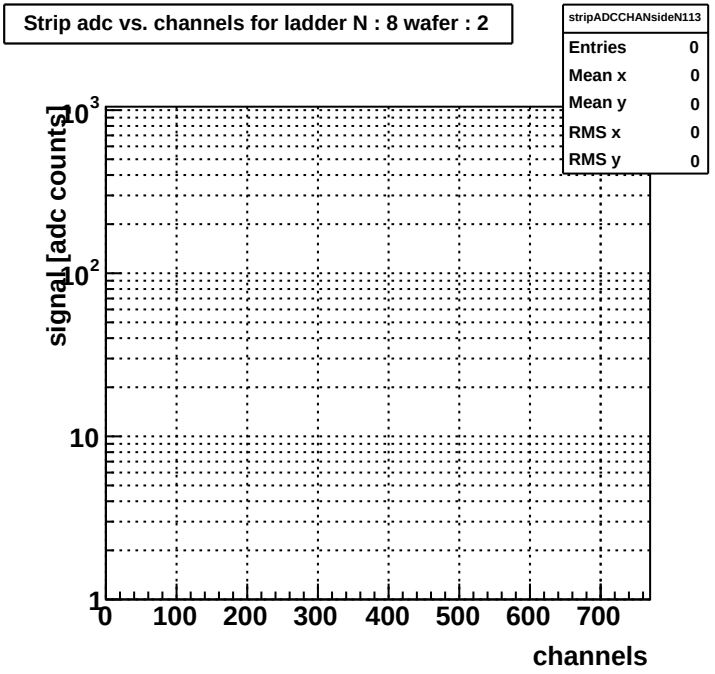

Strip Adc vs. channels for ladder P : 8 wafer : 1
stripADCCHANsideP112
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

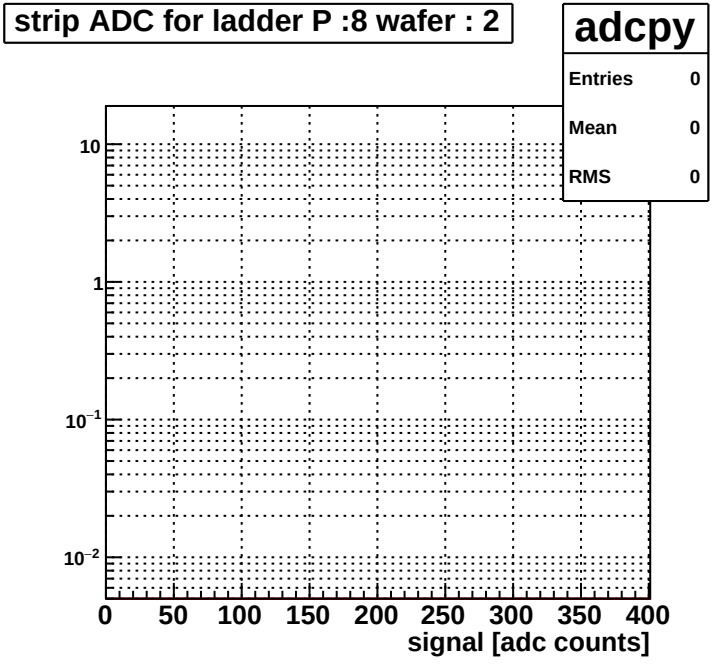

Strip adc vs. channels for ladder N : 8 wafer : 1
stripADCCHANsideN112
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

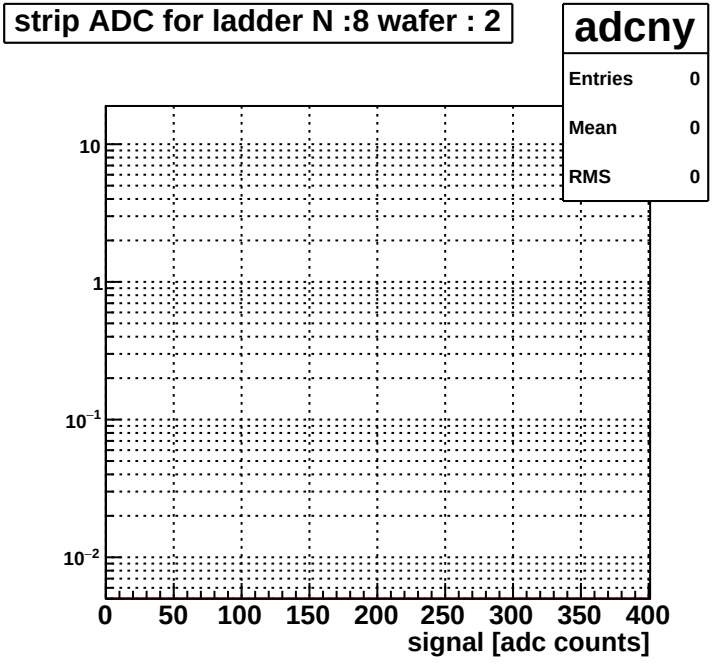

strip ADC for ladder P :8 wafer : 1
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

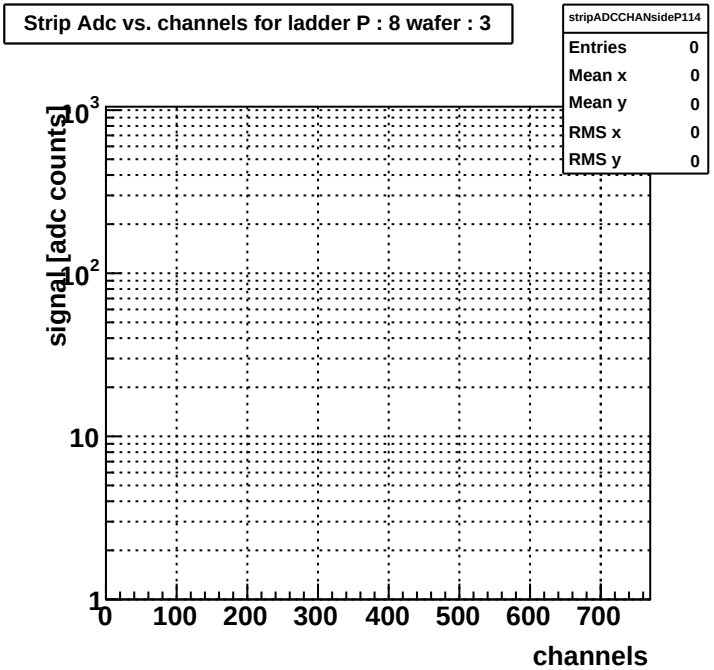

strip ADC for ladder N :8 wafer : 1
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

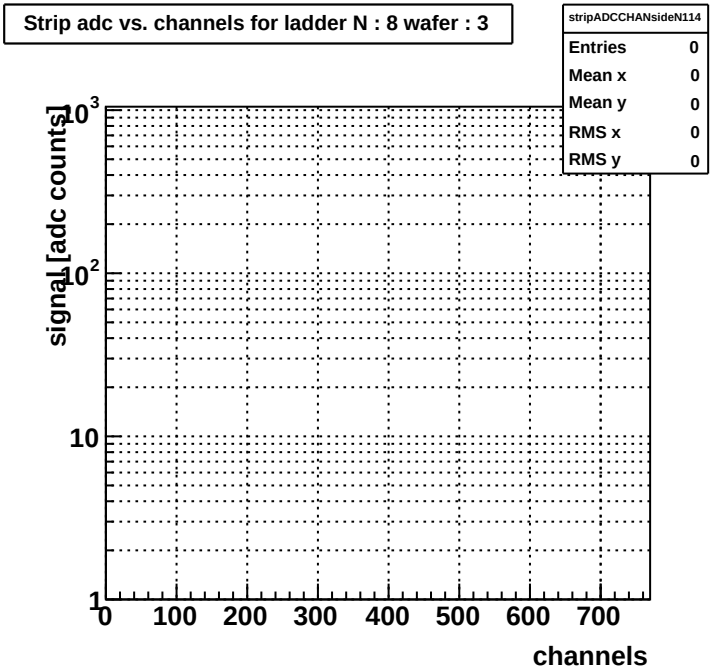

Strip Adc vs. channels for ladder P : 8 wafer : 2
stripADCCHANsideP113
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

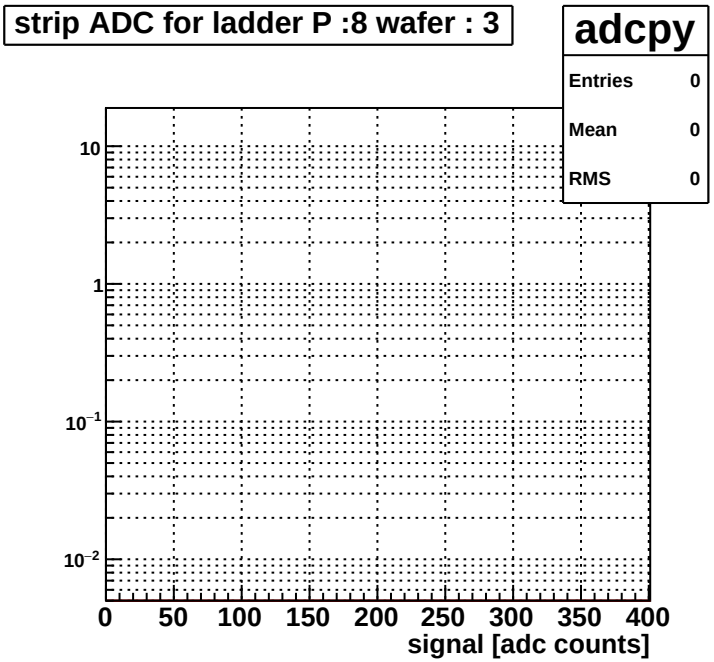

Strip adc vs. channels for ladder N : 8 wafer : 2
stripADCCHANsideN113
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

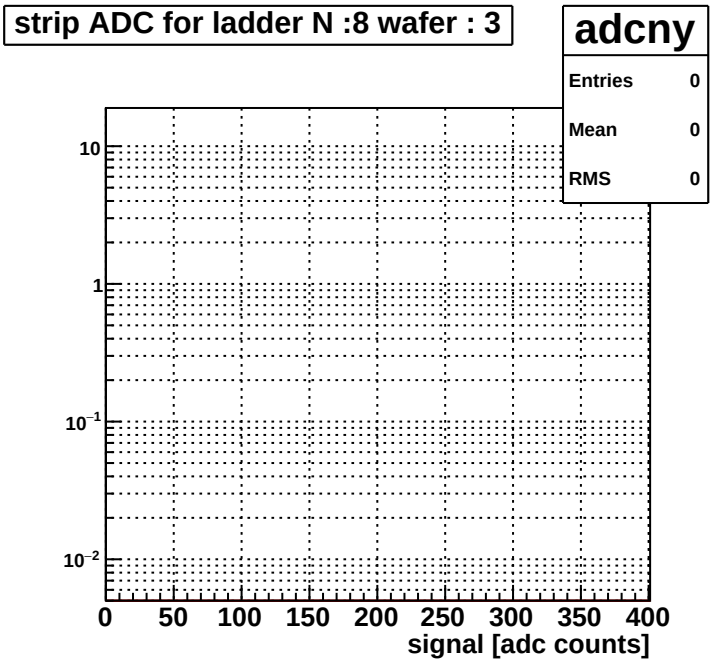

strip ADC for ladder P :8 wafer : 2
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

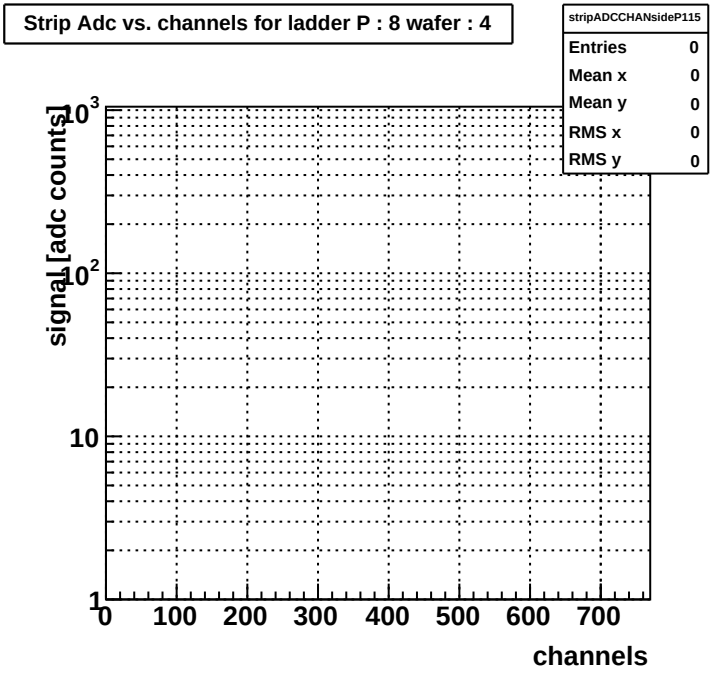

strip ADC for ladder N :8 wafer : 2
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

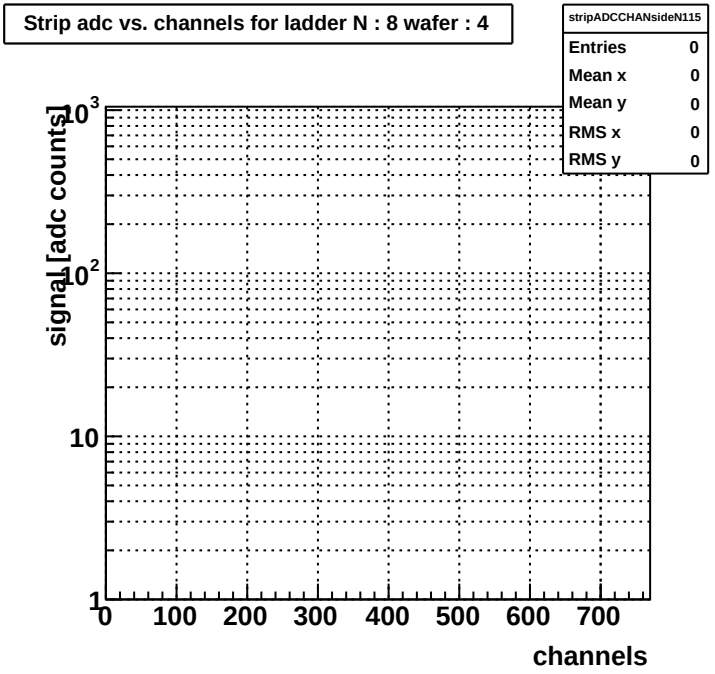

Strip Adc vs. channels for ladder P : 8 wafer : 3
stripADCCHANsideP114
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

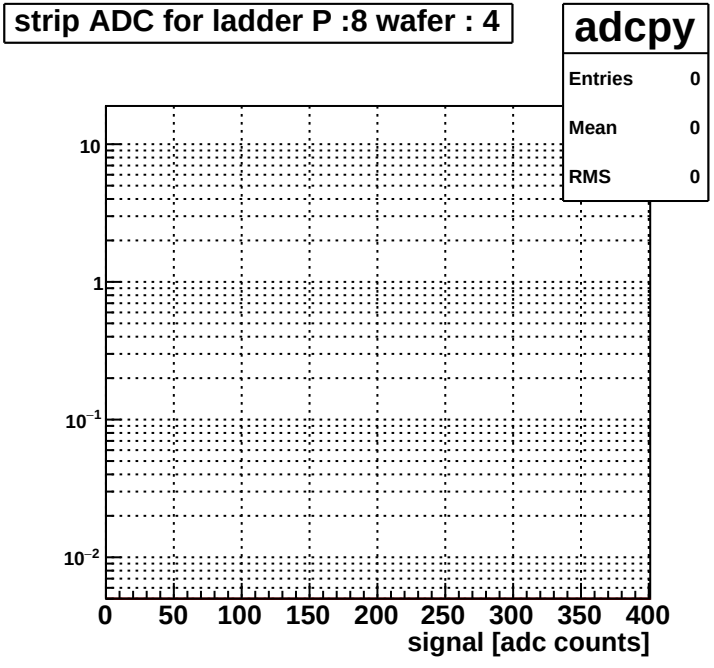

Strip adc vs. channels for ladder N : 8 wafer : 3
stripADCCHANsideN114
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

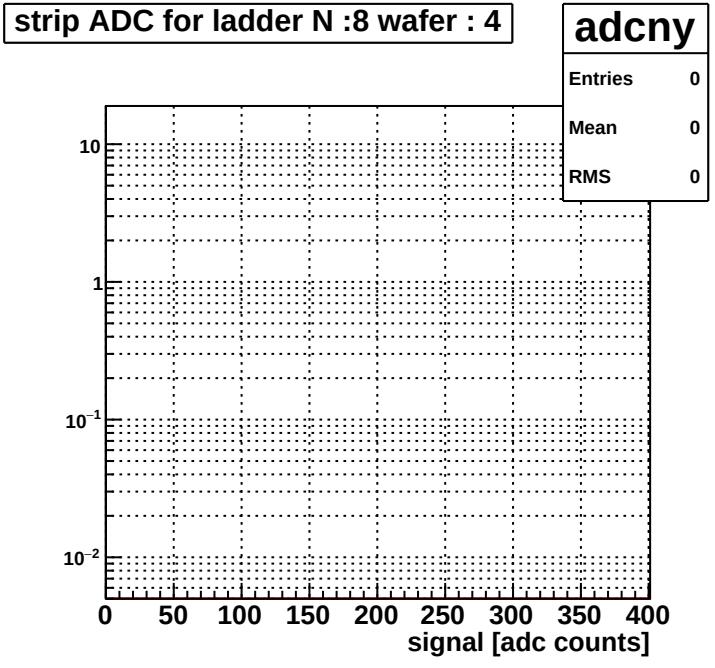

strip ADC for ladder P :8 wafer : 3
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

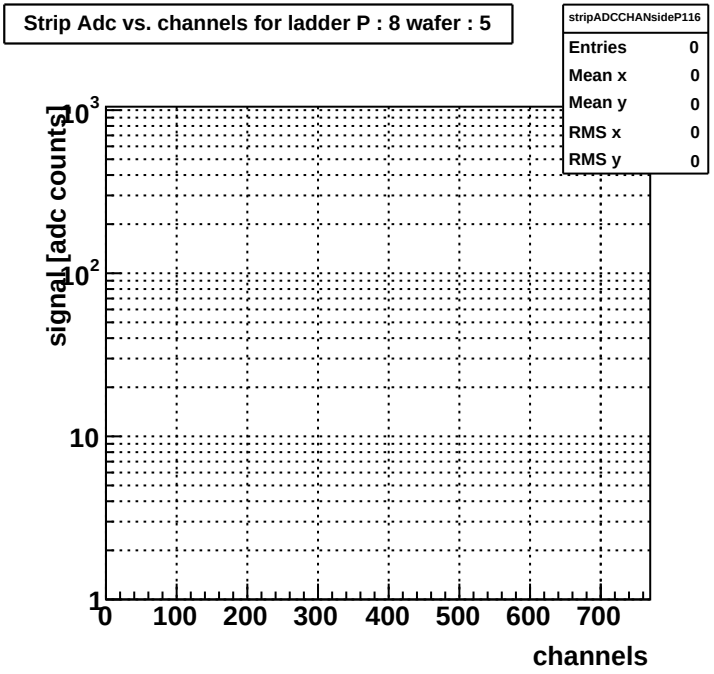

strip ADC for ladder N :8 wafer : 3
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

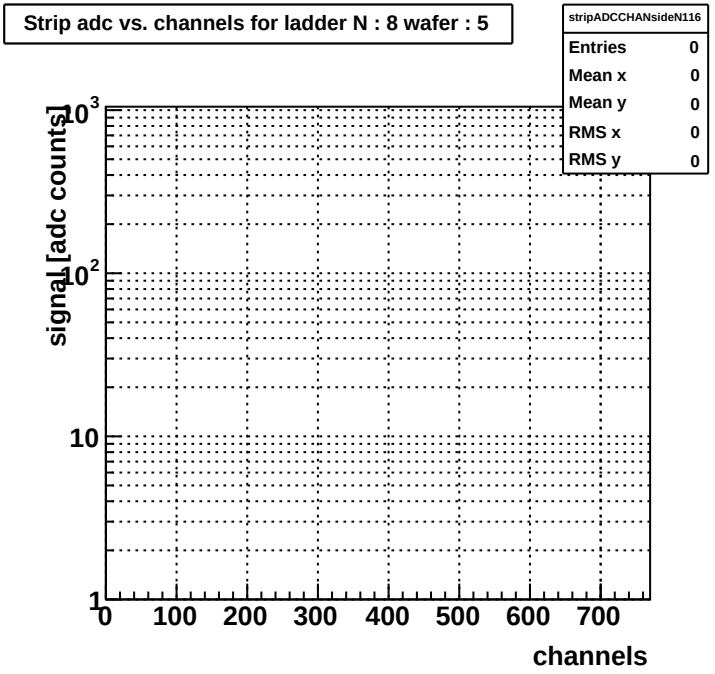

Strip Adc vs. channels for ladder P : 8 wafer : 4
stripADCCHANsideP115
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

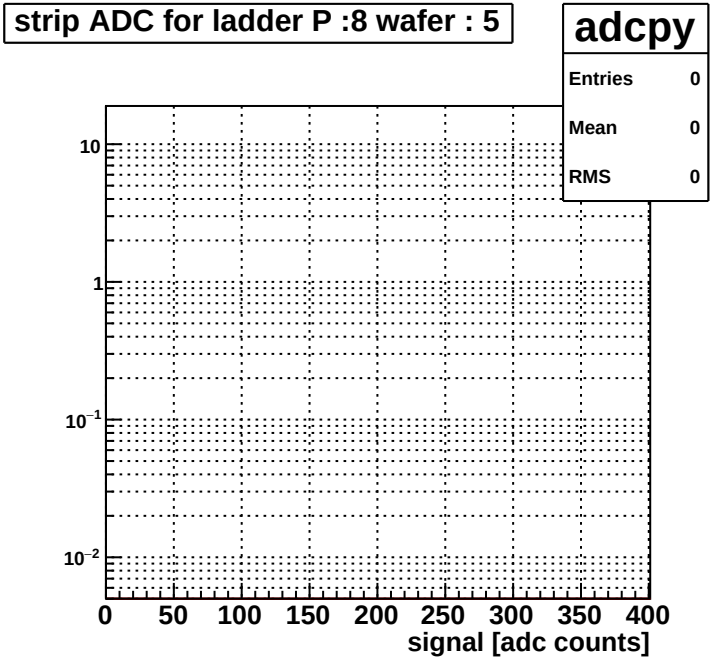

Strip adc vs. channels for ladder N : 8 wafer : 4
stripADCCHANsideN115
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

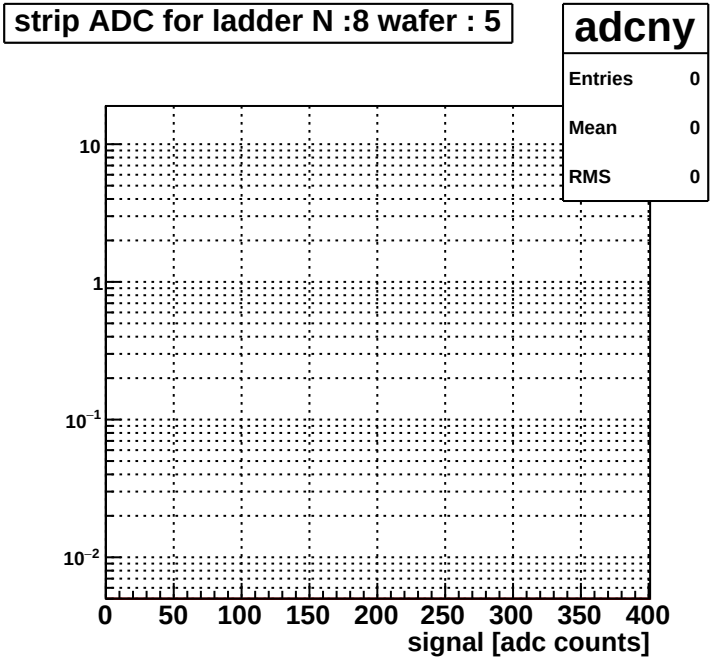

strip ADC for ladder P :8 wafer : 4
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

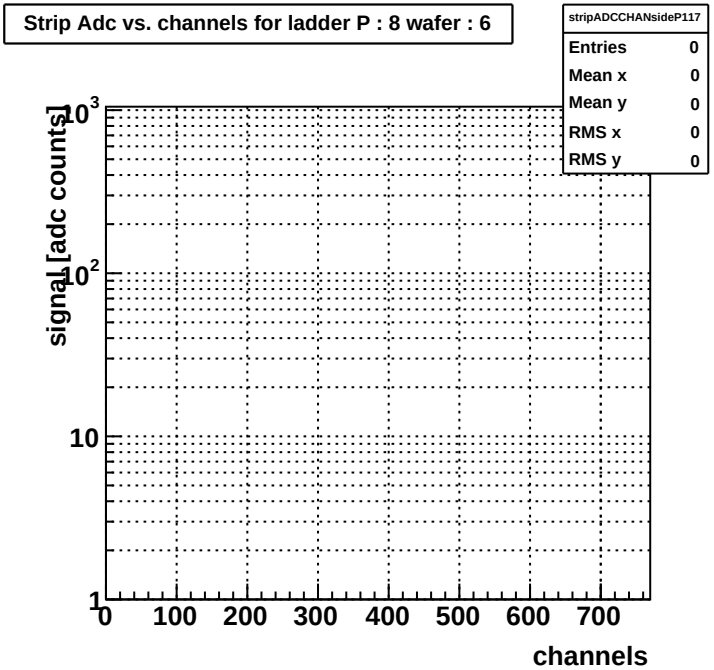

strip ADC for ladder N :8 wafer : 4
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

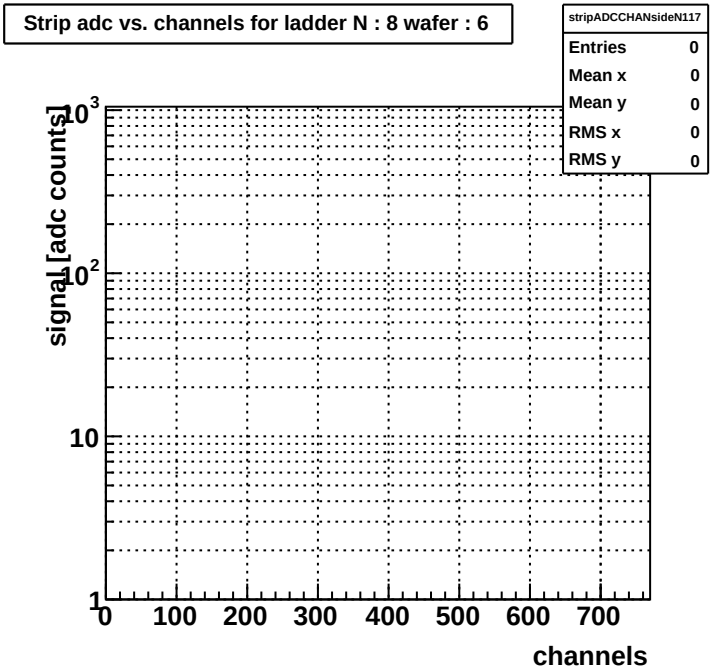

Strip Adc vs. channels for ladder P : 8 wafer : 5
stripADCCHANsideP116
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

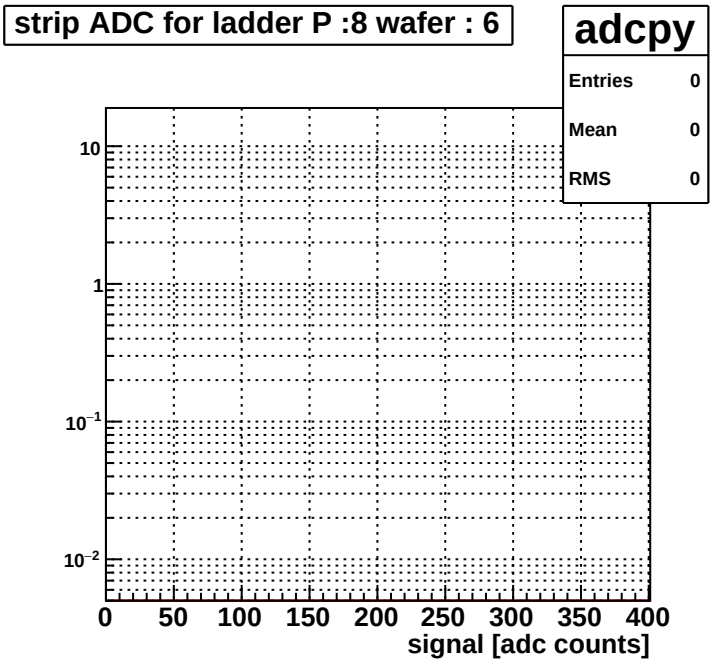

Strip adc vs. channels for ladder N : 8 wafer : 5
stripADCCHANsideN116
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

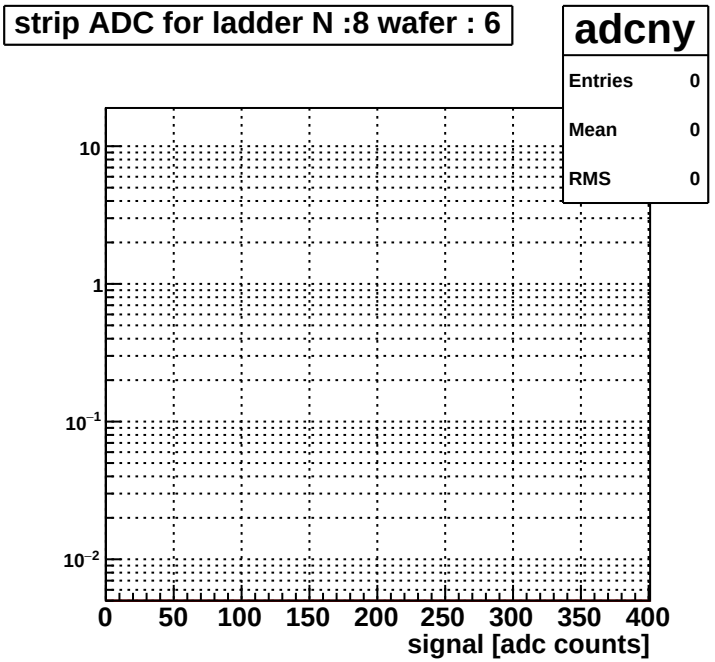

strip ADC for ladder P :8 wafer : 5
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

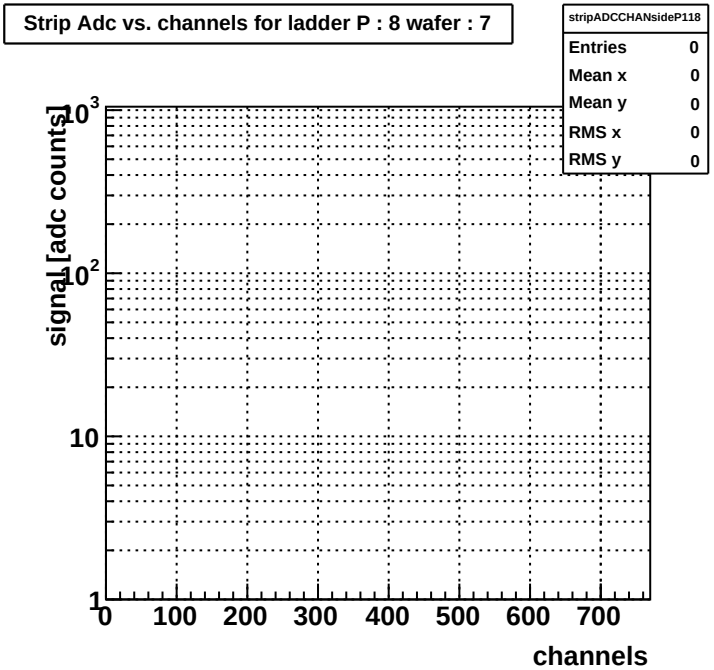

strip ADC for ladder N :8 wafer : 5
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

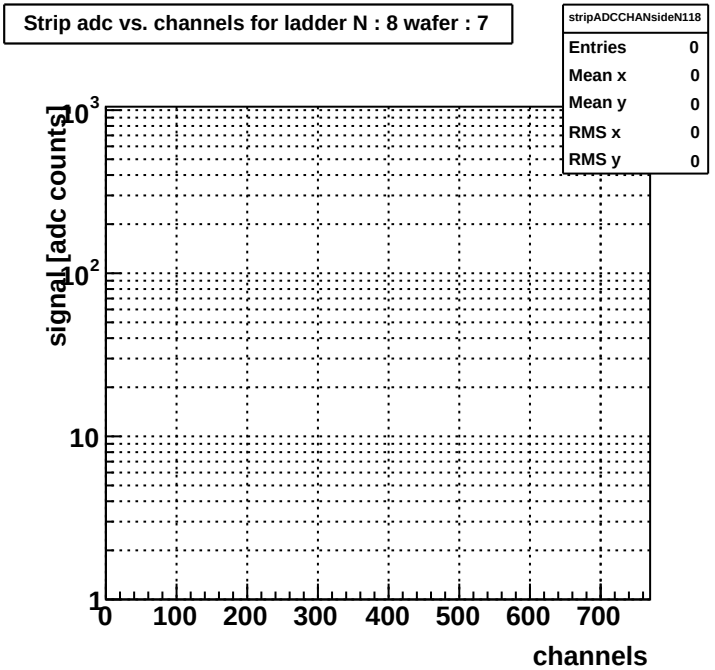

Strip Adc vs. channels for ladder P : 8 wafer : 6
stripADCCHANsideP117
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

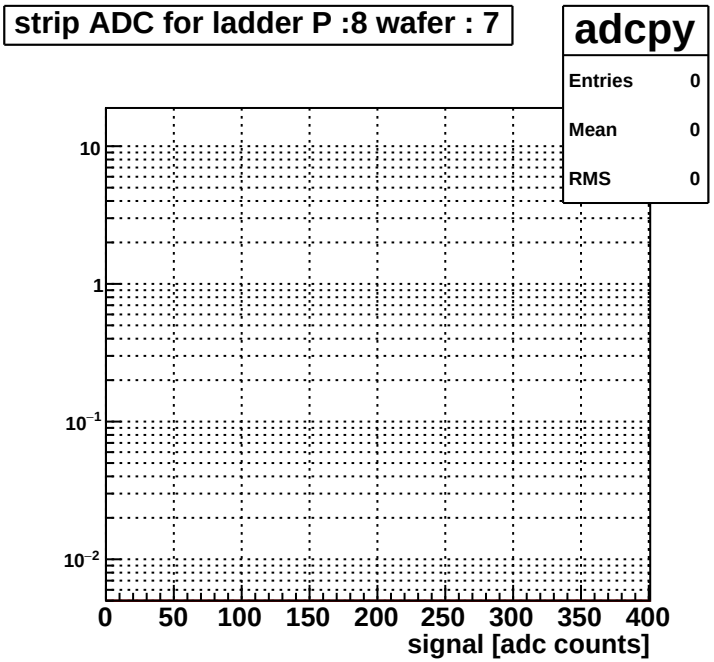

Strip adc vs. channels for ladder N : 8 wafer : 6
stripADCCHANsideN117
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

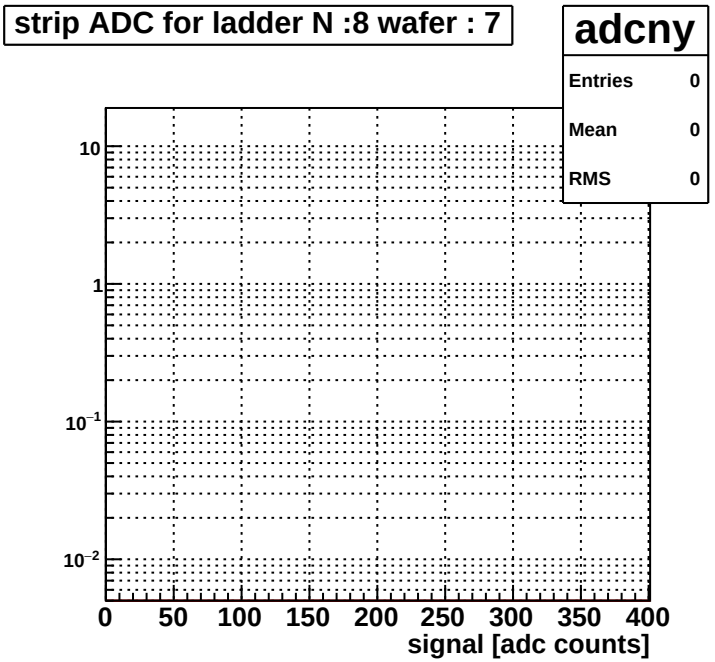

strip ADC for ladder P :8 wafer : 6
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

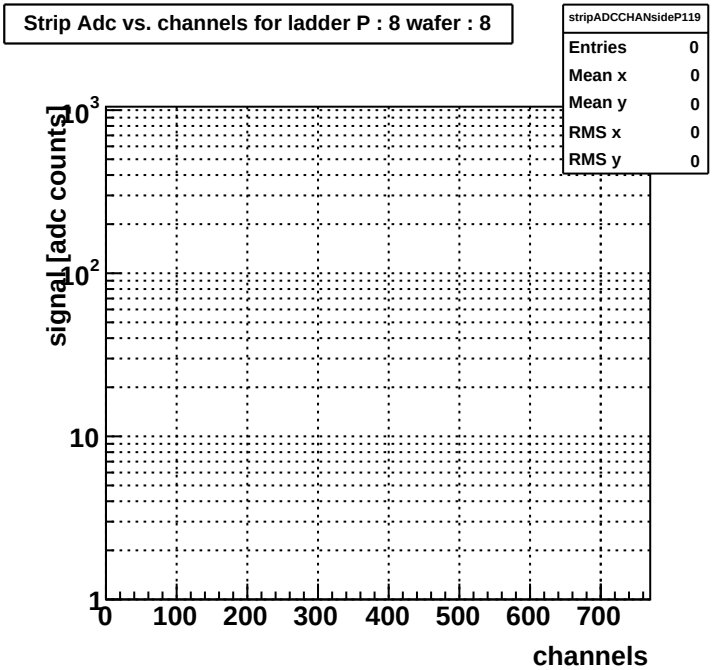

strip ADC for ladder N :8 wafer : 6
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

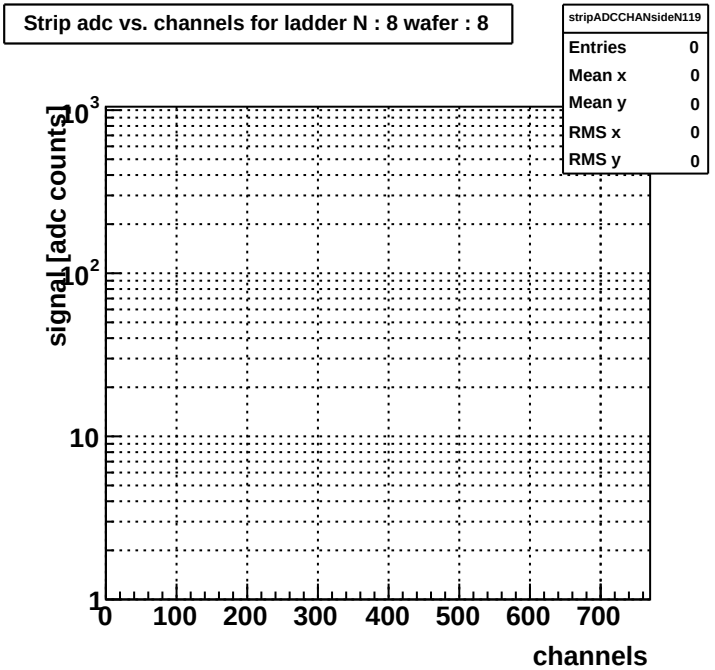

Strip Adc vs. channels for ladder P : 8 wafer : 7
stripADCCHANsideP118
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

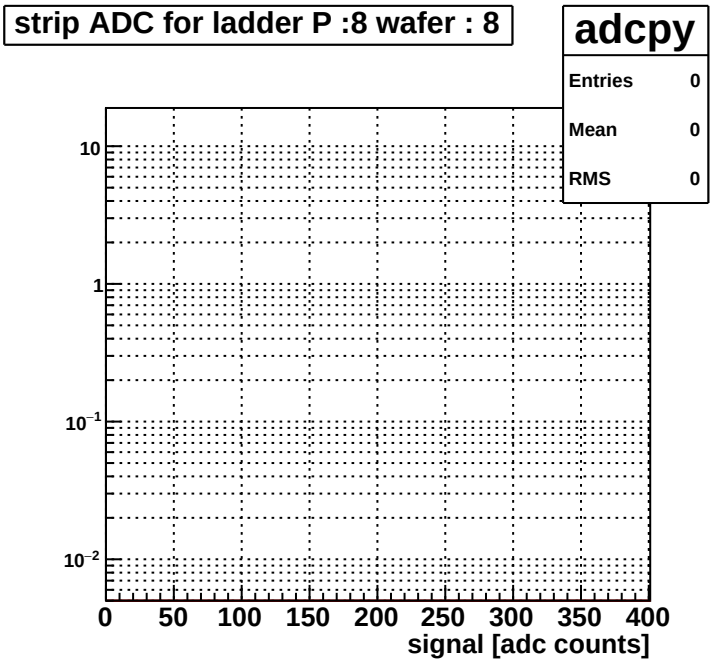

Strip adc vs. channels for ladder N : 8 wafer : 7
stripADCCHANsideN118
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

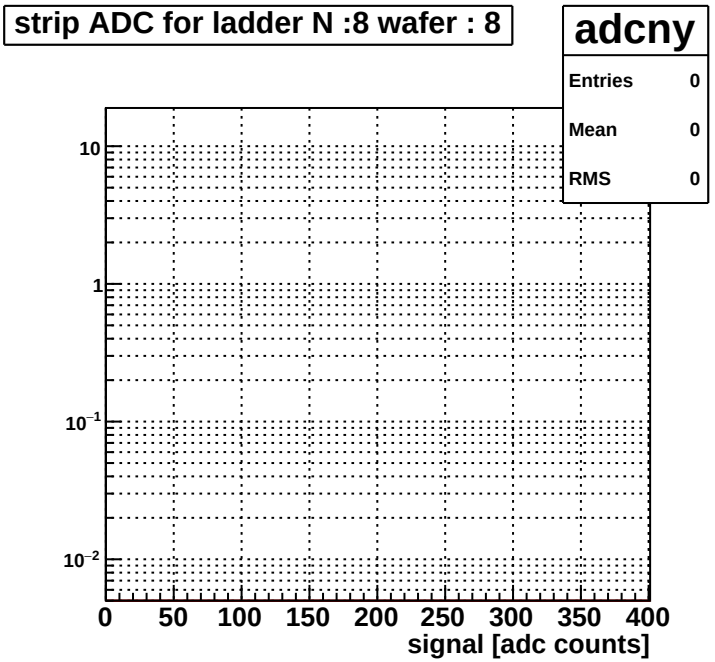

strip ADC for ladder P :8 wafer : 7
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

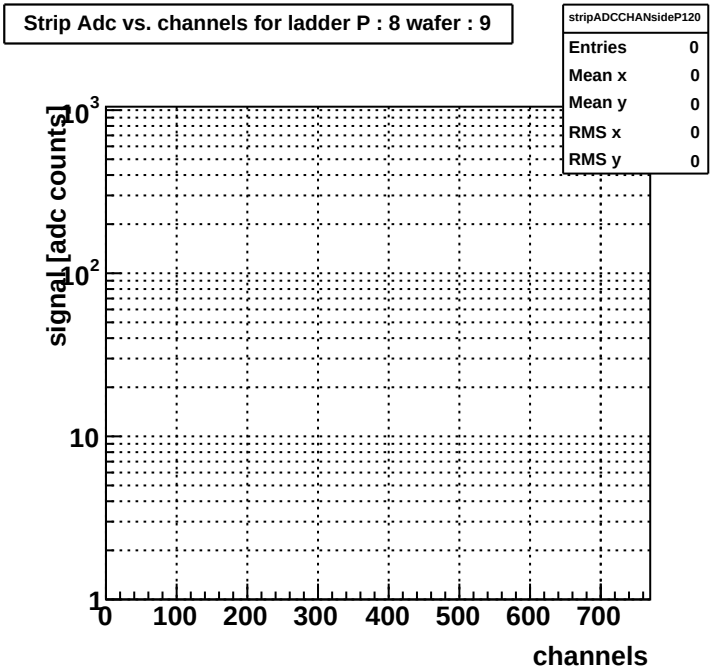

strip ADC for ladder N :8 wafer : 7
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

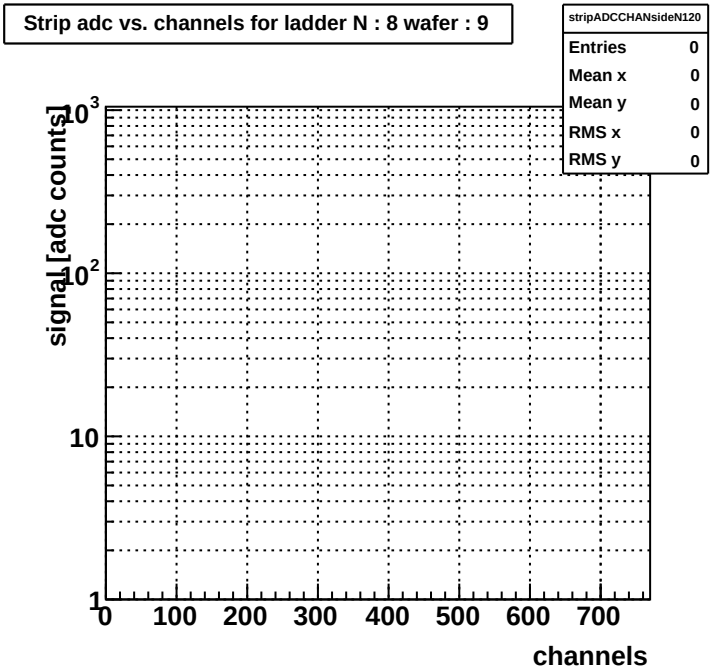

Strip Adc vs. channels for ladder P : 8 wafer : 8
stripADCCHANsideP119
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

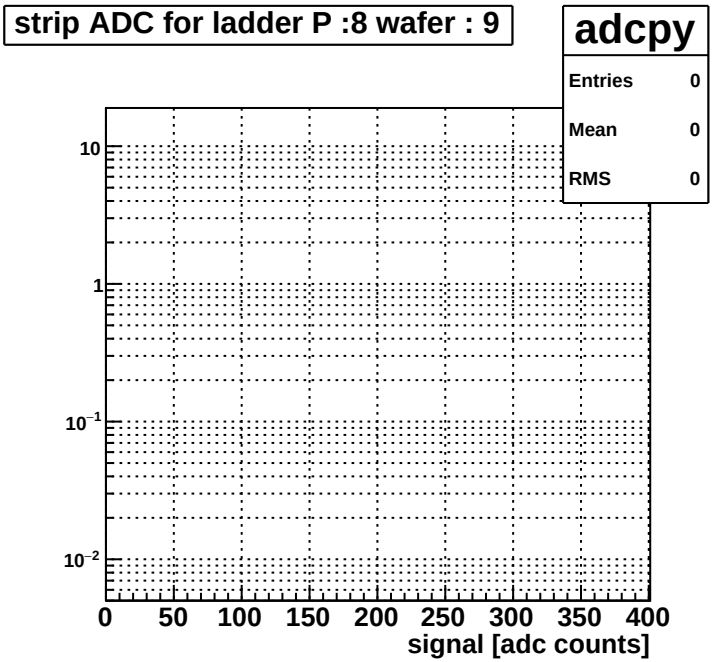

Strip adc vs. channels for ladder N : 8 wafer : 8
stripADCCHANsideN119
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

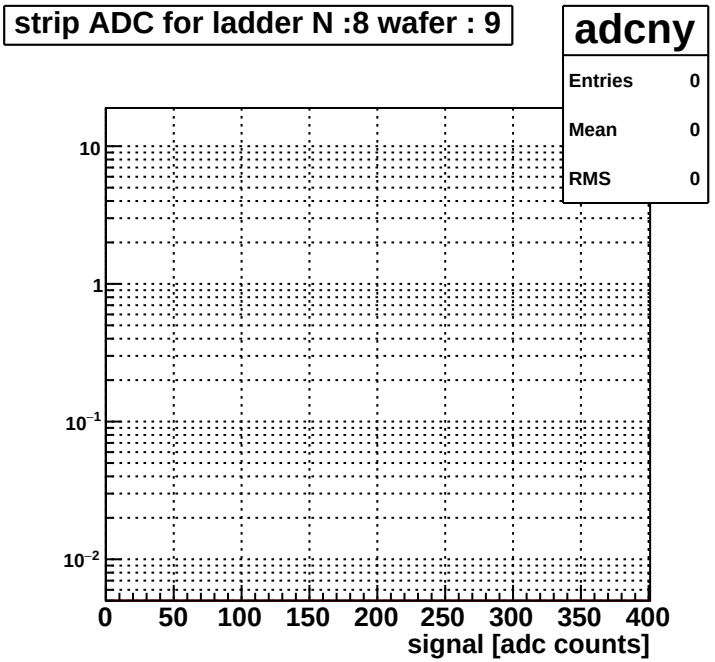

strip ADC for ladder P :8 wafer : 8
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

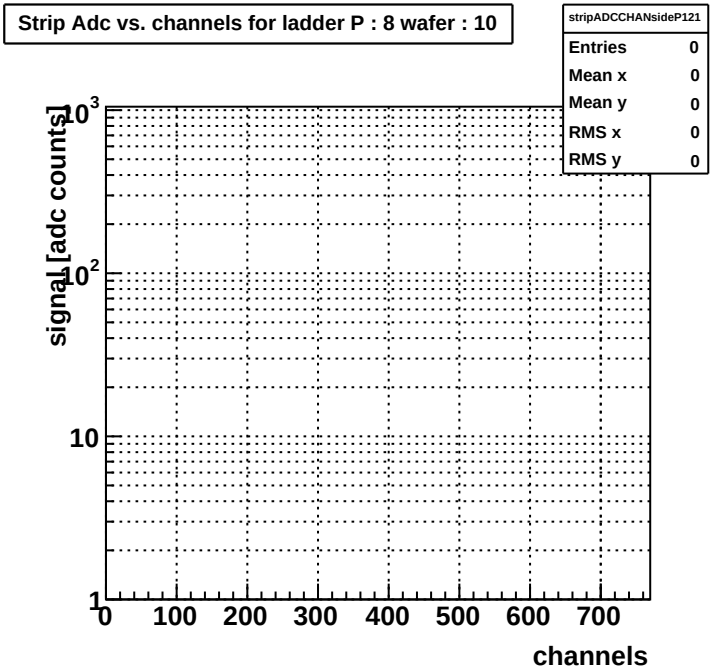

strip ADC for ladder N :8 wafer : 8
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

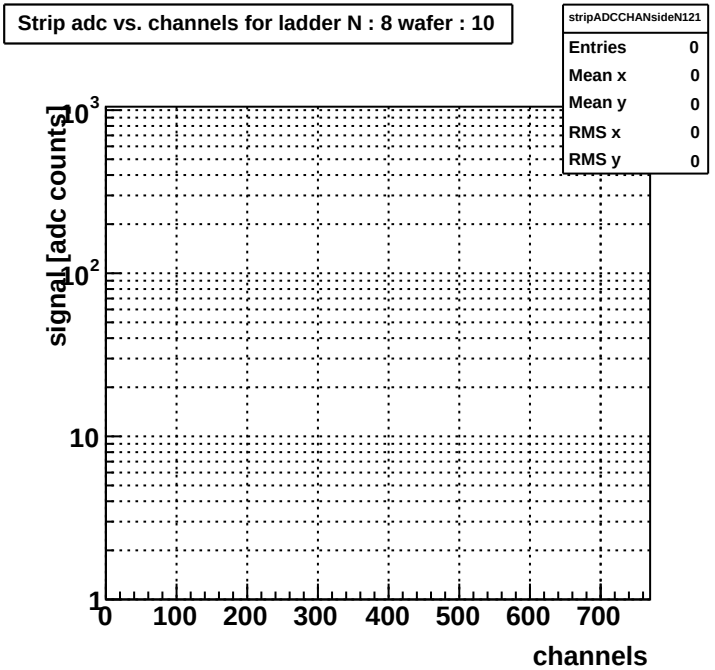

Strip Adc vs. channels for ladder P : 8 wafer : 9
stripADCCHANsideP120
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

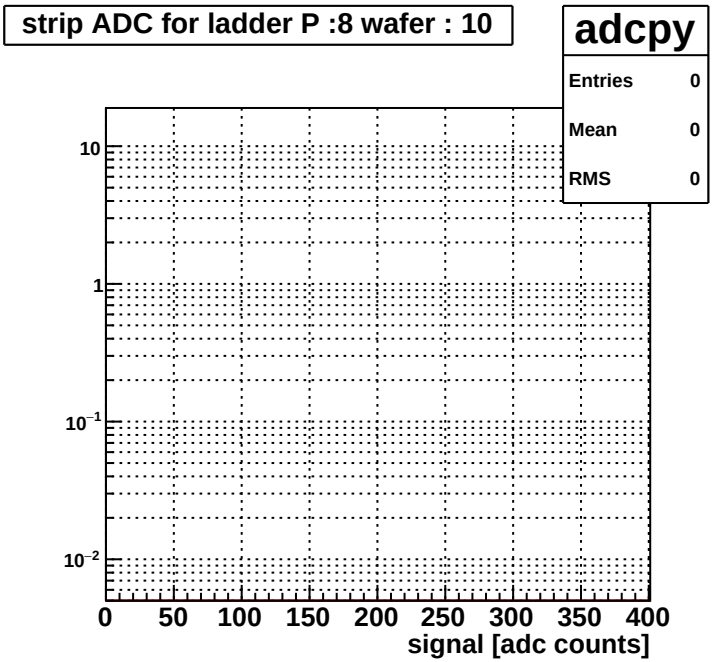

Strip adc vs. channels for ladder N : 8 wafer : 9
stripADCCHANsideN120
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

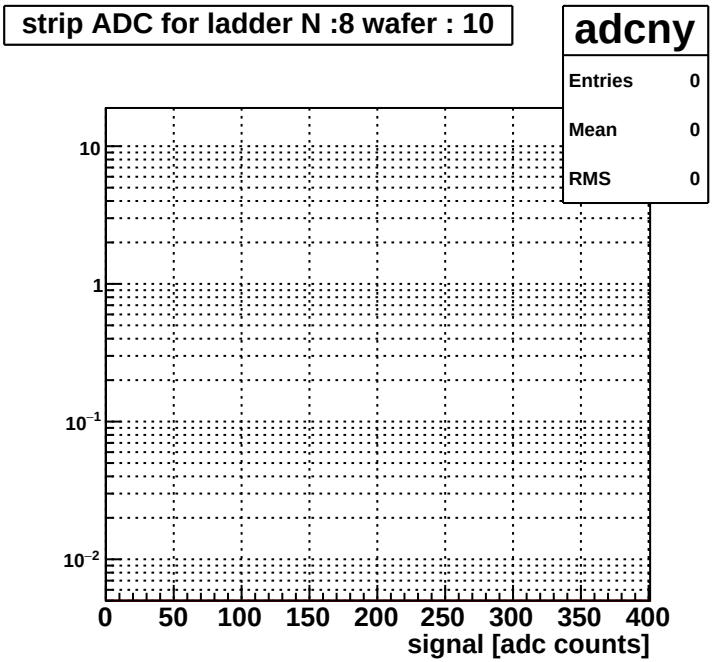

strip ADC for ladder P :8 wafer : 9
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

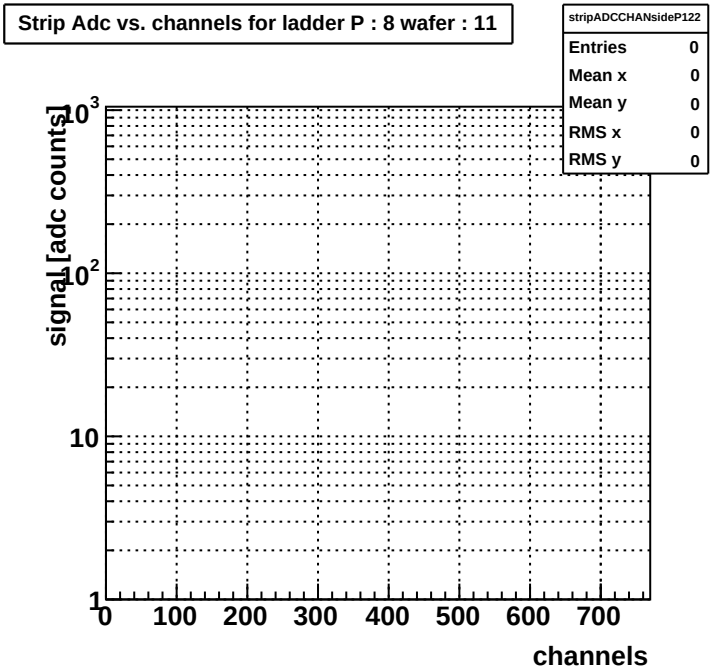

strip ADC for ladder N :8 wafer : 9
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

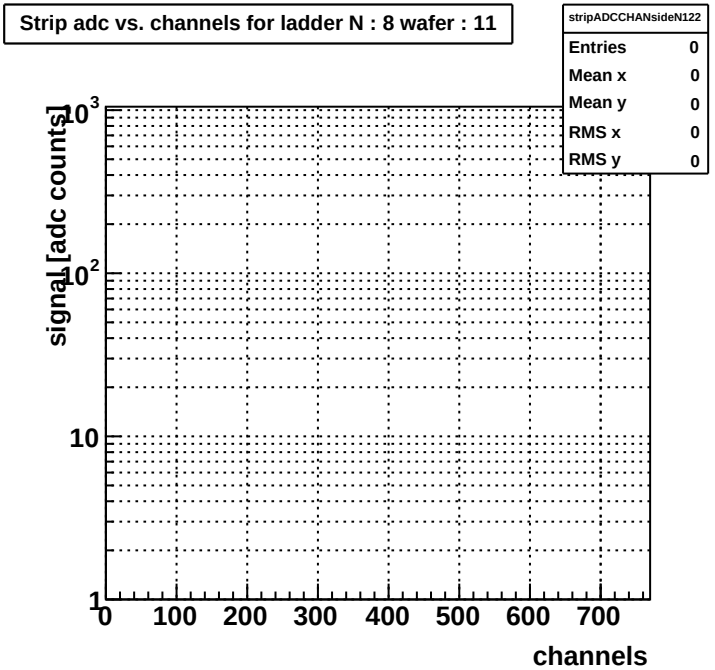

Strip Adc vs. channels for ladder P : 8 wafer : 10
stripADCCHANsideP121
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

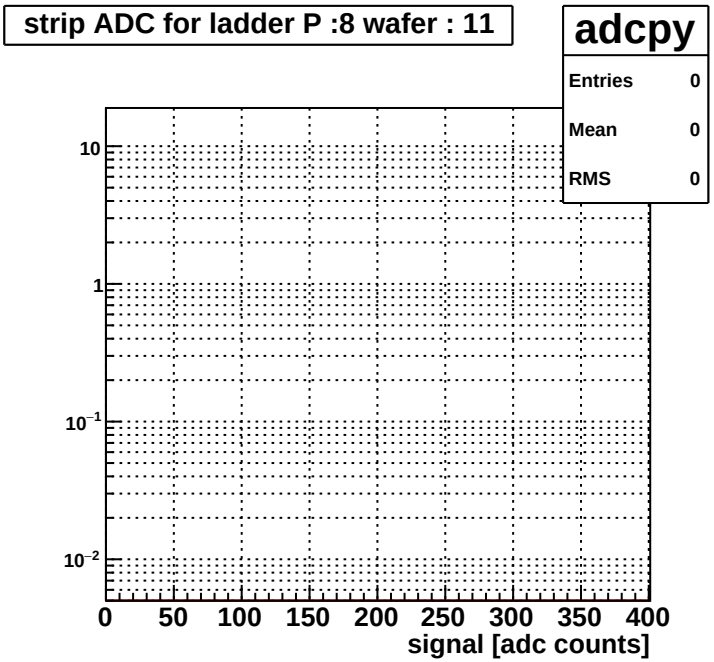

Strip adc vs. channels for ladder N : 8 wafer : 10
stripADCCHANsideN121
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

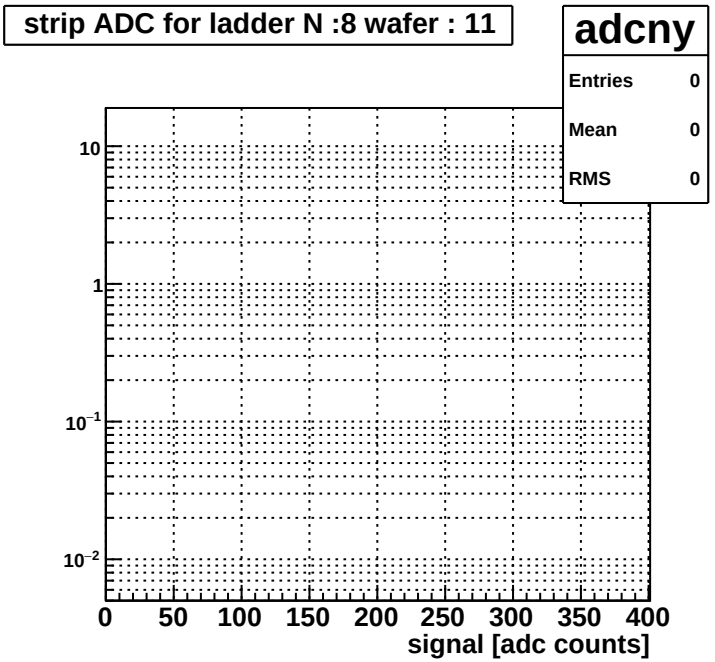

strip ADC for ladder P :8 wafer : 10
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

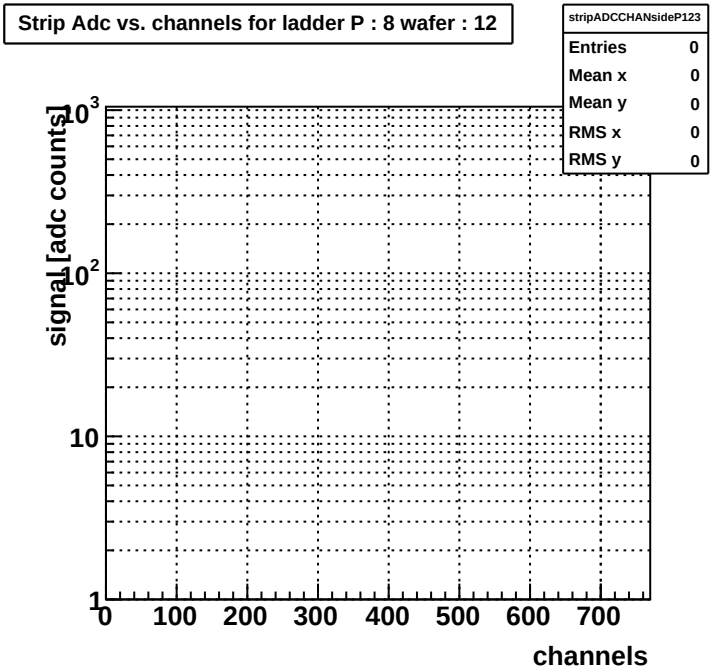

strip ADC for ladder N :8 wafer : 10
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

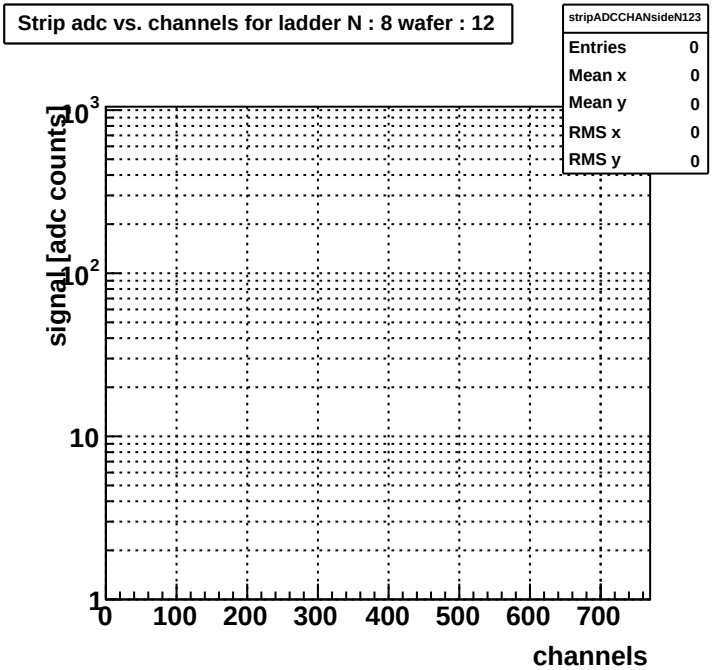

Strip Adc vs. channels for ladder P : 8 wafer : 11
stripADCCHANsideP122
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

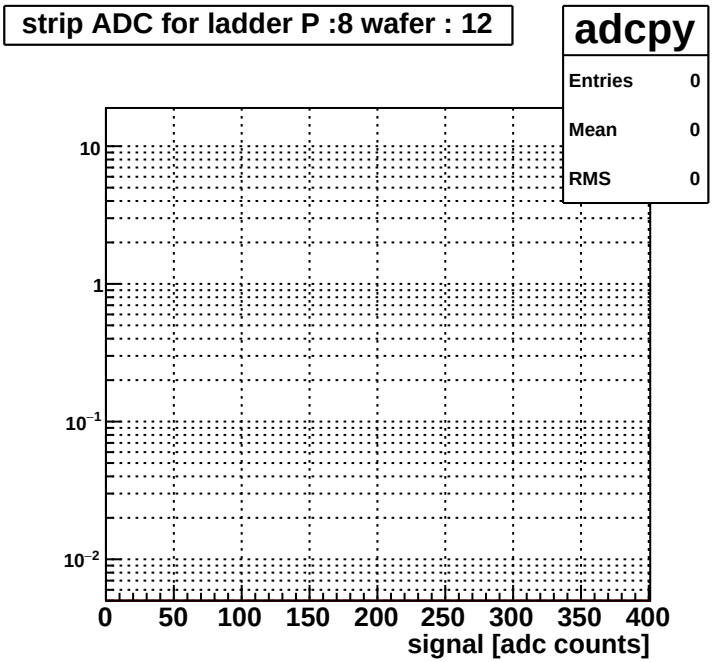

Strip adc vs. channels for ladder N : 8 wafer : 11
stripADCCHANsideN122
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

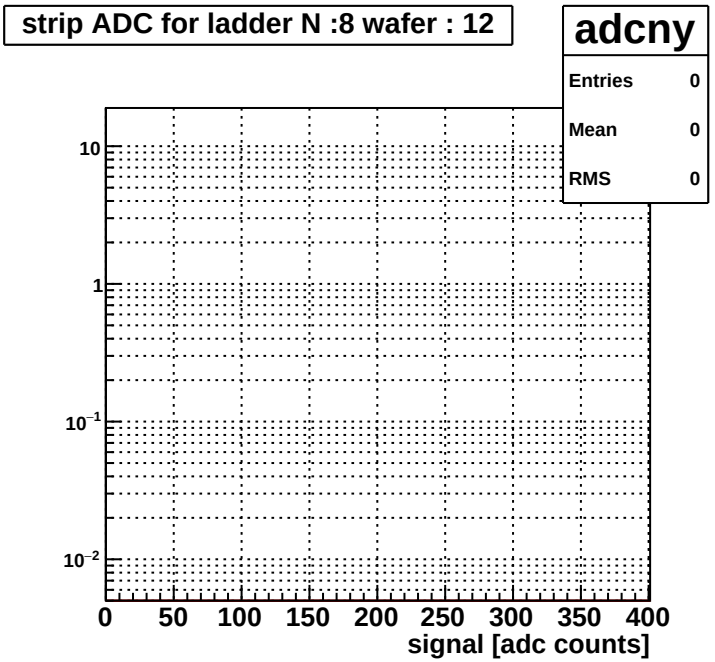

strip ADC for ladder P :8 wafer : 11
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

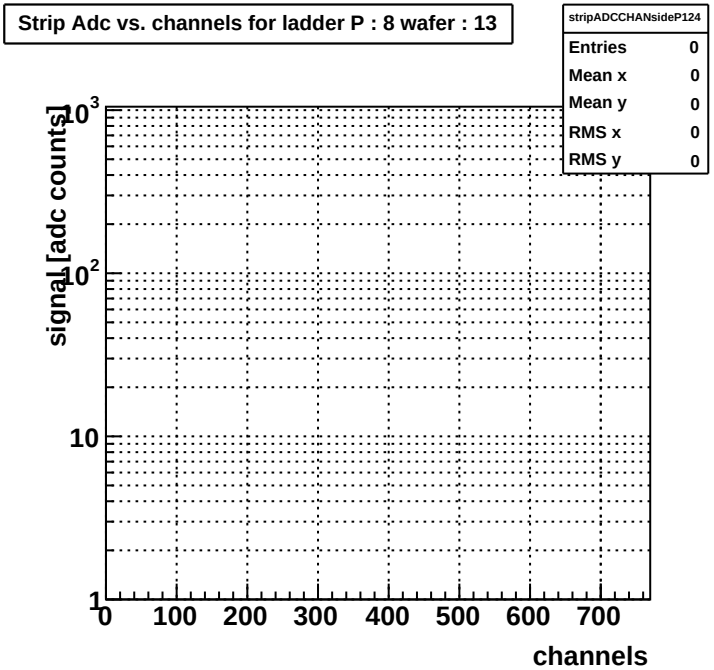

strip ADC for ladder N :8 wafer : 11
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

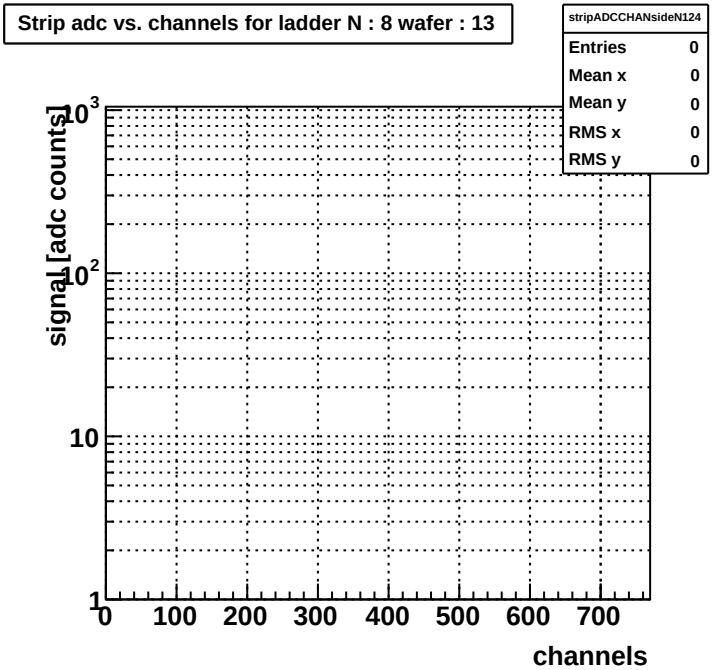

Strip Adc vs. channels for ladder P : 8 wafer : 12
stripADCCHANsideP123
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

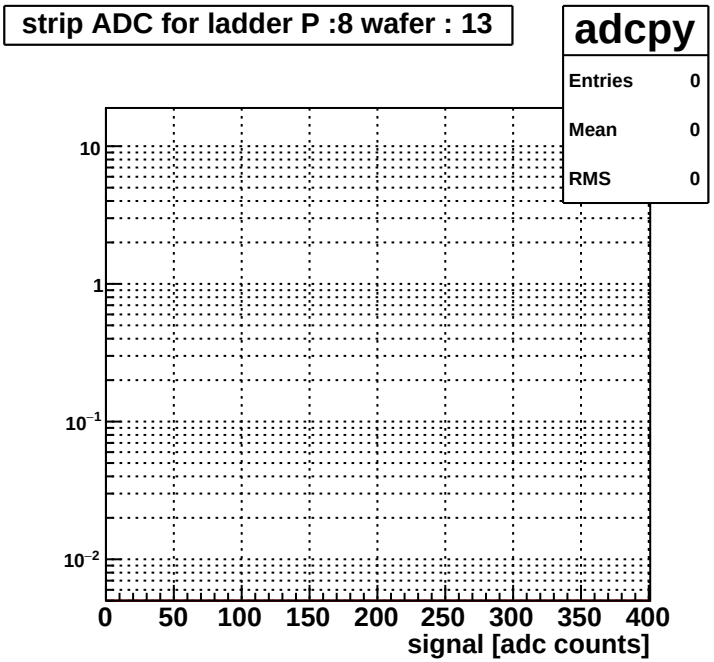

Strip adc vs. channels for ladder N : 8 wafer : 12
stripADCCHANsideN123
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

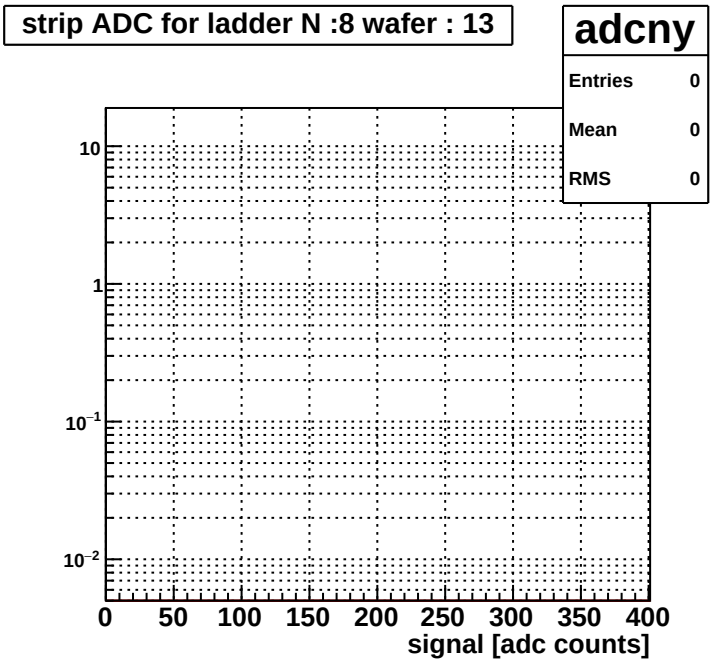

strip ADC for ladder P :8 wafer : 12
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

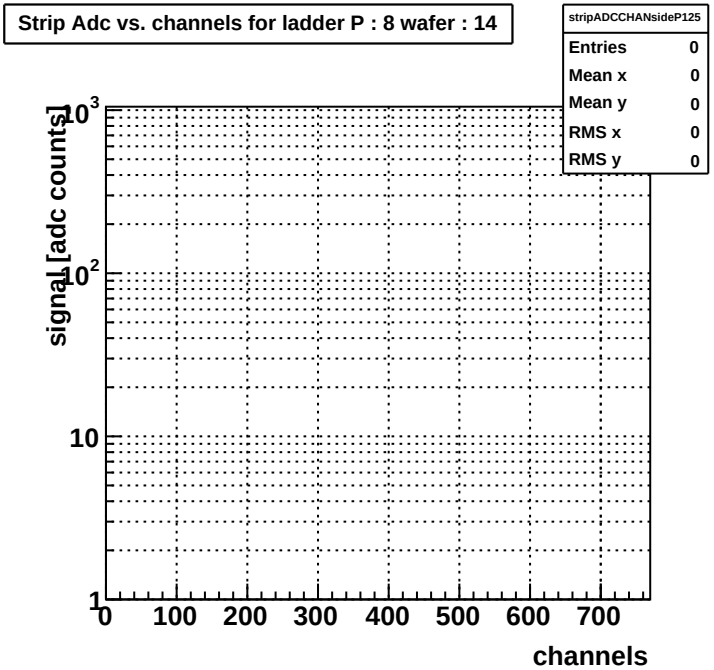

strip ADC for ladder N :8 wafer : 12
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

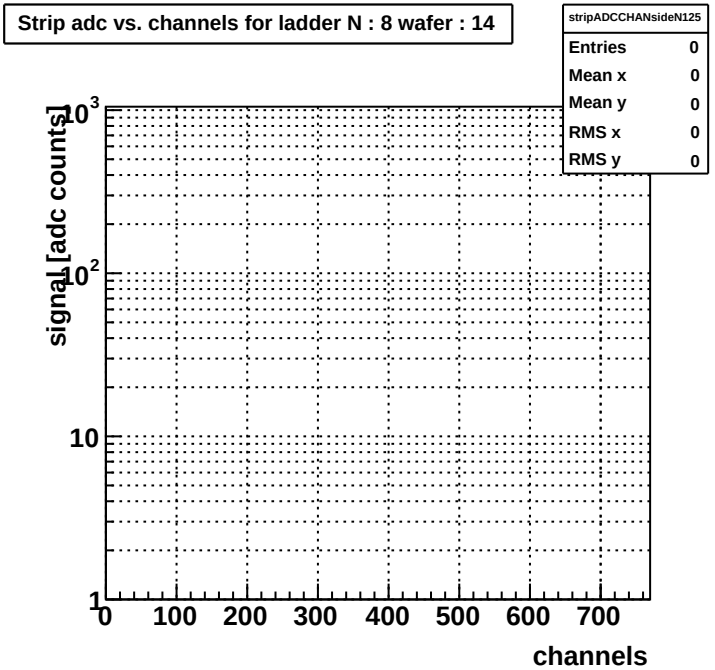

Strip Adc vs. channels for ladder P : 8 wafer : 13
stripADCCHANsideP124
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

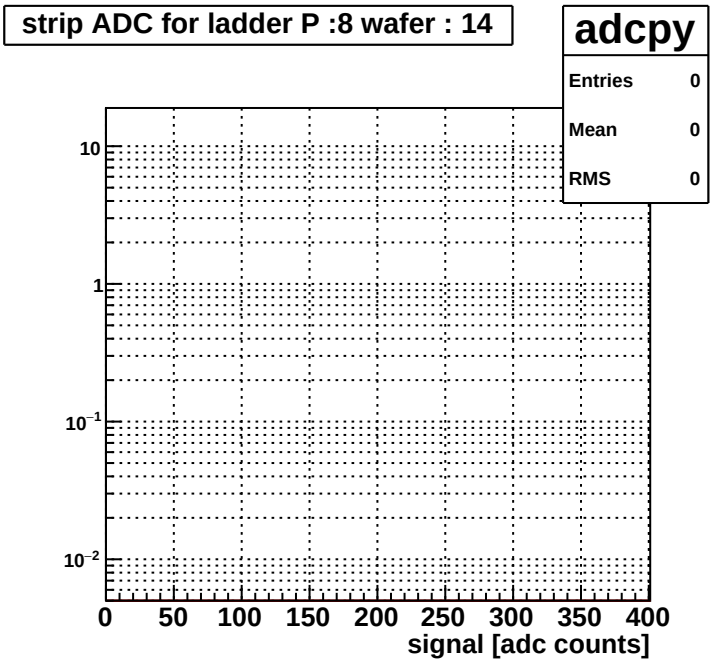

Strip adc vs. channels for ladder N : 8 wafer : 13
stripADCCHANsideN124
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

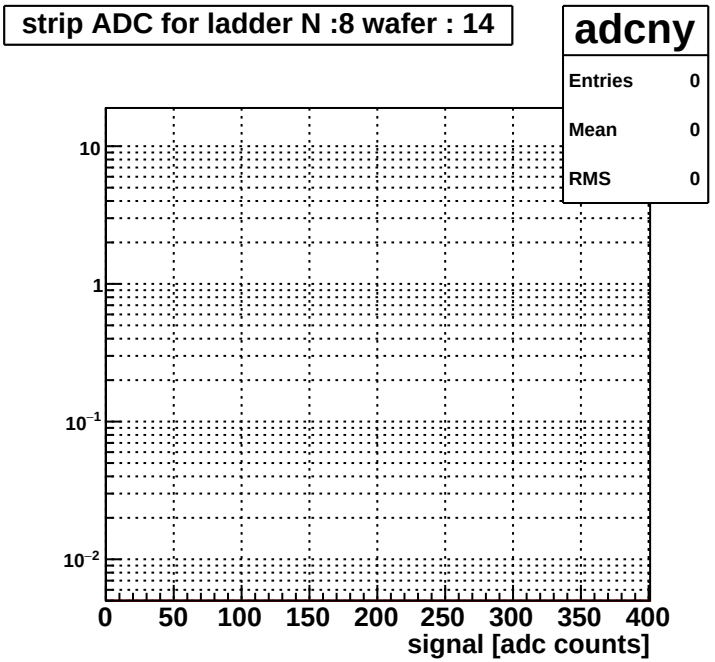

strip ADC for ladder P :8 wafer : 13
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

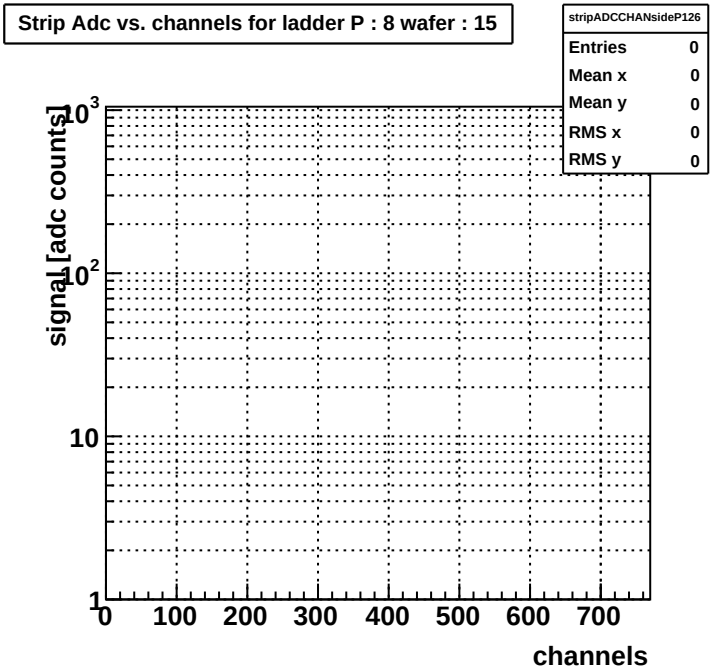

strip ADC for ladder N :8 wafer : 13
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

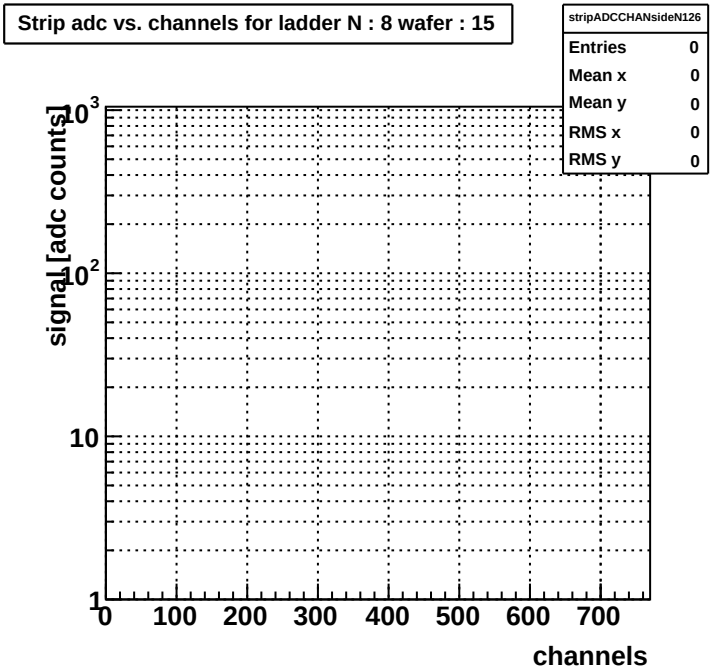

Strip Adc vs. channels for ladder P : 8 wafer : 14
stripADCCHANsideP125
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

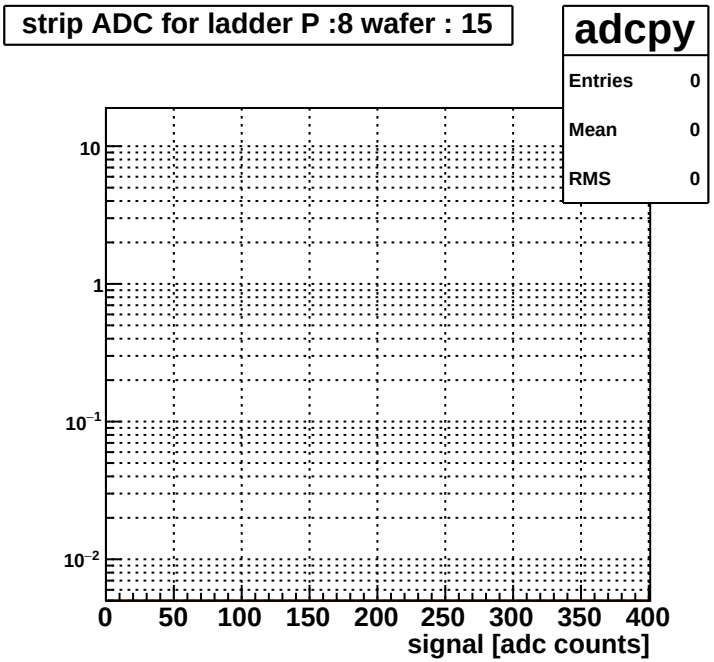

Strip adc vs. channels for ladder N : 8 wafer : 14
stripADCCHANsideN125
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

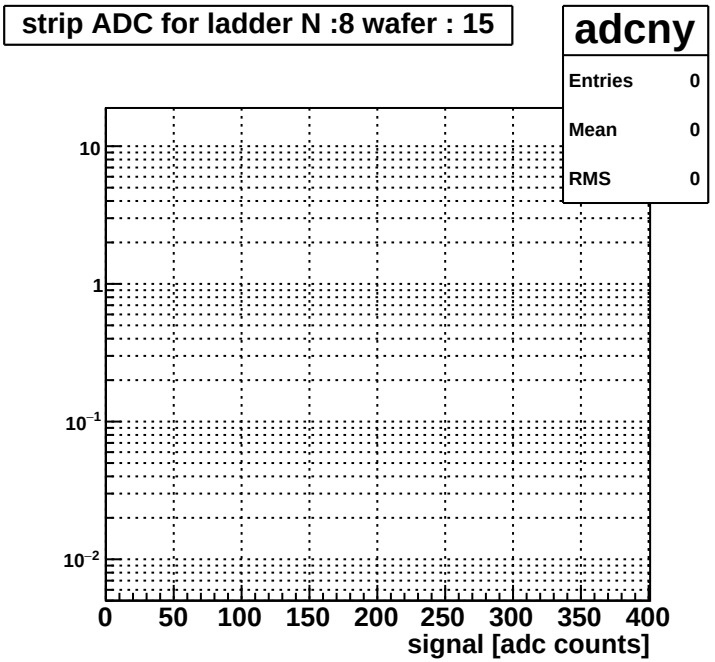

strip ADC for ladder P :8 wafer : 14
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

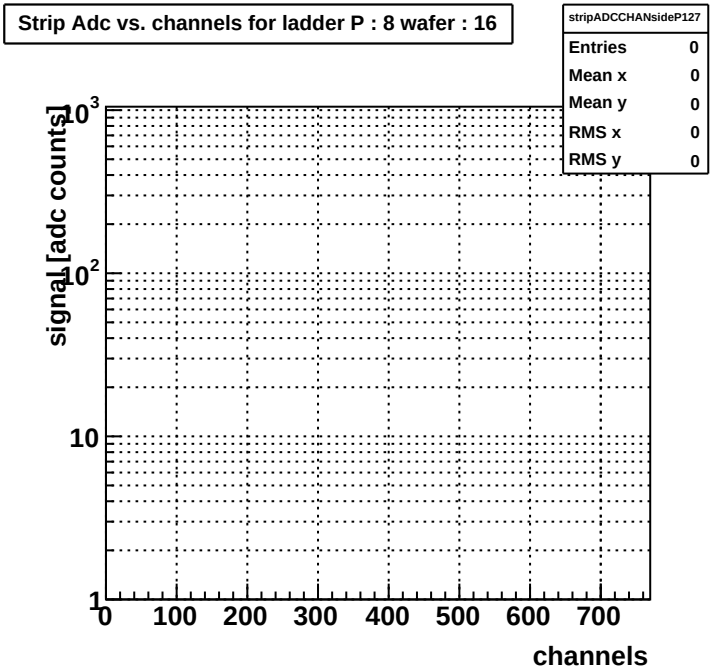

strip ADC for ladder N :8 wafer : 14
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

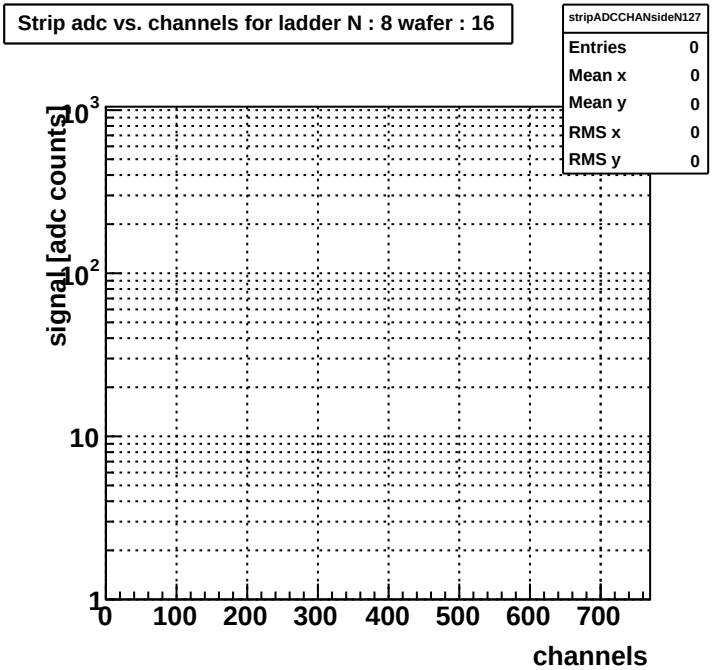

Strip Adc vs. channels for ladder P : 8 wafer : 15
stripADCCHANsideP126
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

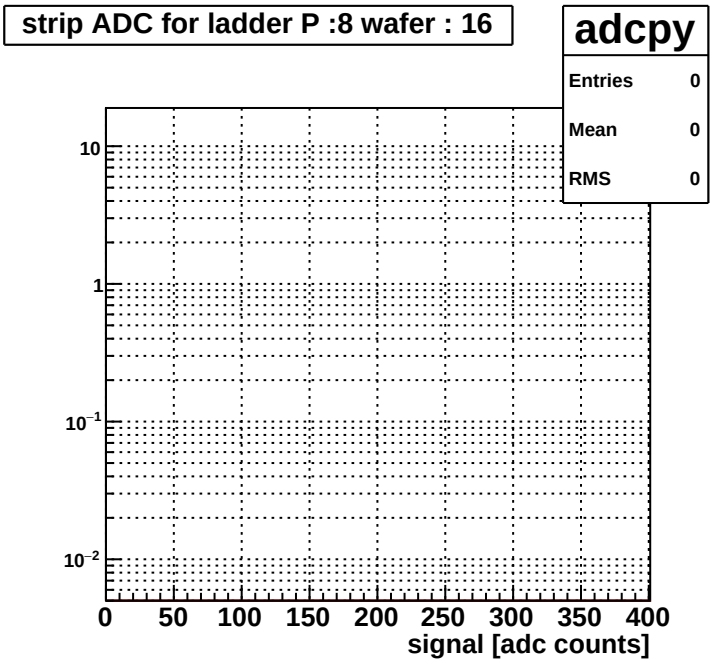

Strip adc vs. channels for ladder N : 8 wafer : 15
stripADCCHANsideN126
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

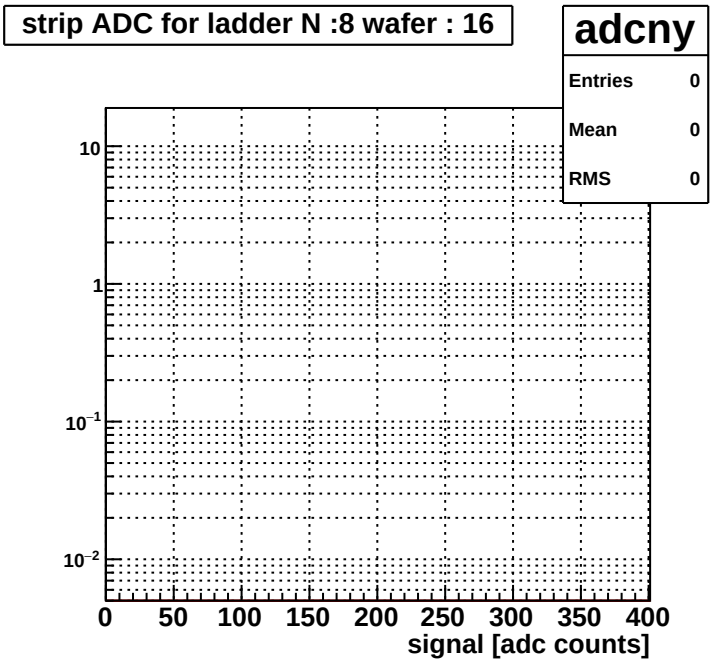

strip ADC for ladder P :8 wafer : 15
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

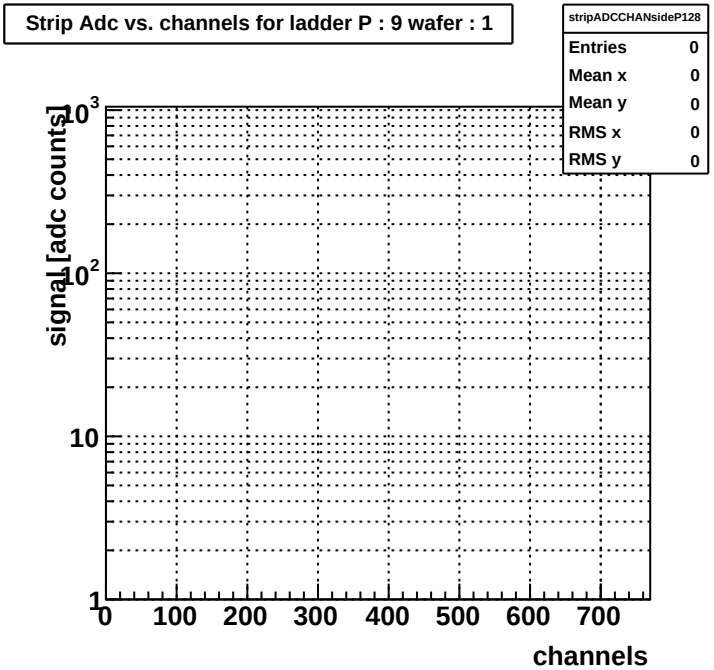

strip ADC for ladder N :8 wafer : 15
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

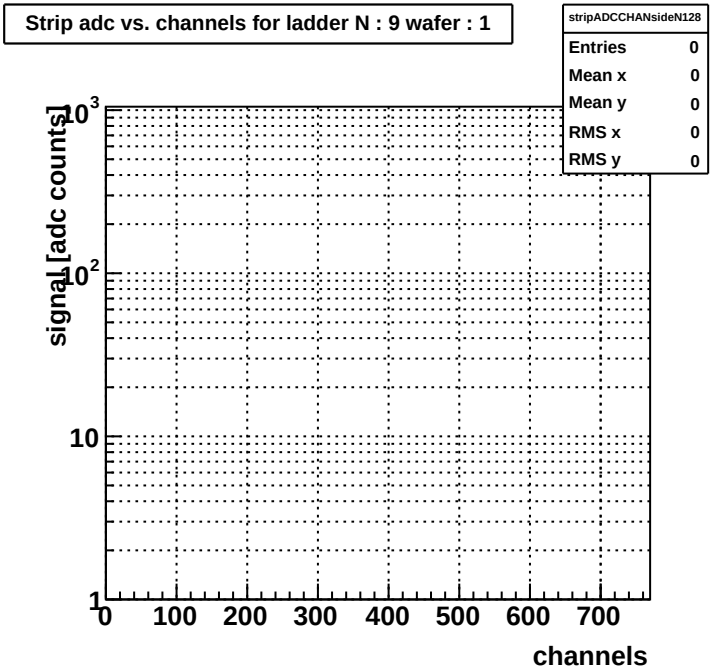

Strip Adc vs. channels for ladder P : 8 wafer : 16
stripADCCHANsideP127
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

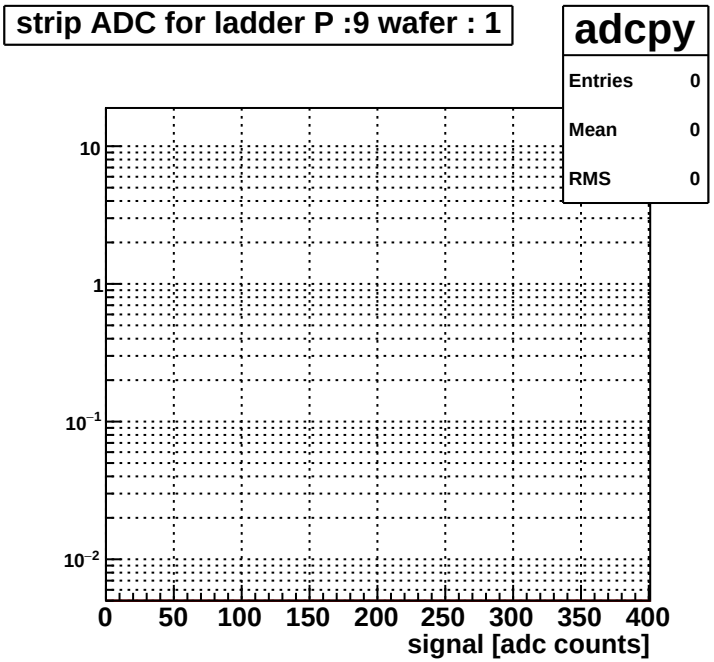

Strip adc vs. channels for ladder N : 8 wafer : 16
stripADCCHANsideN127
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

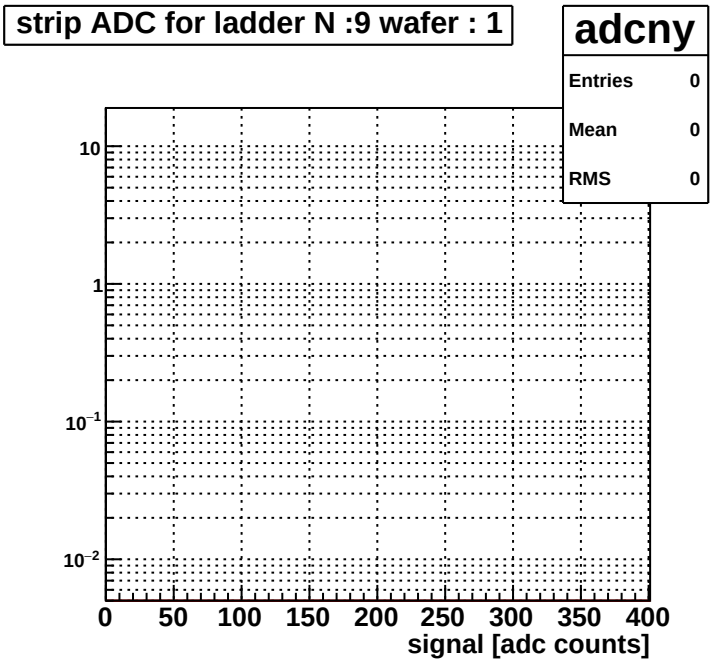

strip ADC for ladder P :8 wafer : 16
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

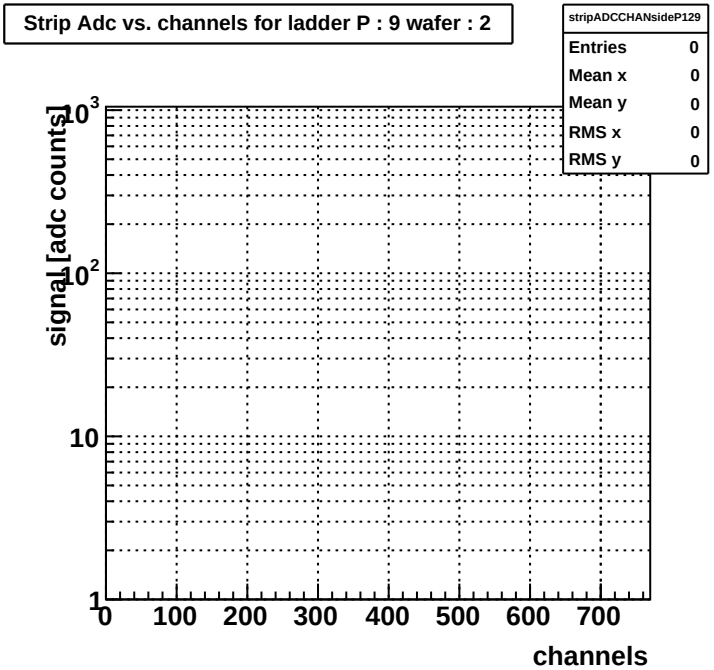

strip ADC for ladder N :8 wafer : 16
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

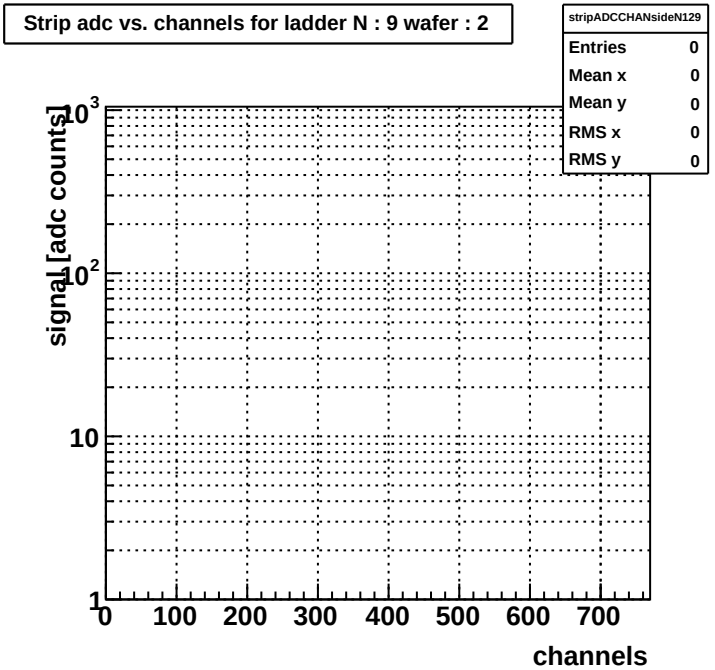

Strip Adc vs. channels for ladder P : 9 wafer : 1
stripADCCHANsideP128
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

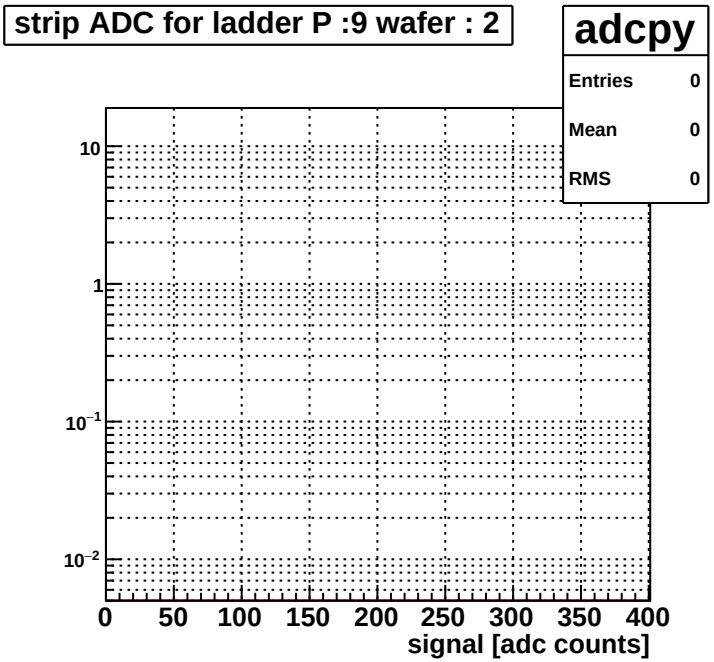

Strip adc vs. channels for ladder N : 9 wafer : 1
stripADCCHANsideN128
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

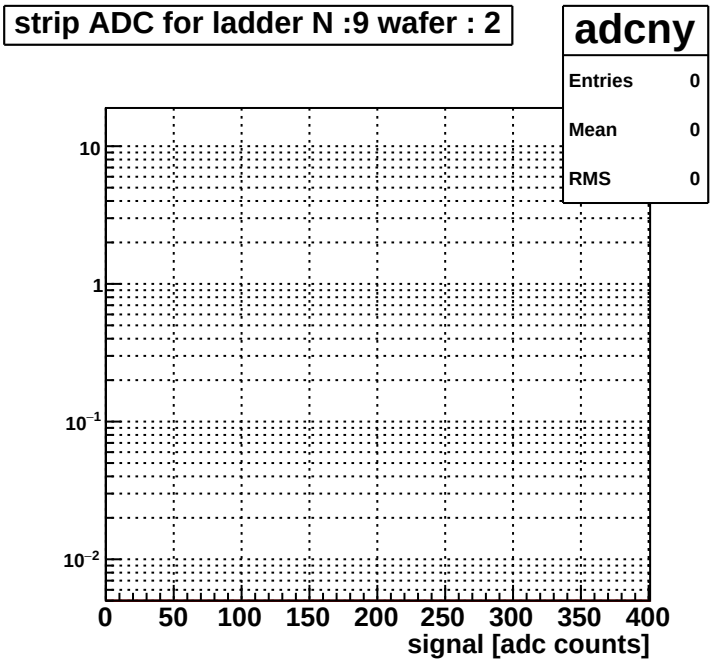

strip ADC for ladder P :9 wafer : 1
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

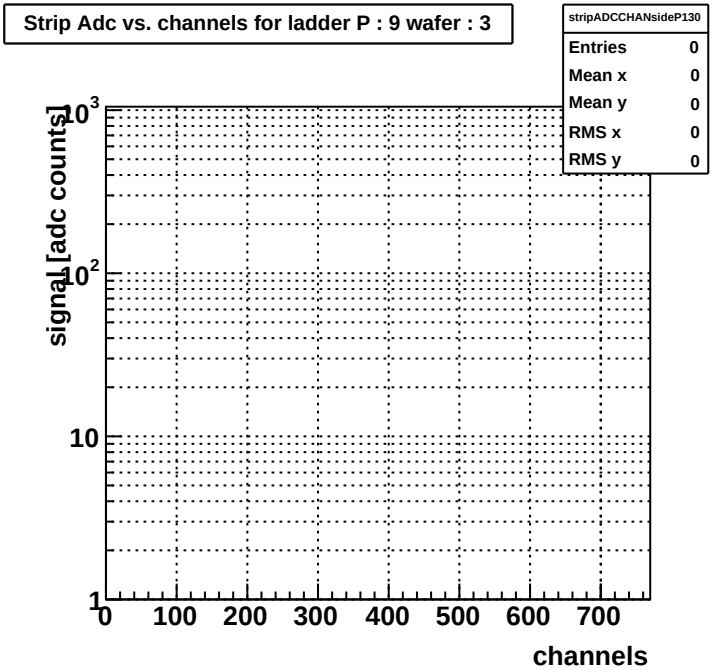

strip ADC for ladder N :9 wafer : 1
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

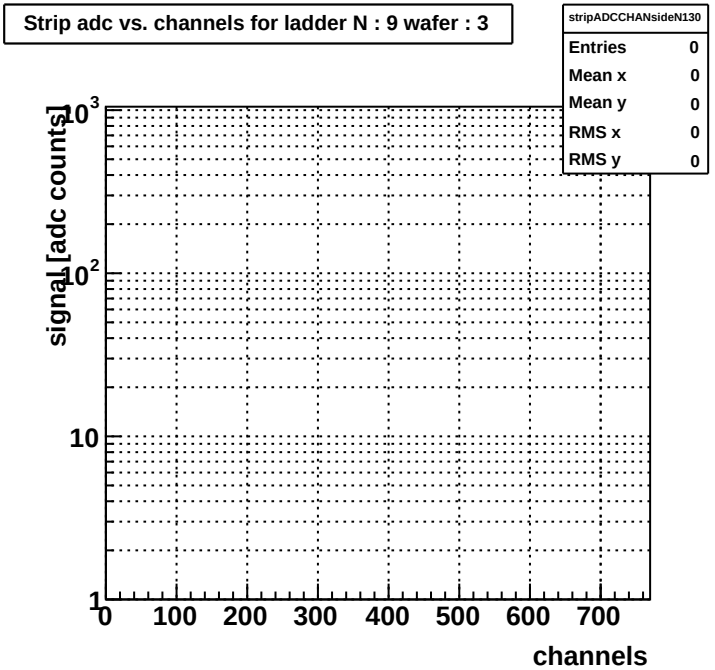

Strip Adc vs. channels for ladder P : 9 wafer : 2
stripADCCHANsideP129
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

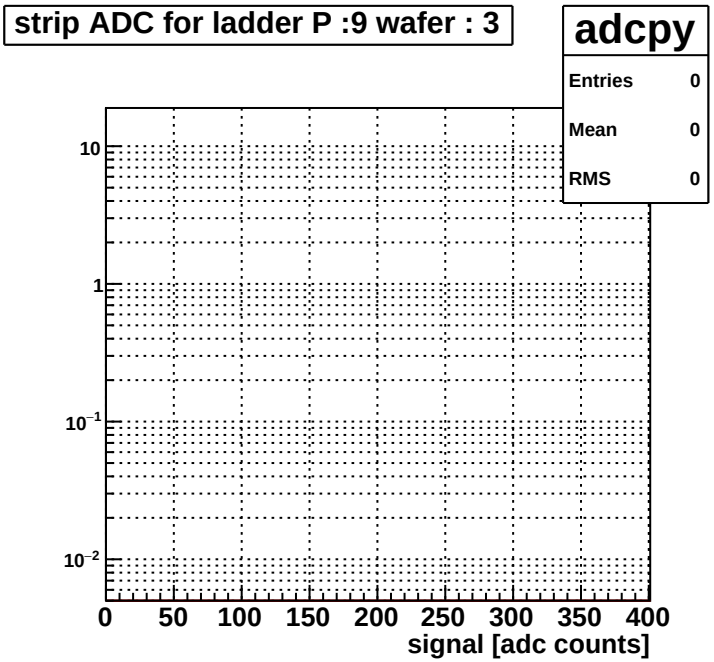

Strip adc vs. channels for ladder N : 9 wafer : 2
stripADCCHANsideN129
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

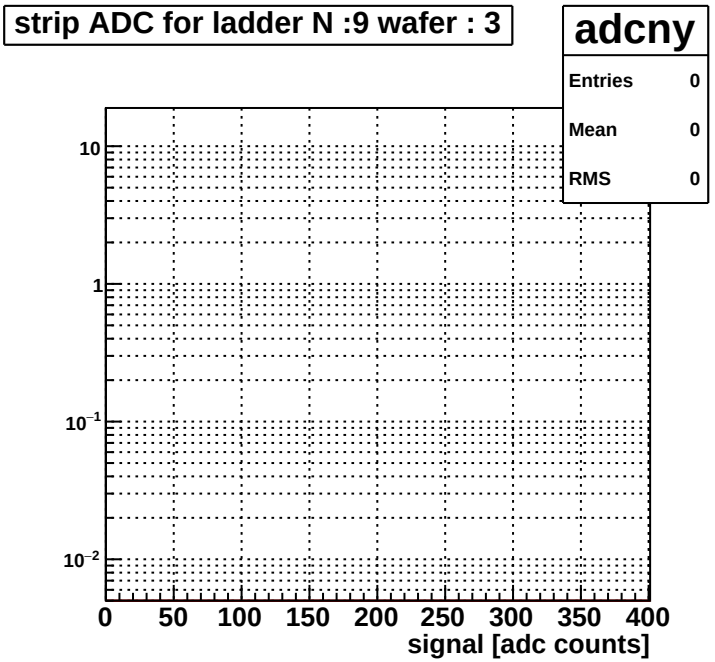

strip ADC for ladder P :9 wafer : 2
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

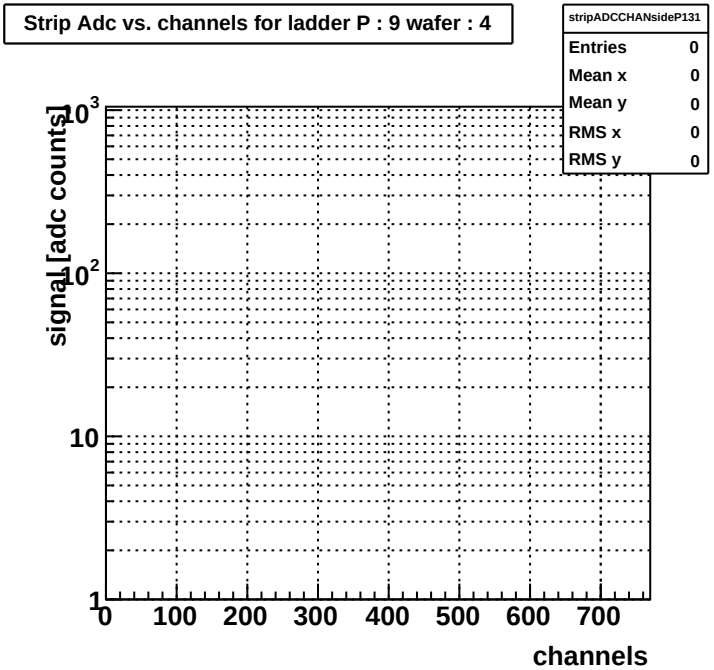

strip ADC for ladder N :9 wafer : 2
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

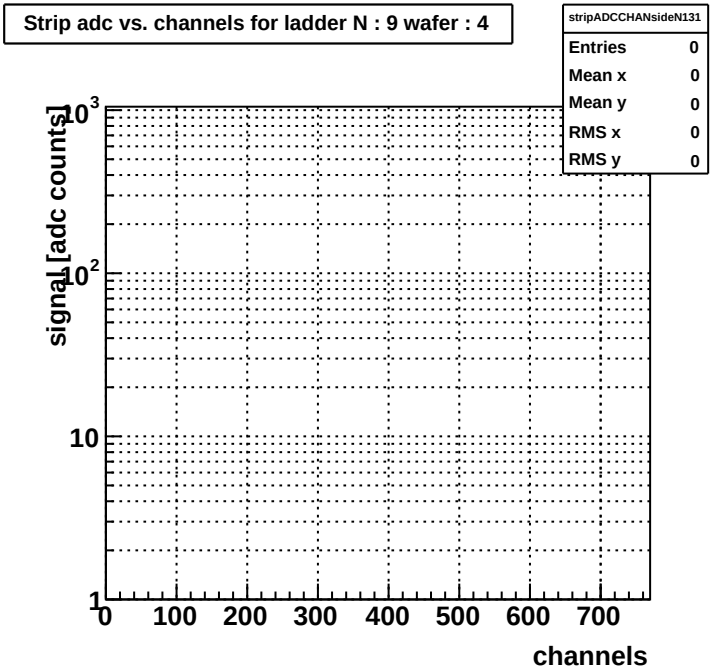

Strip Adc vs. channels for ladder P : 9 wafer : 3
stripADCCHANsideP130
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

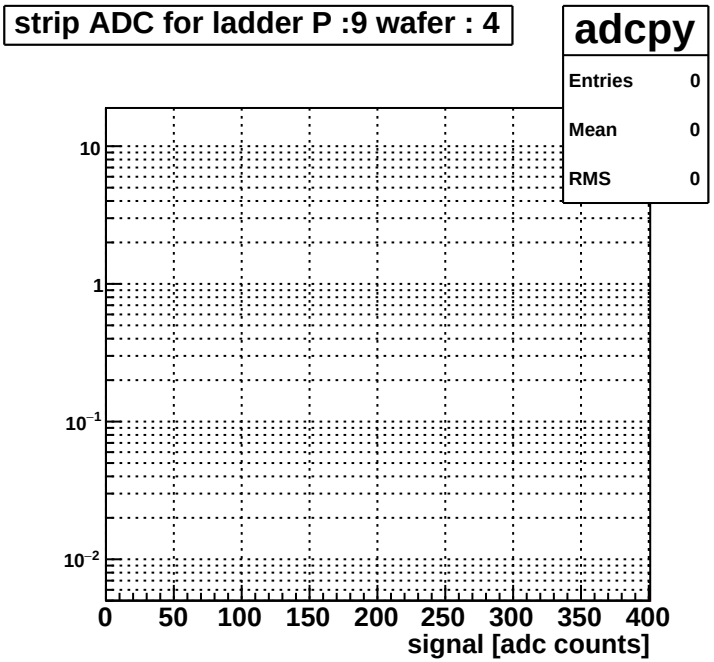

Strip adc vs. channels for ladder N : 9 wafer : 3
stripADCCHANsideN130
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

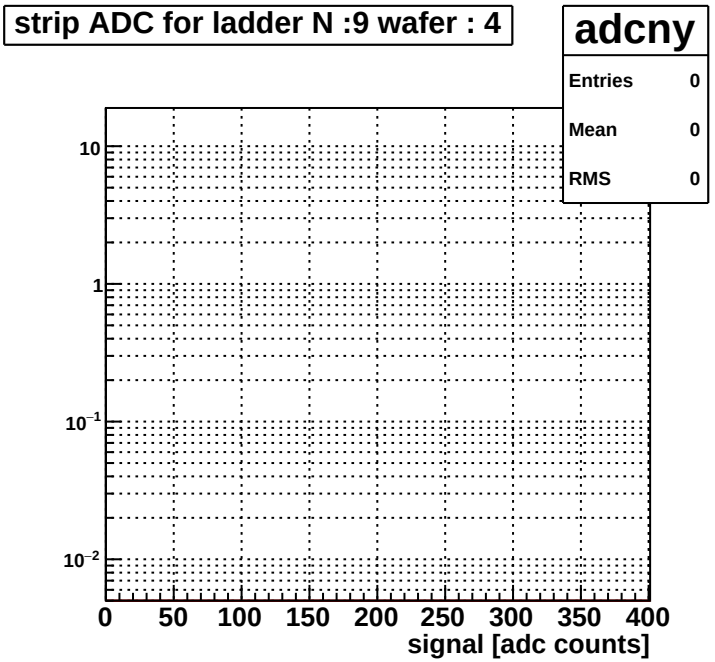

strip ADC for ladder P :9 wafer : 3
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

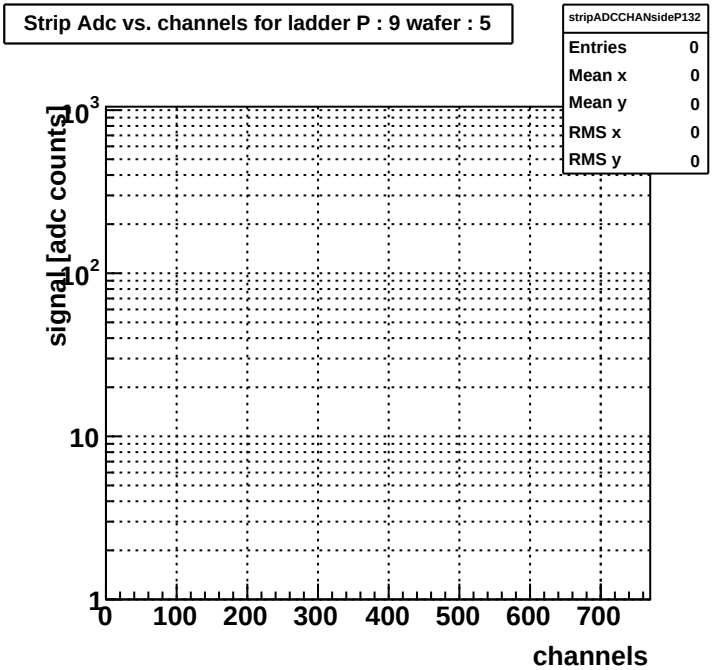

strip ADC for ladder N :9 wafer : 3
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

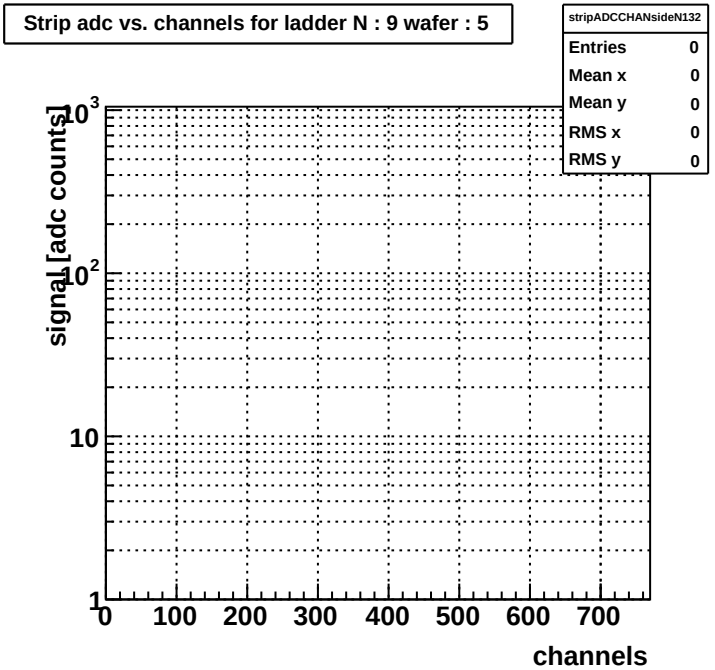

Strip Adc vs. channels for ladder P : 9 wafer : 4
stripADCCHANsideP131
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

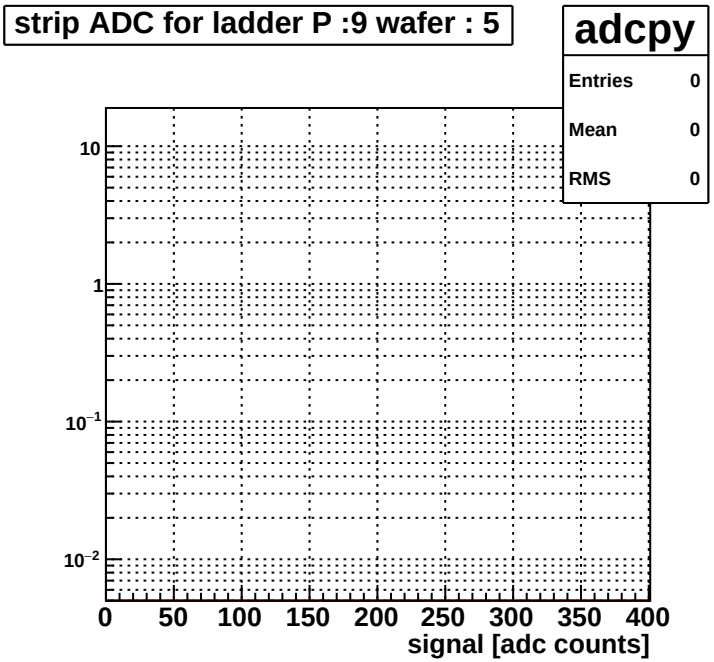

Strip adc vs. channels for ladder N : 9 wafer : 4
stripADCCHANsideN131
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

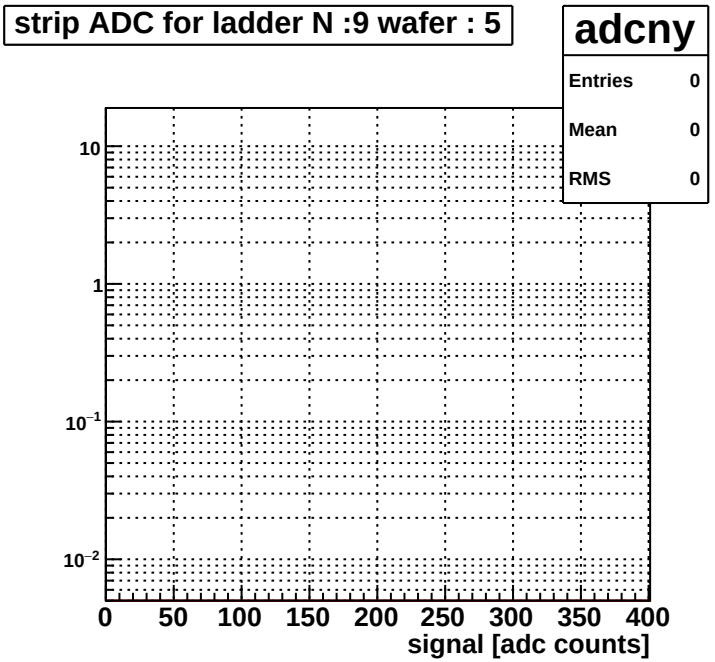

strip ADC for ladder P :9 wafer : 4
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

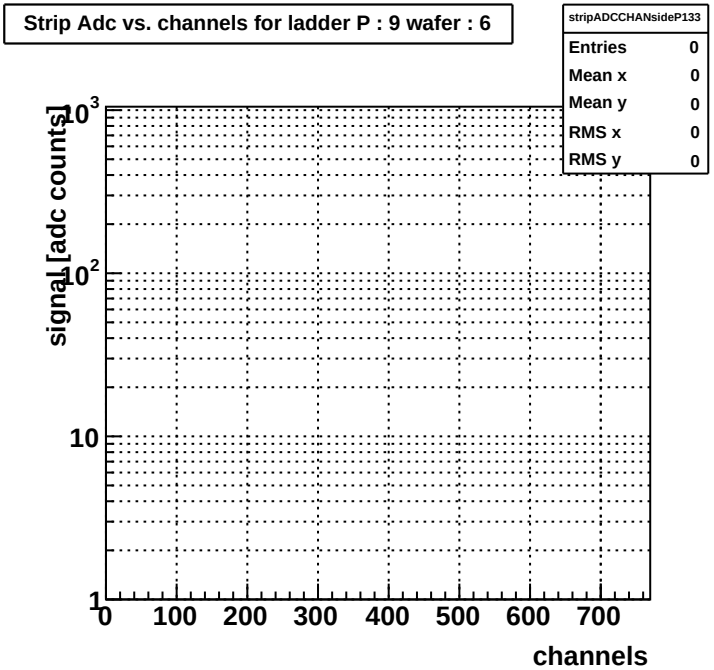

strip ADC for ladder N :9 wafer : 4
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

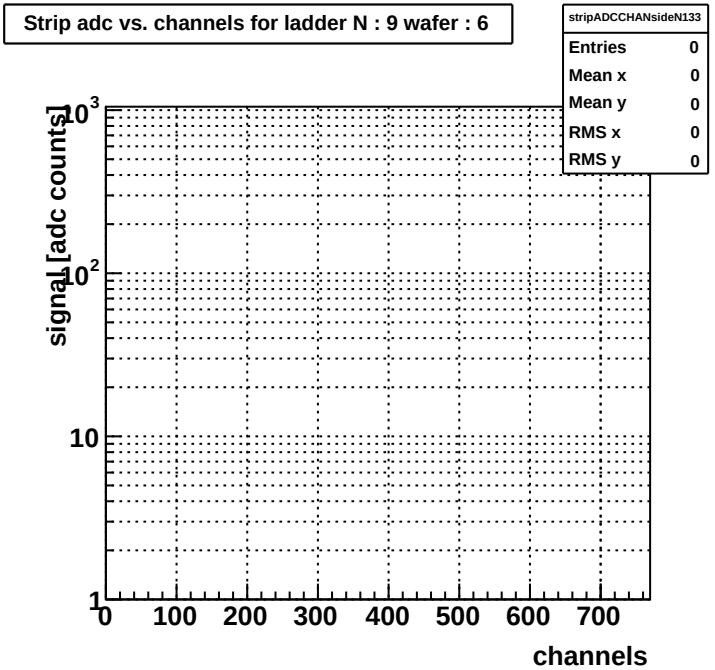

Strip Adc vs. channels for ladder P : 9 wafer : 5
stripADCCHANsideP132
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

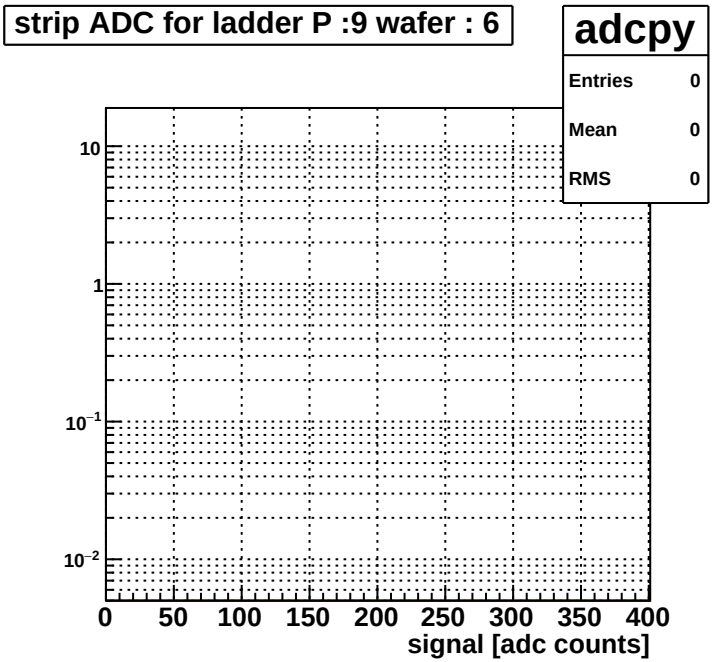

Strip adc vs. channels for ladder N : 9 wafer : 5
stripADCCHANsideN132
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

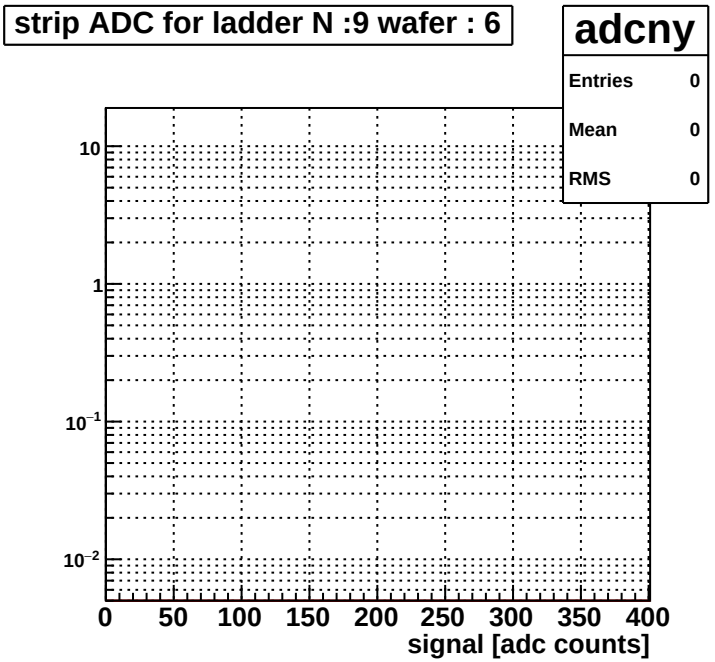

strip ADC for ladder P :9 wafer : 5
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

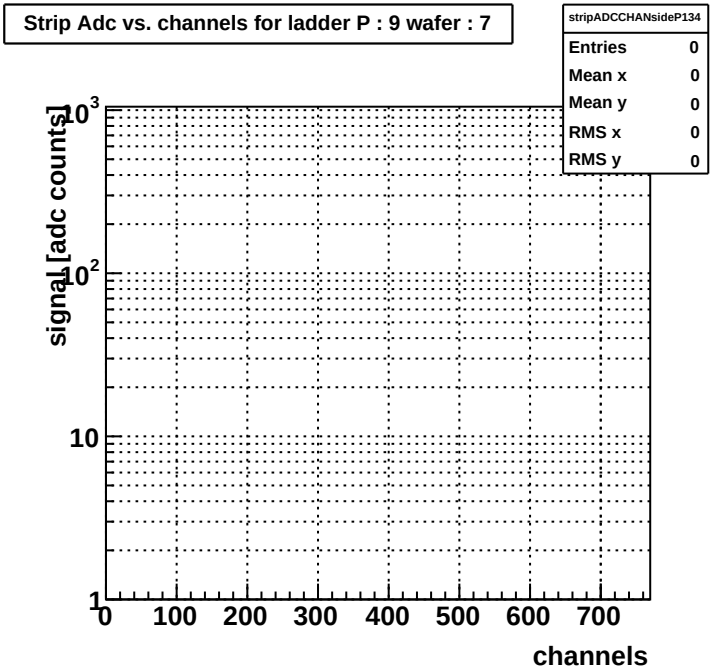

strip ADC for ladder N :9 wafer : 5
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

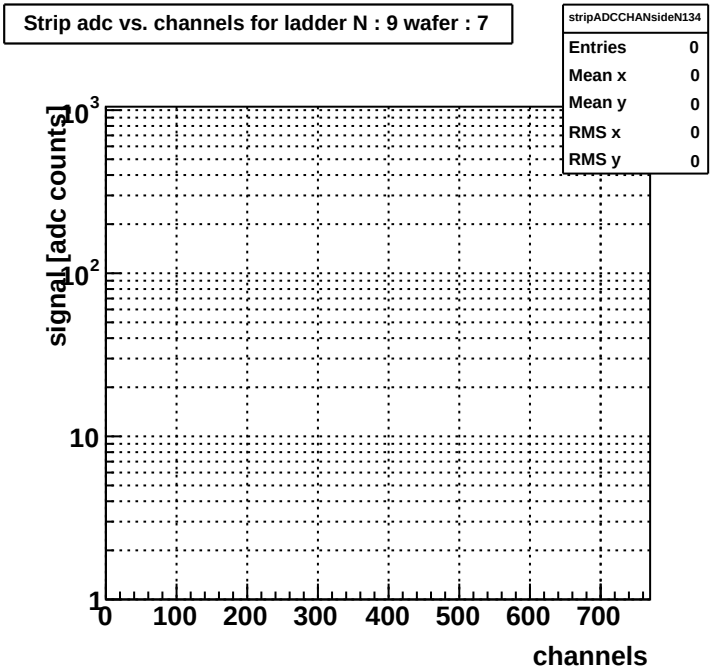

Strip Adc vs. channels for ladder P : 9 wafer : 6
stripADCCHANsideP133
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

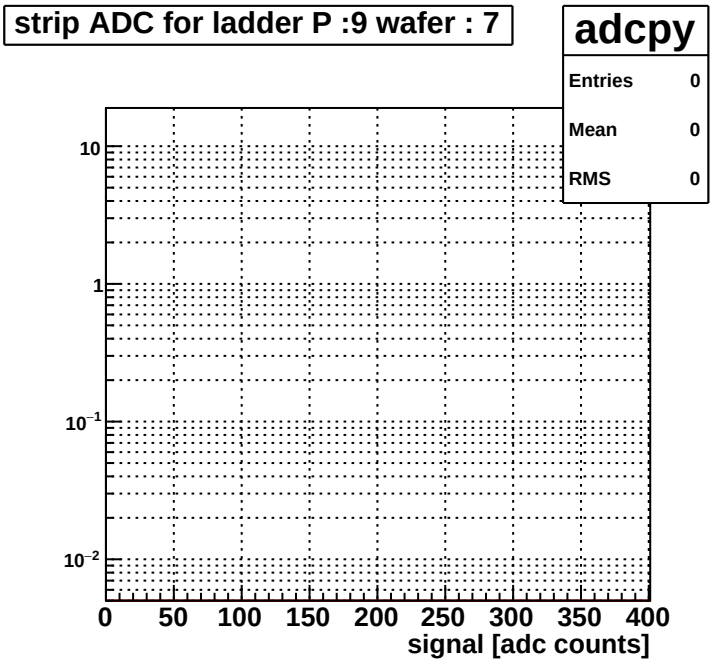

Strip adc vs. channels for ladder N : 9 wafer : 6
stripADCCHANsideN133
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

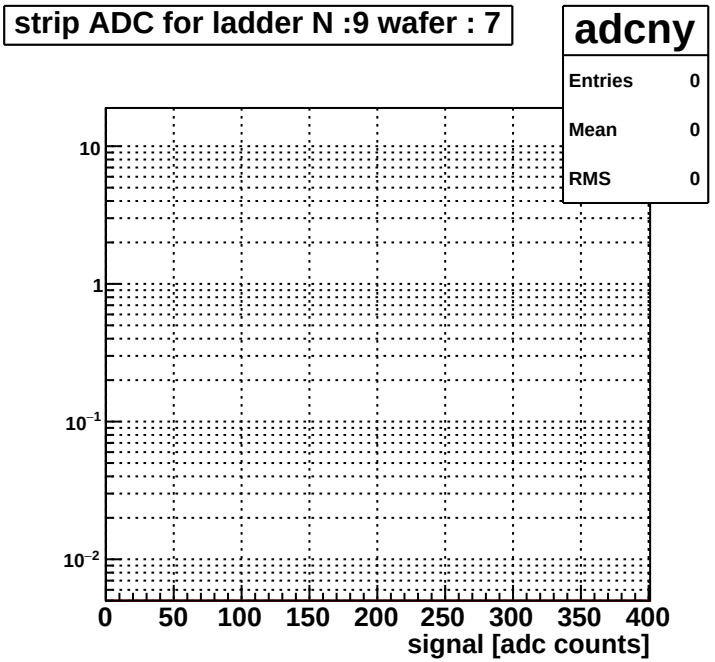

strip ADC for ladder P :9 wafer : 6
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

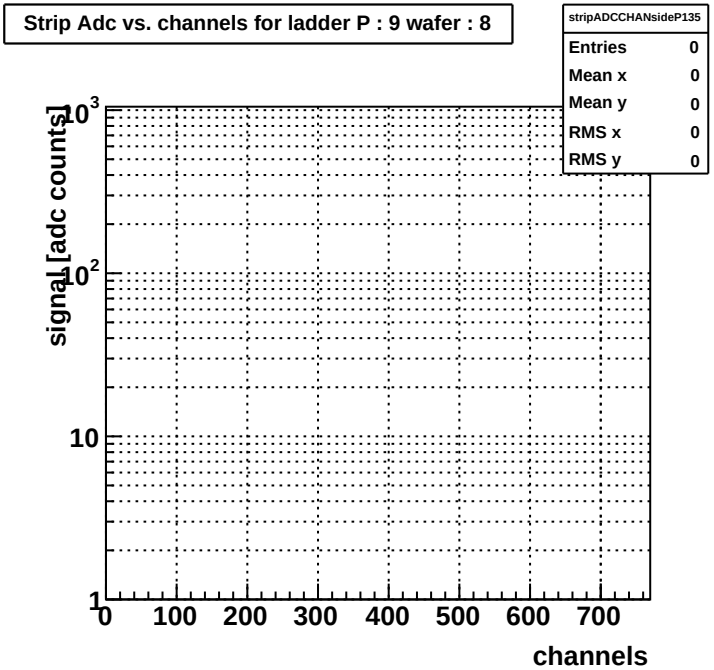

strip ADC for ladder N :9 wafer : 6
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

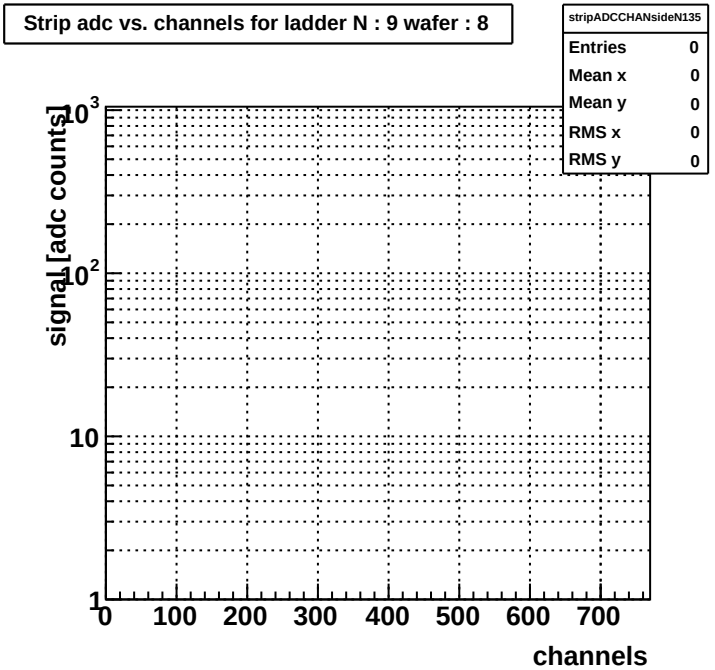

Strip Adc vs. channels for ladder P : 9 wafer : 7
stripADCCHANsideP134
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

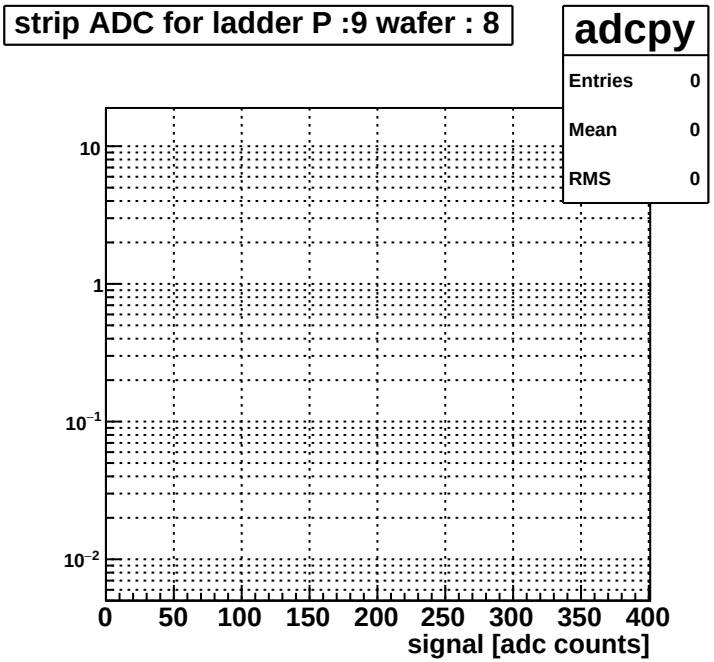

Strip adc vs. channels for ladder N : 9 wafer : 7
stripADCCHANsideN134
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

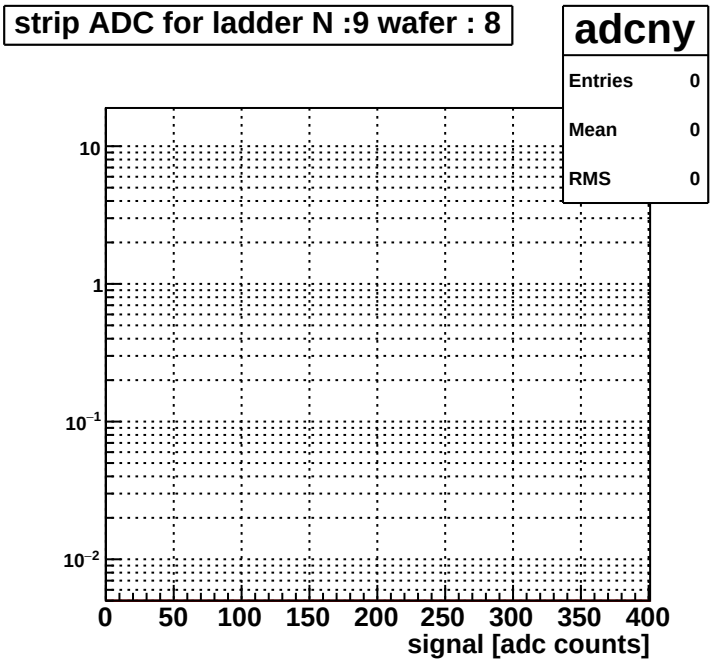

strip ADC for ladder P :9 wafer : 7
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

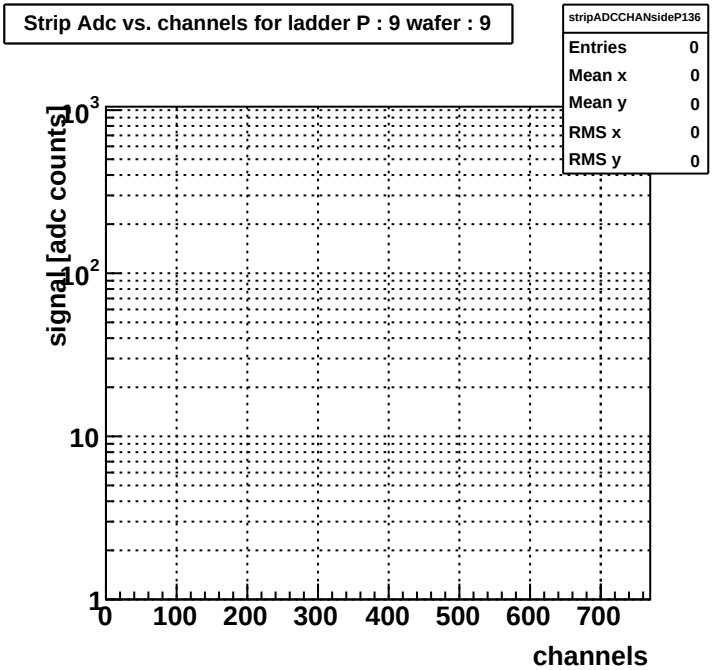

strip ADC for ladder N :9 wafer : 7
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

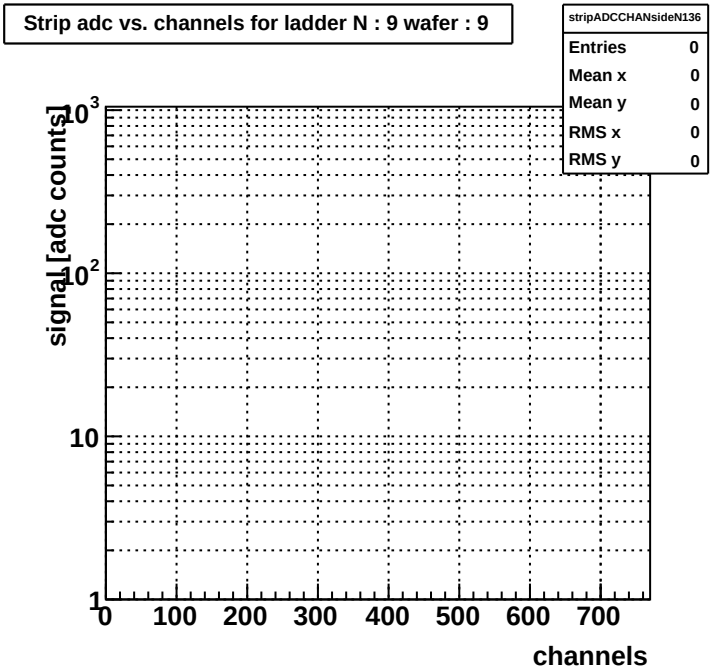

Strip Adc vs. channels for ladder P : 9 wafer : 8
stripADCCHANsideP135
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

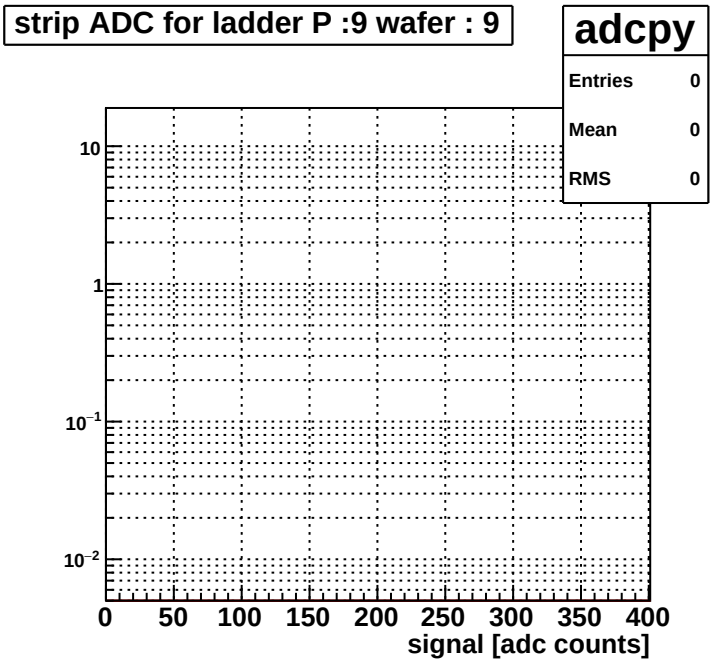

Strip adc vs. channels for ladder N : 9 wafer : 8
stripADCCHANsideN135
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

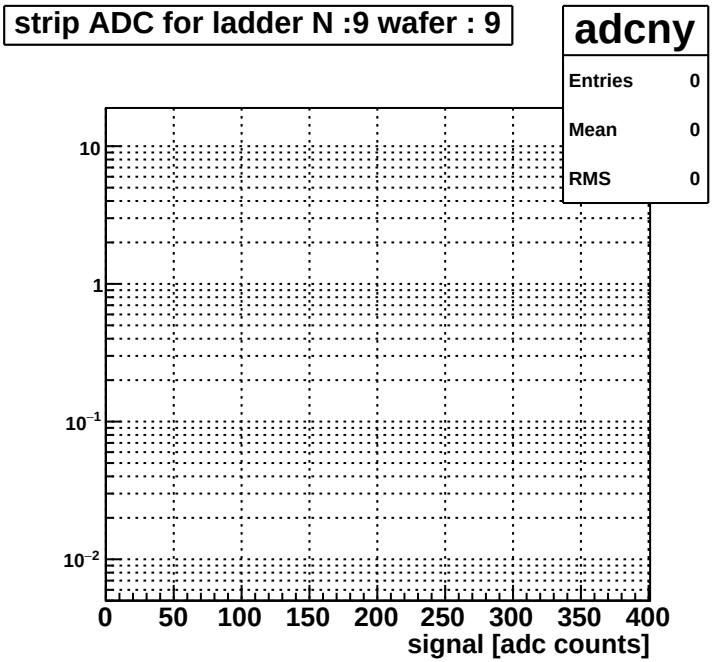

strip ADC for ladder P :9 wafer : 8
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

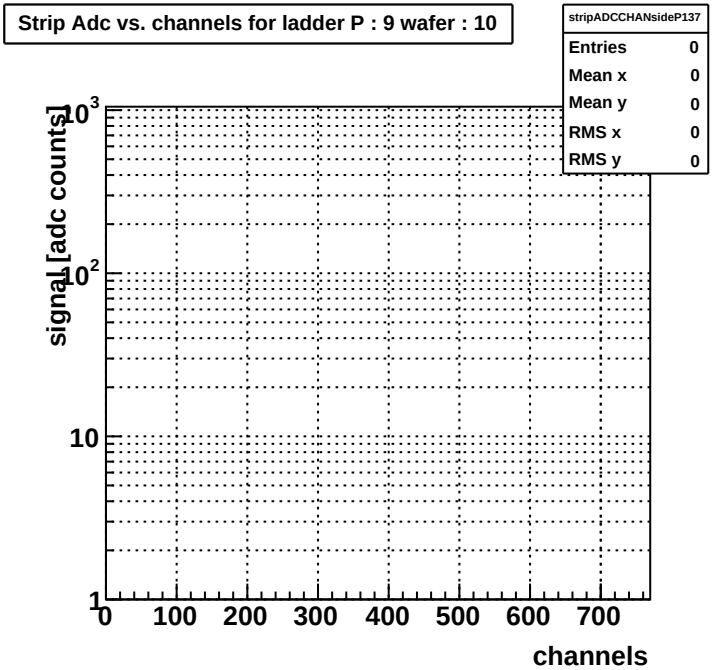

strip ADC for ladder N :9 wafer : 8
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

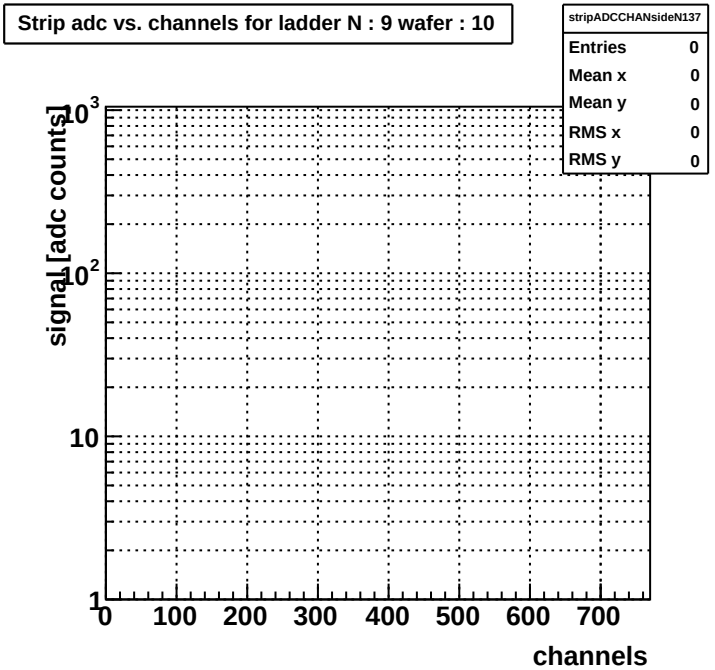

Strip Adc vs. channels for ladder P : 9 wafer : 9
stripADCCHANsideP136
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

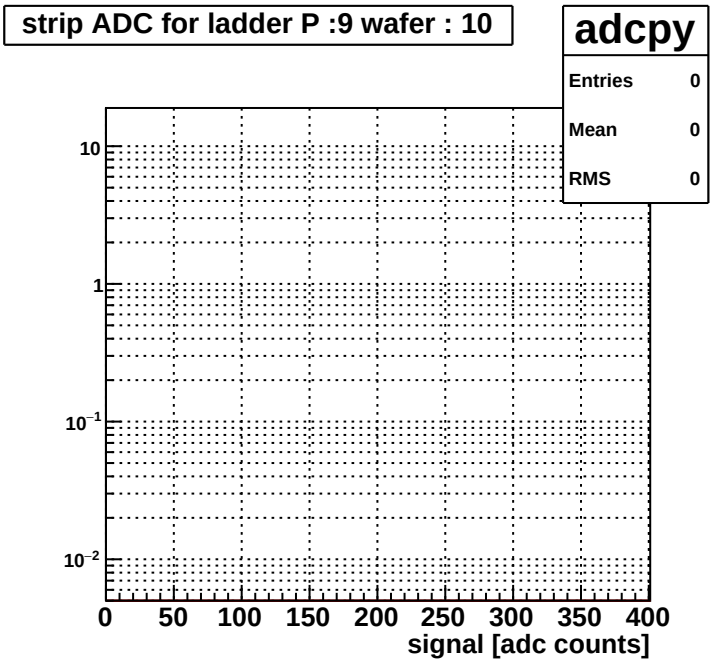

Strip adc vs. channels for ladder N : 9 wafer : 9
stripADCCHANsideN136
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

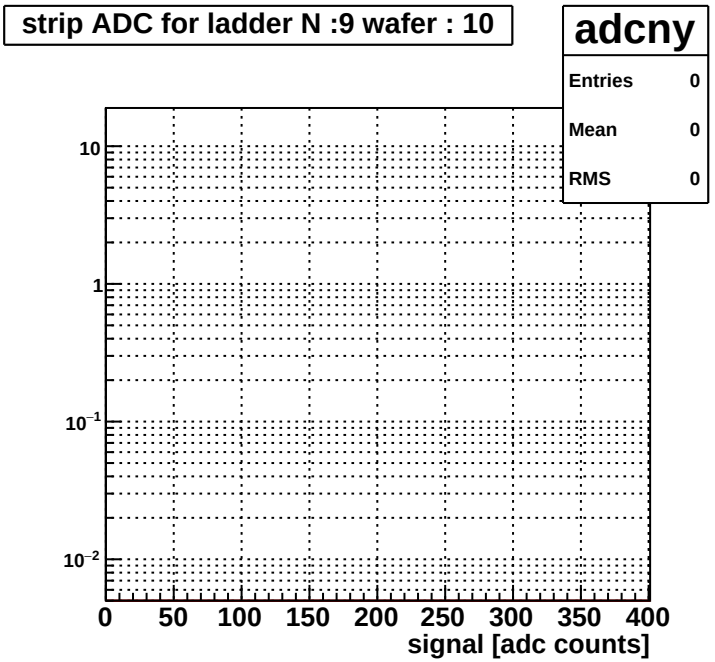

strip ADC for ladder P :9 wafer : 9
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

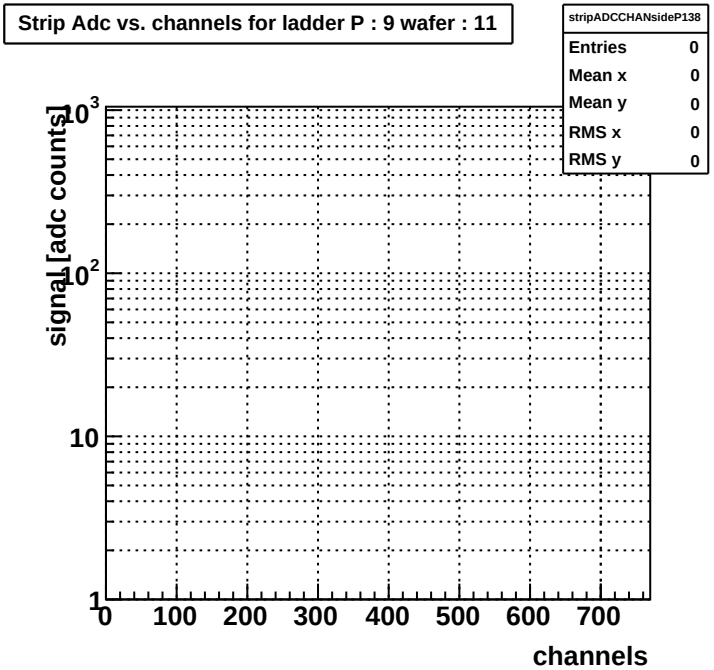

strip ADC for ladder N :9 wafer : 9
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

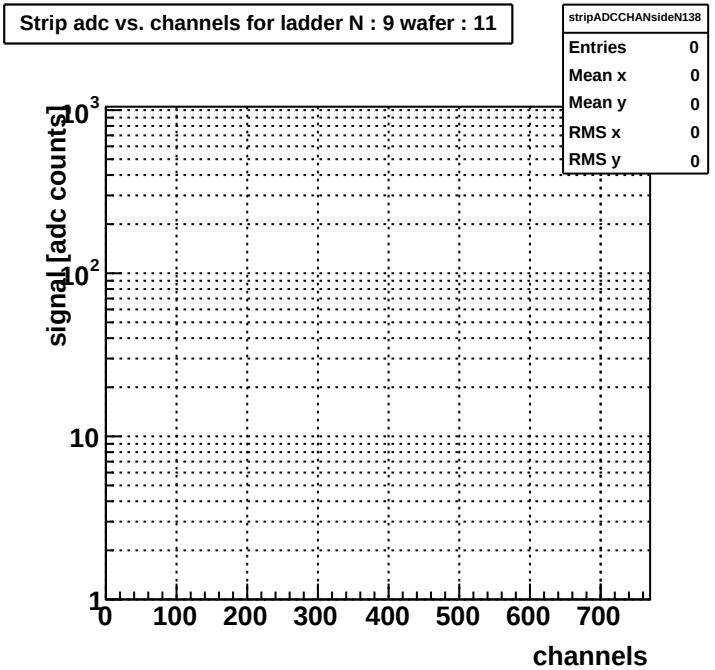

Strip Adc vs. channels for ladder P : 9 wafer : 10
stripADCCHANsideP137
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

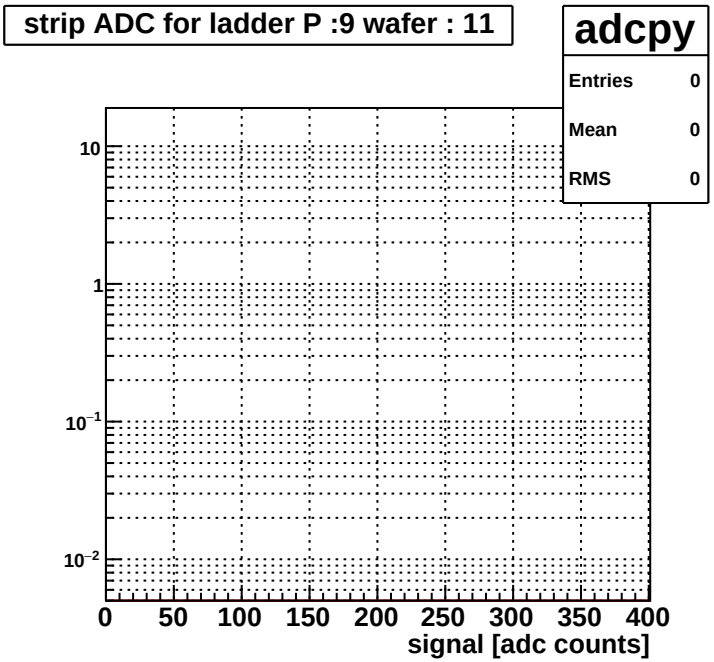

Strip adc vs. channels for ladder N : 9 wafer : 10
stripADCCHANsideN137
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

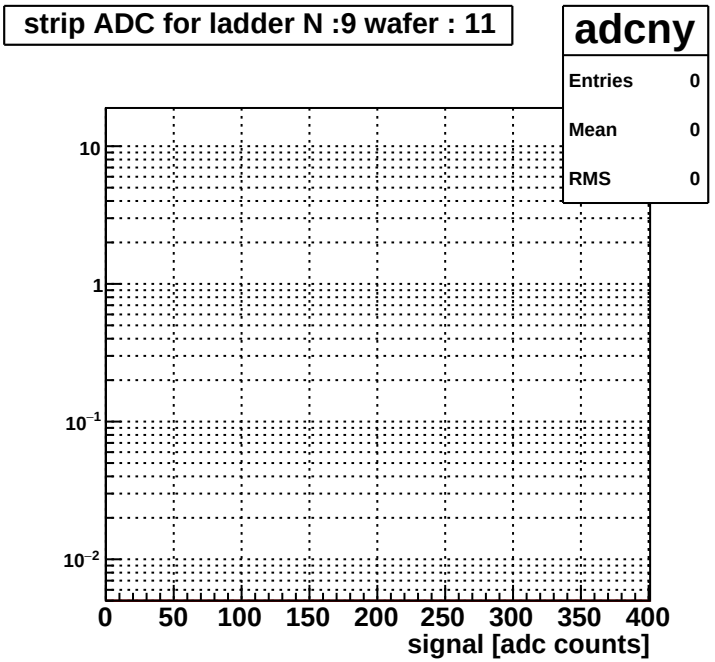

strip ADC for ladder P :9 wafer : 10
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

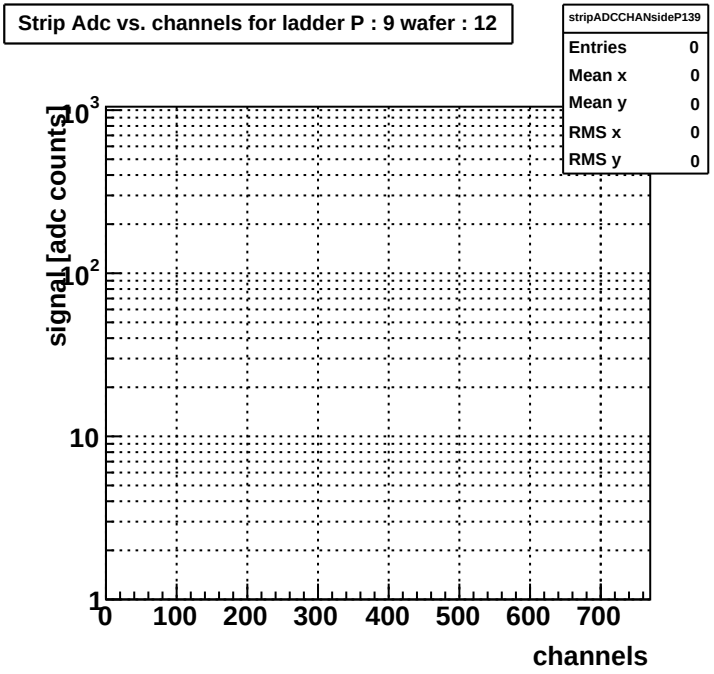

strip ADC for ladder N :9 wafer : 10
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

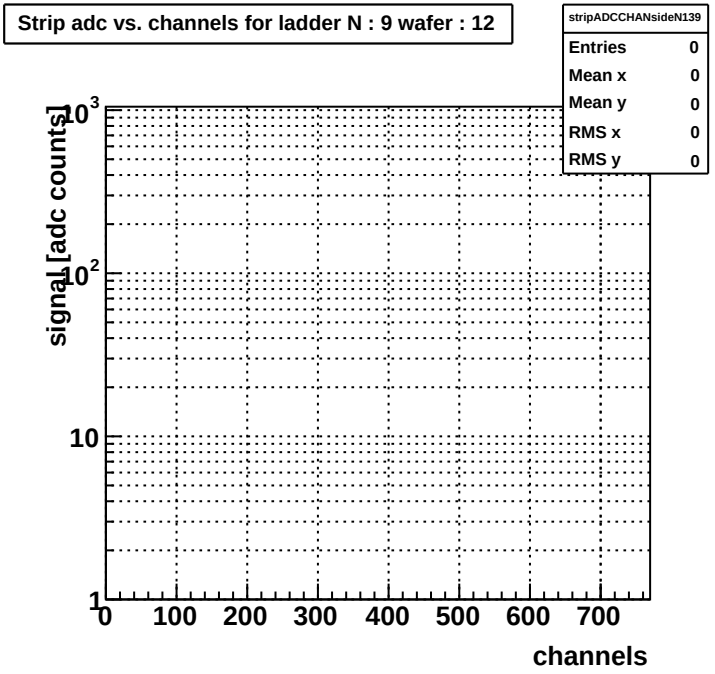

Strip Adc vs. channels for ladder P : 9 wafer : 11
stripADCCHANsideP138
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

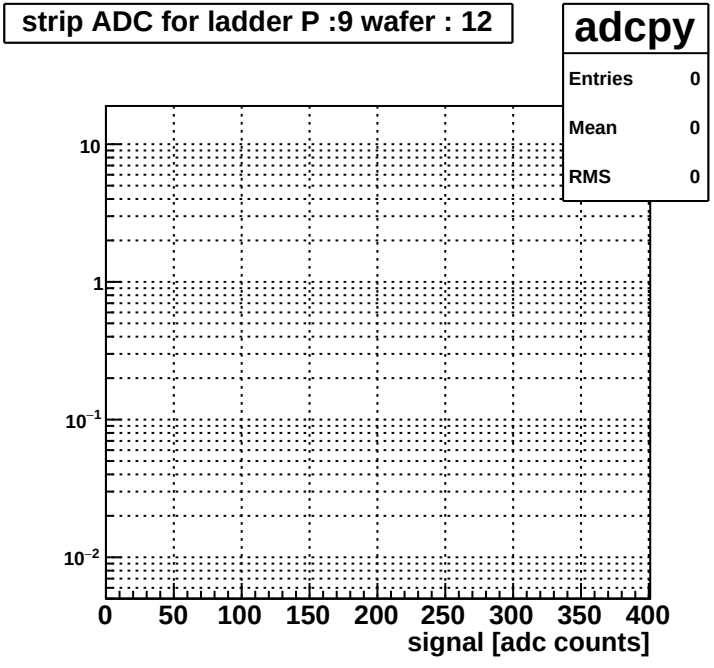

Strip adc vs. channels for ladder N : 9 wafer : 11
stripADCCHANsideN138
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

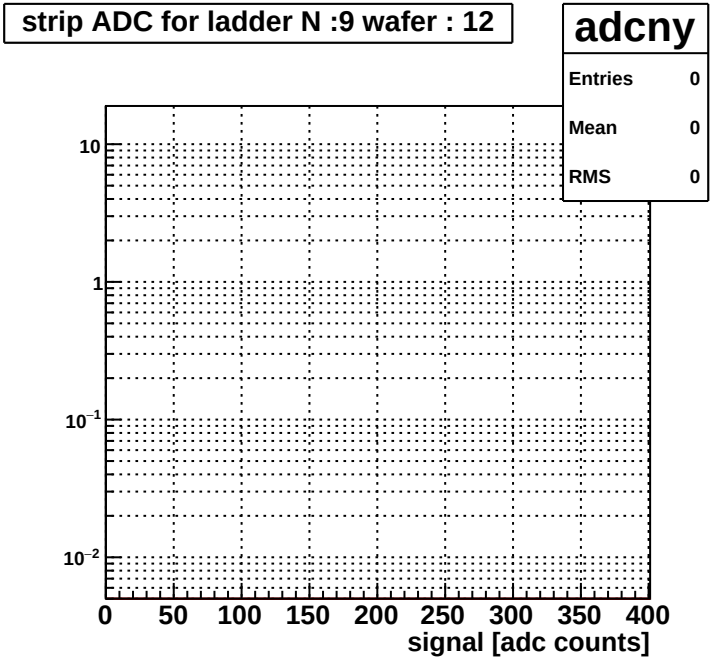

strip ADC for ladder P :9 wafer : 11
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

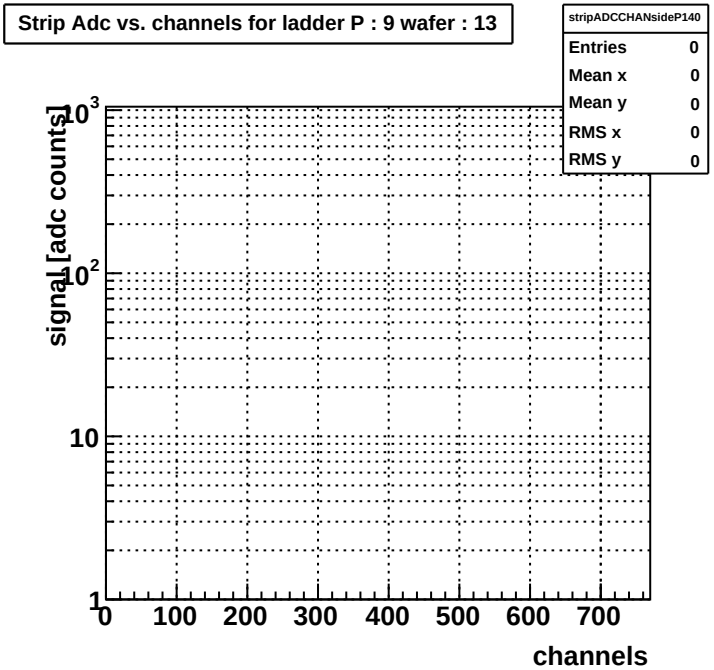

strip ADC for ladder N :9 wafer : 11
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

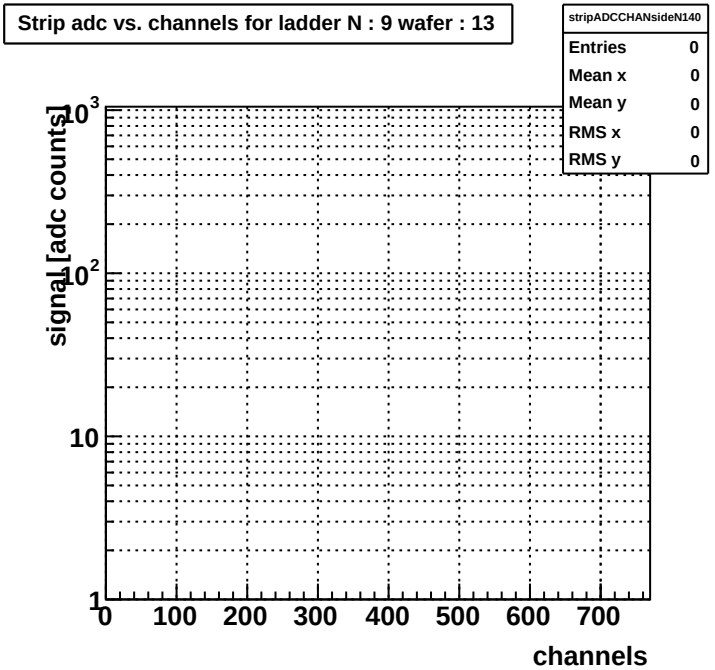

Strip Adc vs. channels for ladder P : 9 wafer : 12
stripADCCHANsideP139
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

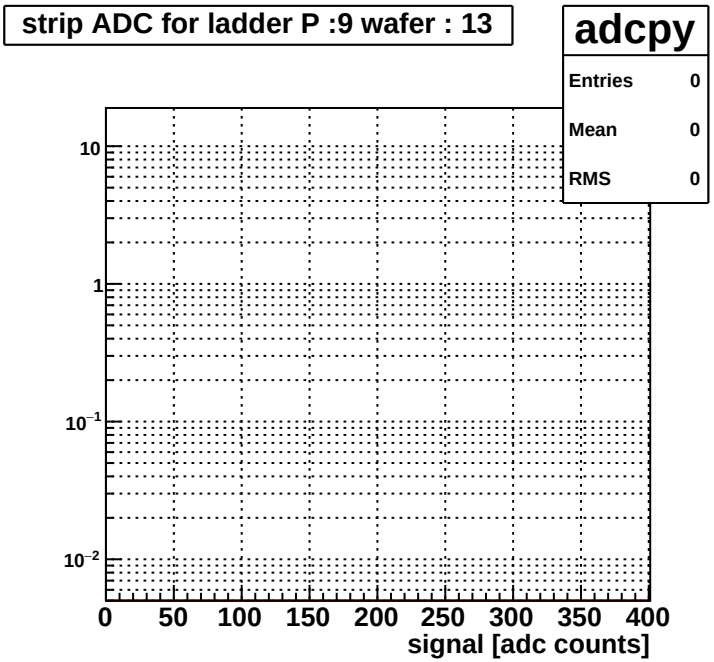

Strip adc vs. channels for ladder N : 9 wafer : 12
stripADCCHANsideN139
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

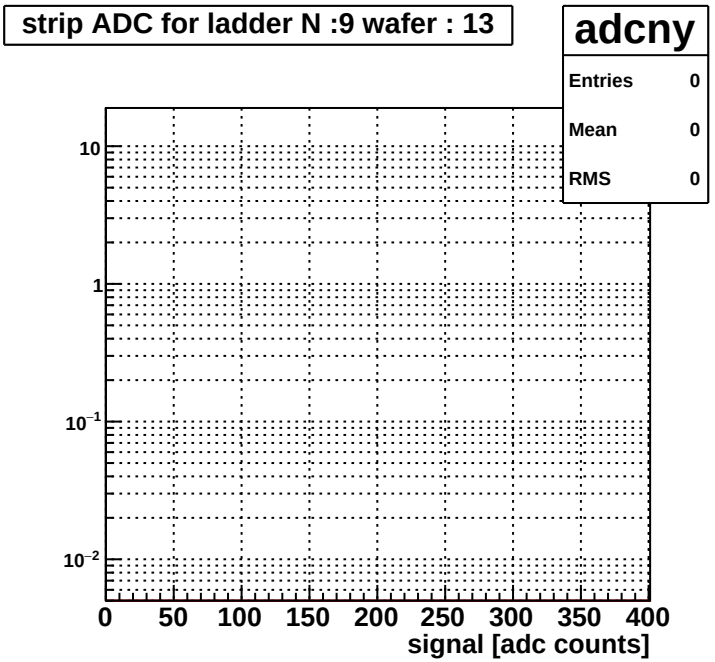

strip ADC for ladder P :9 wafer : 12
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

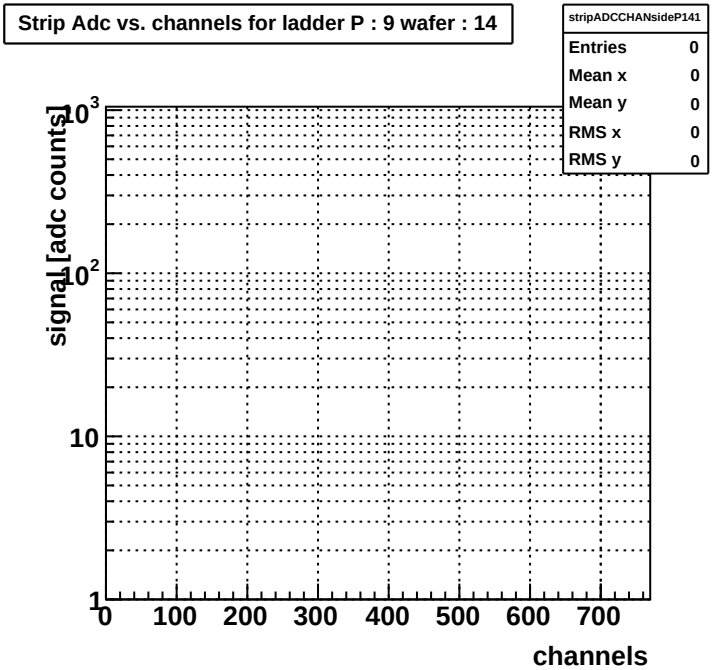

strip ADC for ladder N :9 wafer : 12
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

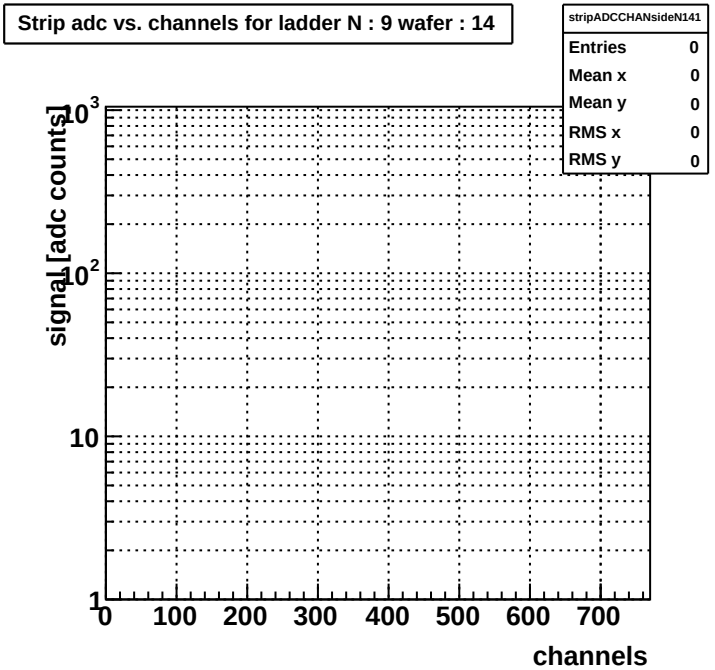

Strip Adc vs. channels for ladder P : 9 wafer : 13
stripADCCHANsideP140
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

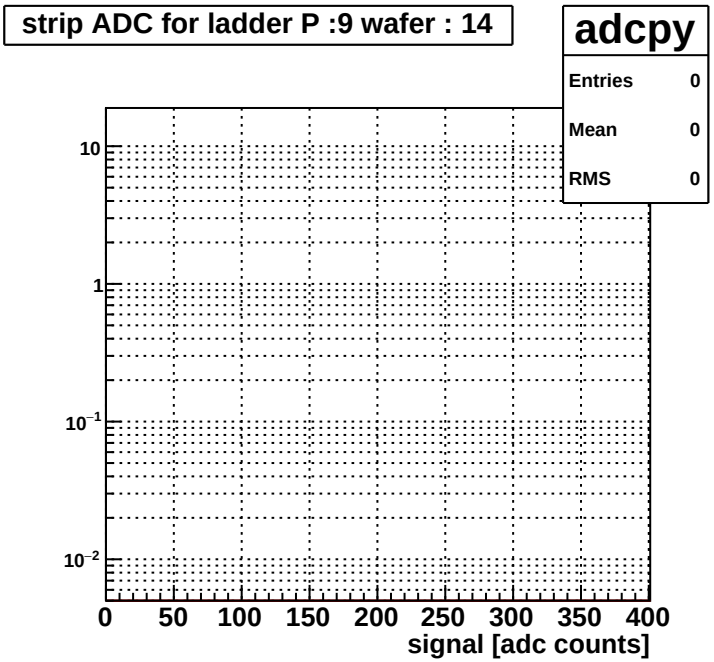

Strip adc vs. channels for ladder N : 9 wafer : 13
stripADCCHANsideN140
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

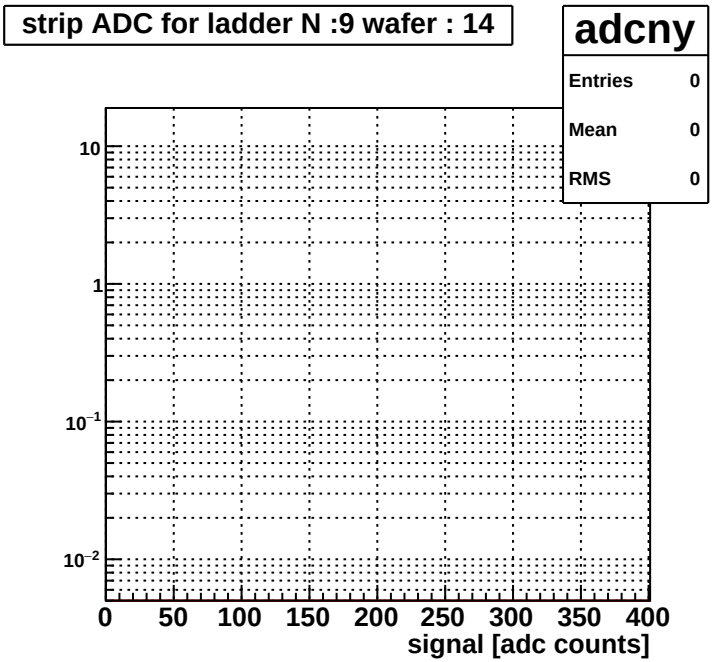

strip ADC for ladder P :9 wafer : 13
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

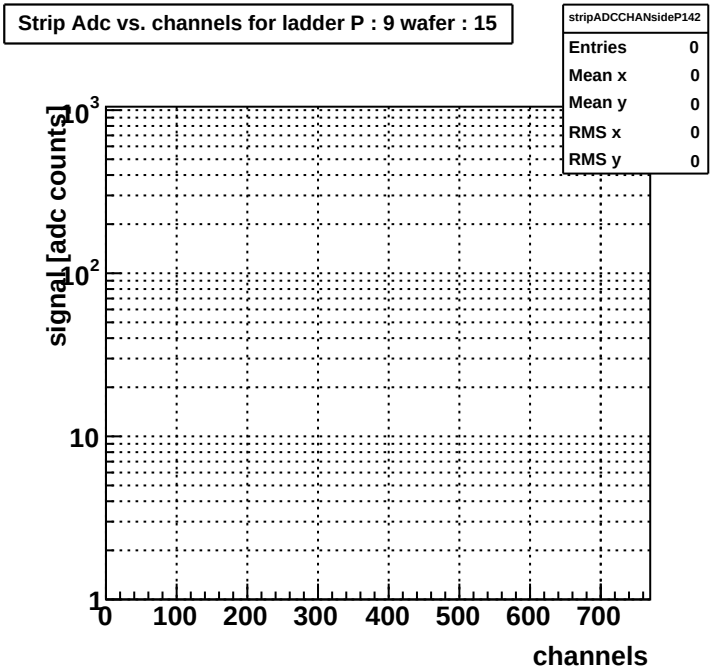

strip ADC for ladder N :9 wafer : 13
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

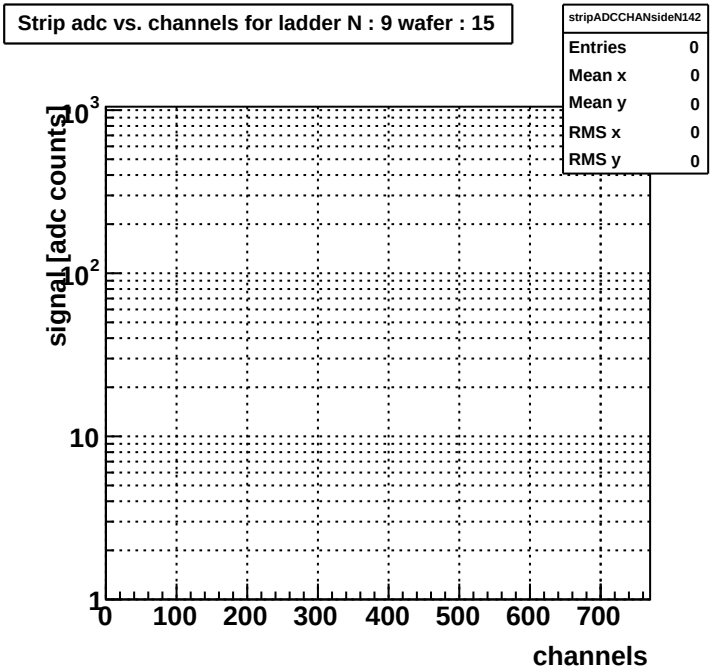

Strip Adc vs. channels for ladder P : 9 wafer : 14
stripADCCHANsideP141
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

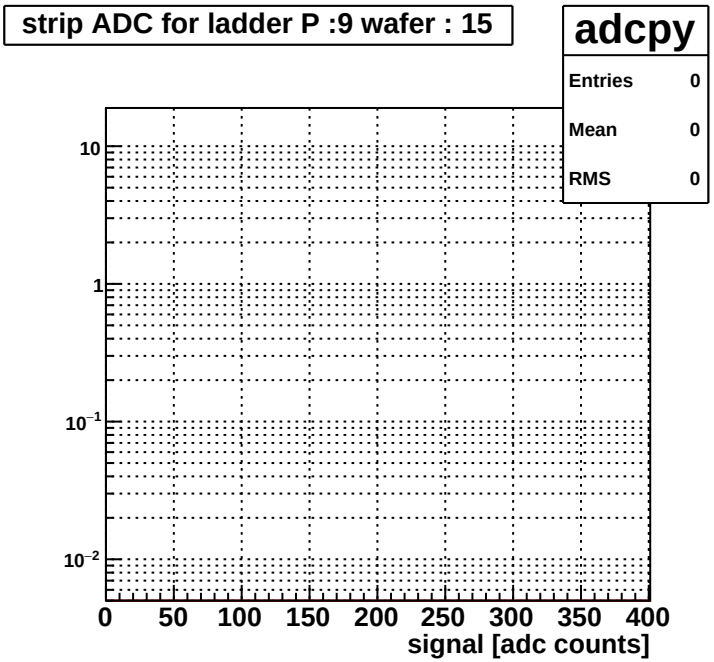

Strip adc vs. channels for ladder N : 9 wafer : 14
stripADCCHANsideN141
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

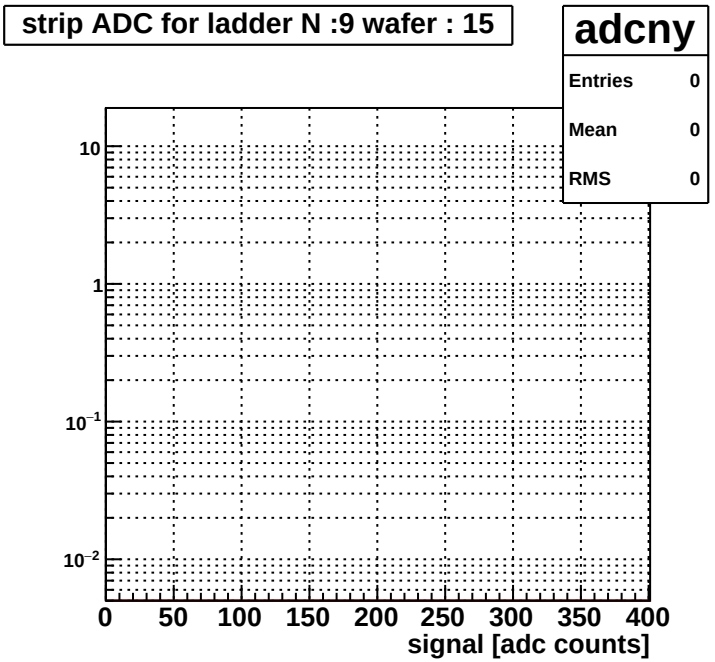

strip ADC for ladder P :9 wafer : 14
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

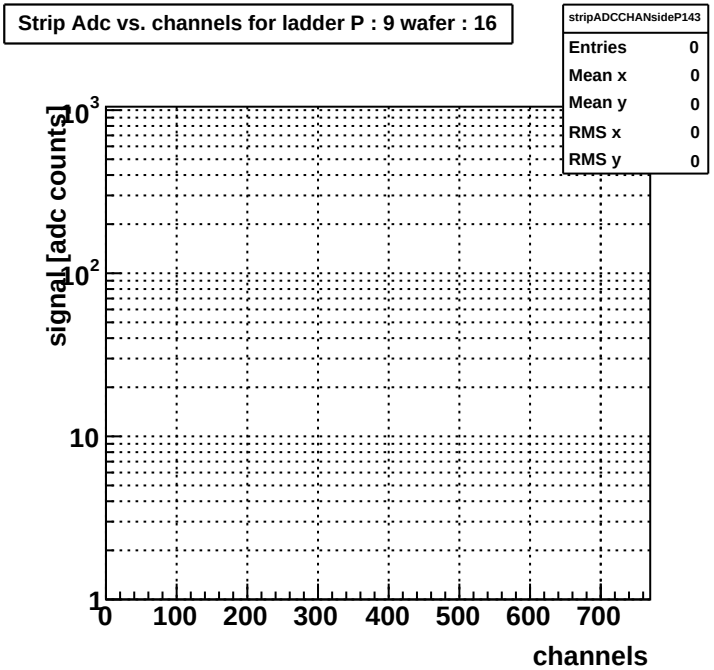

strip ADC for ladder N :9 wafer : 14
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

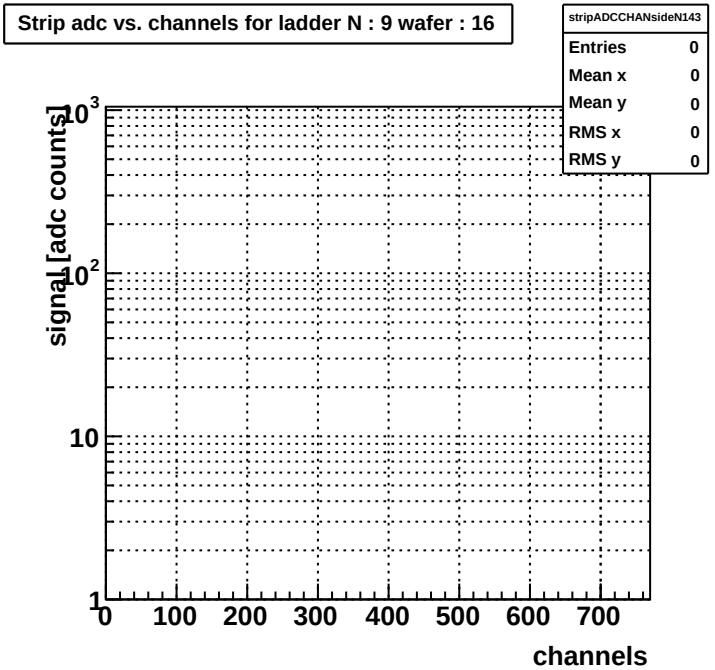

Strip Adc vs. channels for ladder P : 9 wafer : 15
stripADCCHANsideP142
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

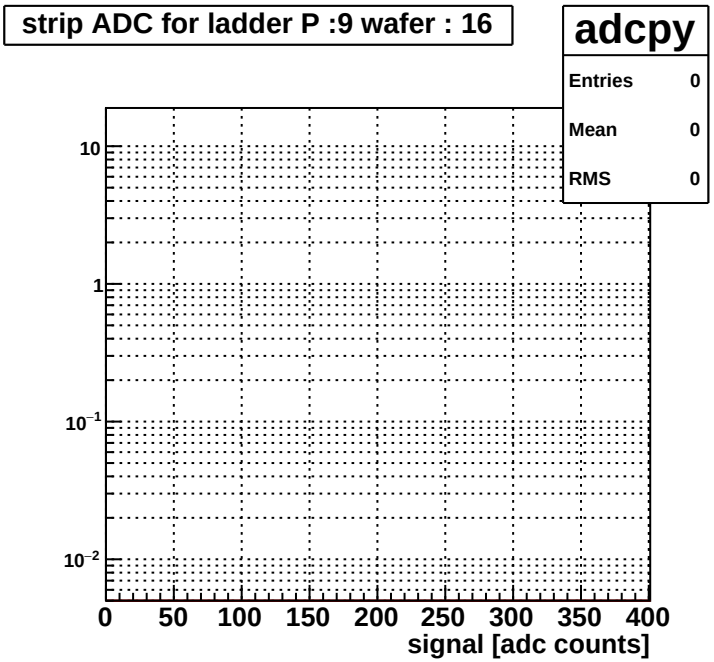

Strip adc vs. channels for ladder N : 9 wafer : 15
stripADCCHANsideN142
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

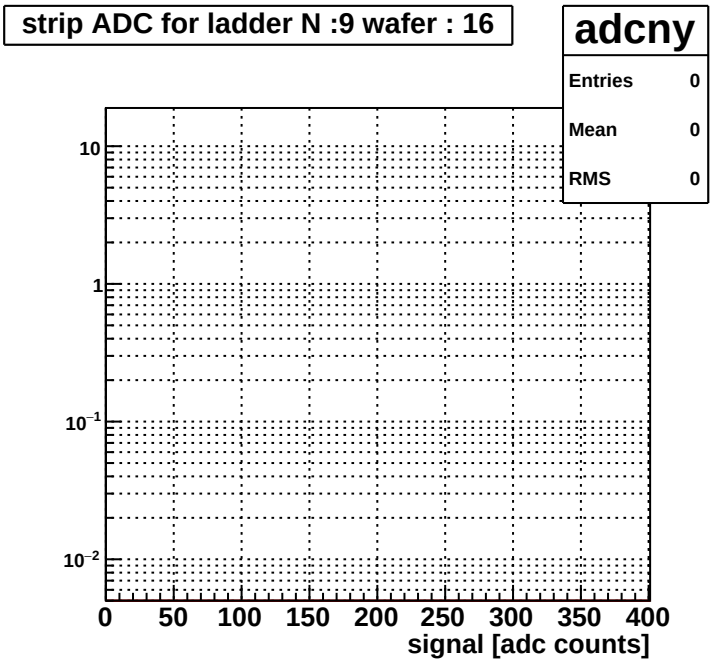

strip ADC for ladder P :9 wafer : 15
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

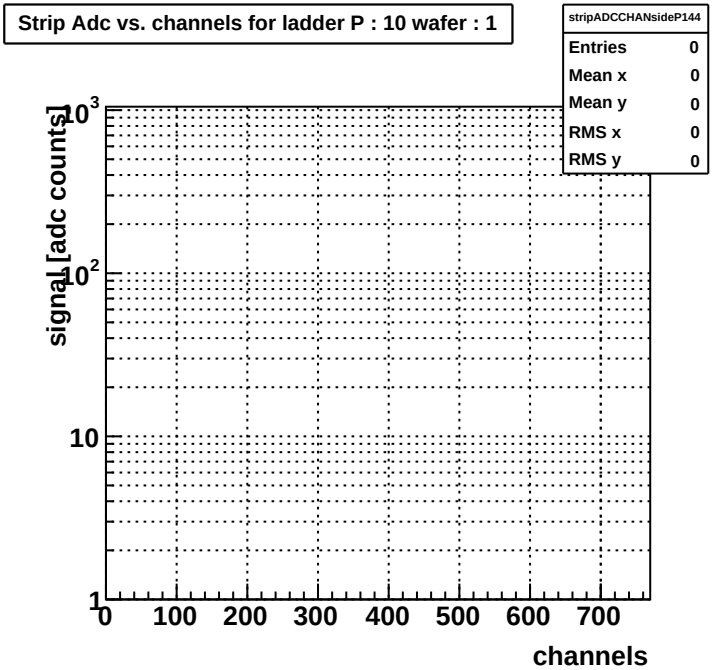

strip ADC for ladder N :9 wafer : 15
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

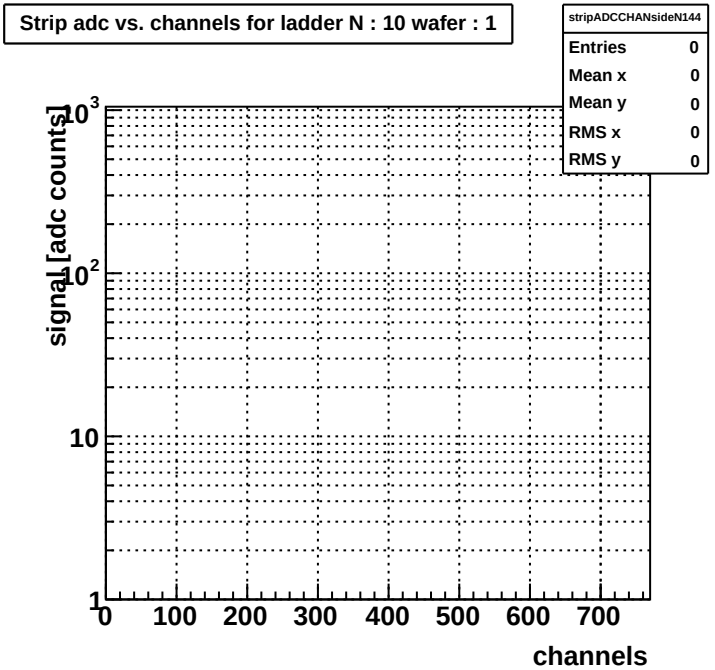

Strip Adc vs. channels for ladder P : 9 wafer : 16
stripADCCHANsideP143
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

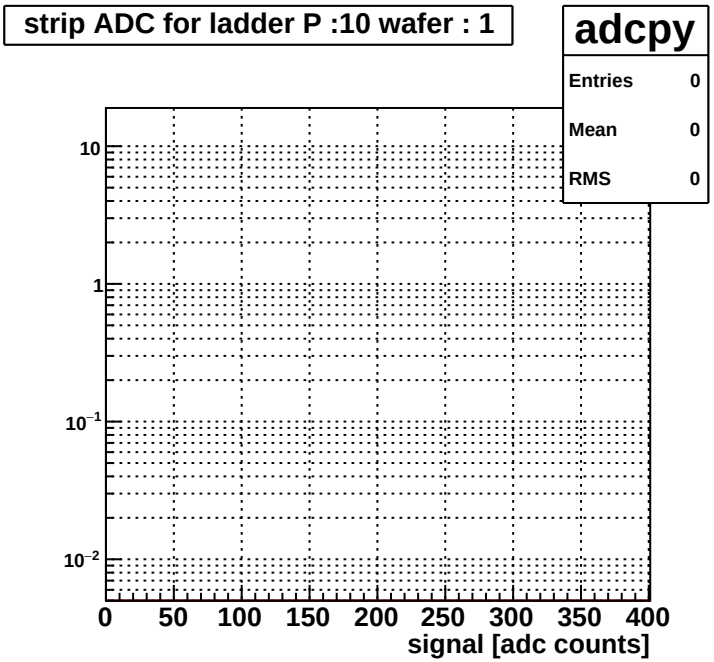

Strip adc vs. channels for ladder N : 9 wafer : 16
stripADCCHANsideN143
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

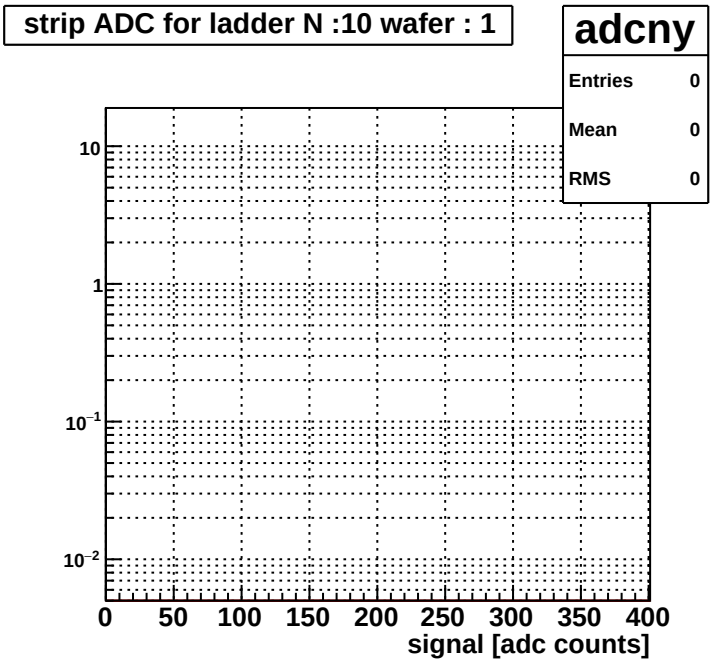

strip ADC for ladder P :9 wafer : 16
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

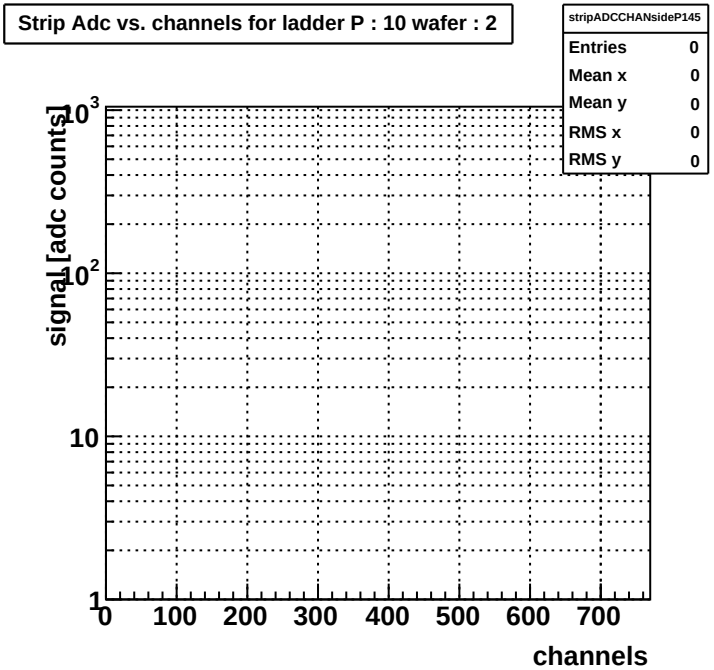

strip ADC for ladder N :9 wafer : 16
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

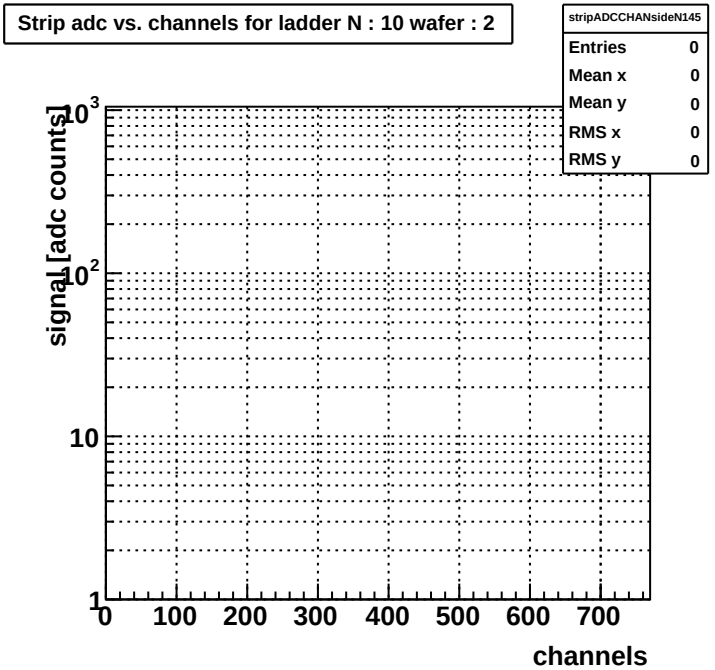

Strip Adc vs. channels for ladder P : 10 wafer : 1
stripADCCHANsideP144
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

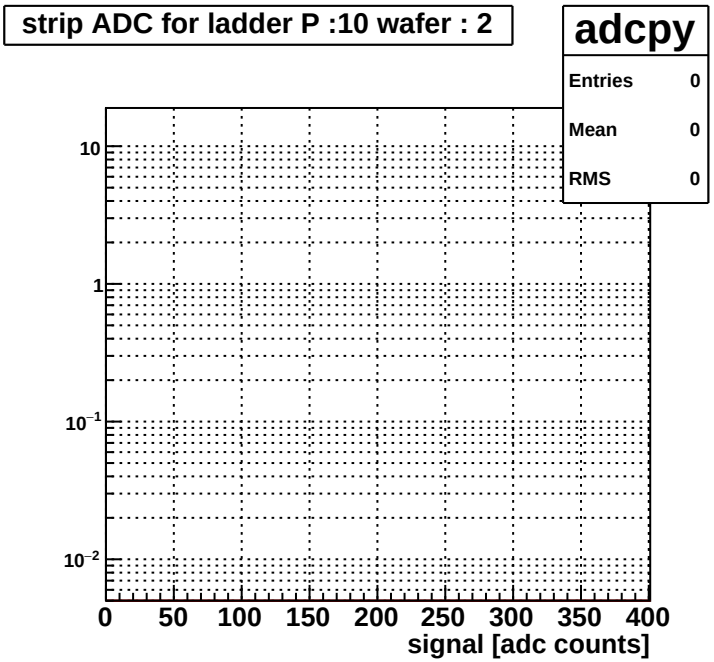

Strip adc vs. channels for ladder N : 10 wafer : 1
stripADCCHANsideN144
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

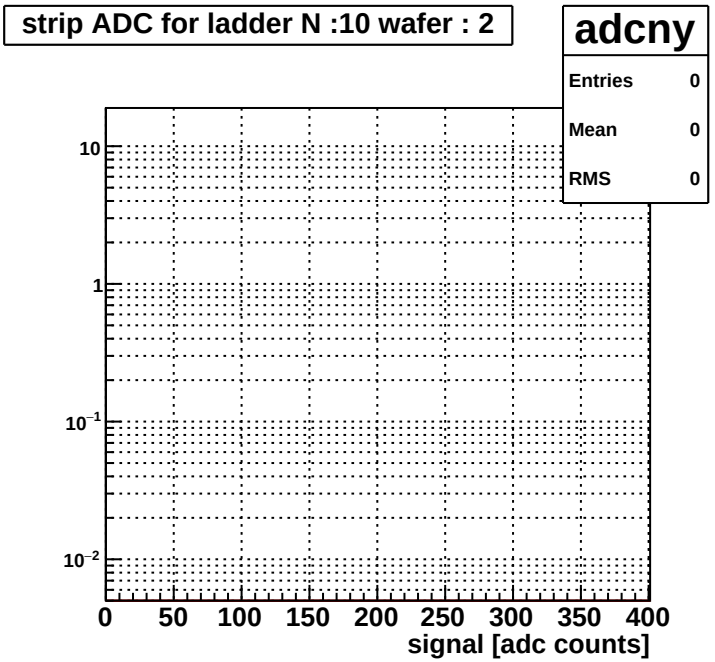

strip ADC for ladder P :10 wafer : 1
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

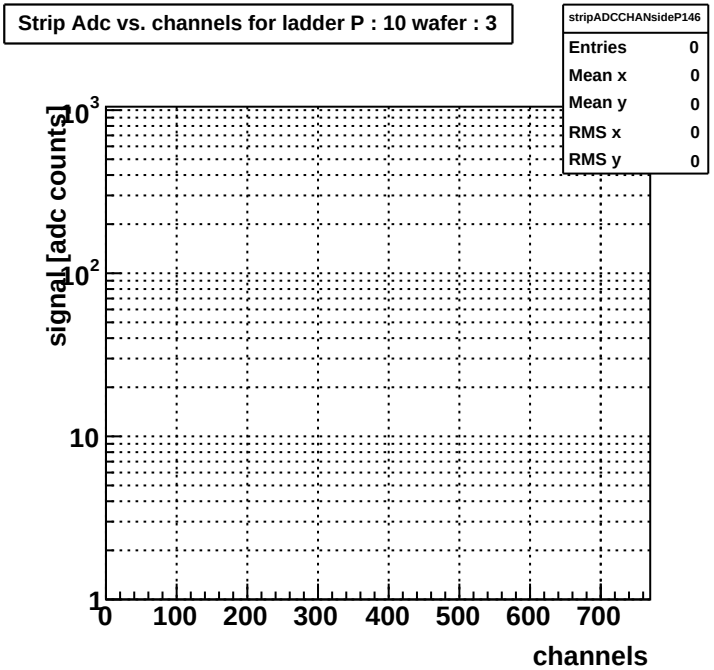

strip ADC for ladder N :10 wafer : 1
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

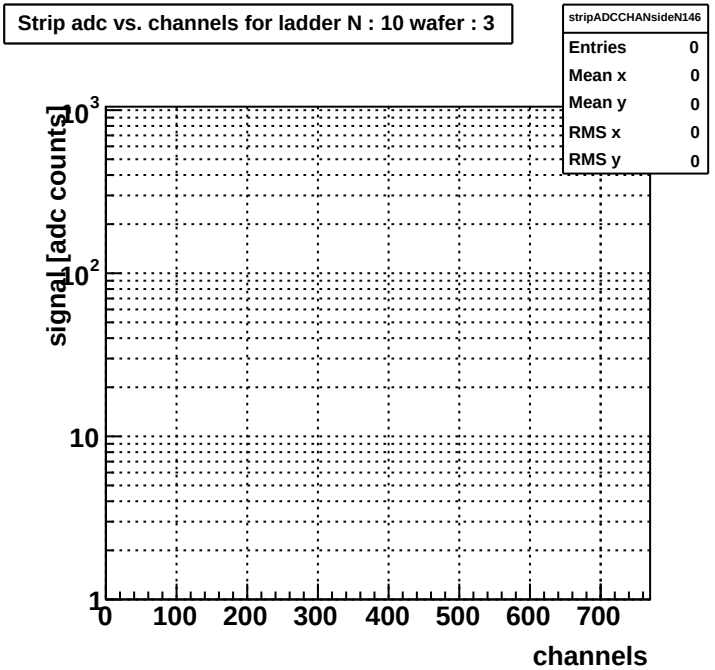

Strip Adc vs. channels for ladder P : 10 wafer : 2
stripADCCHANsideP145
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

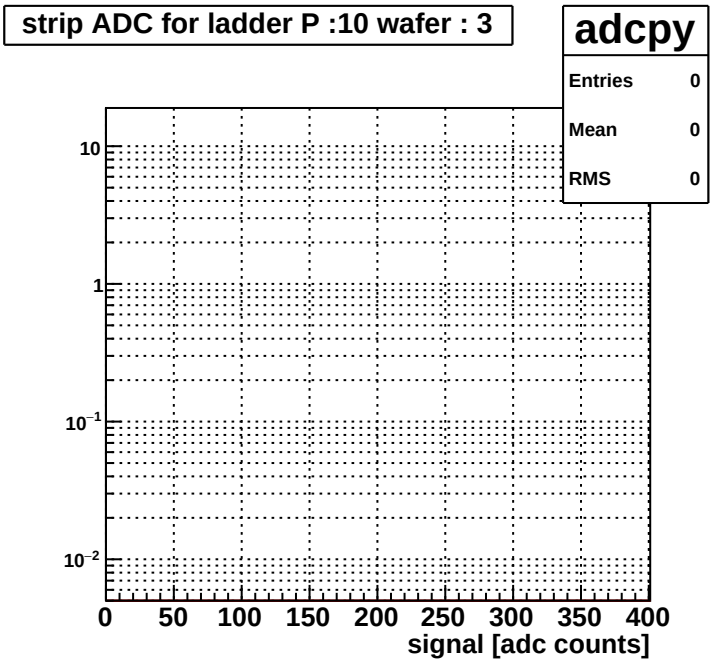

Strip adc vs. channels for ladder N : 10 wafer : 2
stripADCCHANsideN145
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

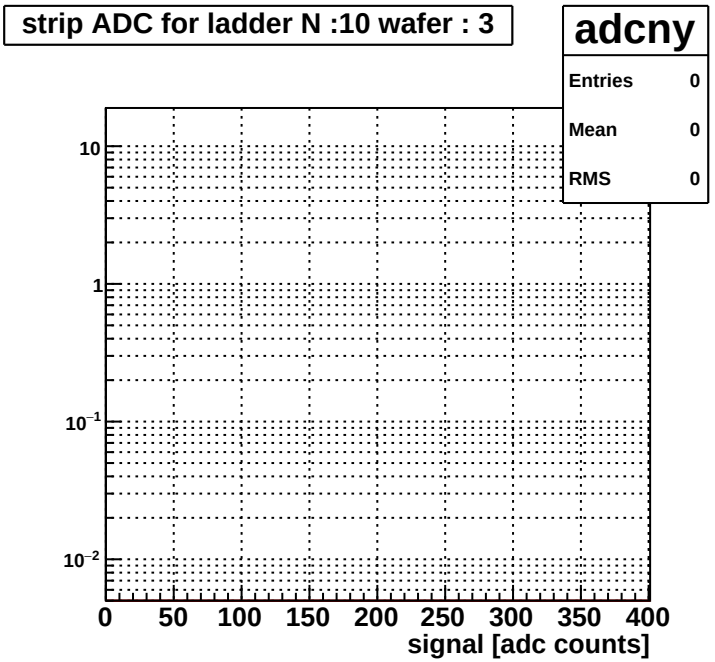

strip ADC for ladder P :10 wafer : 2
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

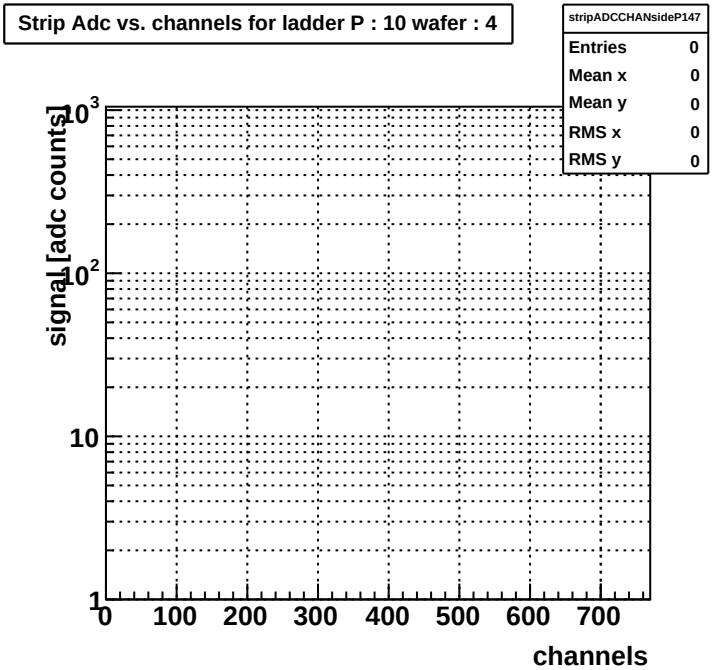

strip ADC for ladder N :10 wafer : 2
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

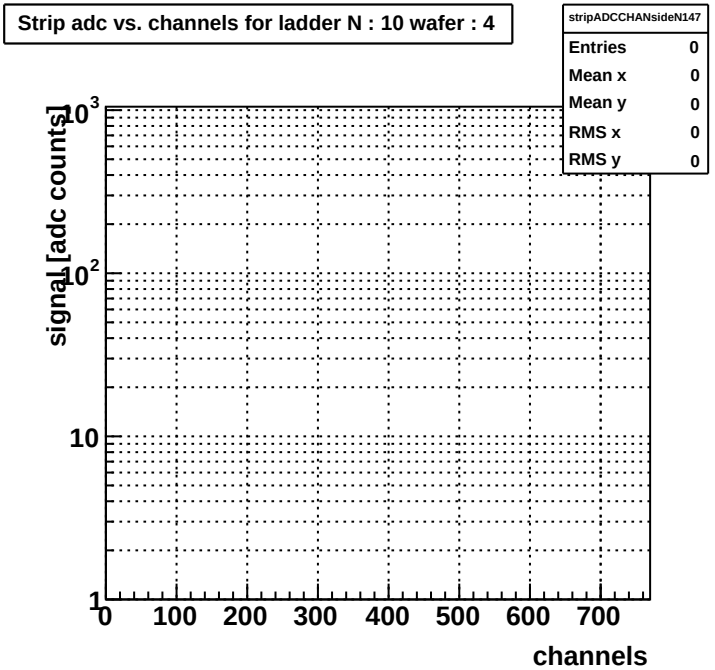

Strip Adc vs. channels for ladder P : 10 wafer : 3
stripADCCHANsideP146
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

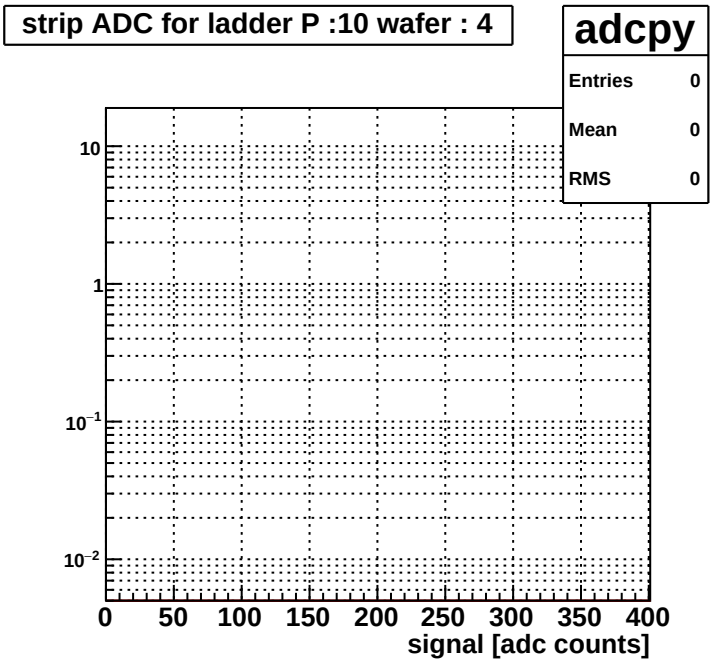

Strip adc vs. channels for ladder N : 10 wafer : 3
stripADCCHANsideN146
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

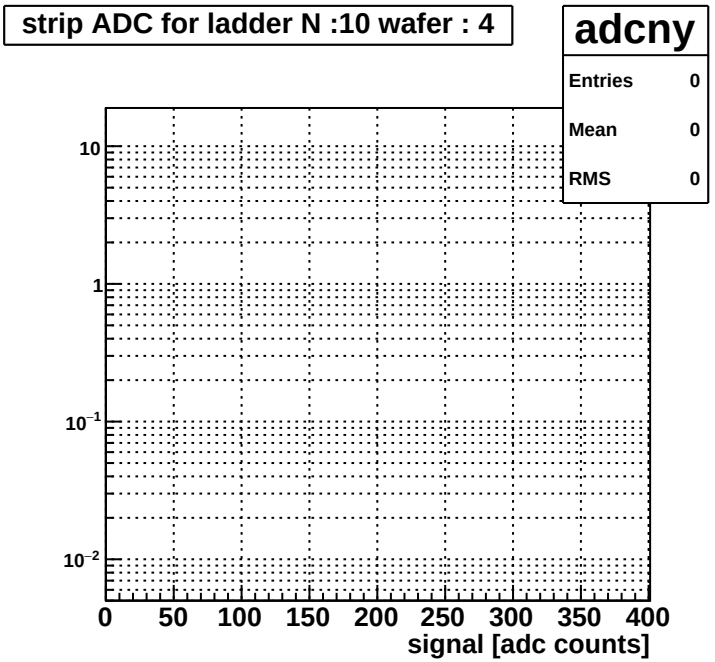

strip ADC for ladder P :10 wafer : 3
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

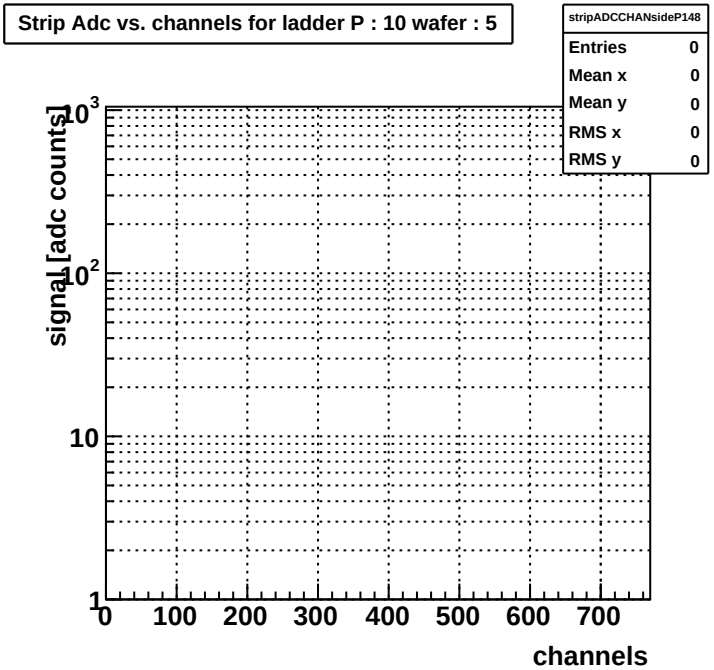

strip ADC for ladder N :10 wafer : 3
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

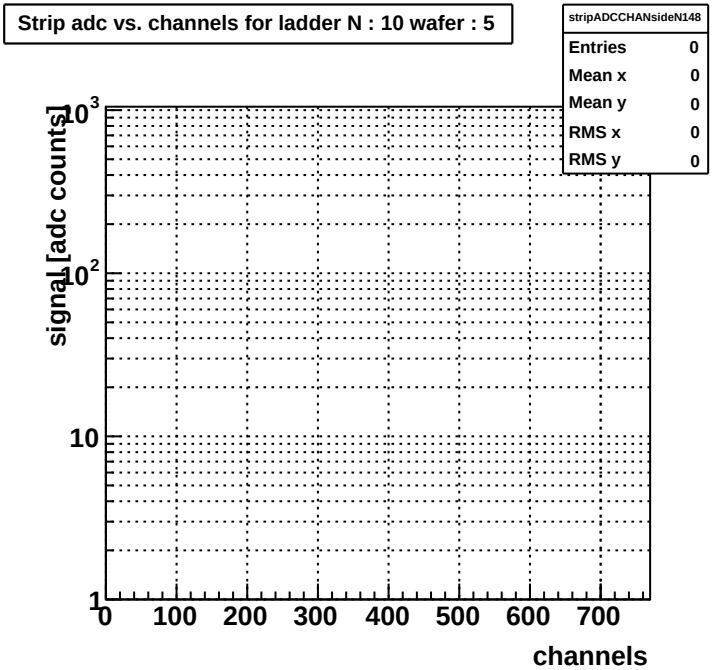

Strip Adc vs. channels for ladder P : 10 wafer : 4
stripADCCHANsideP147
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

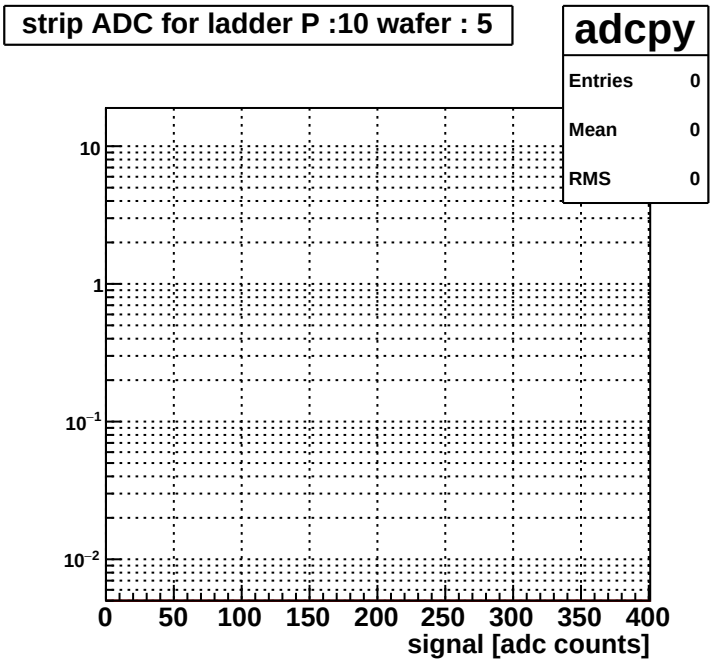

Strip adc vs. channels for ladder N : 10 wafer : 4
stripADCCHANsideN147
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

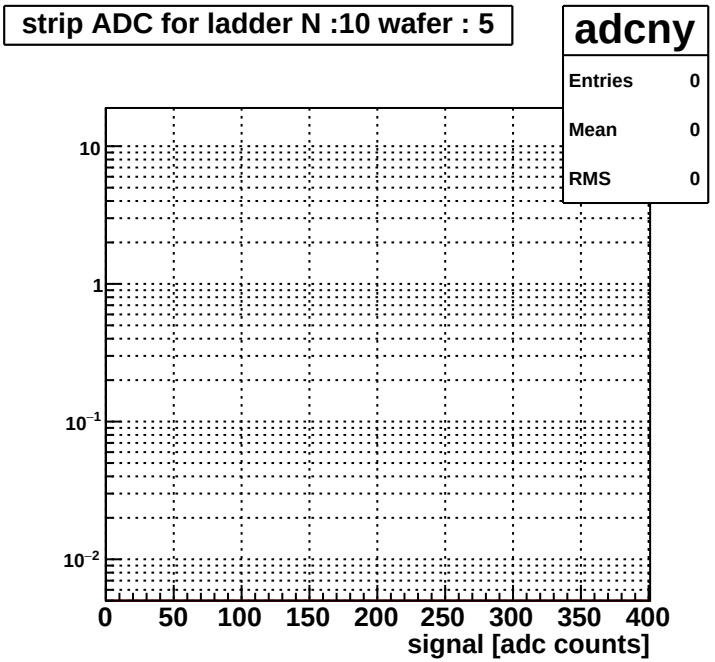

strip ADC for ladder P :10 wafer : 4
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

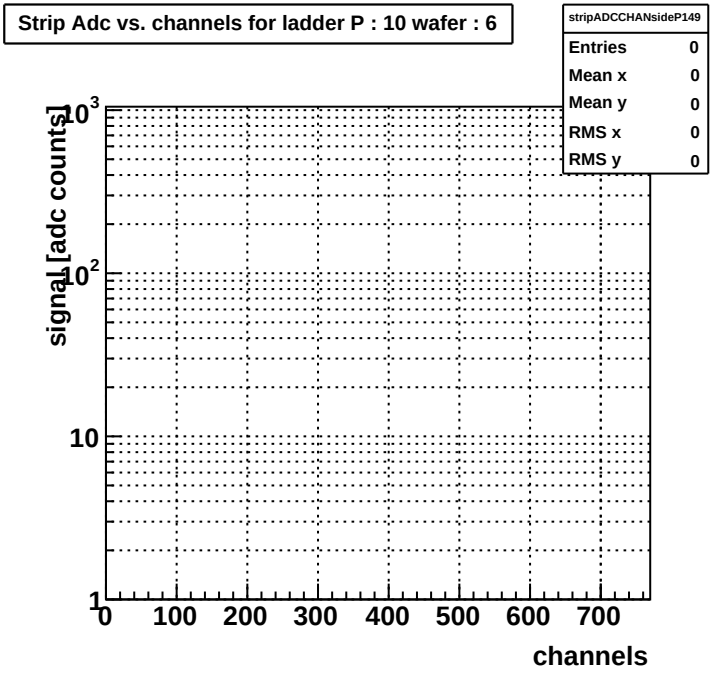

strip ADC for ladder N :10 wafer : 4
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

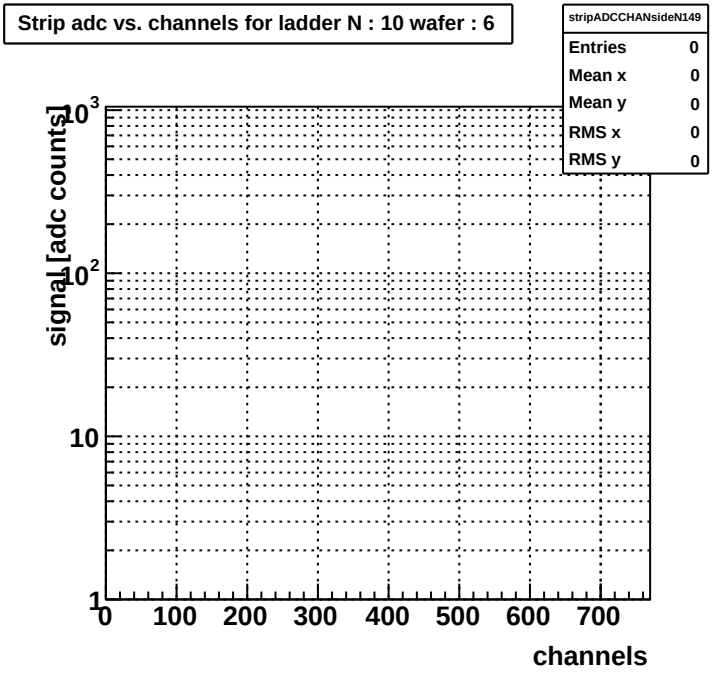

Strip Adc vs. channels for ladder P : 10 wafer : 5
stripADCCHANsideP148
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

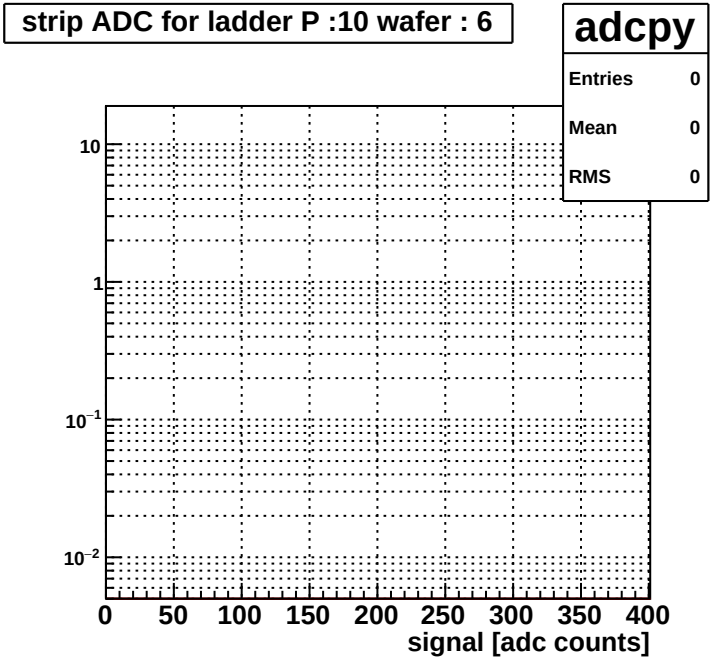

Strip adc vs. channels for ladder N : 10 wafer : 5
stripADCCHANsideN148
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

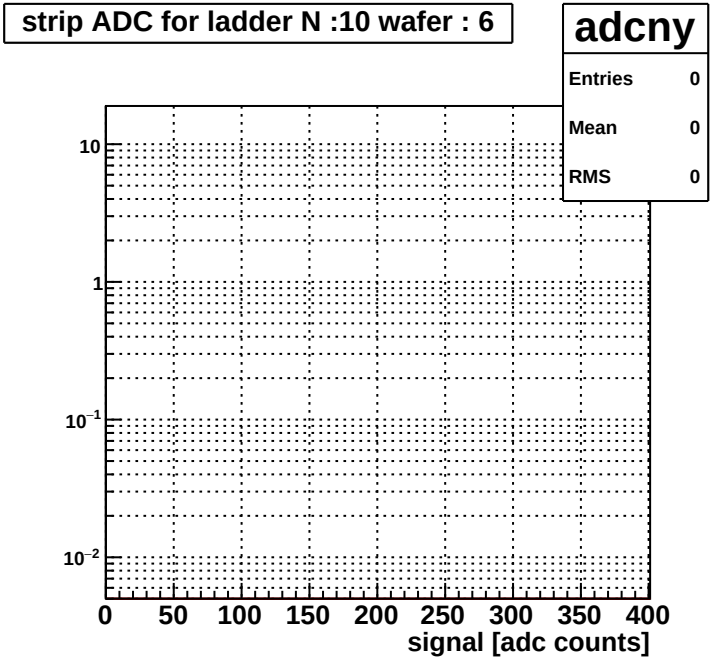

strip ADC for ladder P :10 wafer : 5
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

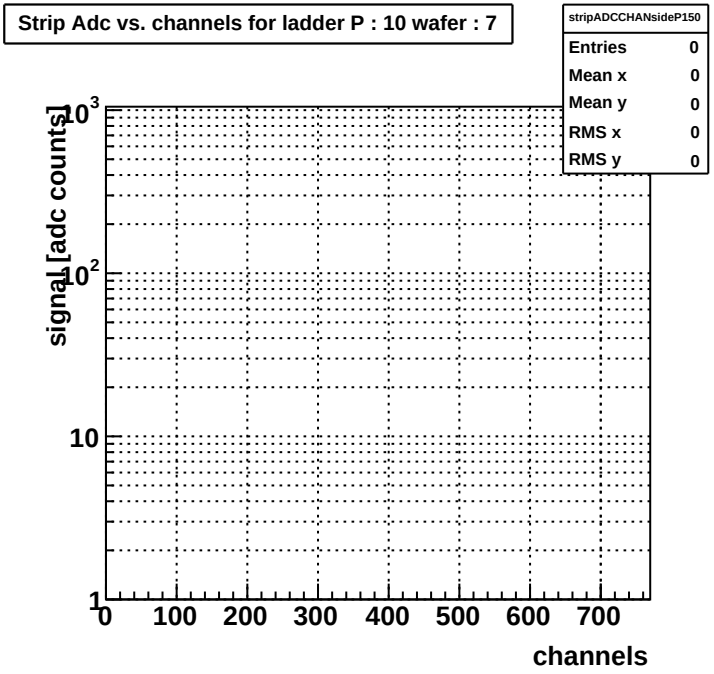

strip ADC for ladder N :10 wafer : 5
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

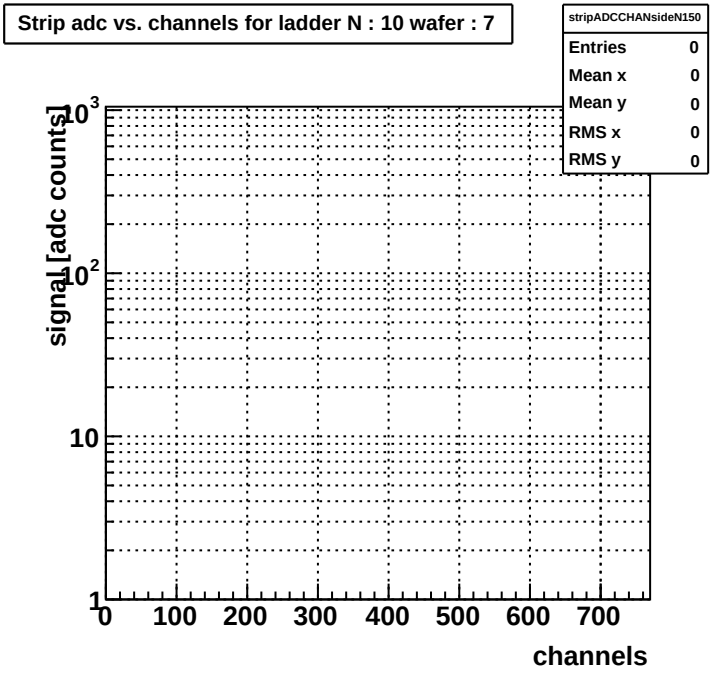

Strip Adc vs. channels for ladder P : 10 wafer : 6
stripADCCHANsideP149
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

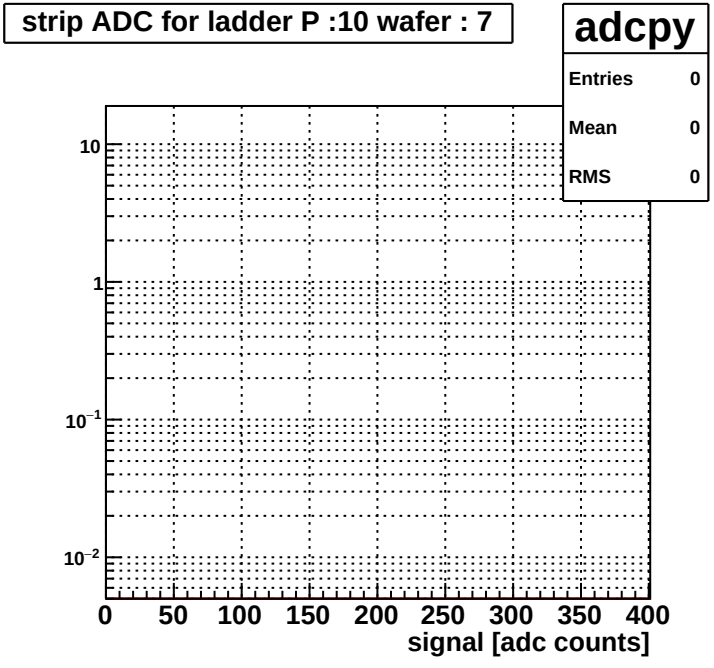

Strip adc vs. channels for ladder N : 10 wafer : 6
stripADCCHANsideN149
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

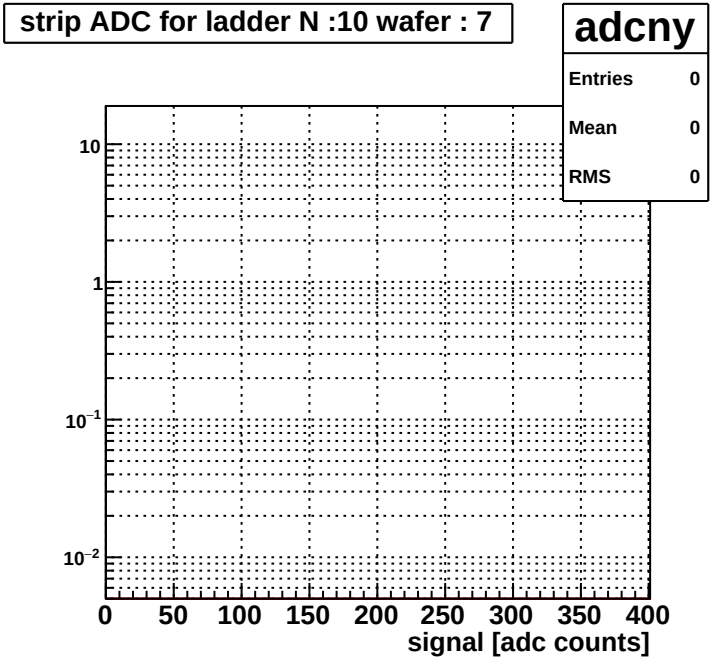

strip ADC for ladder P :10 wafer : 6
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

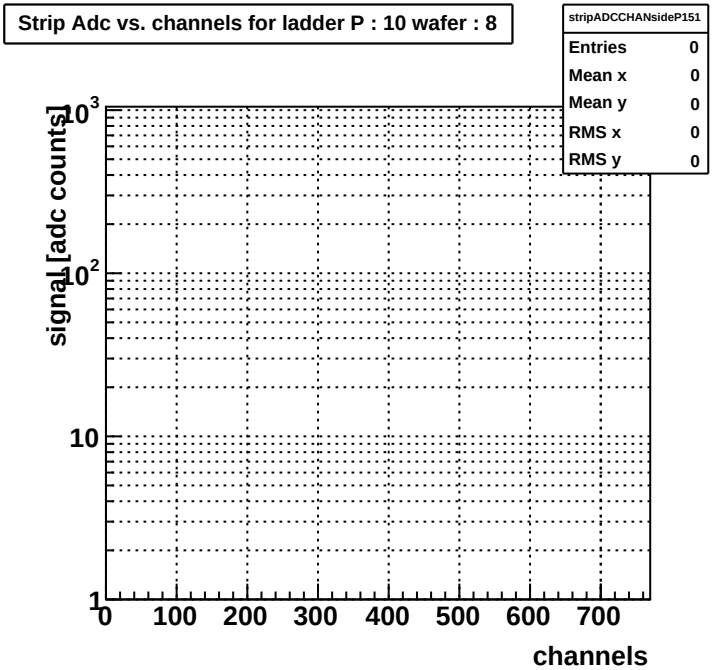

strip ADC for ladder N :10 wafer : 6
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

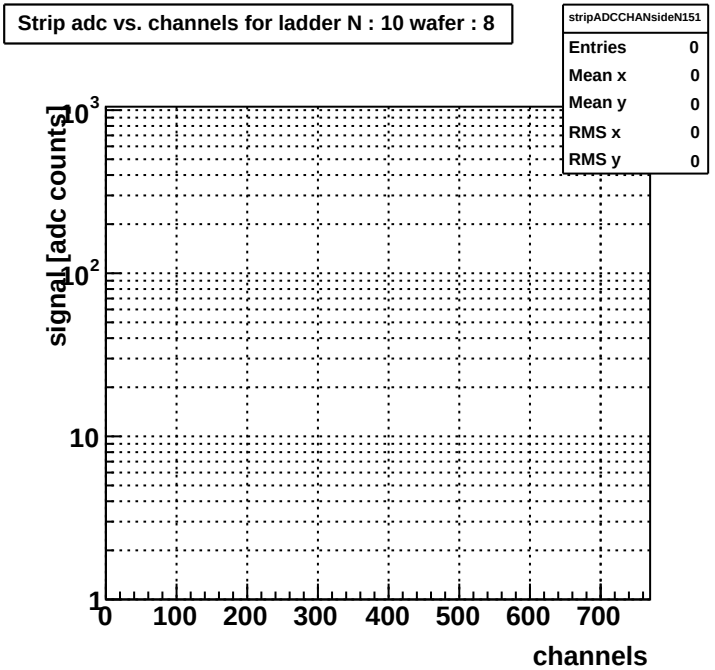

Strip Adc vs. channels for ladder P : 10 wafer : 7
stripADCCHANsideP150
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

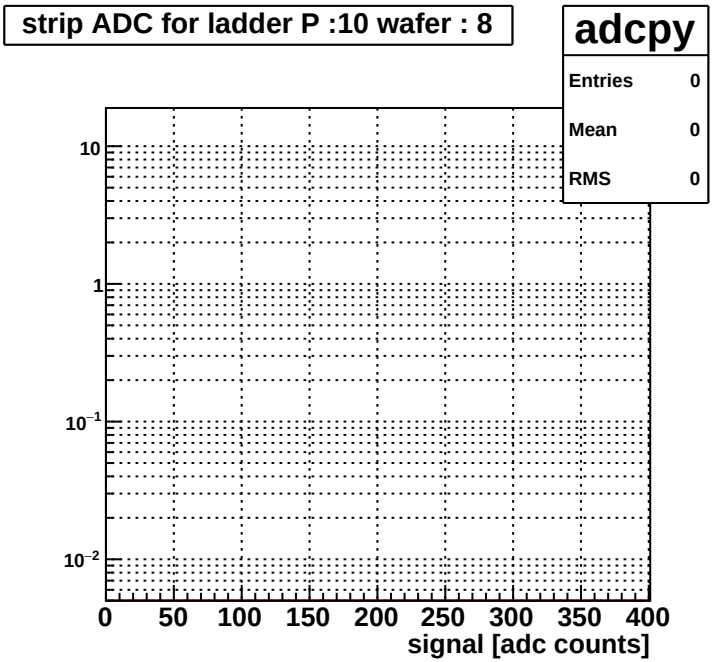

Strip adc vs. channels for ladder N : 10 wafer : 7
stripADCCHANsideN150
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

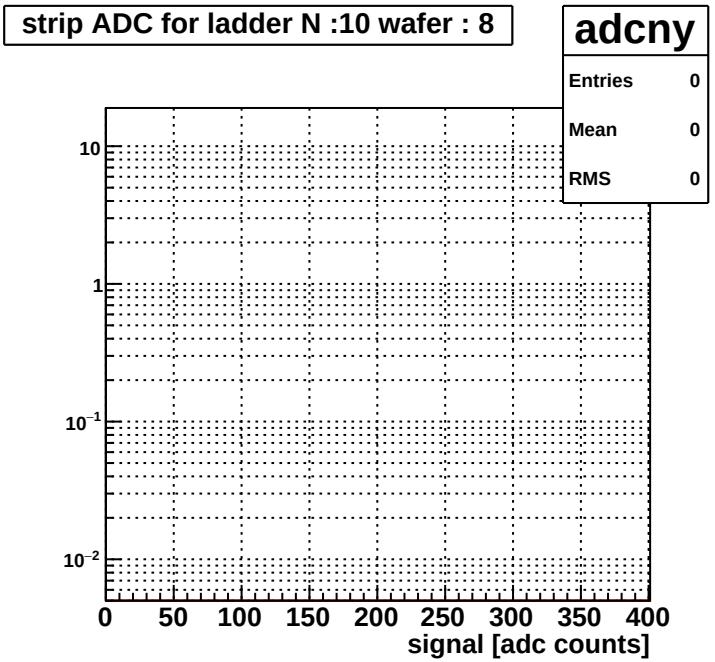

strip ADC for ladder P :10 wafer : 7
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

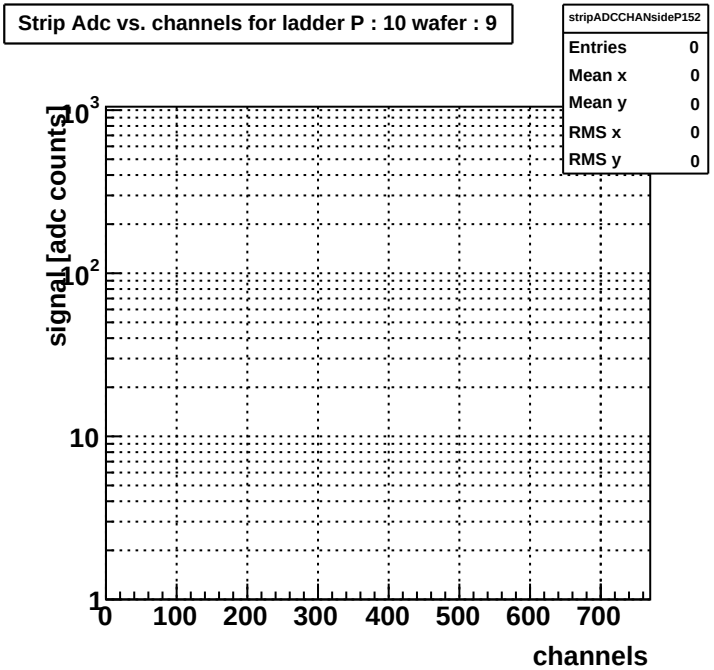

strip ADC for ladder N :10 wafer : 7
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

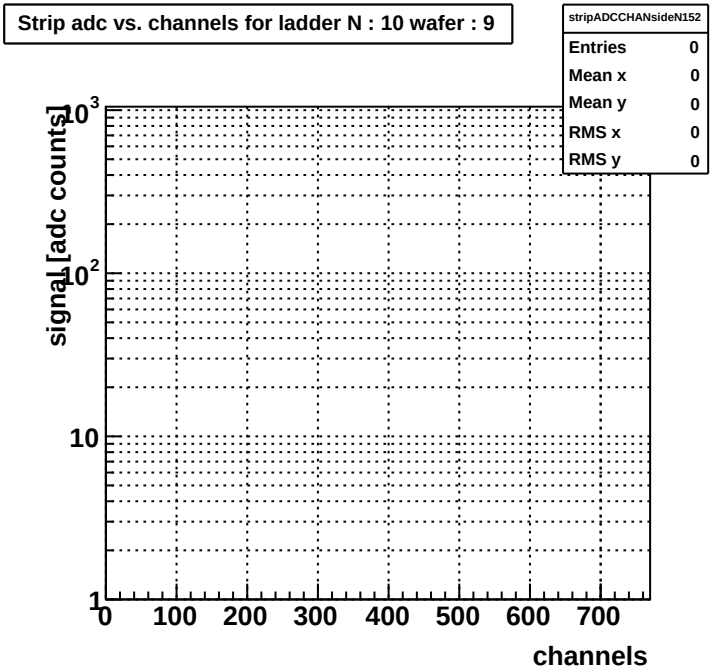

Strip Adc vs. channels for ladder P : 10 wafer : 8
stripADCCHANsideP151
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

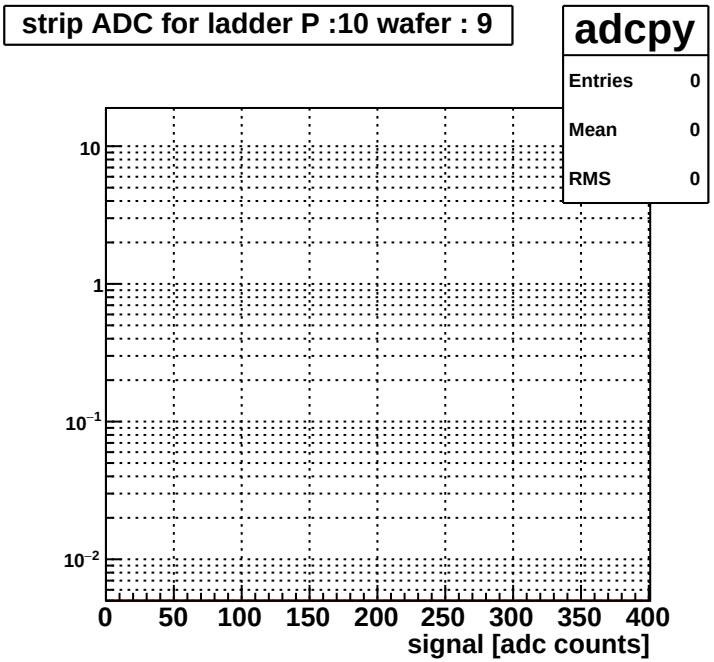

Strip adc vs. channels for ladder N : 10 wafer : 8
stripADCCHANsideN151
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

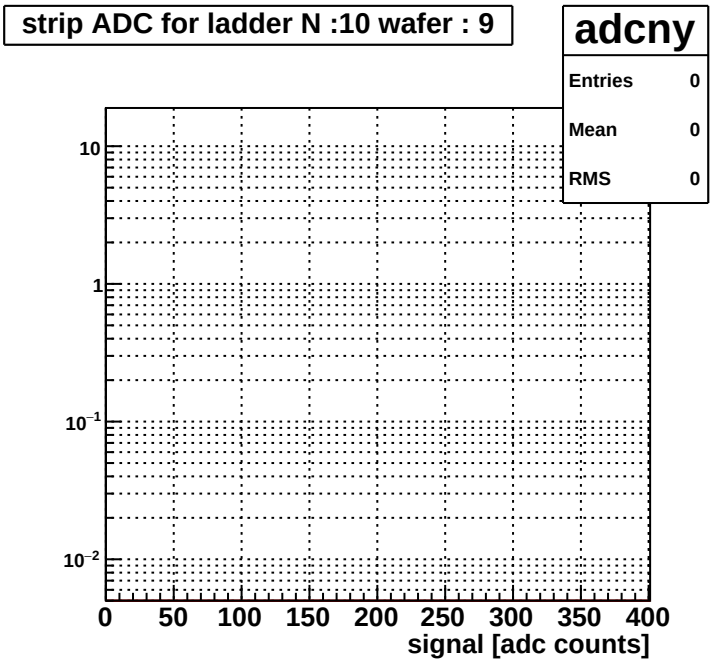

strip ADC for ladder P :10 wafer : 8
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

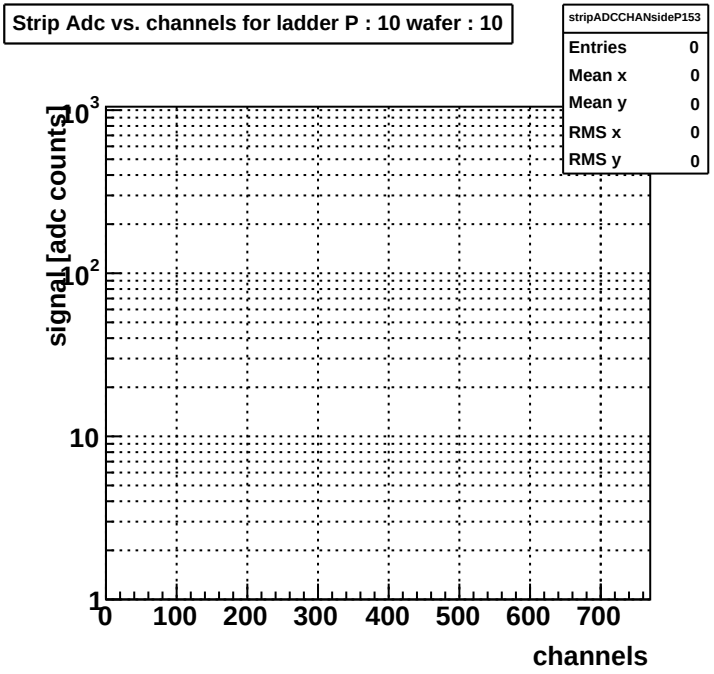

strip ADC for ladder N :10 wafer : 8
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

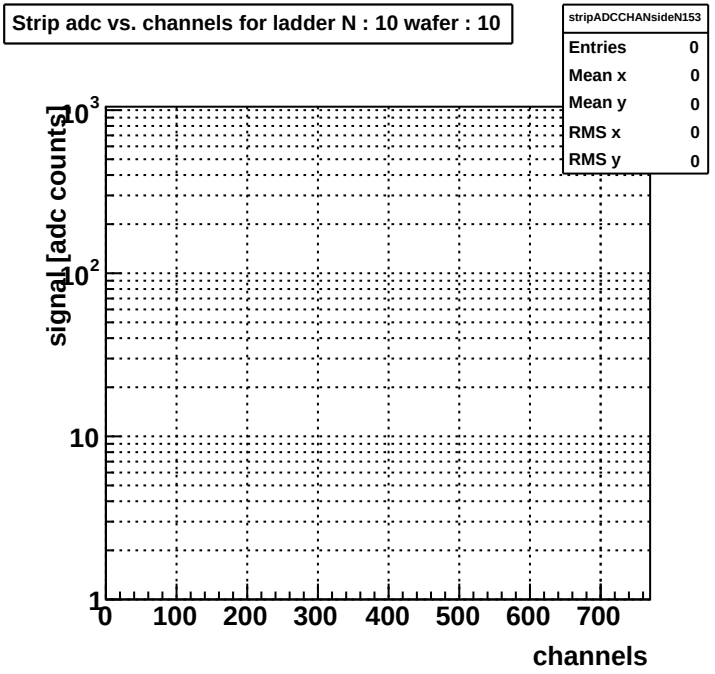

Strip Adc vs. channels for ladder P : 10 wafer : 9
stripADCCHANsideP152
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

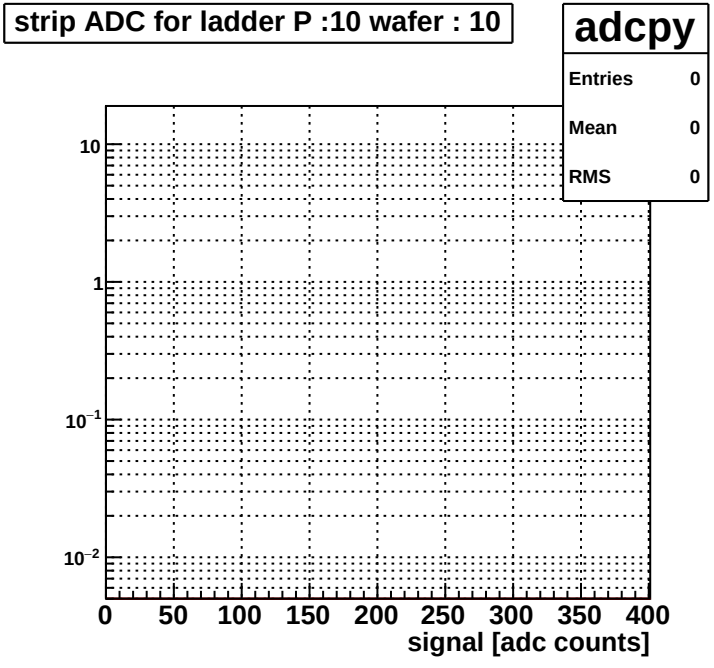

Strip adc vs. channels for ladder N : 10 wafer : 9
stripADCCHANsideN152
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

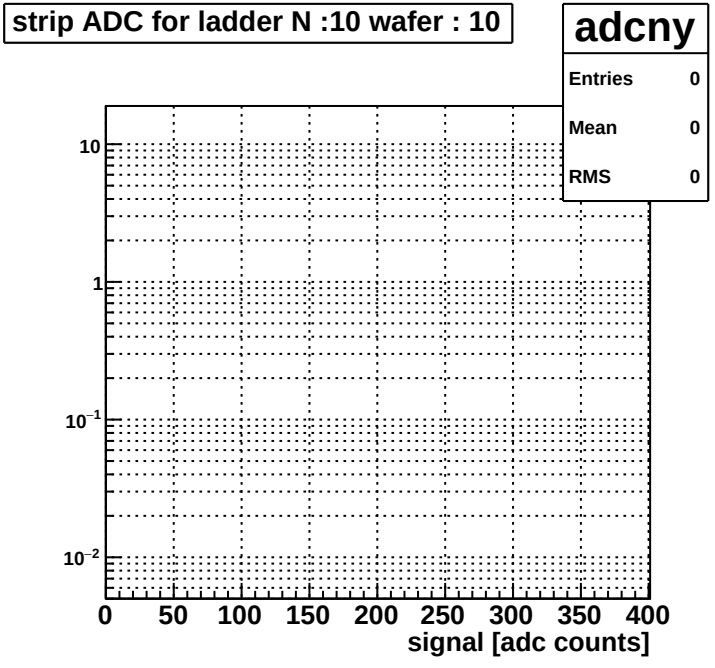

strip ADC for ladder P :10 wafer : 9
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

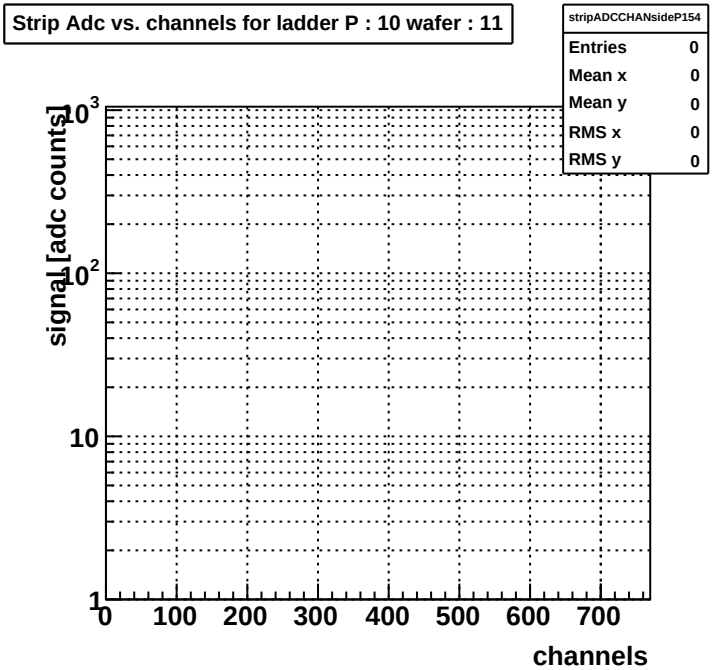

strip ADC for ladder N :10 wafer : 9
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

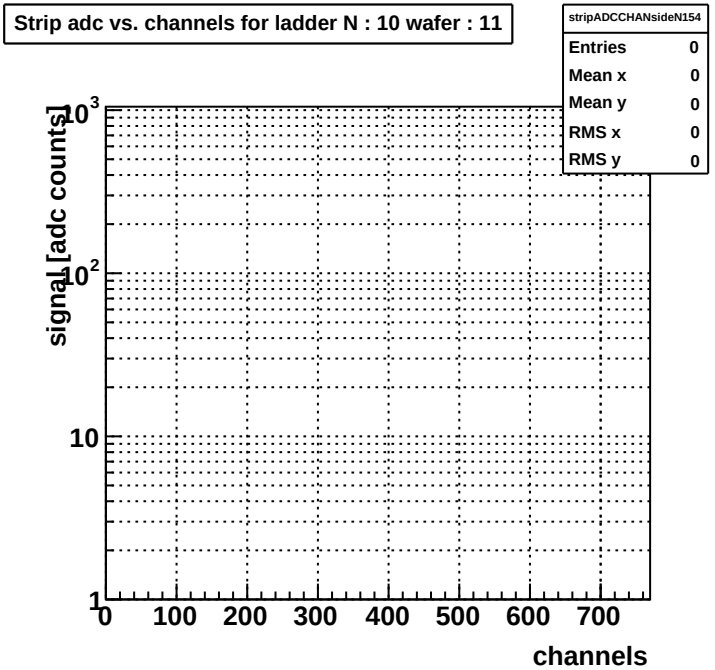

Strip Adc vs. channels for ladder P : 10 wafer : 10
stripADCCHANsideP153
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

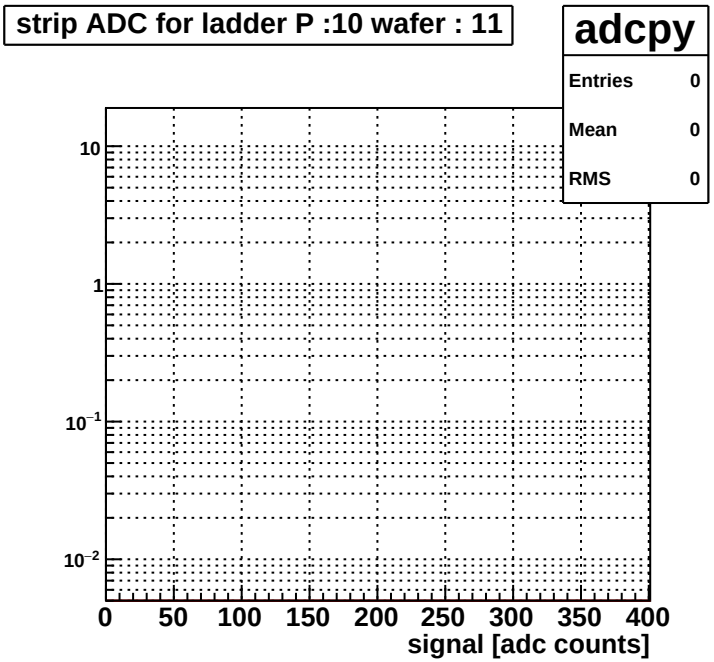

Strip adc vs. channels for ladder N : 10 wafer : 10
stripADCCHANsideN153
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

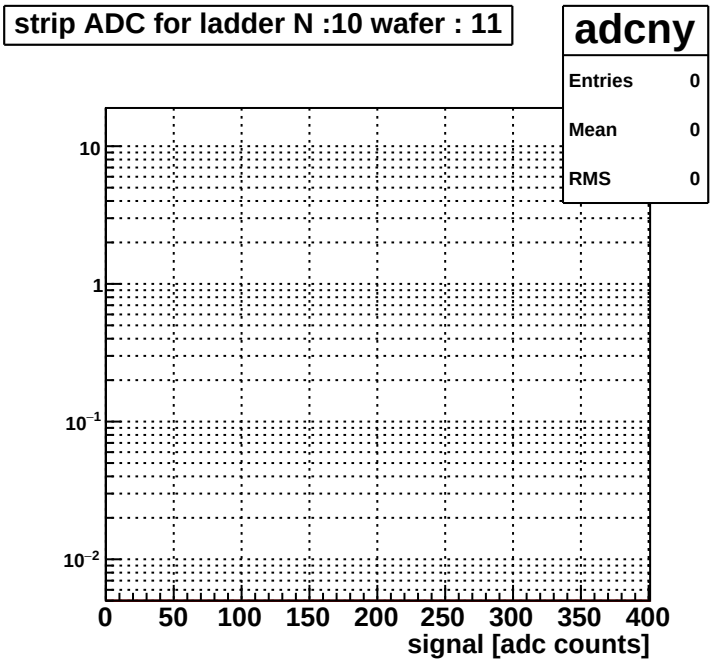

strip ADC for ladder P :10 wafer : 10
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

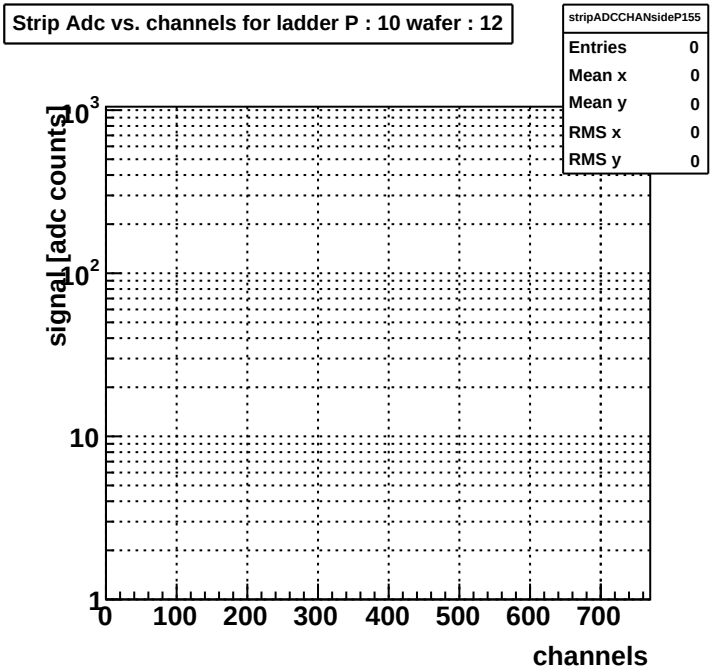

strip ADC for ladder N :10 wafer : 10
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

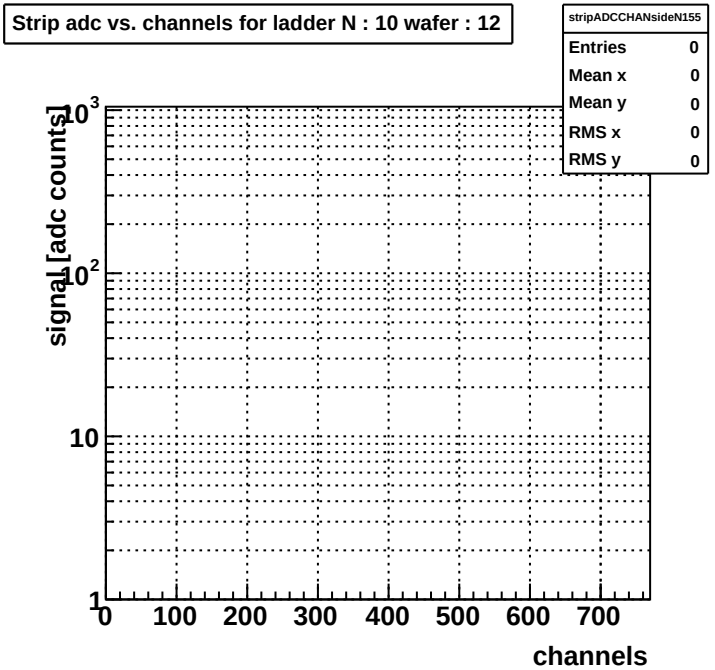

Strip Adc vs. channels for ladder P : 10 wafer : 11
stripADCCHANsideP154
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

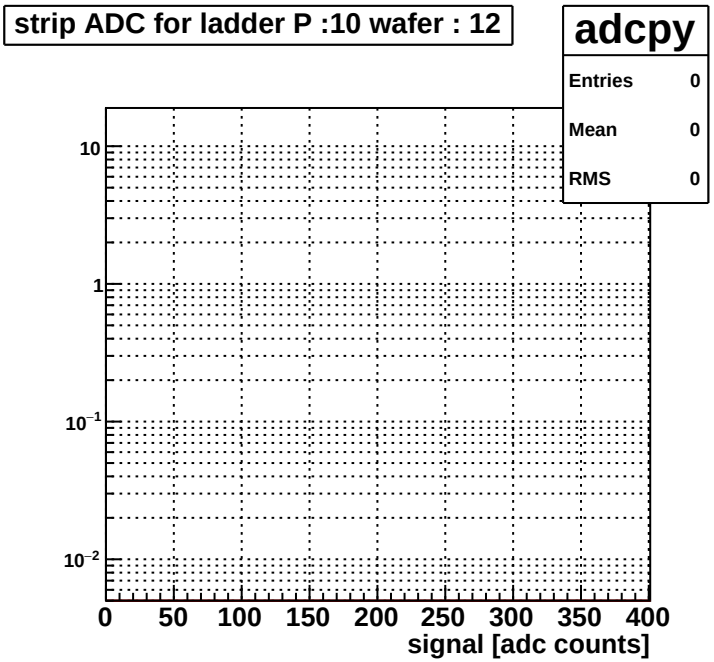

Strip adc vs. channels for ladder N : 10 wafer : 11
stripADCCHANsideN154
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

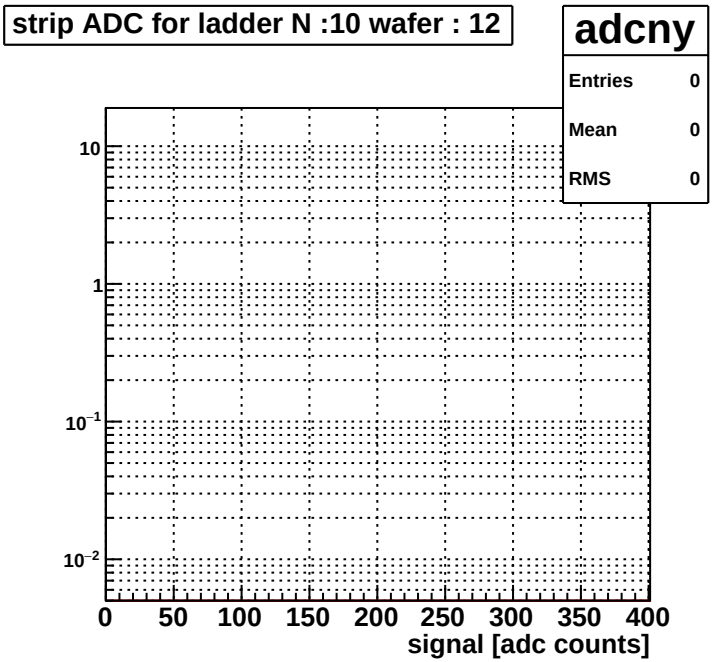

strip ADC for ladder P :10 wafer : 11
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

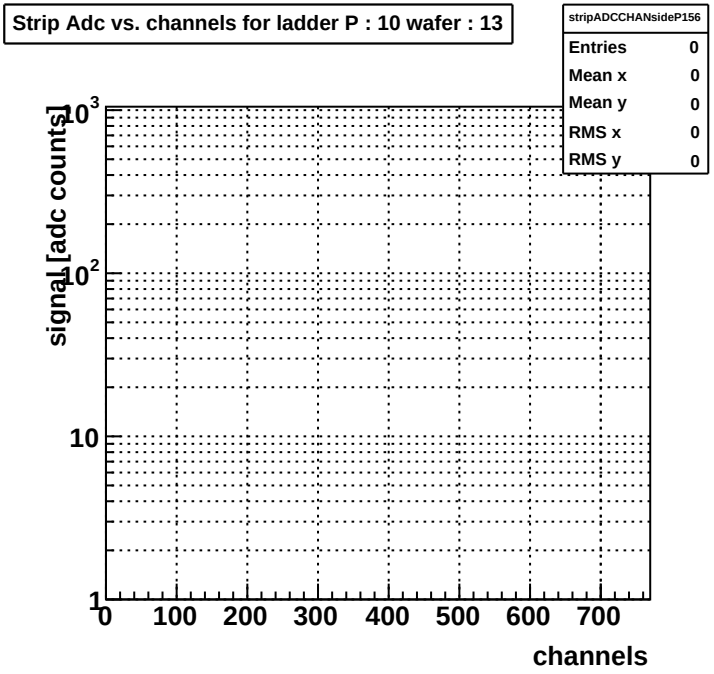

strip ADC for ladder N :10 wafer : 11
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

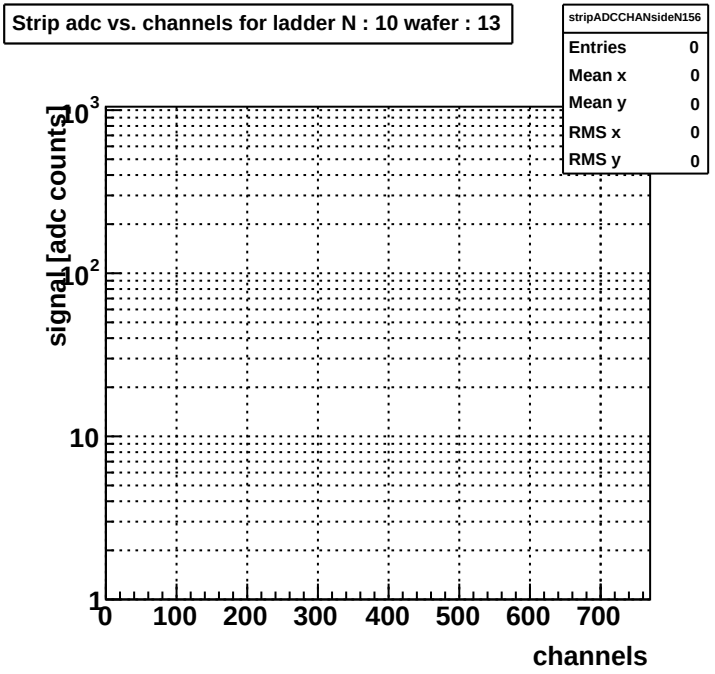

Strip Adc vs. channels for ladder P : 10 wafer : 12
stripADCCHANsideP155
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

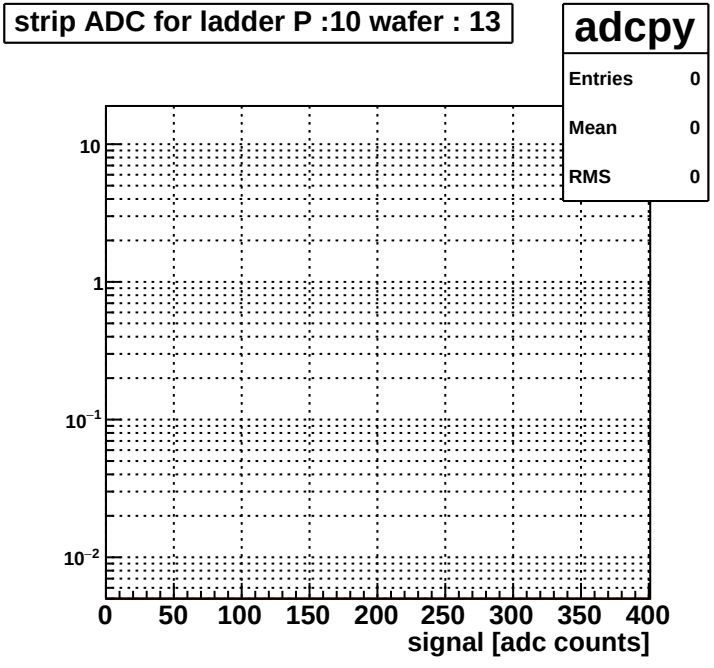

Strip adc vs. channels for ladder N : 10 wafer : 12
stripADCCHANsideN155
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

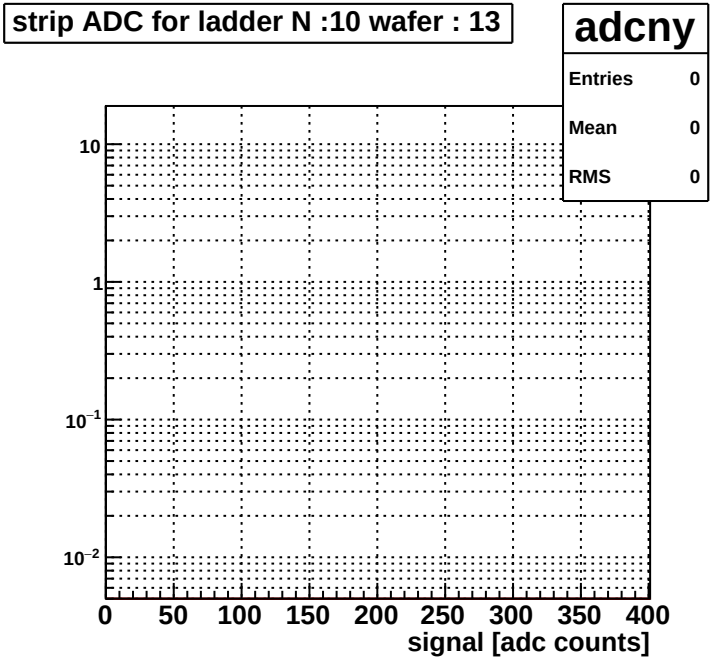

strip ADC for ladder P :10 wafer : 12
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

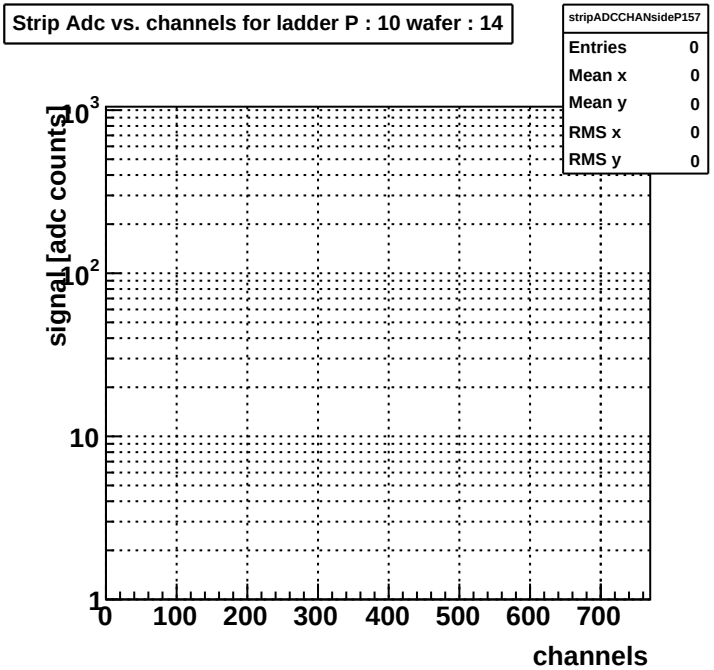

strip ADC for ladder N :10 wafer : 12
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

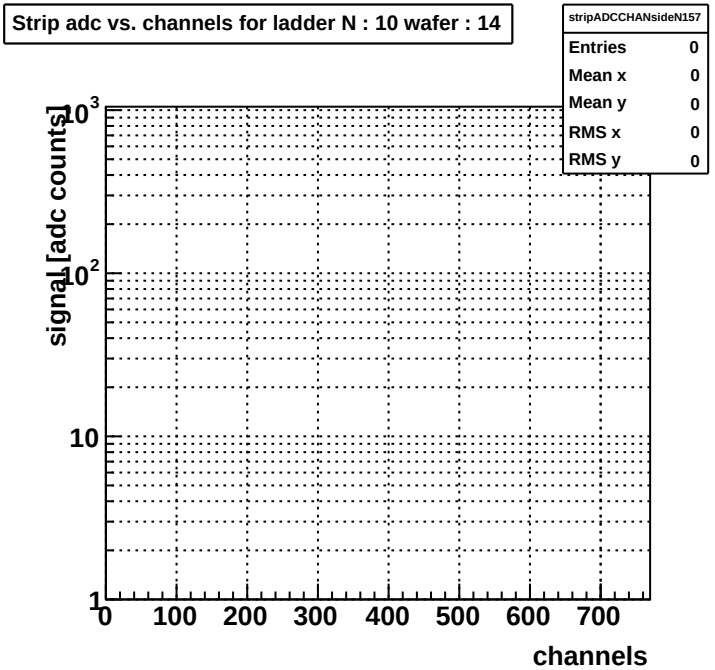

Strip Adc vs. channels for ladder P : 10 wafer : 13
stripADCCHANsideP156
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

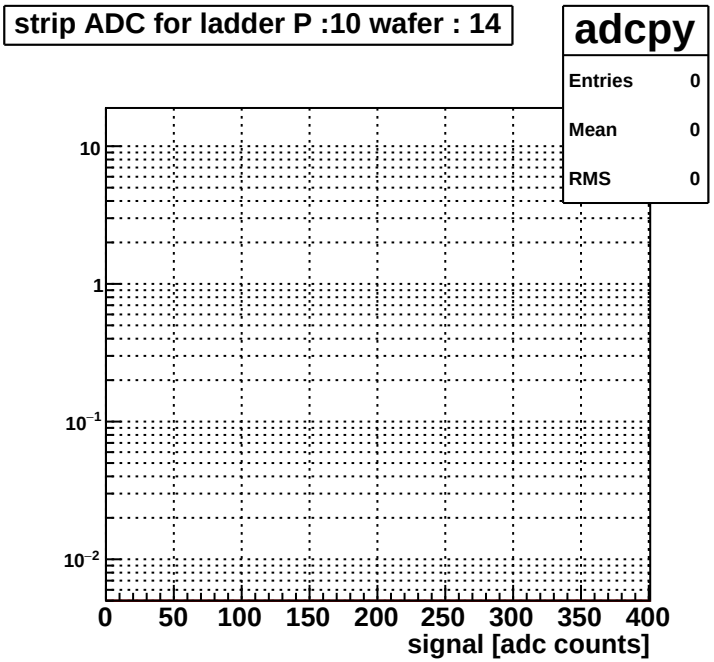

Strip adc vs. channels for ladder N : 10 wafer : 13
stripADCCHANsideN156
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

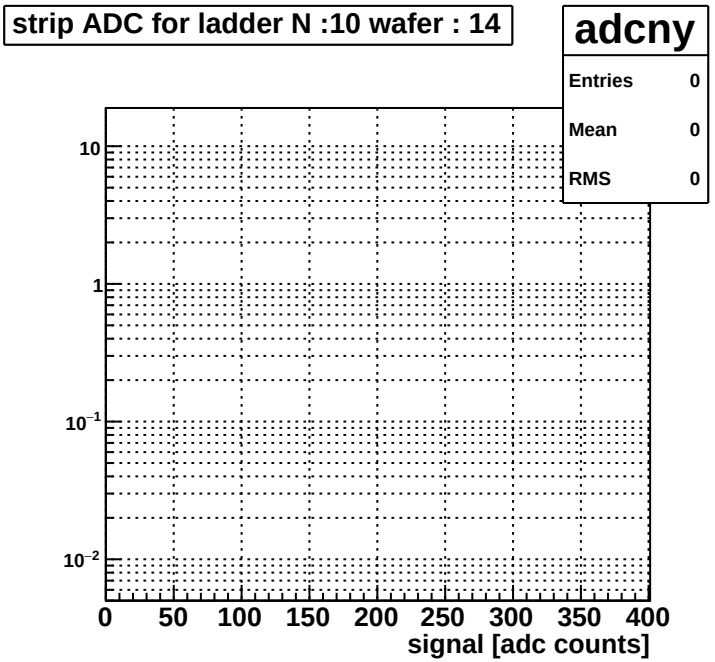

strip ADC for ladder P :10 wafer : 13
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

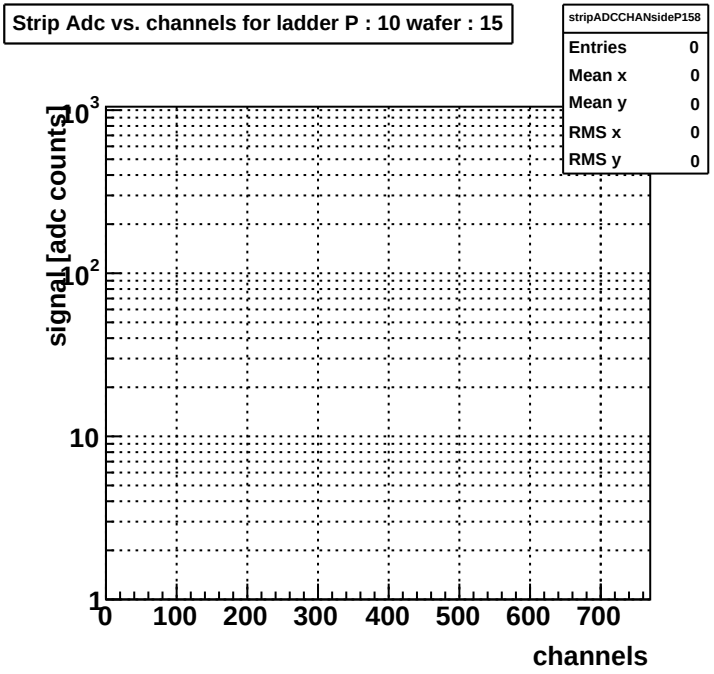

strip ADC for ladder N :10 wafer : 13
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

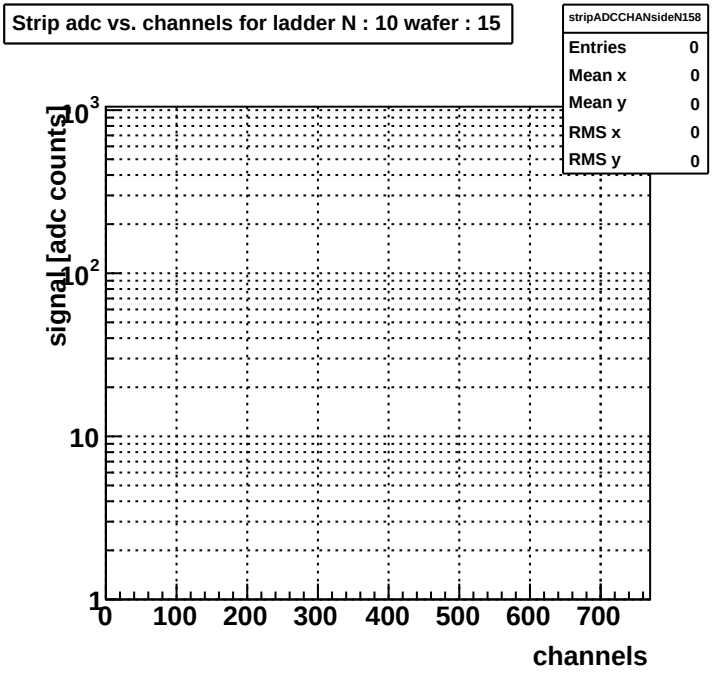

Strip Adc vs. channels for ladder P : 10 wafer : 14
stripADCCHANsideP157
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

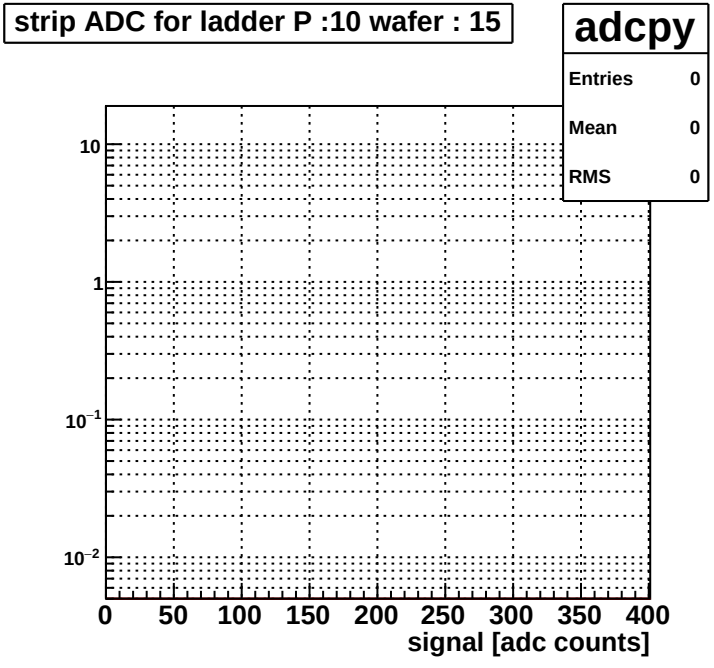

Strip adc vs. channels for ladder N : 10 wafer : 14
stripADCCHANsideN157
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

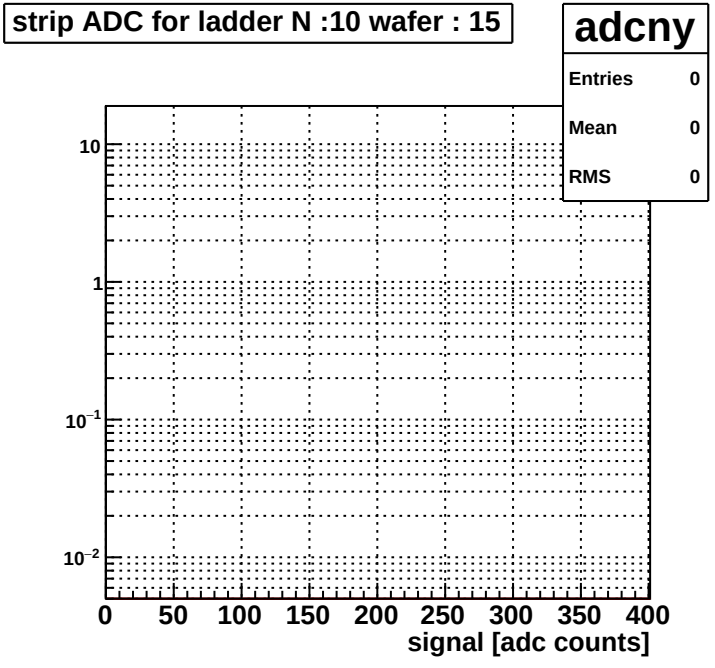

strip ADC for ladder P :10 wafer : 14
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

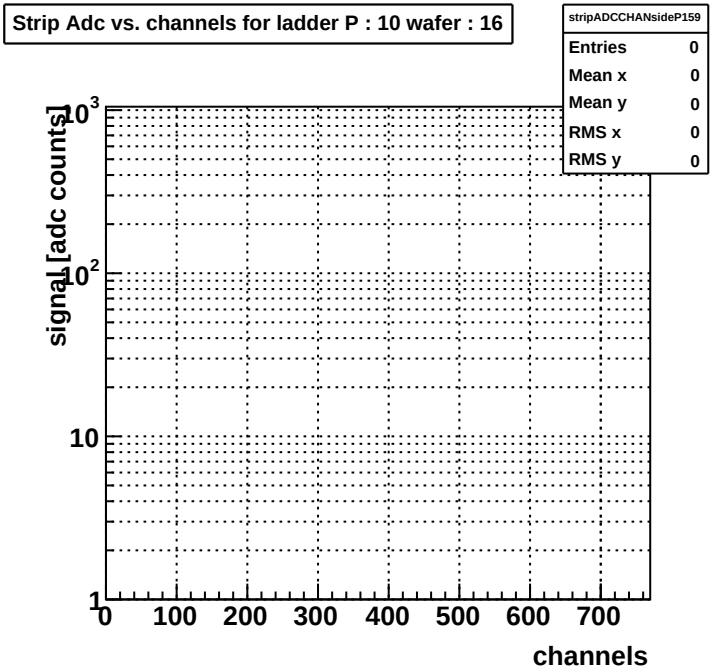

strip ADC for ladder N :10 wafer : 14
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

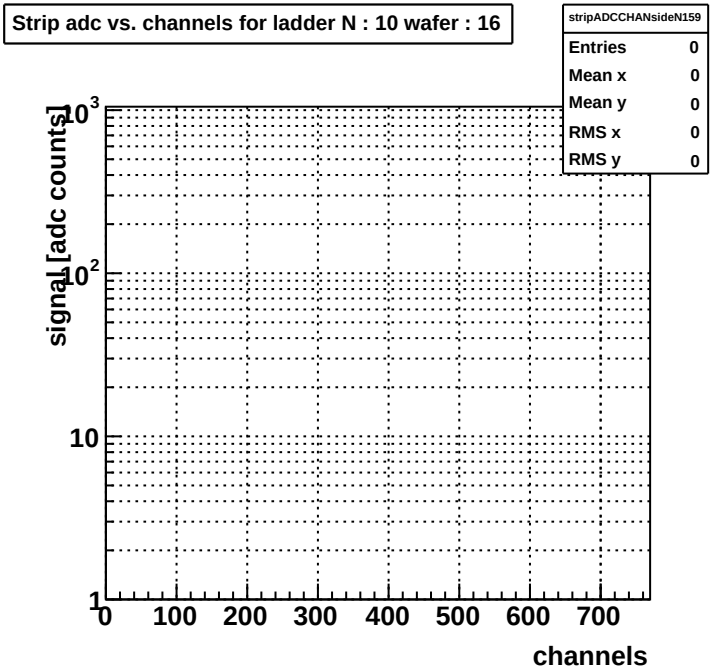

Strip Adc vs. channels for ladder P : 10 wafer : 15
stripADCCHANsideP158
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

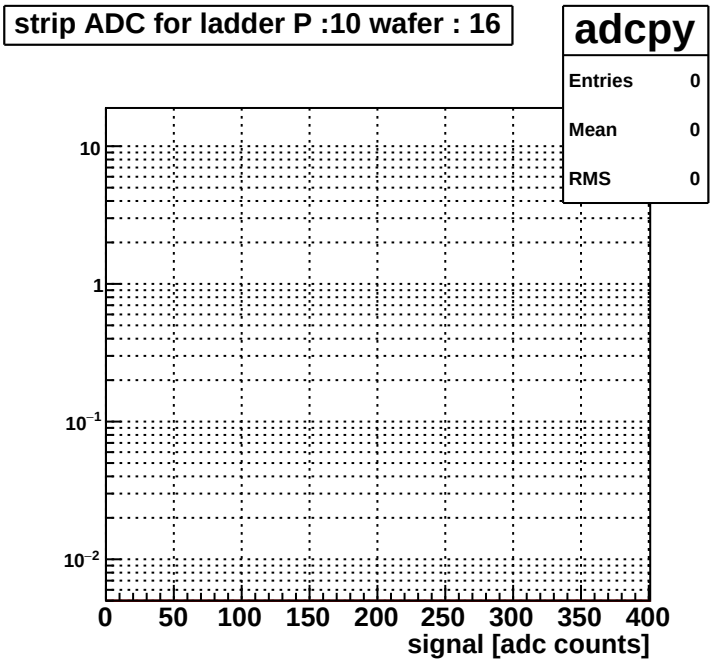

Strip adc vs. channels for ladder N : 10 wafer : 15
stripADCCHANsideN158
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

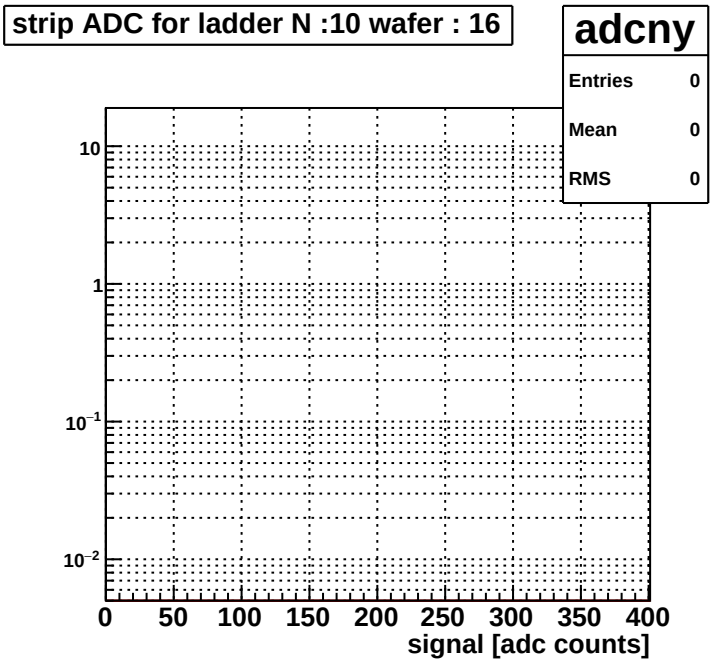

strip ADC for ladder P :10 wafer : 15
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

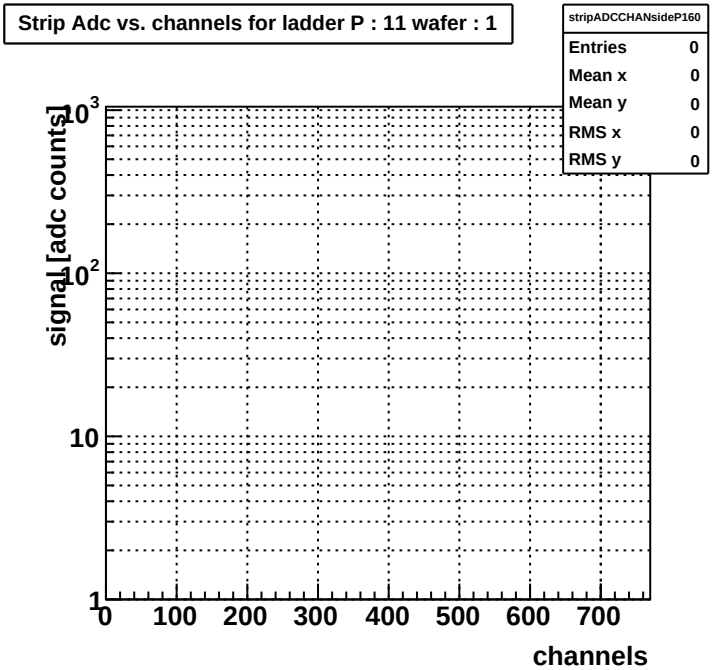

strip ADC for ladder N :10 wafer : 15
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

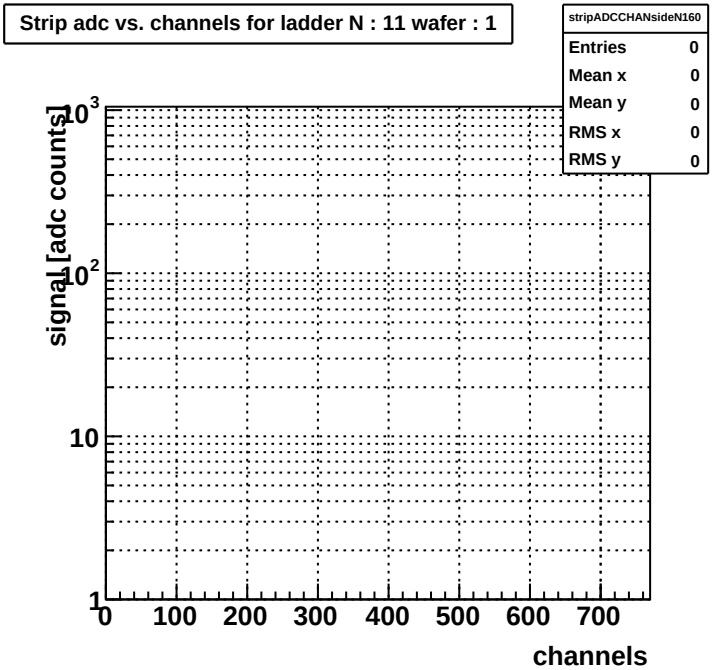

Strip Adc vs. channels for ladder P : 10 wafer : 16
stripADCCHANsideP159
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

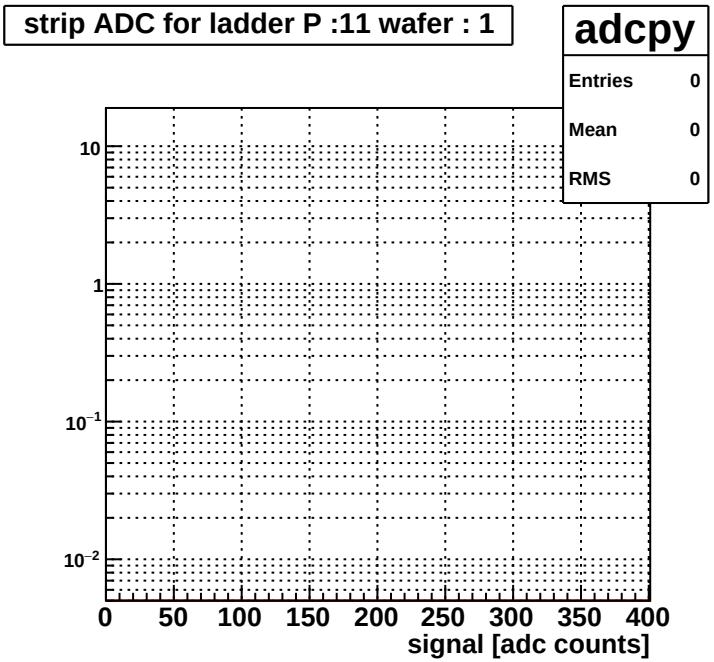

Strip adc vs. channels for ladder N : 10 wafer : 16
stripADCCHANsideN159
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

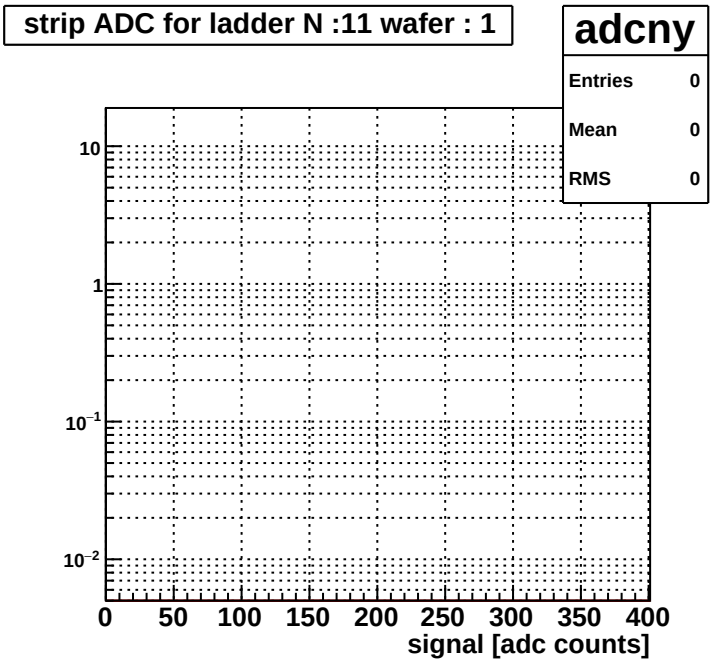

strip ADC for ladder P :10 wafer : 16
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

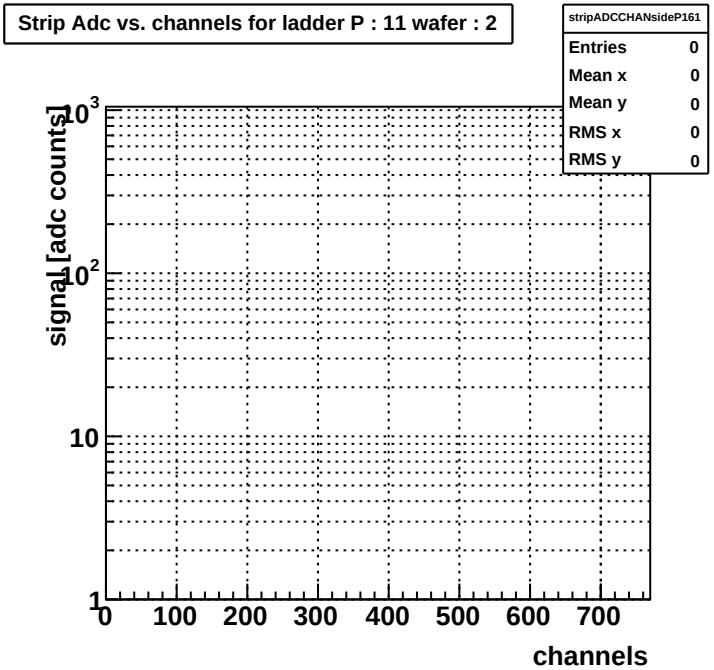

strip ADC for ladder N :10 wafer : 16
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

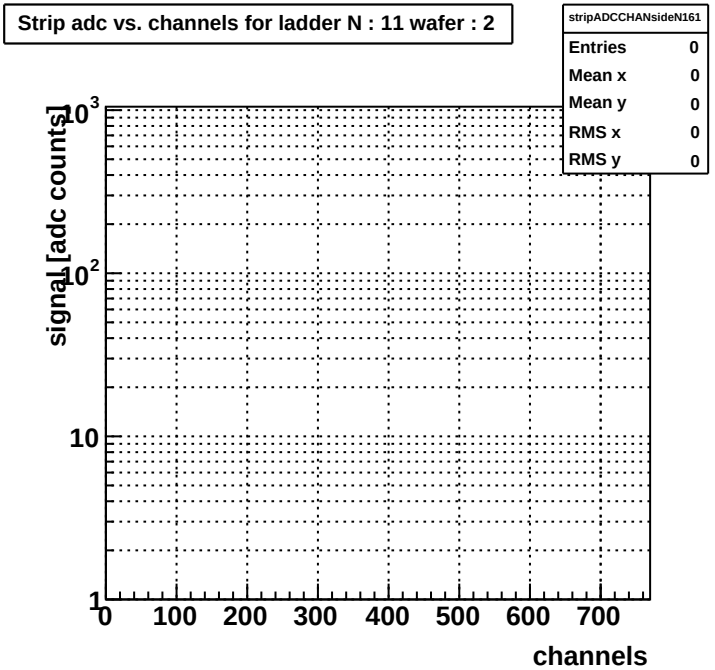

Strip Adc vs. channels for ladder P : 11 wafer : 1
stripADCCHANsideP160
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

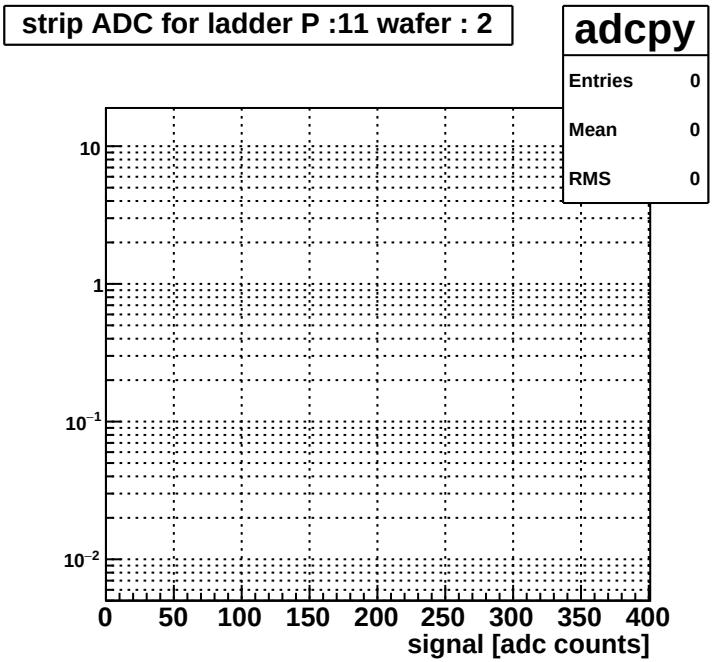

Strip adc vs. channels for ladder N : 11 wafer : 1
stripADCCHANsideN160
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

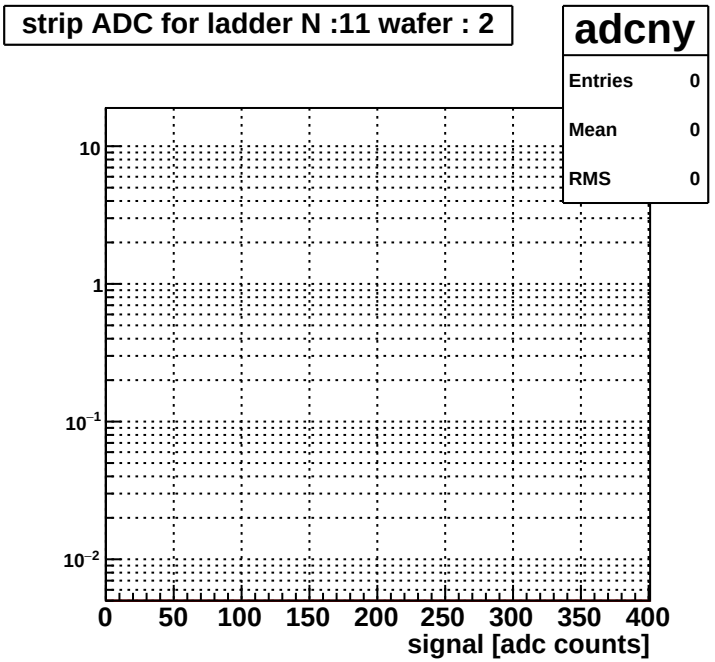

strip ADC for ladder P :11 wafer : 1
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

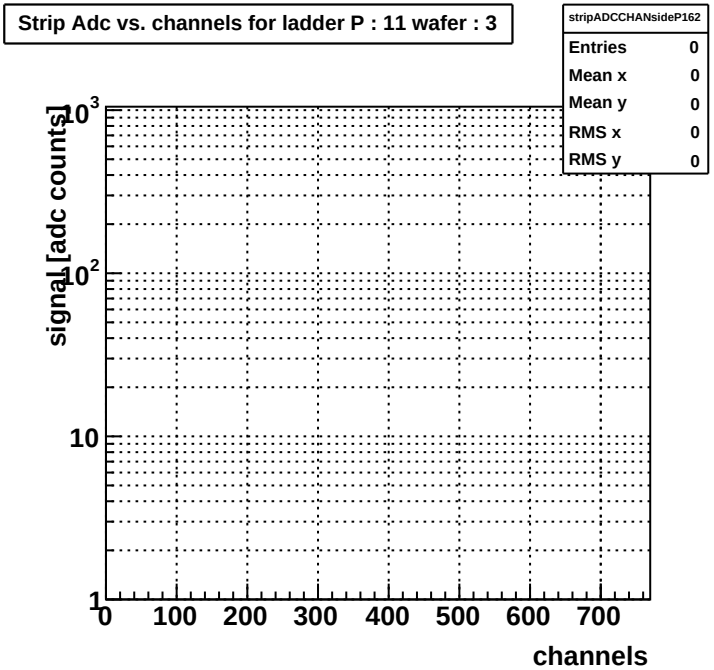

strip ADC for ladder N :11 wafer : 1
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

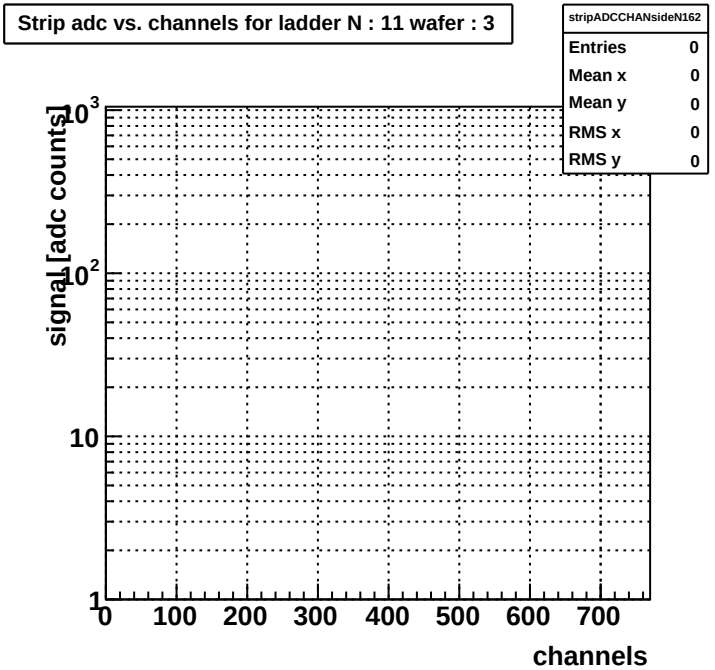

Strip Adc vs. channels for ladder P : 11 wafer : 2
stripADCCHANsideP161
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

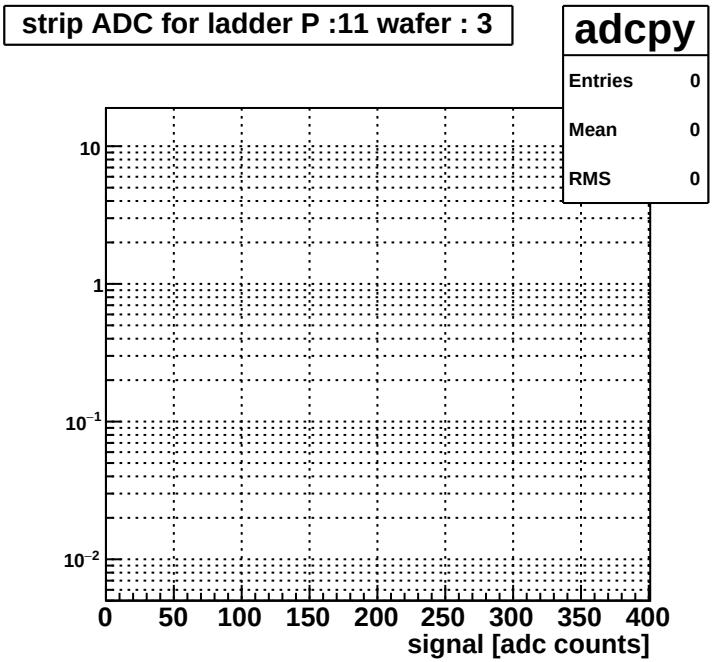

Strip adc vs. channels for ladder N : 11 wafer : 2
stripADCCHANsideN161
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

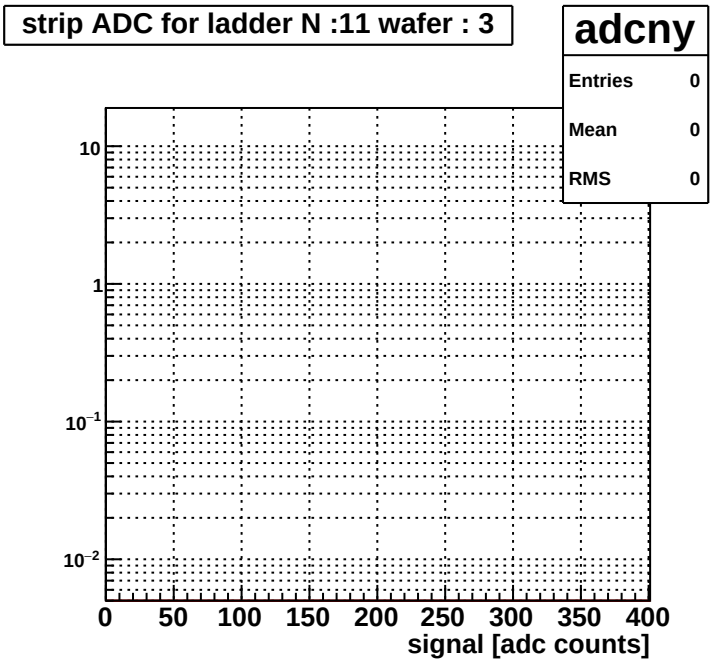

strip ADC for ladder P :11 wafer : 2
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

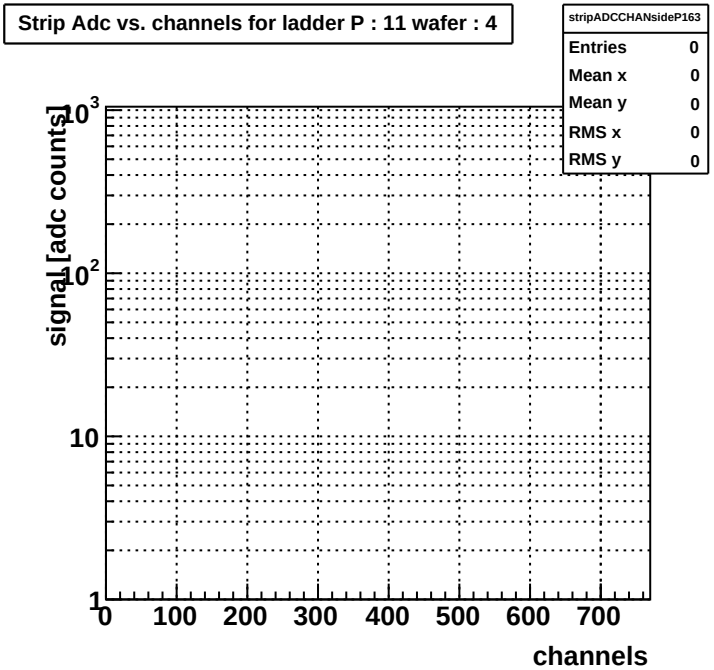

strip ADC for ladder N :11 wafer : 2
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

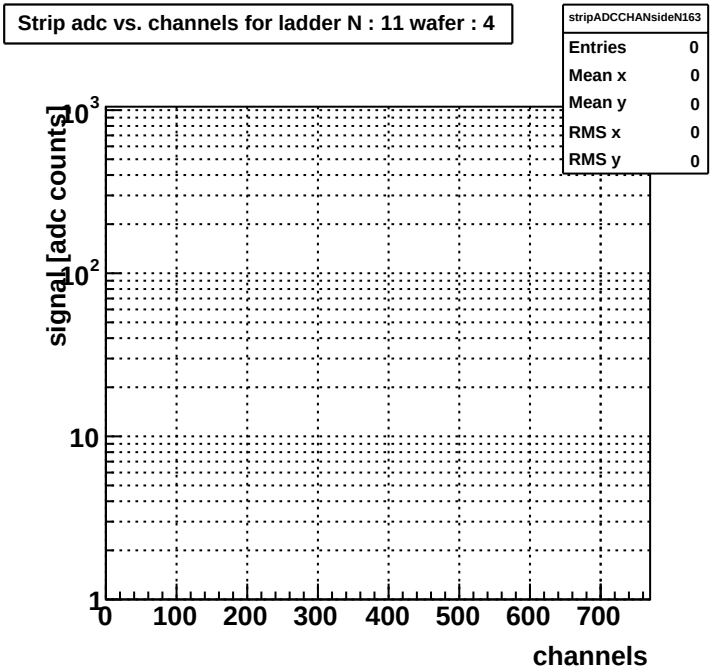

Strip Adc vs. channels for ladder P : 11 wafer : 3
stripADCCHANsideP162
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

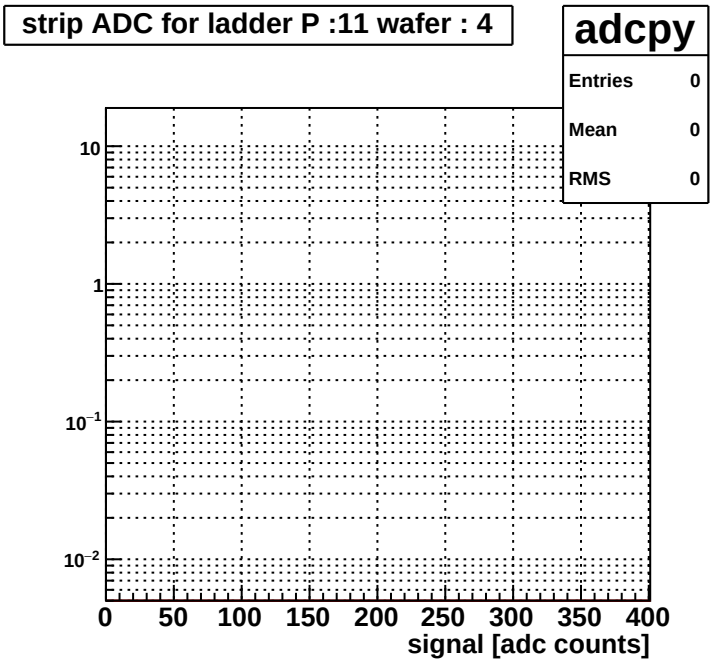

Strip adc vs. channels for ladder N : 11 wafer : 3
stripADCCHANsideN162
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

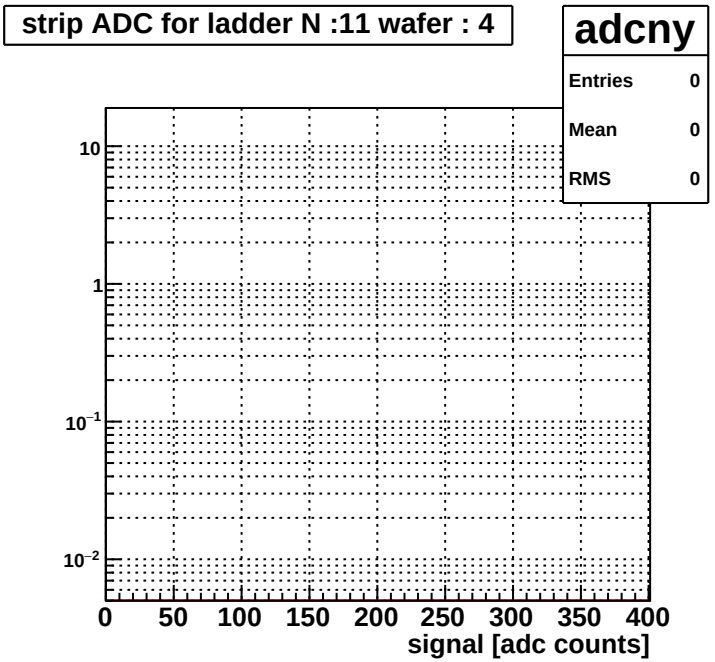

strip ADC for ladder P :11 wafer : 3
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

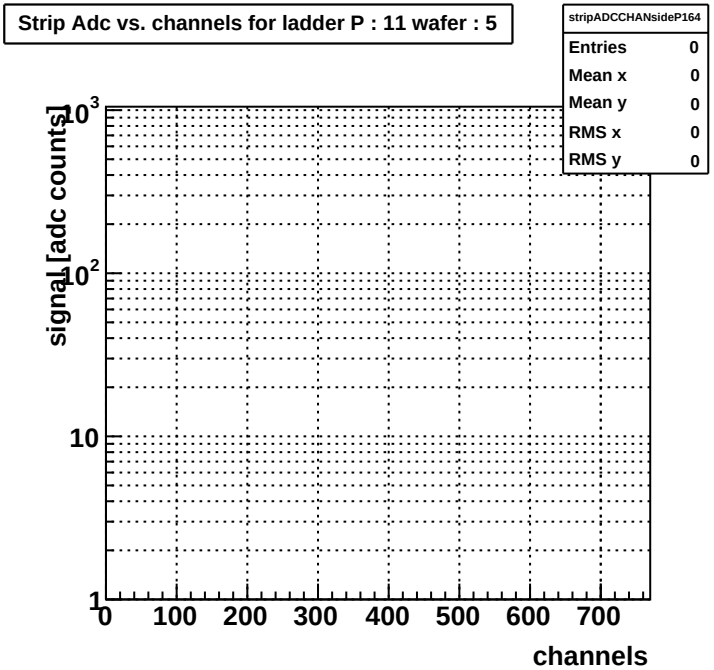

strip ADC for ladder N :11 wafer : 3
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

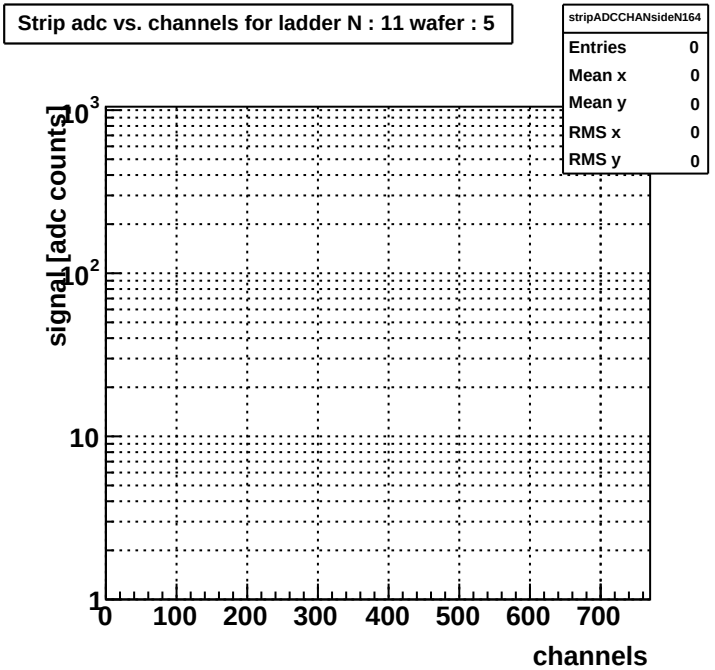

Strip Adc vs. channels for ladder P : 11 wafer : 4
stripADCCHANsideP163
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

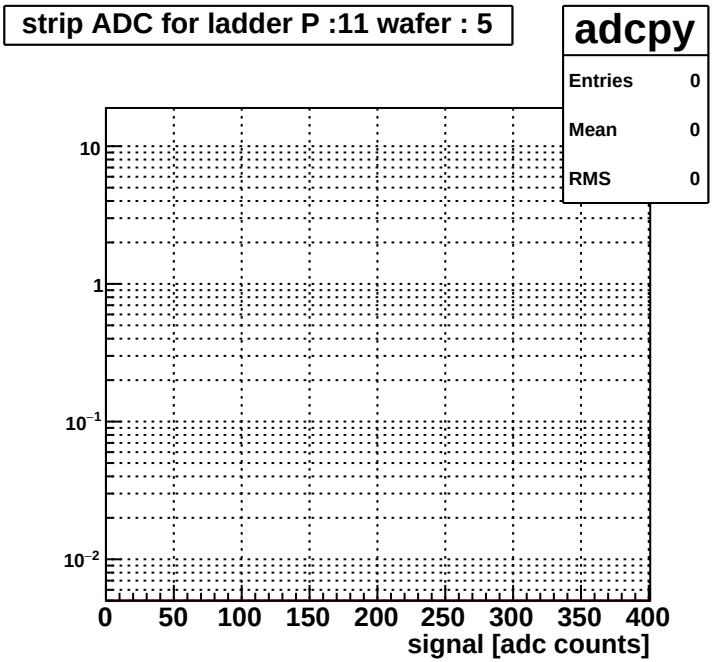

Strip adc vs. channels for ladder N : 11 wafer : 4
stripADCCHANsideN163
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

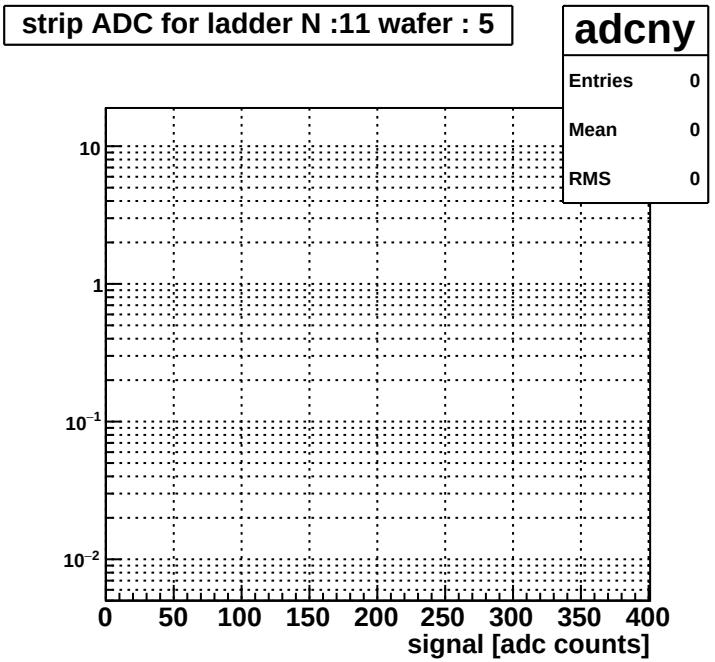

strip ADC for ladder P :11 wafer : 4
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

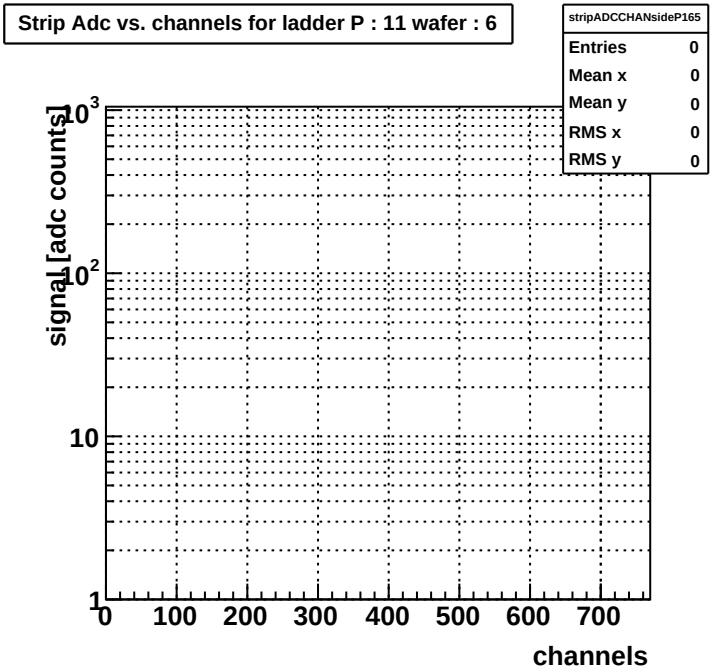

strip ADC for ladder N :11 wafer : 4
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

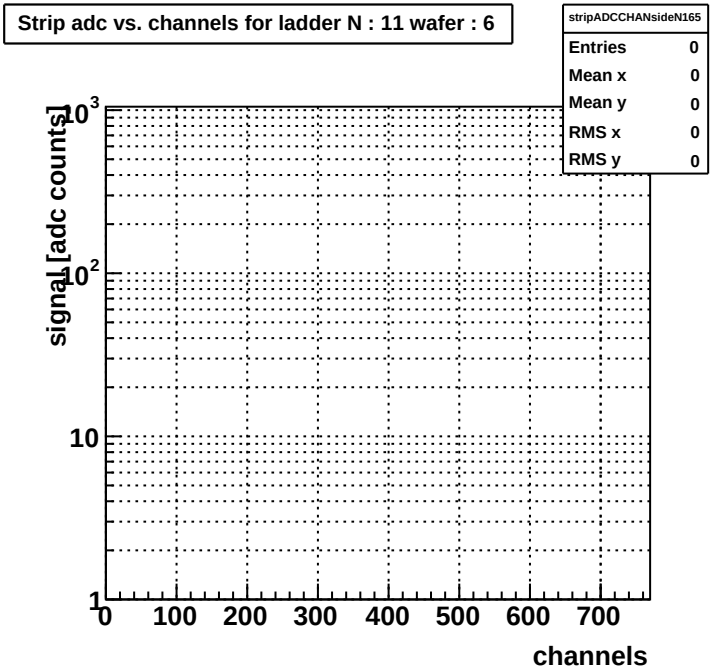

Strip Adc vs. channels for ladder P : 11 wafer : 5
stripADCCHANsideP164
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

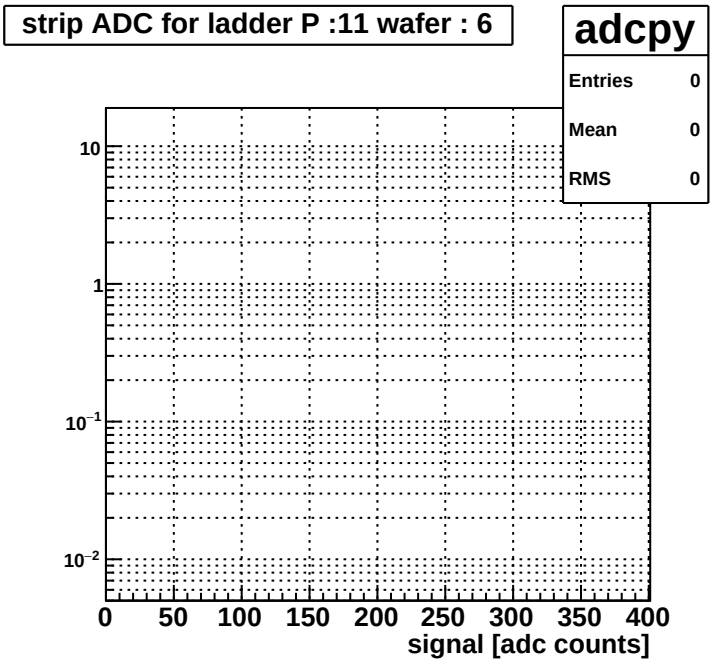

Strip adc vs. channels for ladder N : 11 wafer : 5
stripADCCHANsideN164
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

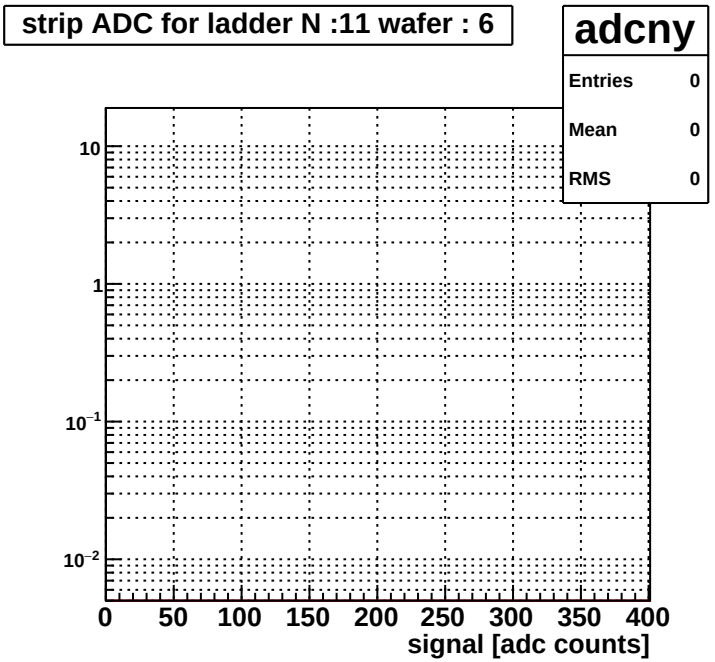

strip ADC for ladder P :11 wafer : 5
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

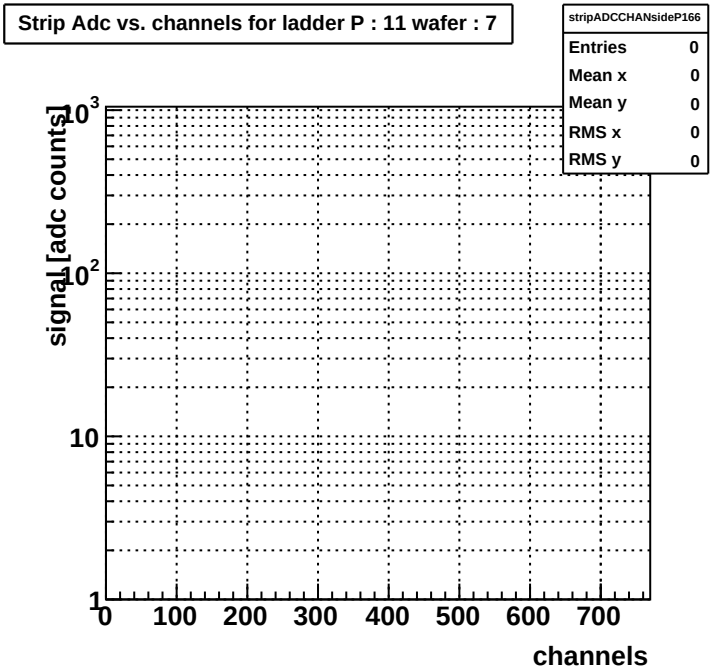

strip ADC for ladder N :11 wafer : 5
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

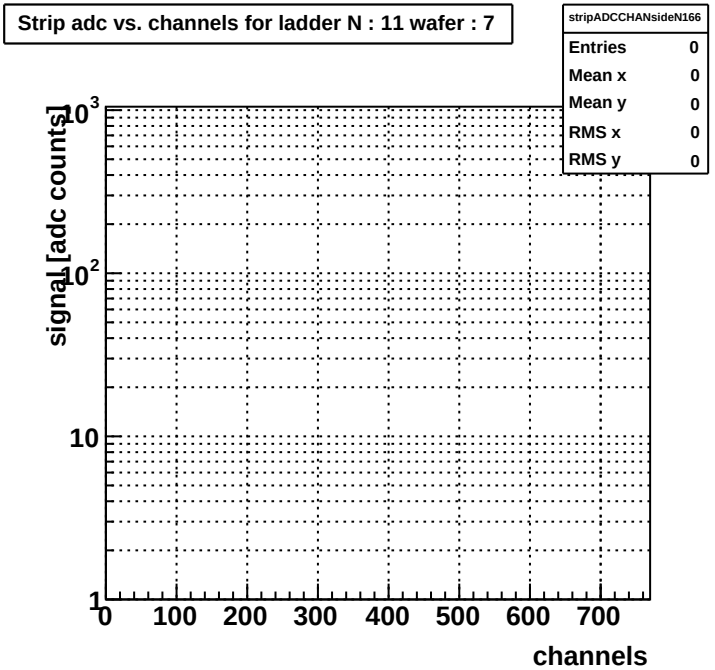

Strip Adc vs. channels for ladder P : 11 wafer : 6
stripADCCHANsideP165
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

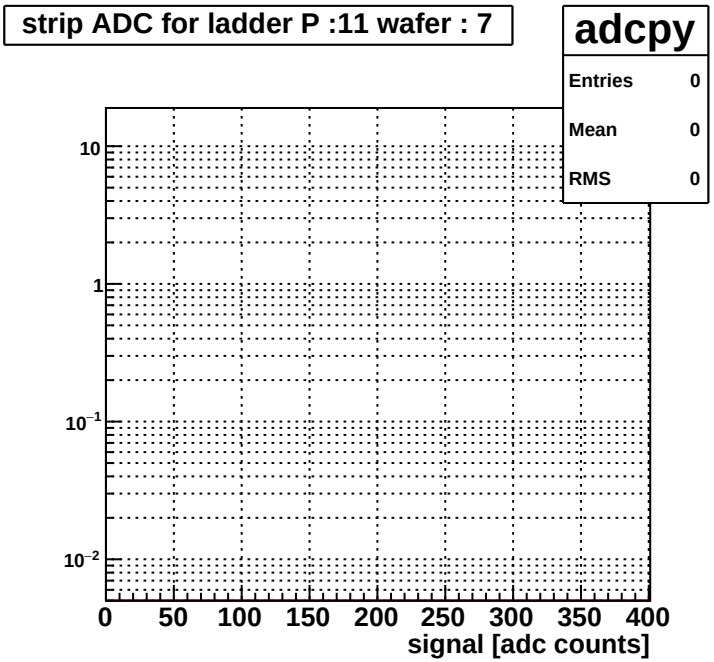

Strip adc vs. channels for ladder N : 11 wafer : 6
stripADCCHANsideN165
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

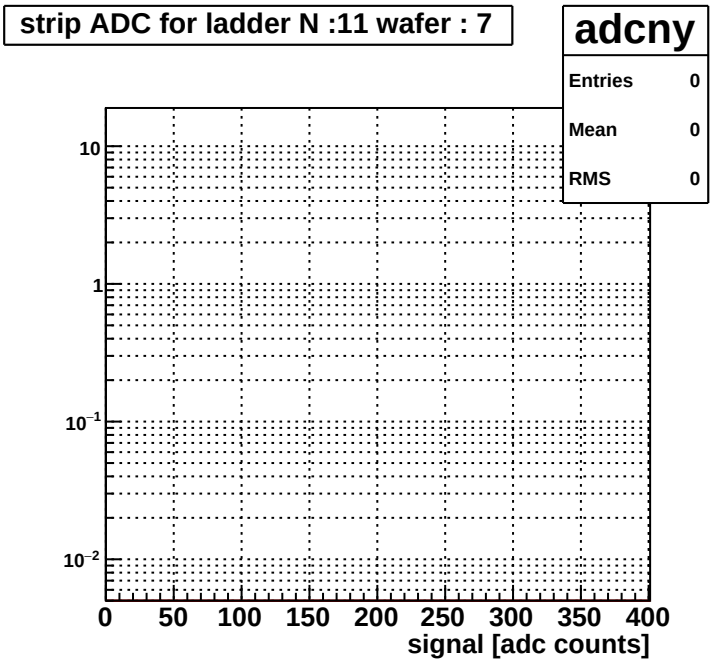

strip ADC for ladder P :11 wafer : 6
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

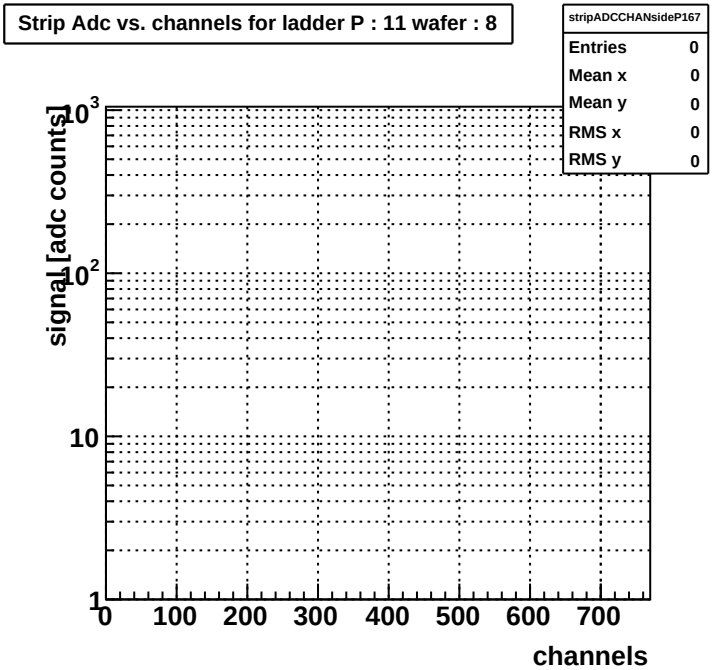

strip ADC for ladder N :11 wafer : 6
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

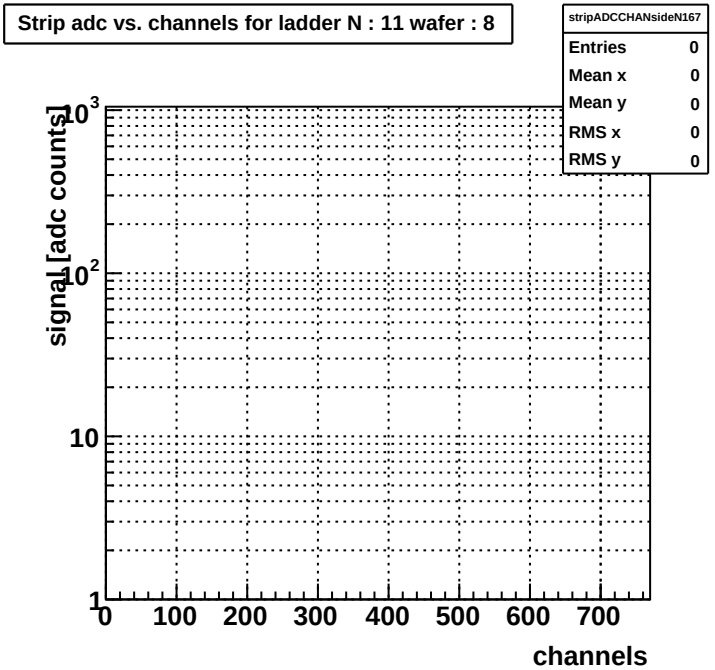

Strip Adc vs. channels for ladder P : 11 wafer : 7
stripADCCHANsideP166
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

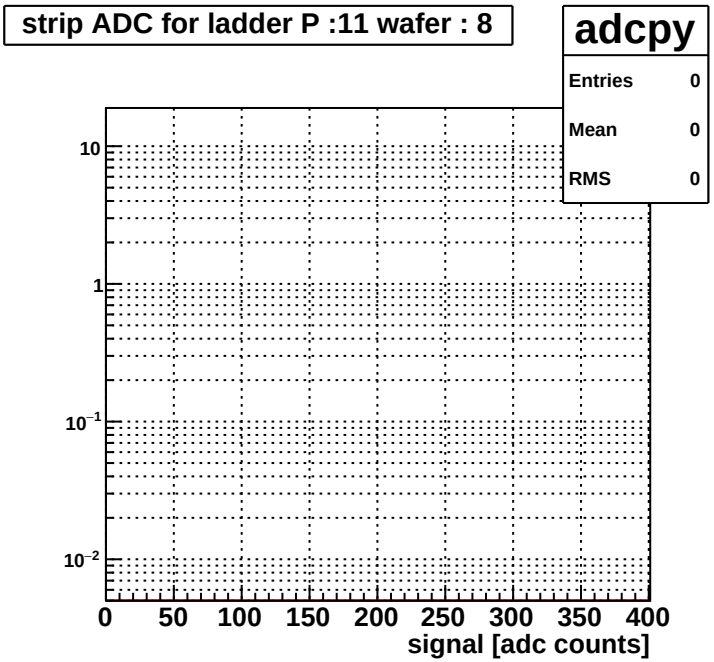

Strip adc vs. channels for ladder N : 11 wafer : 7
stripADCCHANsideN166
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

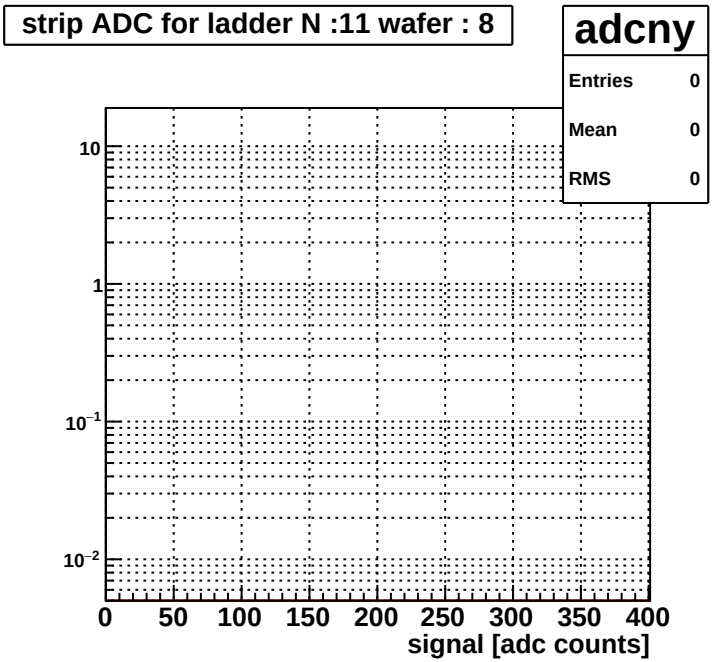

strip ADC for ladder P :11 wafer : 7
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

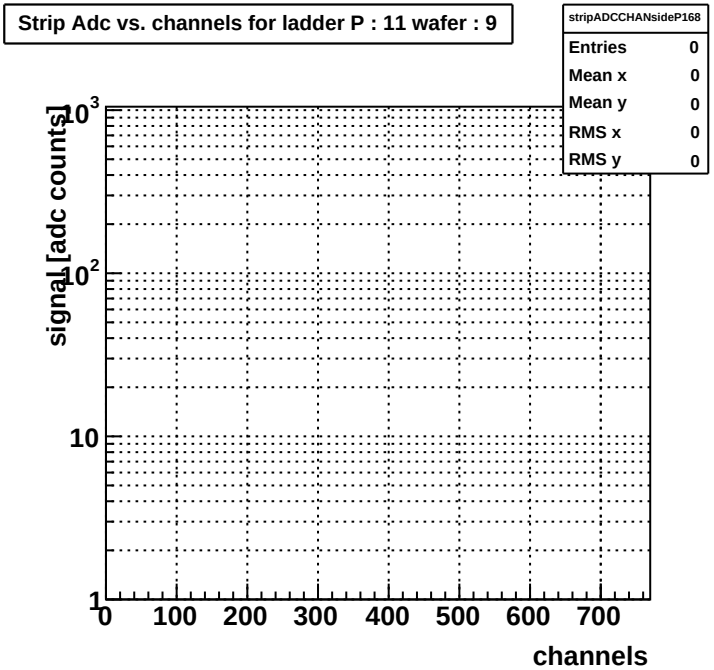

strip ADC for ladder N :11 wafer : 7
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

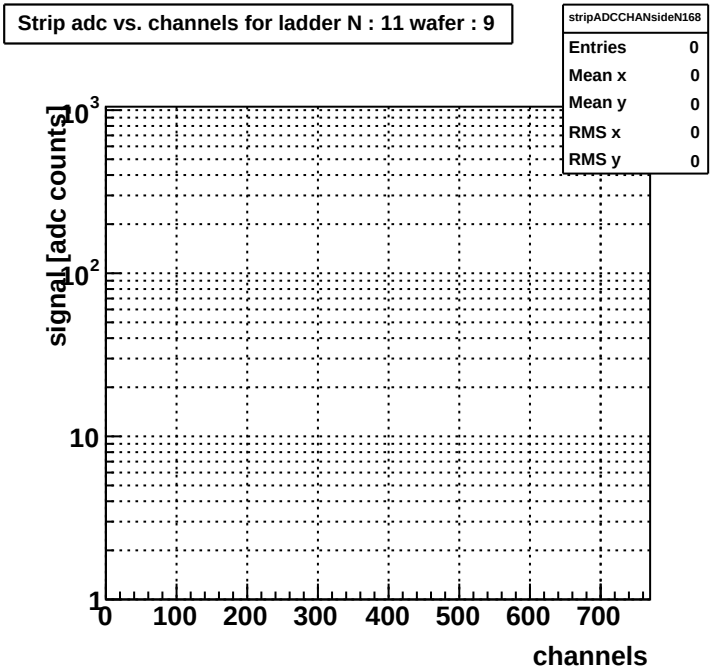

Strip Adc vs. channels for ladder P : 11 wafer : 8
stripADCCHANsideP167
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

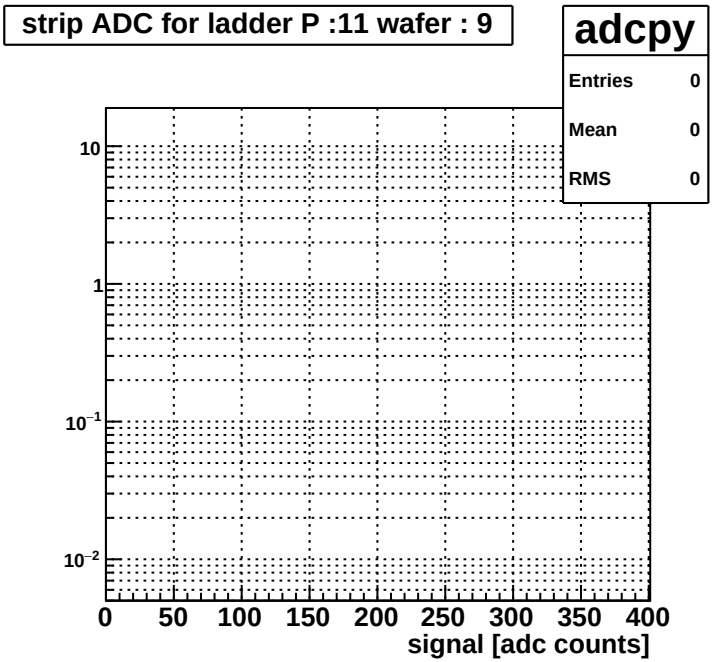

Strip adc vs. channels for ladder N : 11 wafer : 8
stripADCCHANsideN167
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

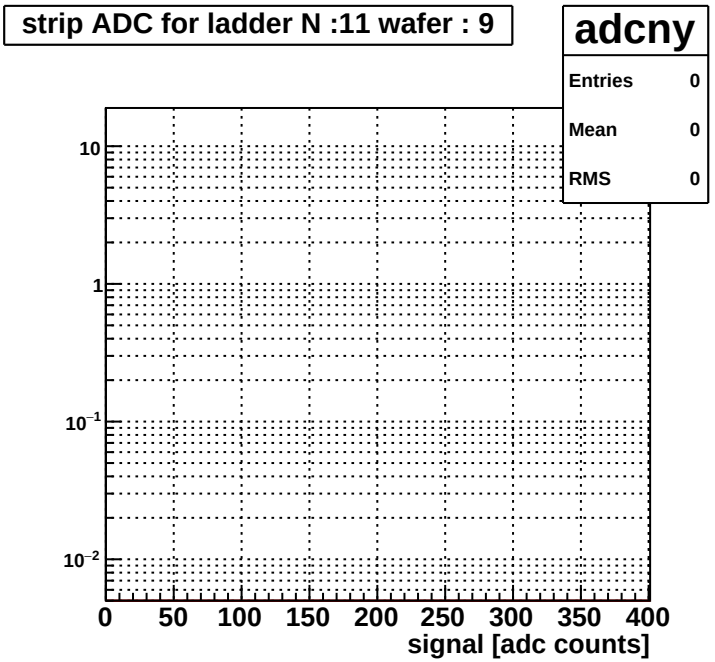

strip ADC for ladder P :11 wafer : 8
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

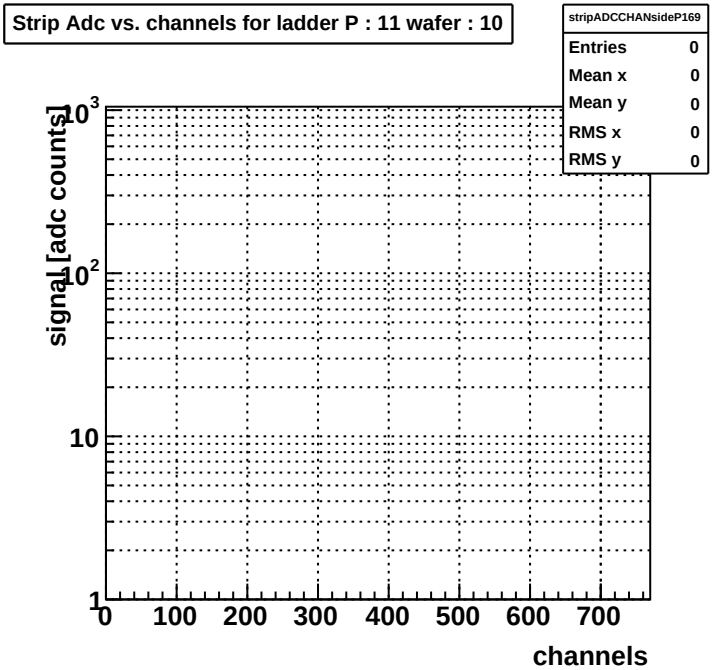

strip ADC for ladder N :11 wafer : 8
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

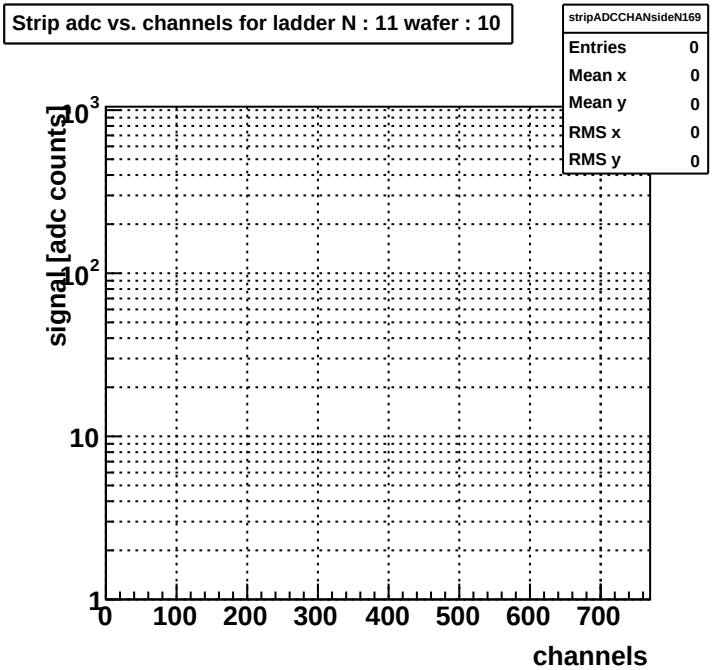

Strip Adc vs. channels for ladder P : 11 wafer : 9
stripADCCHANsideP168
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

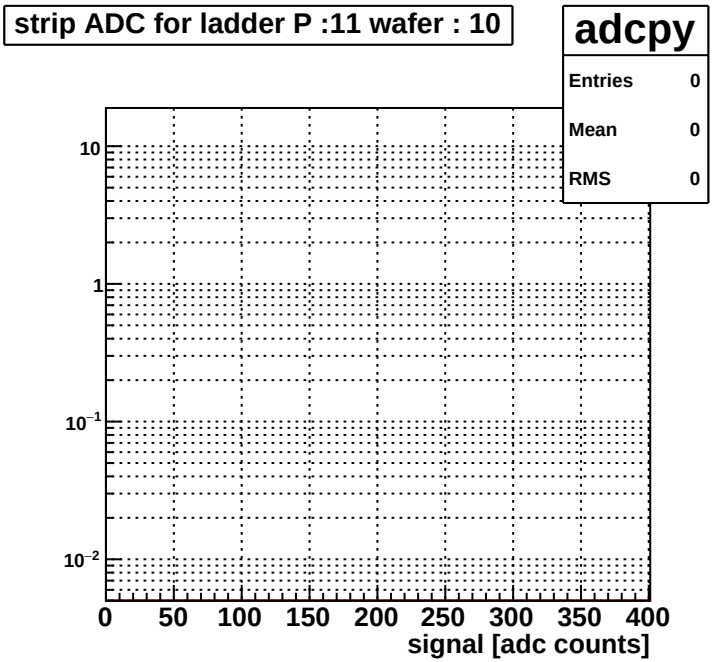

Strip adc vs. channels for ladder N : 11 wafer : 9
stripADCCHANsideN168
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

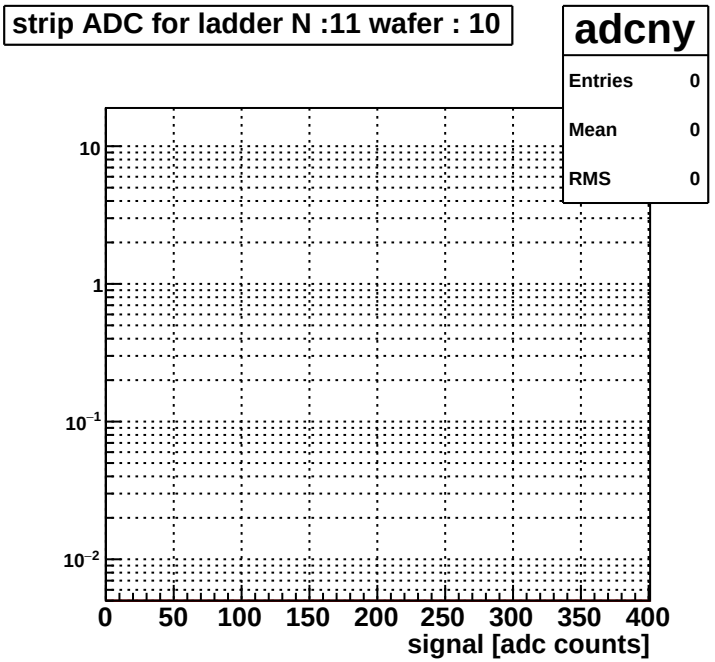

strip ADC for ladder P :11 wafer : 9
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

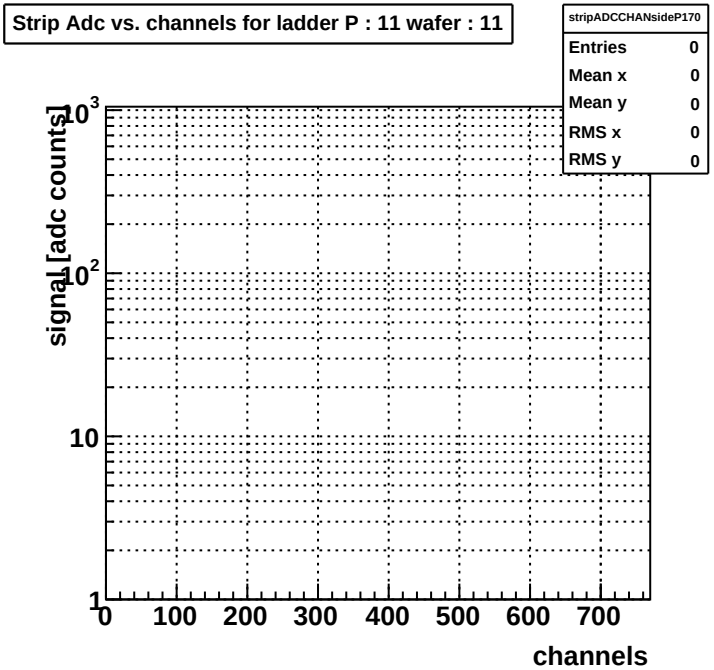

strip ADC for ladder N :11 wafer : 9
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

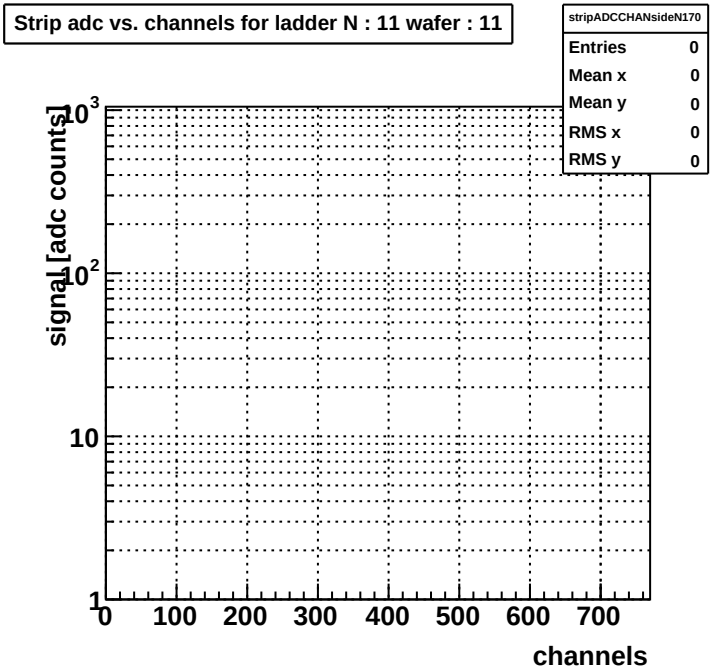

Strip Adc vs. channels for ladder P : 11 wafer : 10
stripADCCHANsideP169
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

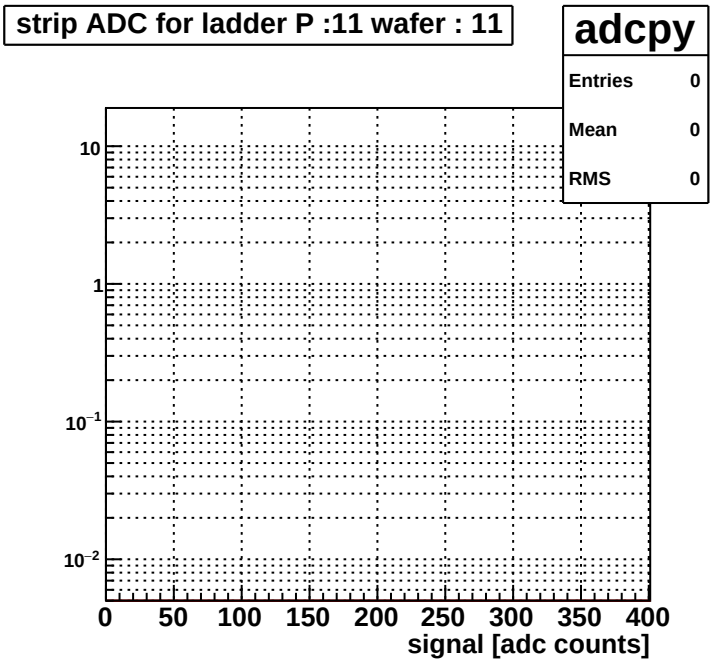

Strip adc vs. channels for ladder N : 11 wafer : 10
stripADCCHANsideN169
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

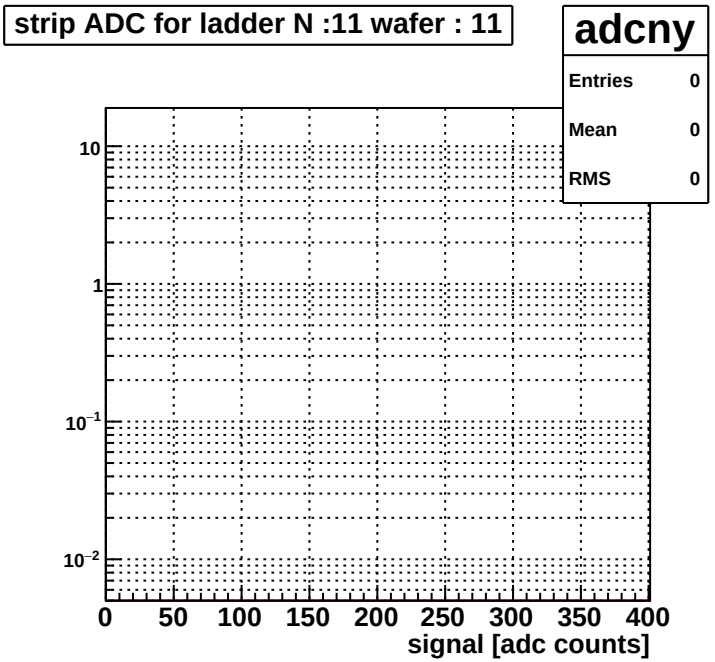

strip ADC for ladder P :11 wafer : 10
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

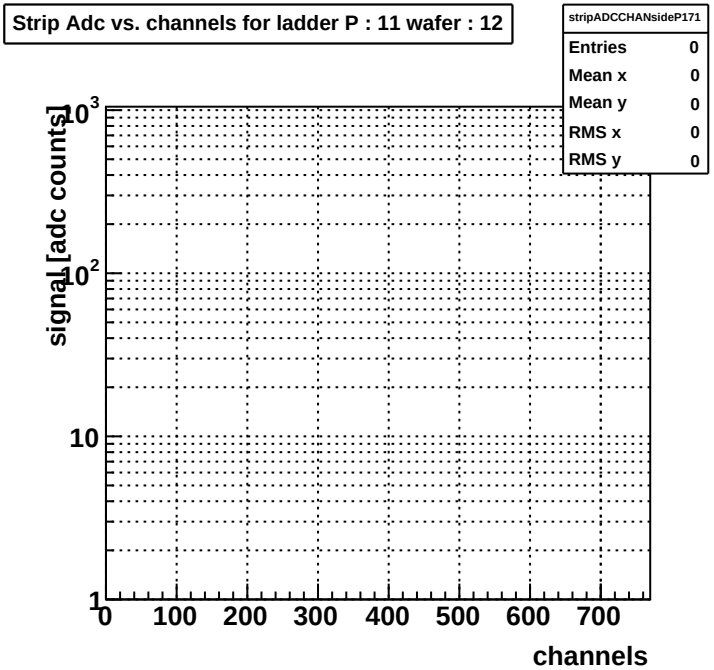

strip ADC for ladder N :11 wafer : 10
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

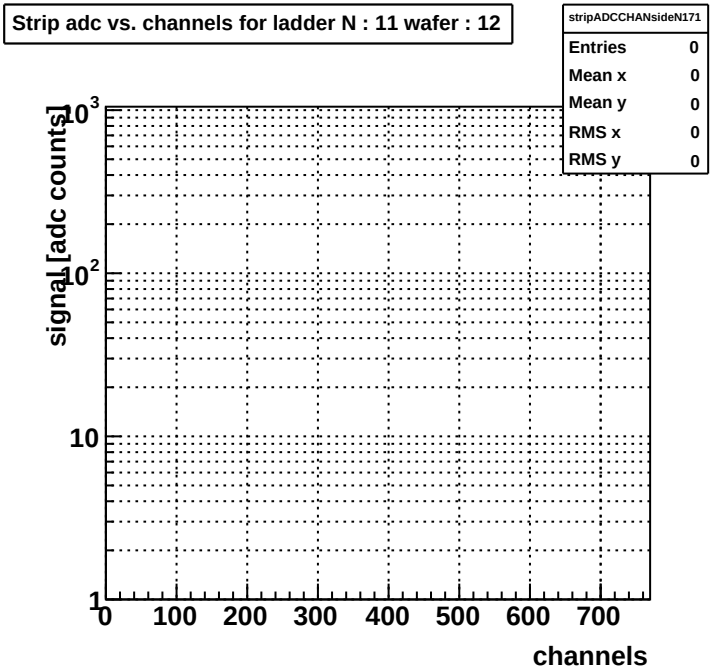

Strip Adc vs. channels for ladder P : 11 wafer : 11
stripADCCHANsideP170
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

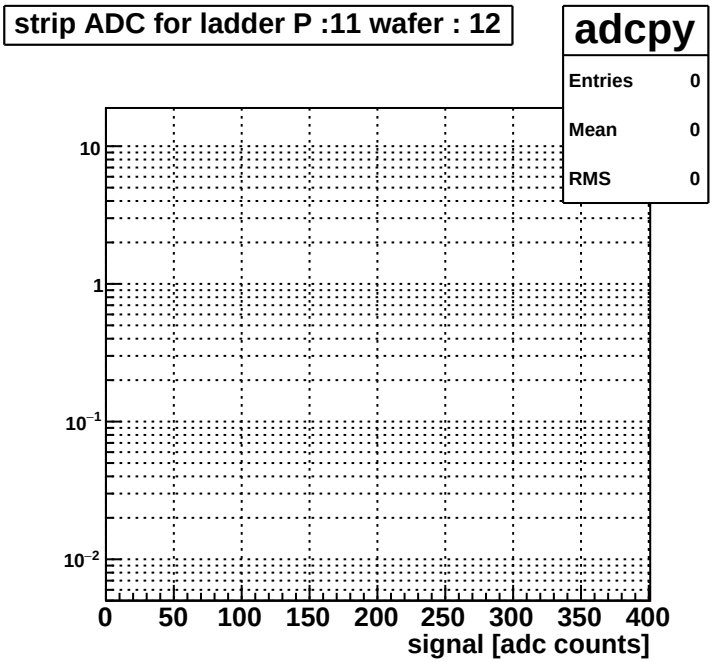

Strip adc vs. channels for ladder N : 11 wafer : 11
stripADCCHANsideN170
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

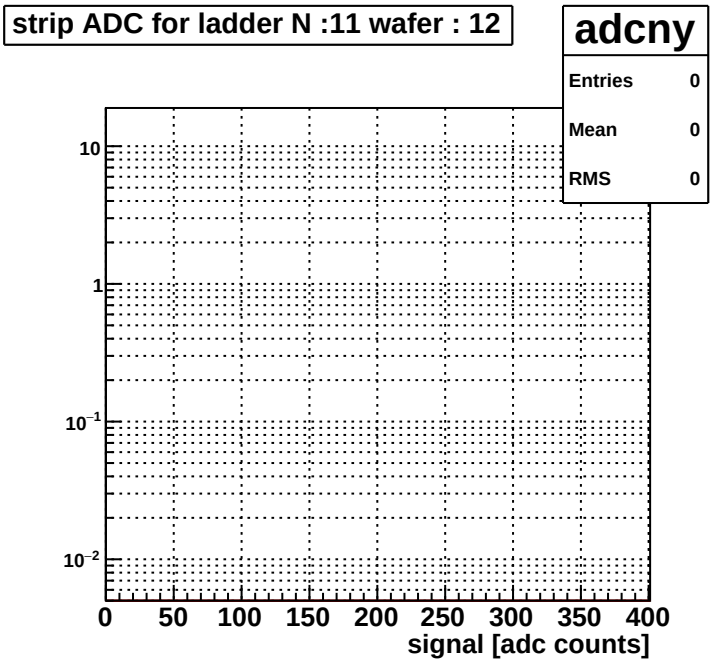

strip ADC for ladder P :11 wafer : 11
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

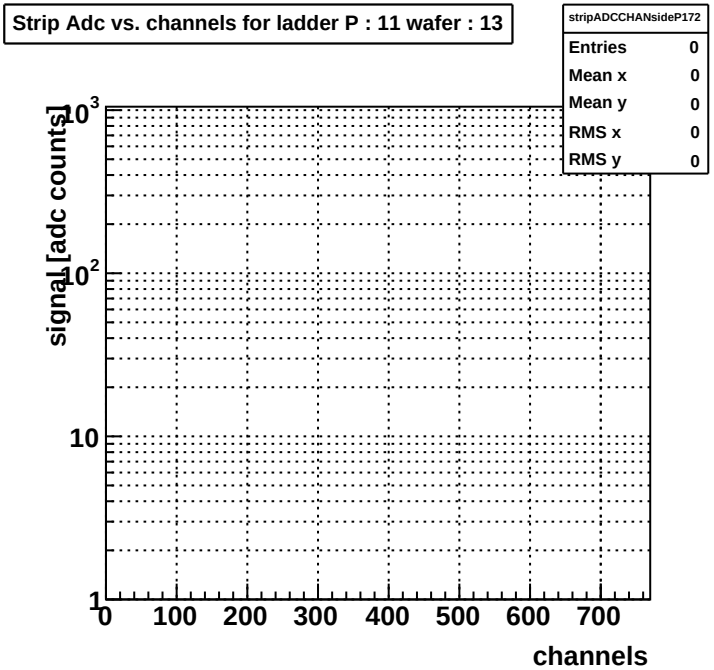

strip ADC for ladder N :11 wafer : 11
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

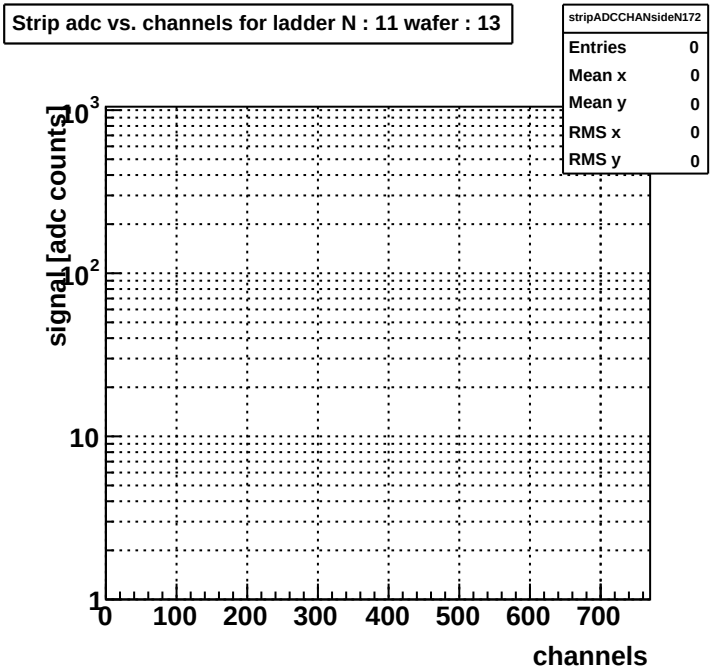

Strip Adc vs. channels for ladder P : 11 wafer : 12
stripADCCHANsideP171
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

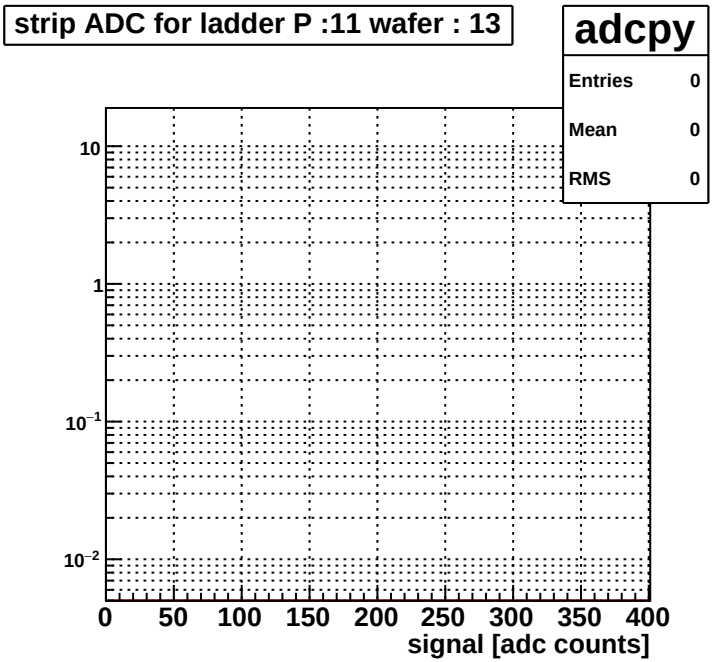

Strip adc vs. channels for ladder N : 11 wafer : 12
stripADCCHANsideN171
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

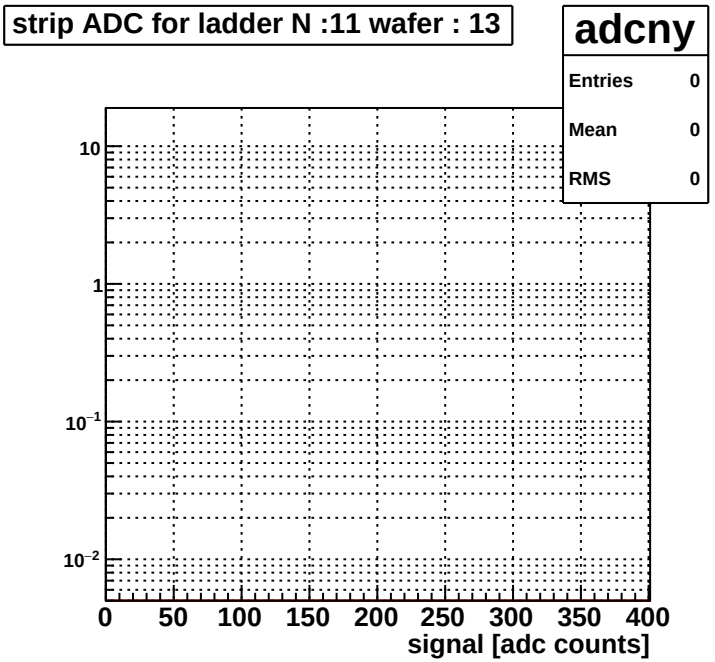

strip ADC for ladder P :11 wafer : 12
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

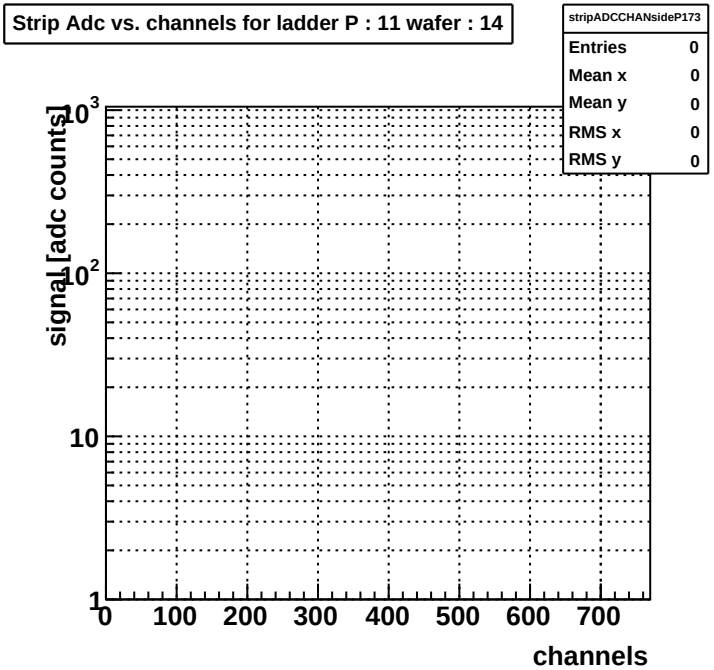

strip ADC for ladder N :11 wafer : 12
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

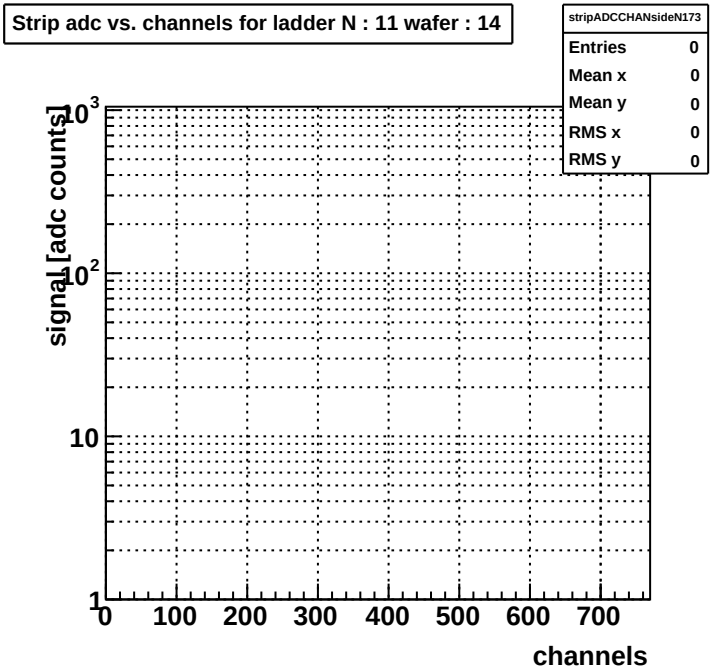

Strip Adc vs. channels for ladder P : 11 wafer : 13
stripADCCHANsideP172
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

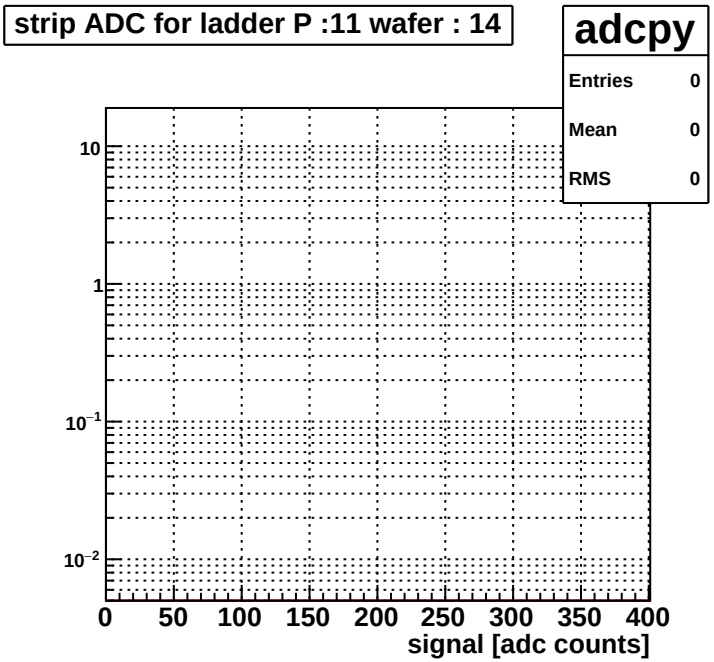

Strip adc vs. channels for ladder N : 11 wafer : 13
stripADCCHANsideN172
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

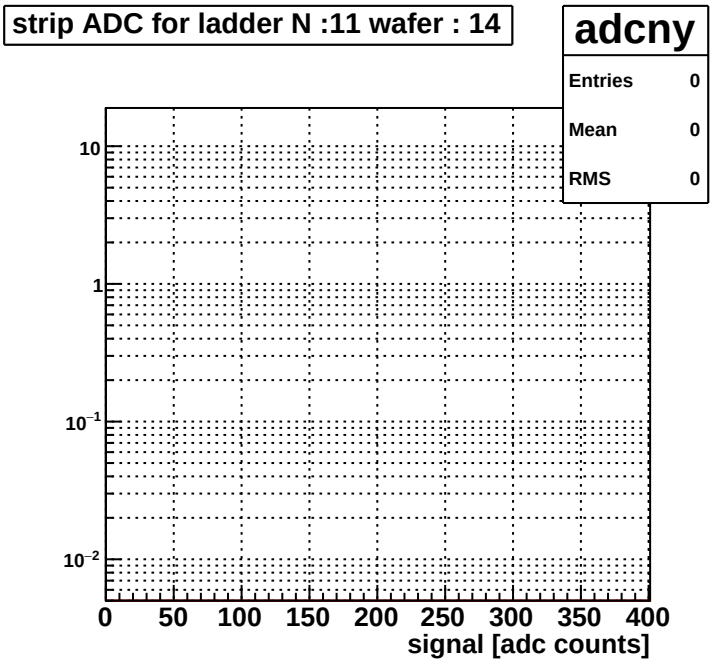

strip ADC for ladder P :11 wafer : 13
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

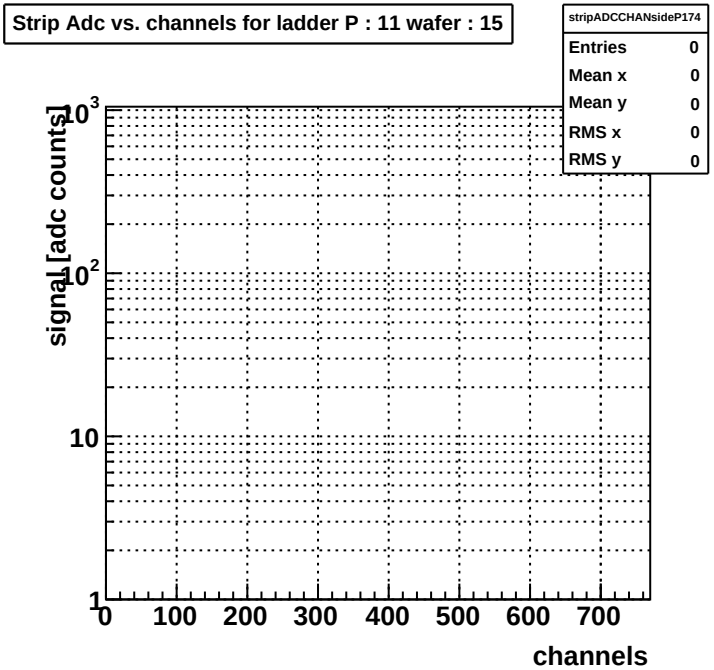

strip ADC for ladder N :11 wafer : 13
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

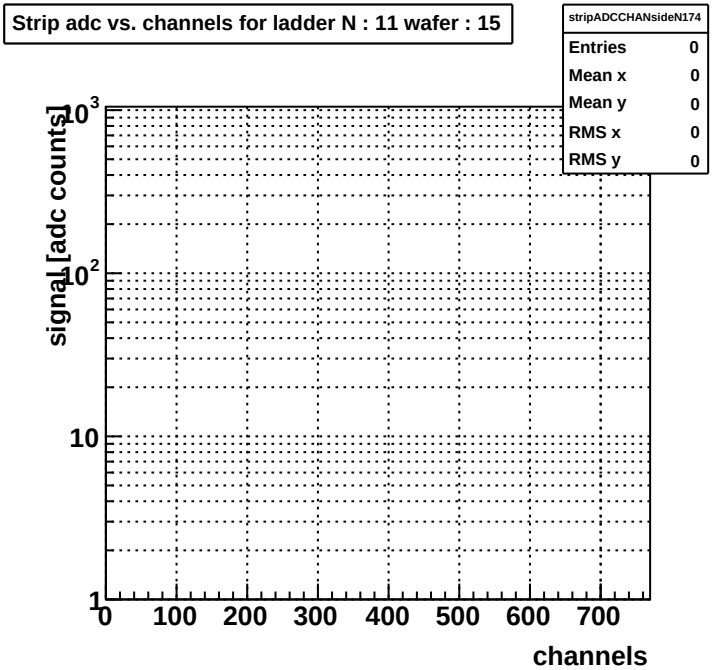

Strip Adc vs. channels for ladder P : 11 wafer : 14
stripADCCHANsideP173
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

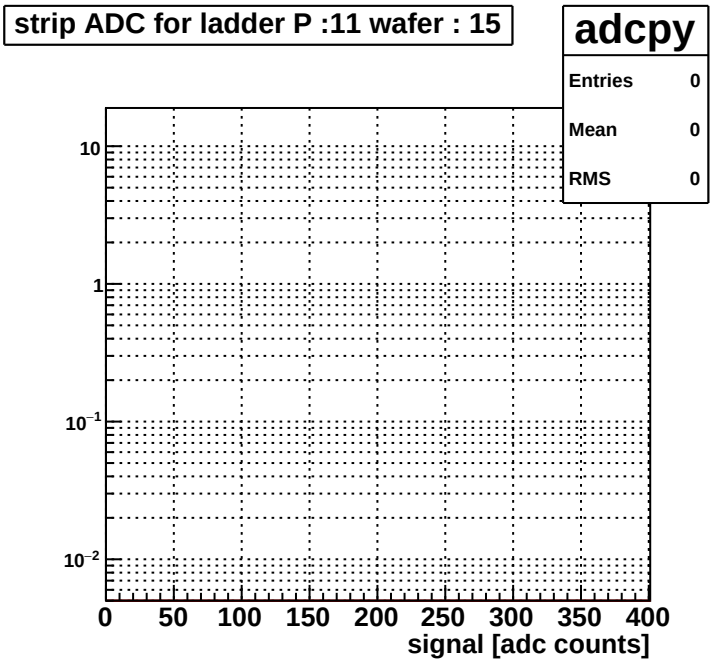

Strip adc vs. channels for ladder N : 11 wafer : 14
stripADCCHANsideN173
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

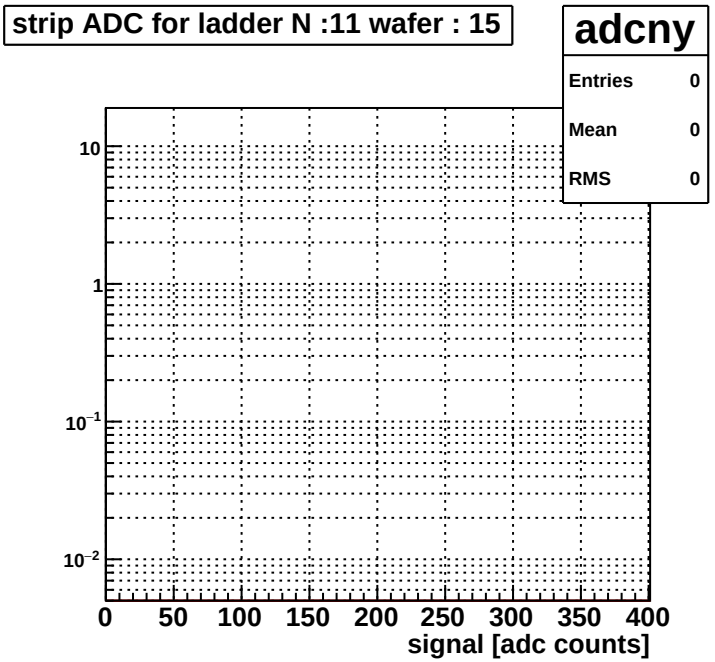

strip ADC for ladder P :11 wafer : 14
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

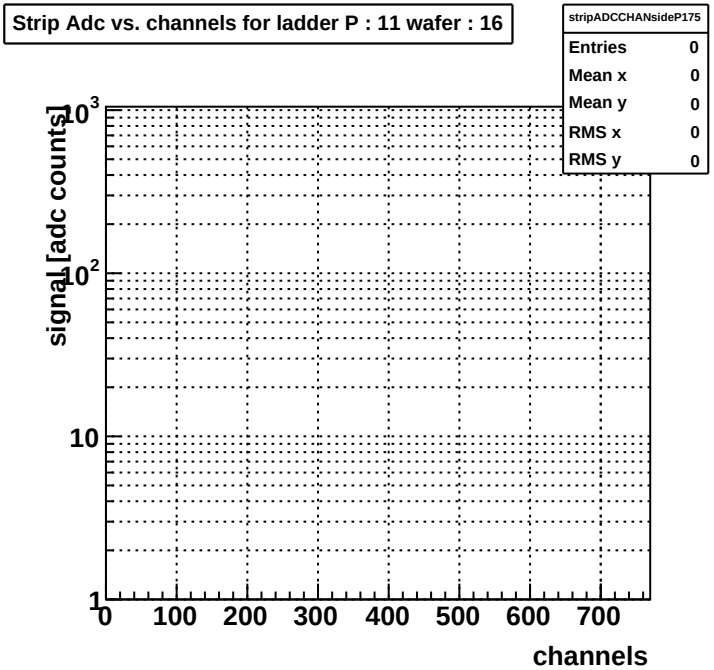

strip ADC for ladder N :11 wafer : 14
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

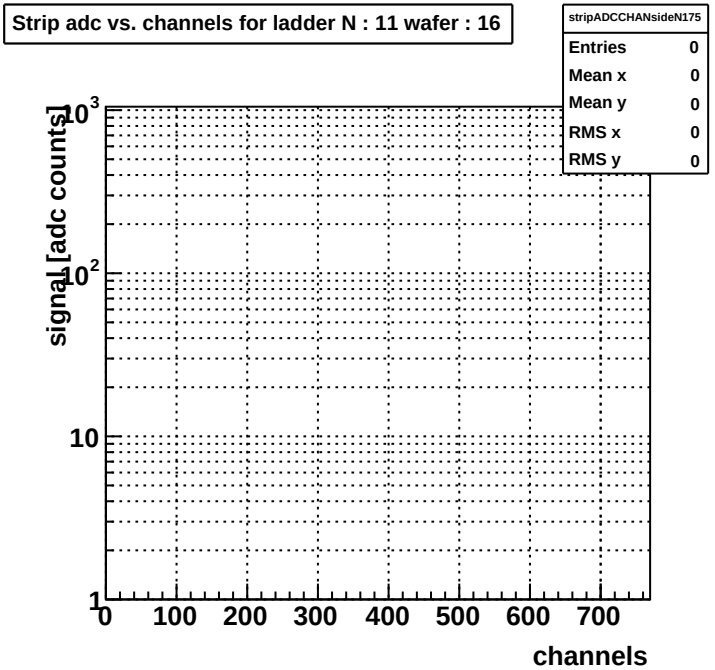

Strip Adc vs. channels for ladder P : 11 wafer : 15
stripADCCHANsideP174
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

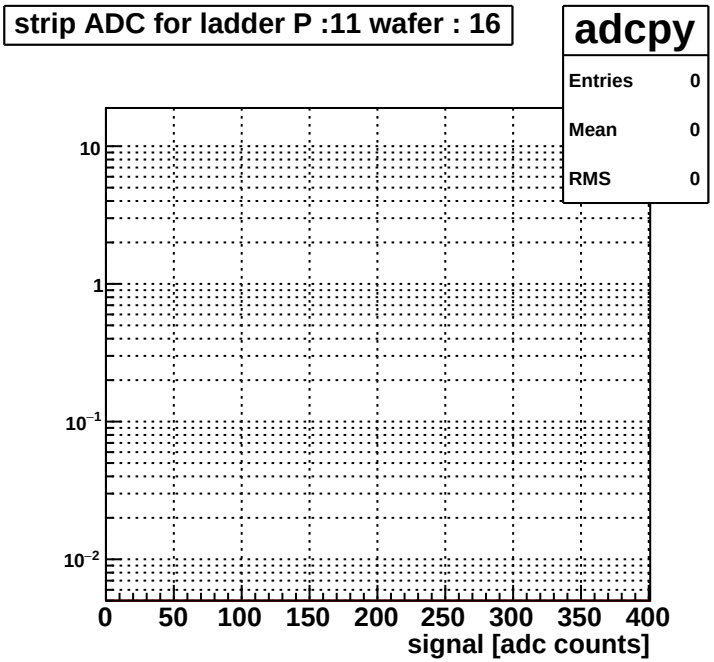

Strip adc vs. channels for ladder N : 11 wafer : 15
stripADCCHANsideN174
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

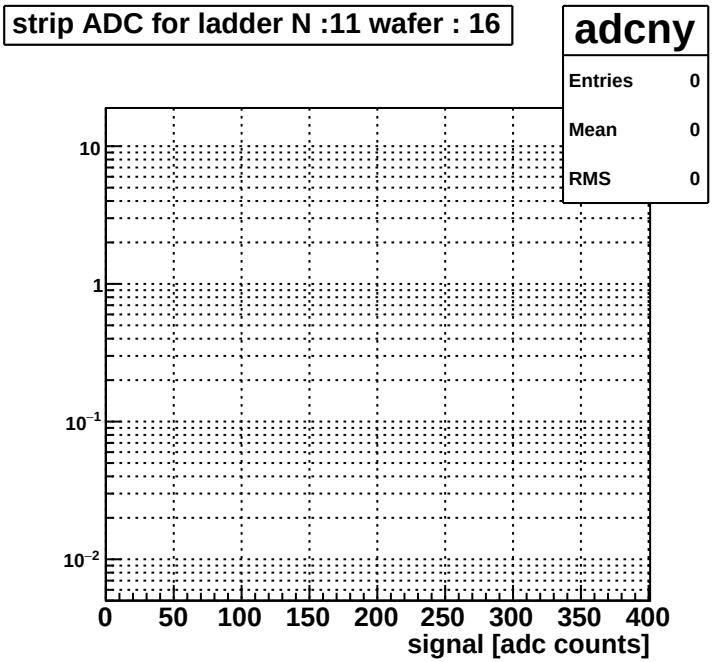

strip ADC for ladder P :11 wafer : 15
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

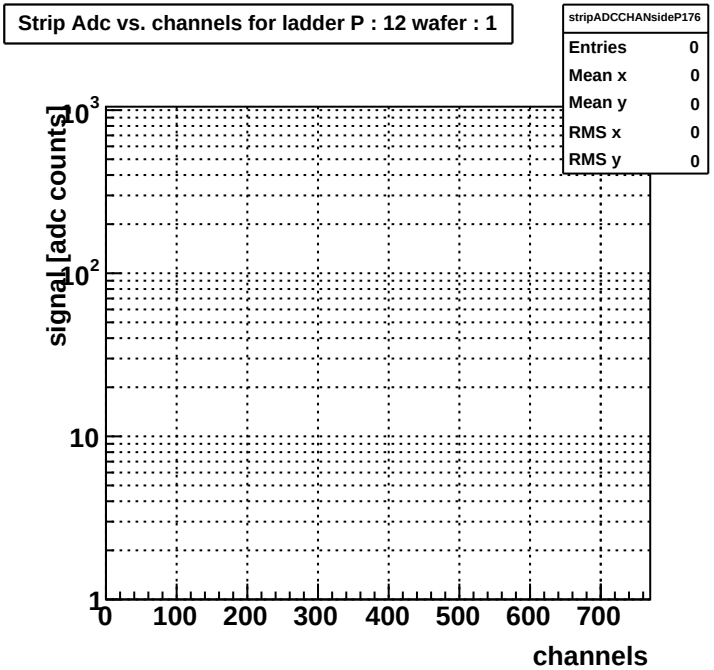

strip ADC for ladder N :11 wafer : 15
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

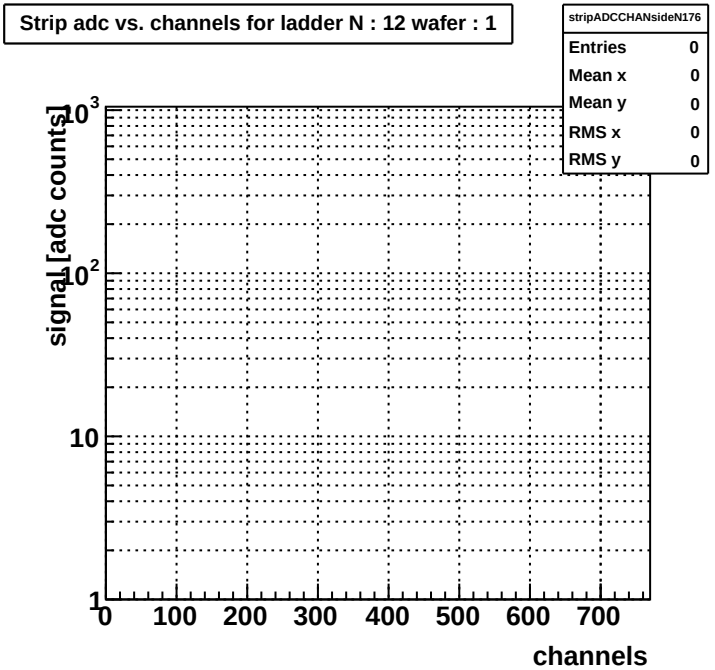

Strip Adc vs. channels for ladder P : 11 wafer : 16
stripADCCHANsideP175
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

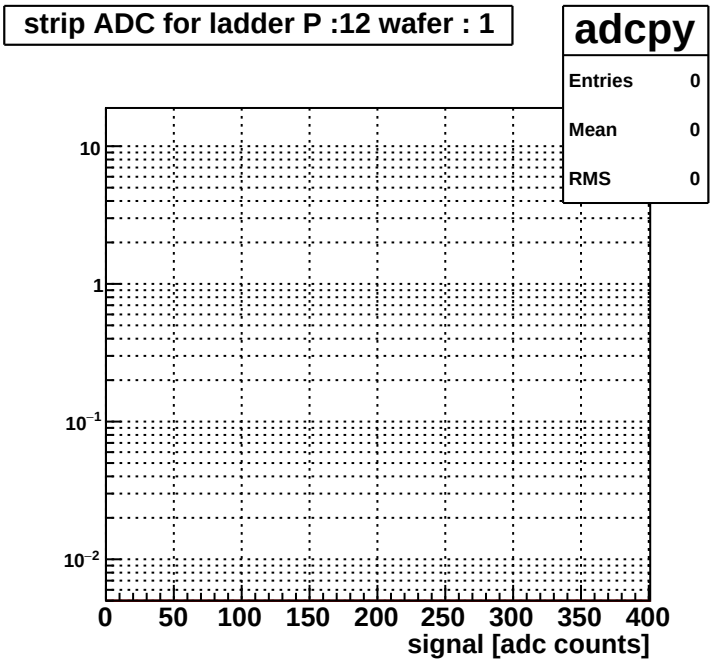

Strip adc vs. channels for ladder N : 11 wafer : 16
stripADCCHANsideN175
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

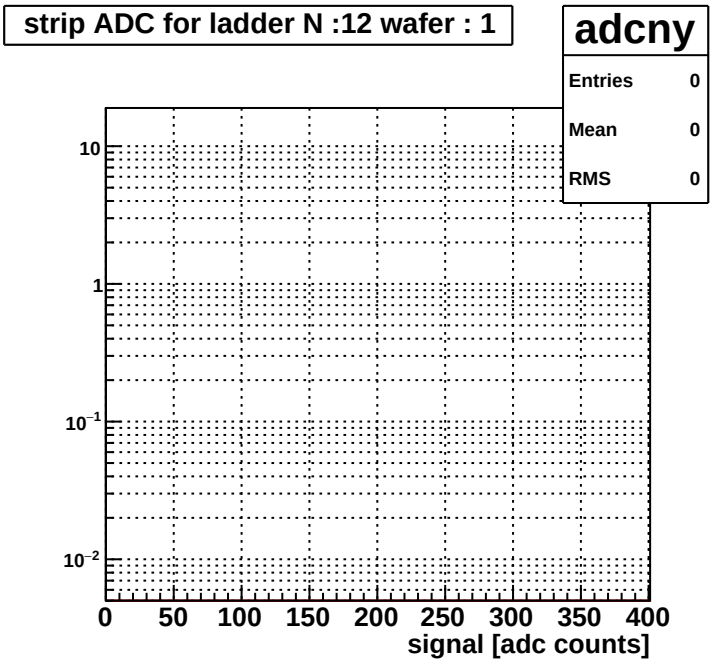

strip ADC for ladder P :11 wafer : 16
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

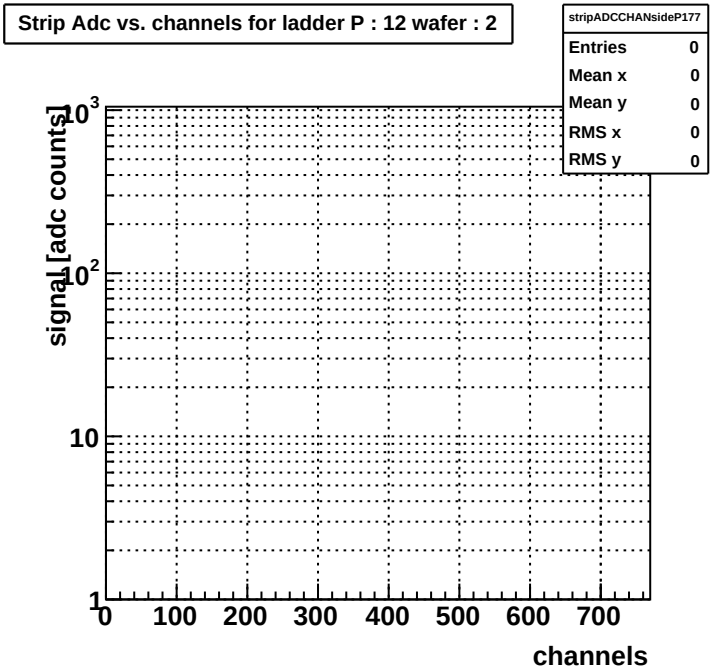

strip ADC for ladder N :11 wafer : 16
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

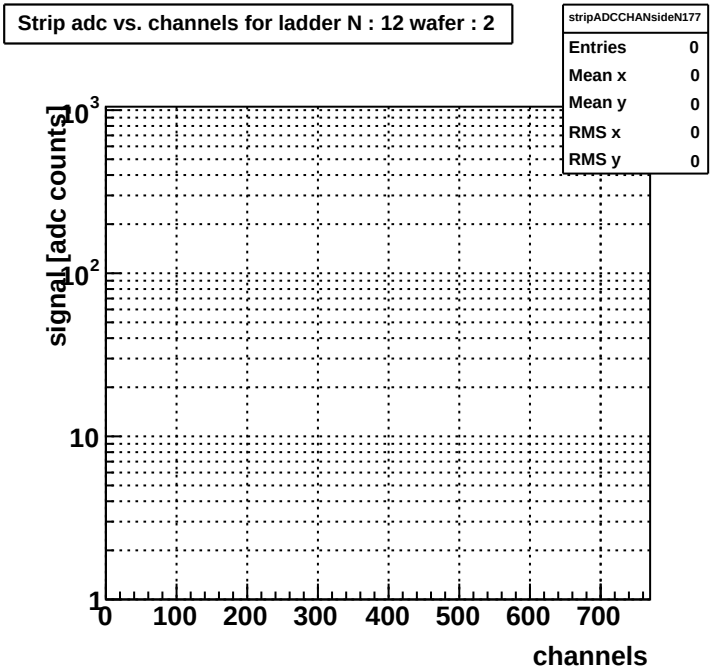

Strip Adc vs. channels for ladder P : 12 wafer : 1
stripADCCHANsideP176
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

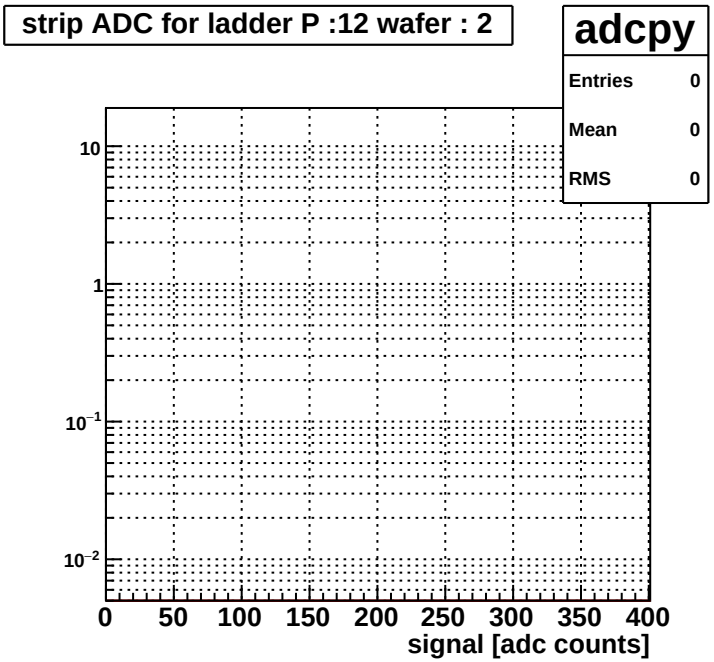

Strip adc vs. channels for ladder N : 12 wafer : 1
stripADCCHANsideN176
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

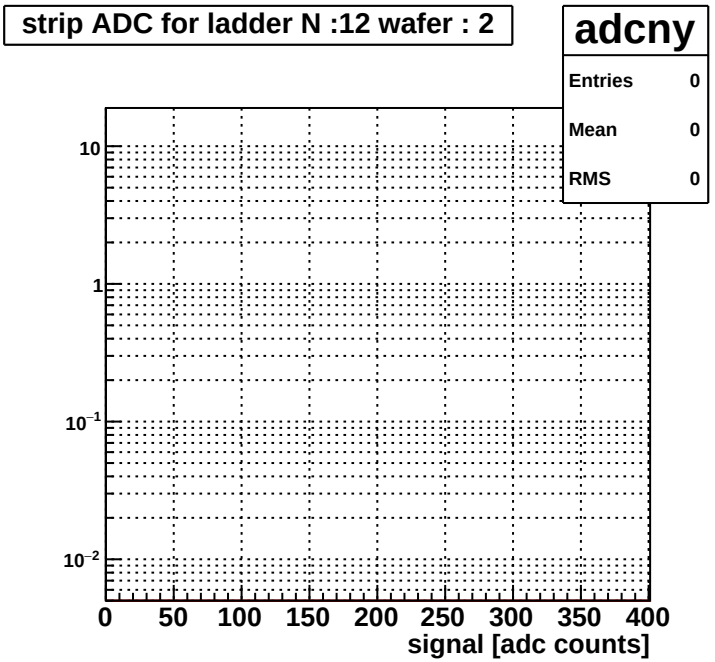

strip ADC for ladder P :12 wafer : 1
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

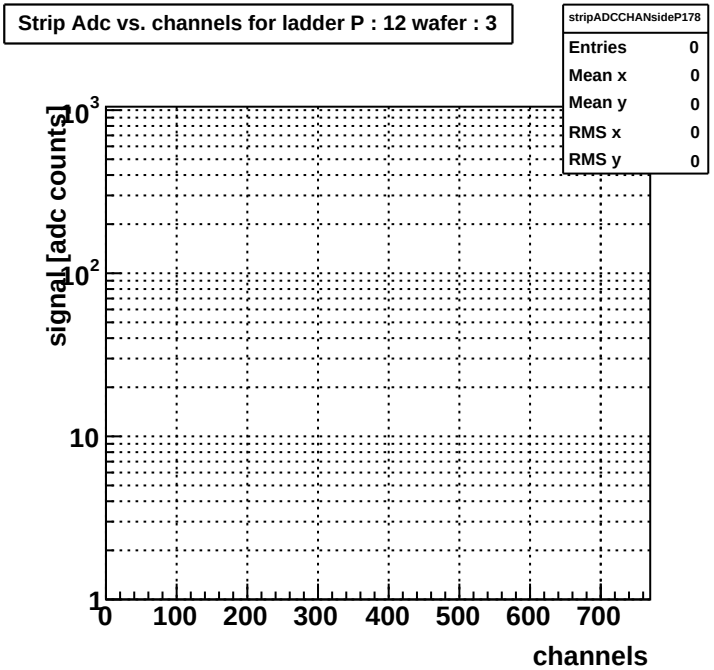

strip ADC for ladder N :12 wafer : 1
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

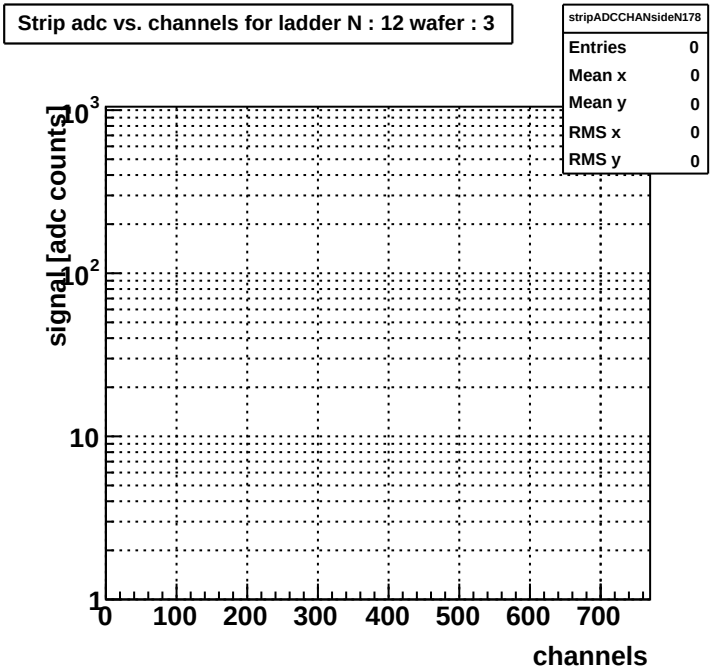

Strip Adc vs. channels for ladder P : 12 wafer : 2
stripADCCHANsideP177
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

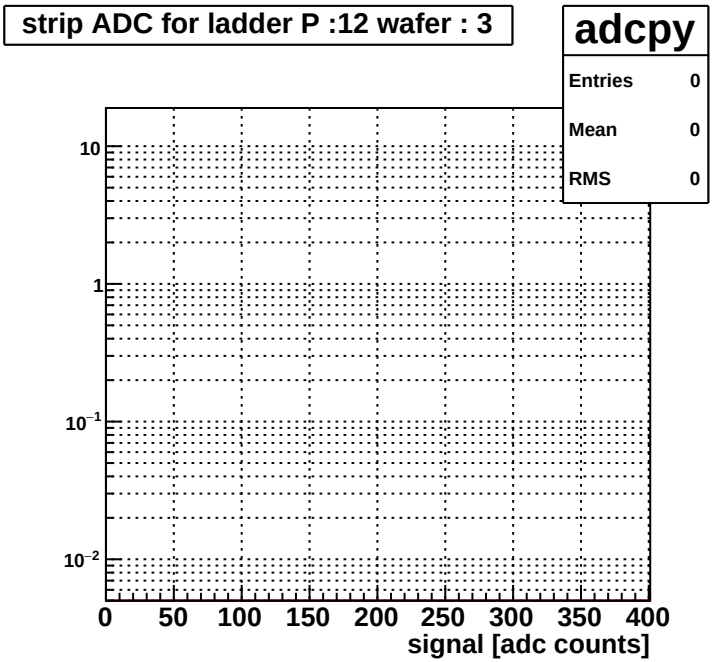

Strip adc vs. channels for ladder N : 12 wafer : 2
stripADCCHANsideN177
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

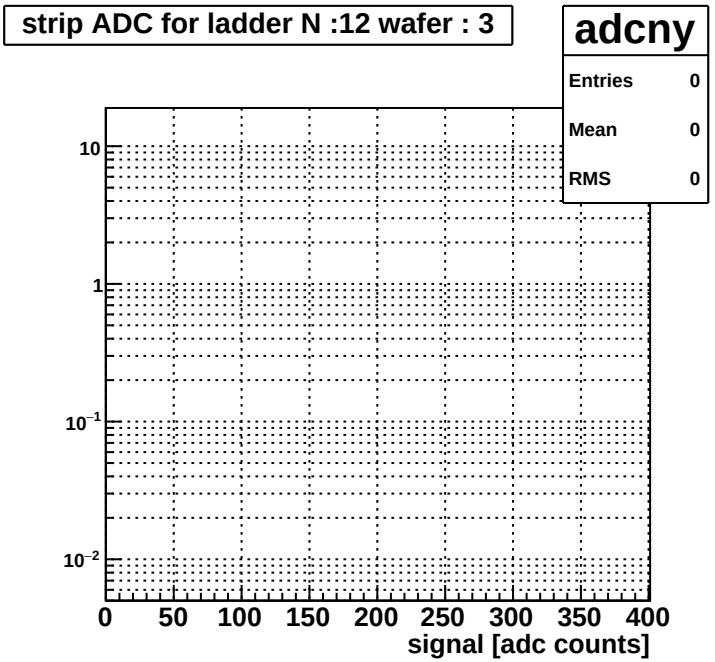

strip ADC for ladder P :12 wafer : 2
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

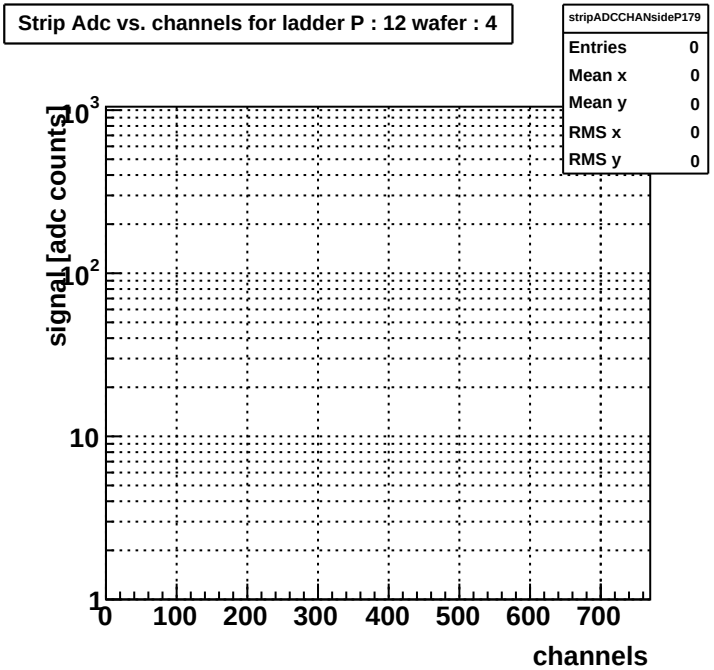

strip ADC for ladder N :12 wafer : 2
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

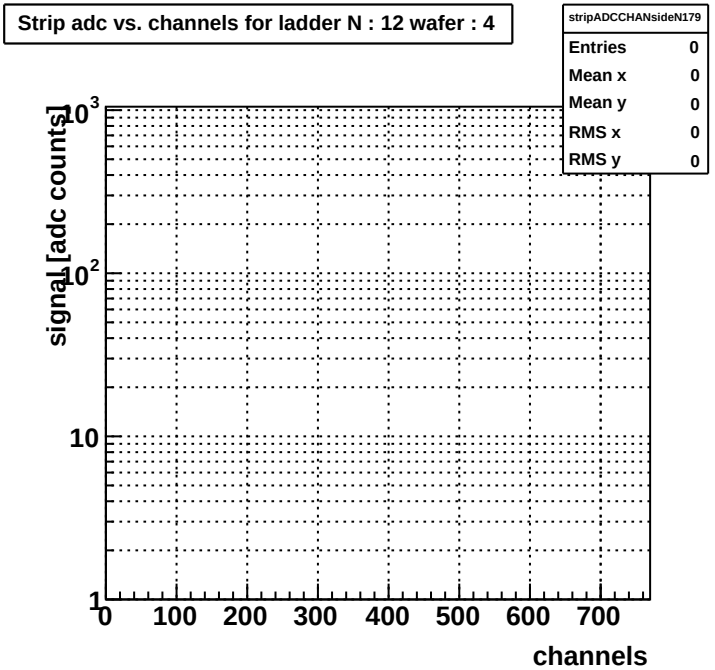

Strip Adc vs. channels for ladder P : 12 wafer : 3
stripADCCHANsideP178
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

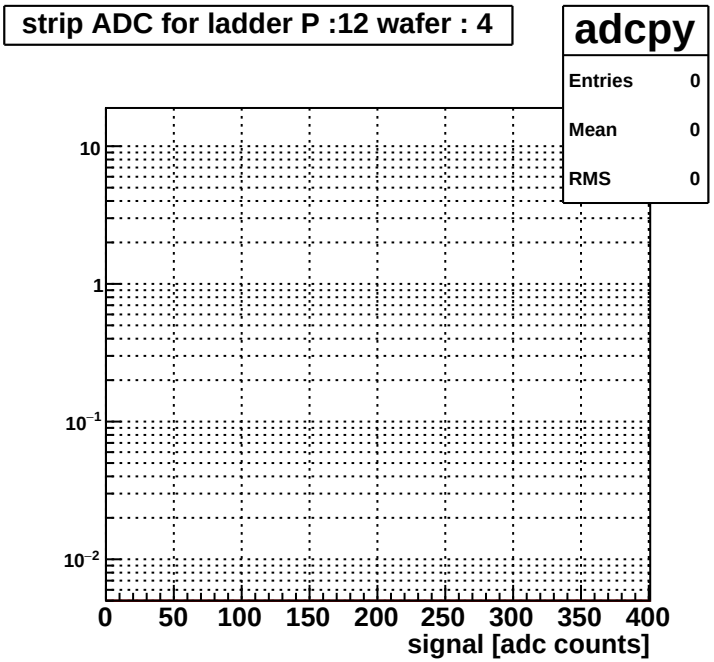

Strip adc vs. channels for ladder N : 12 wafer : 3
stripADCCHANsideN178
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

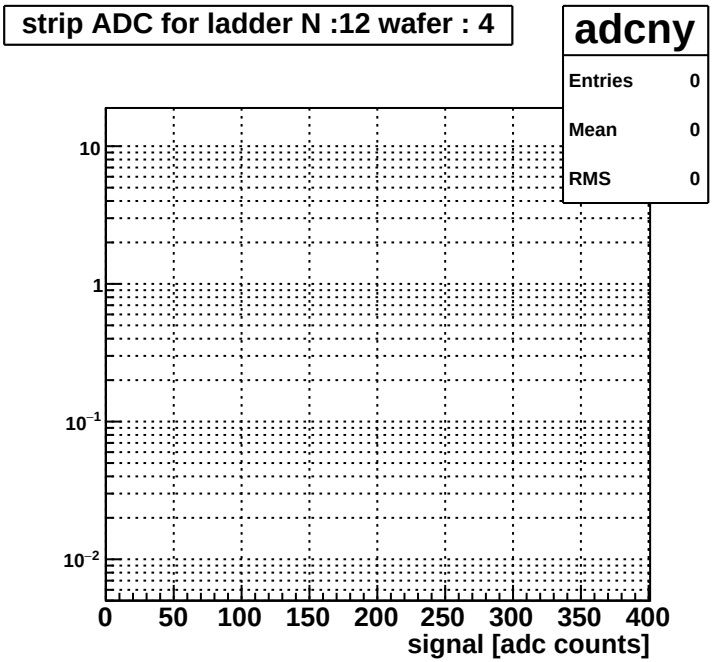

strip ADC for ladder P :12 wafer : 3
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

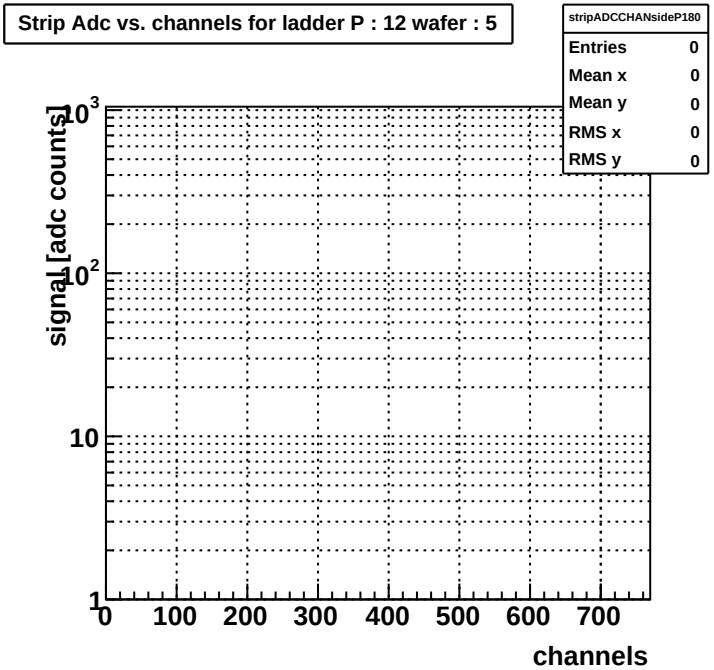

strip ADC for ladder N :12 wafer : 3
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

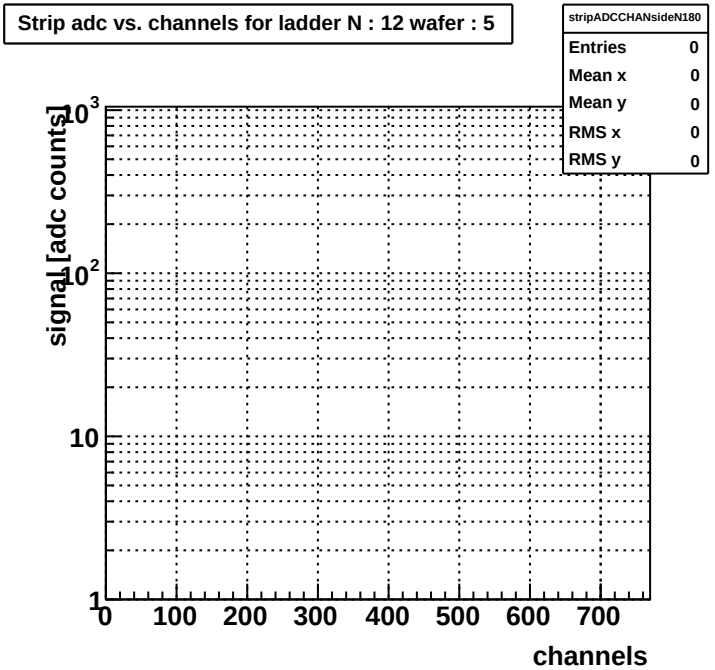

Strip Adc vs. channels for ladder P : 12 wafer : 4
stripADCCHANsideP179
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

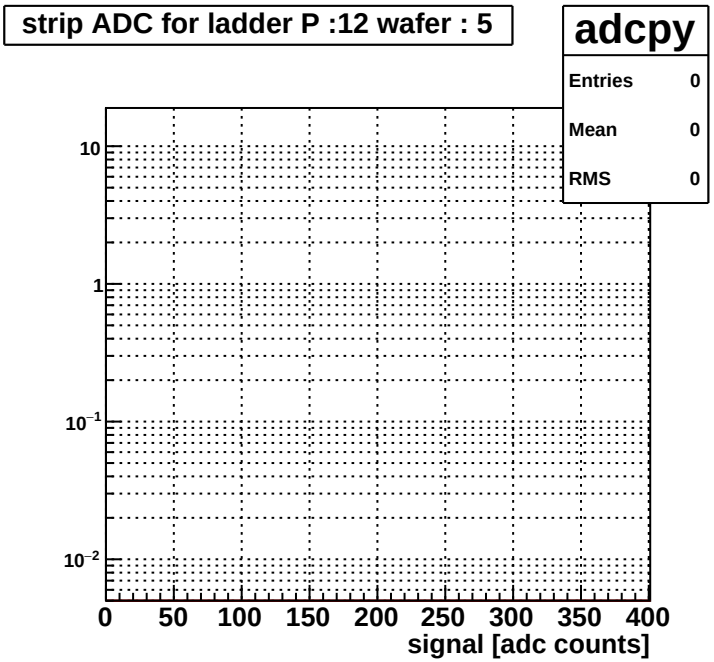

Strip adc vs. channels for ladder N : 12 wafer : 4
stripADCCHANsideN179
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

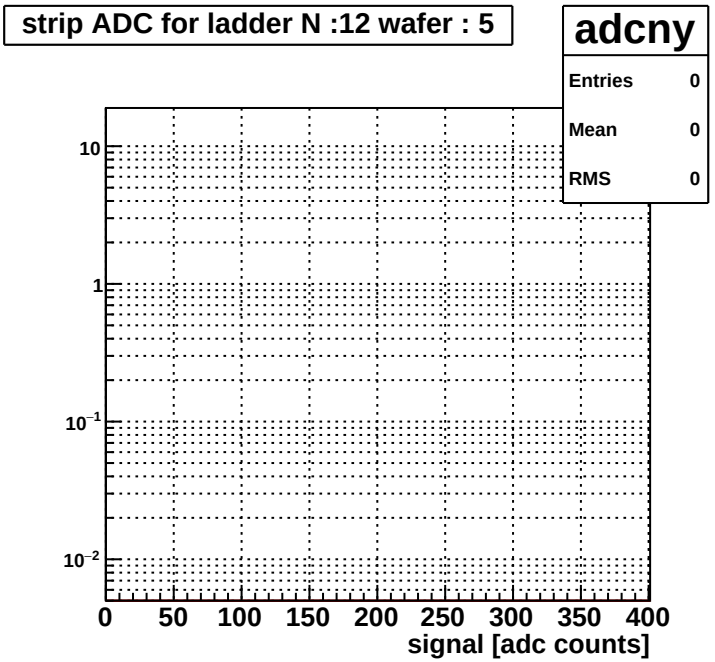

strip ADC for ladder P :12 wafer : 4
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

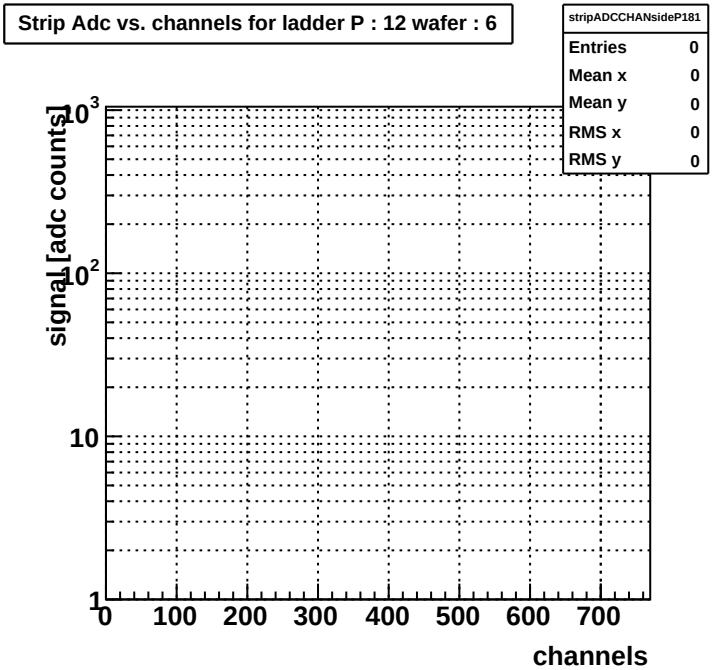

strip ADC for ladder N :12 wafer : 4
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

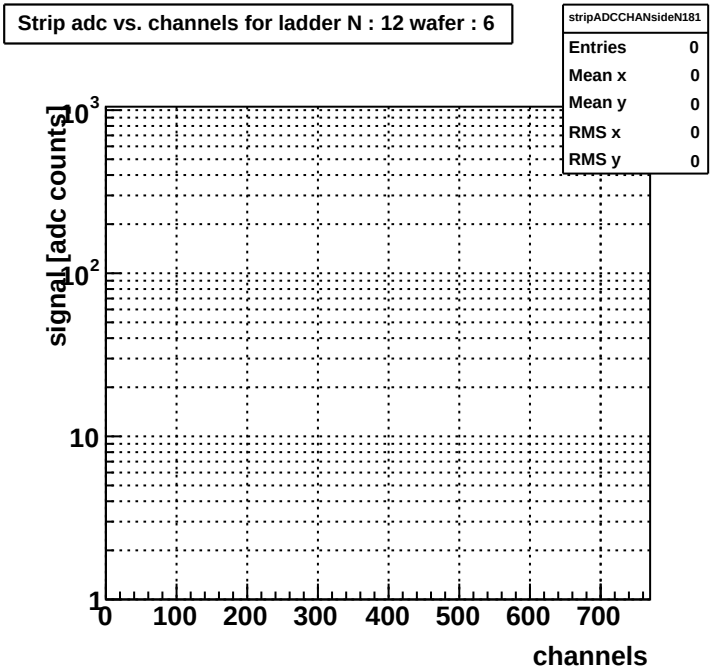

Strip Adc vs. channels for ladder P : 12 wafer : 5
stripADCCHANsideP180
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

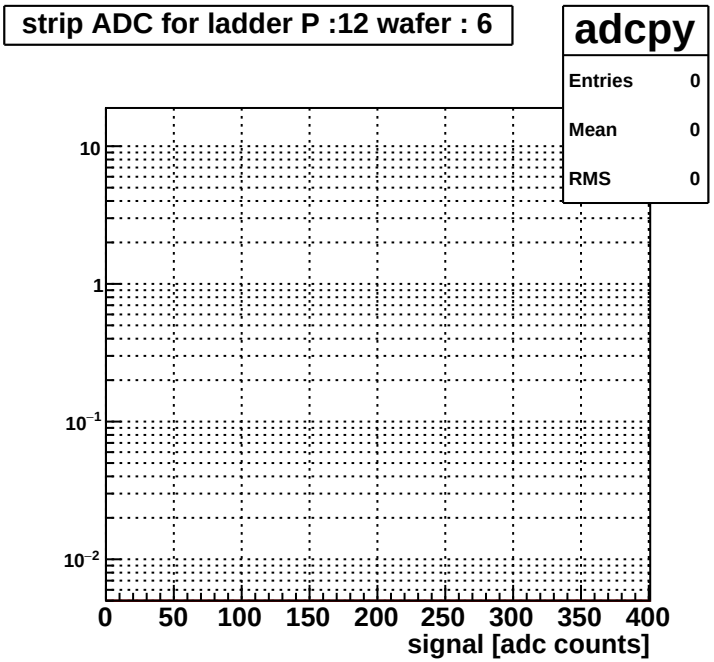

Strip adc vs. channels for ladder N : 12 wafer : 5
stripADCCHANsideN180
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

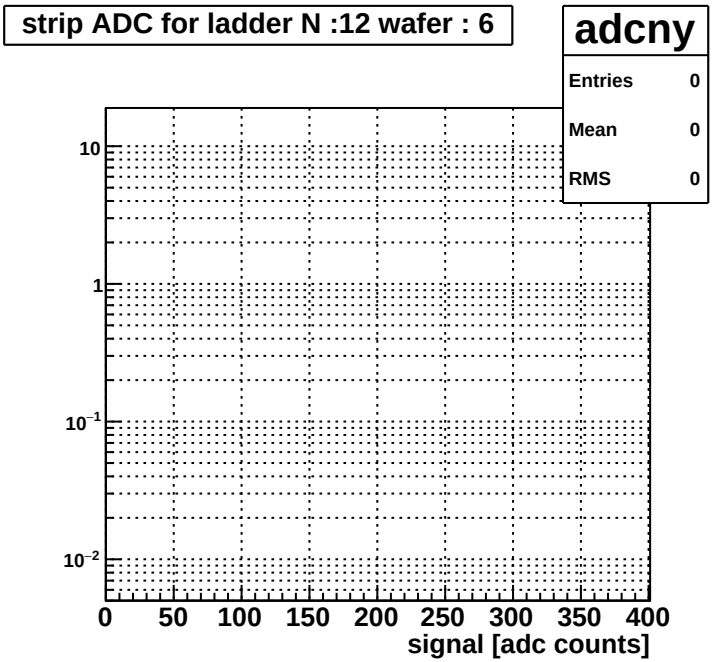

strip ADC for ladder P :12 wafer : 5
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

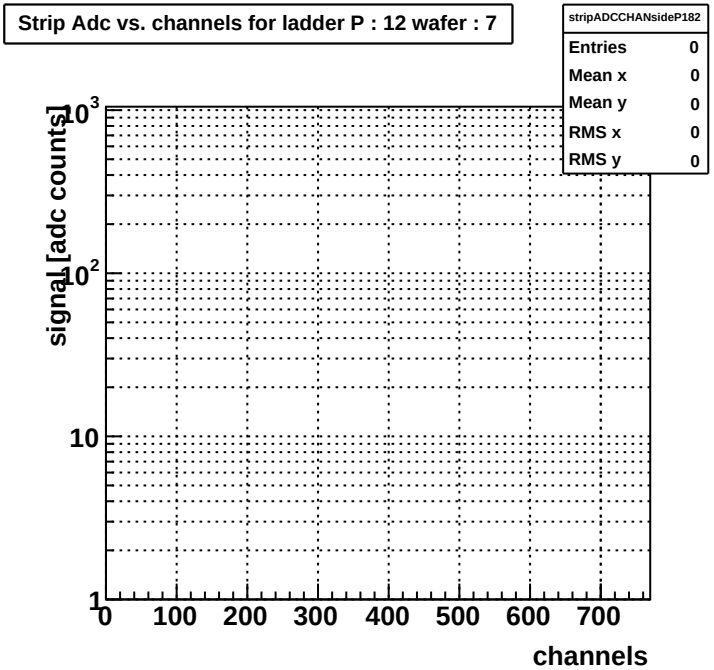

strip ADC for ladder N :12 wafer : 5
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

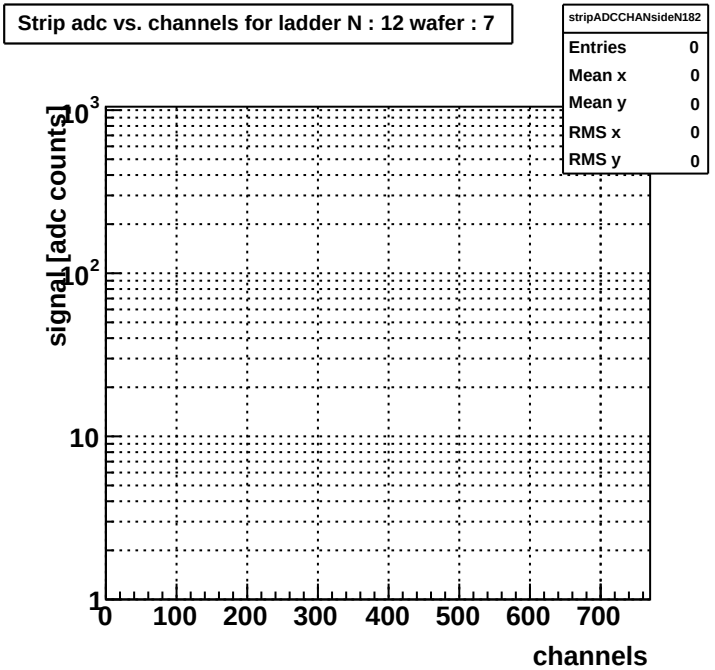

Strip Adc vs. channels for ladder P : 12 wafer : 6
stripADCCHANsideP181
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

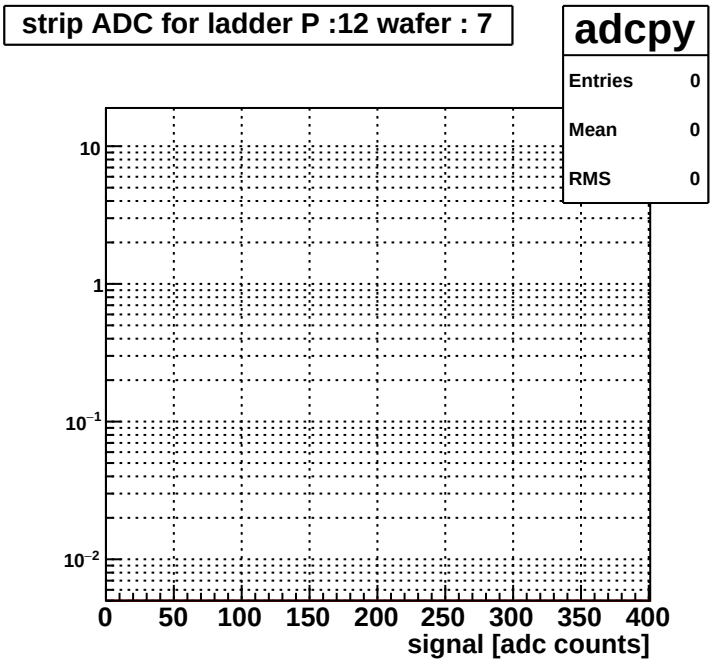

Strip adc vs. channels for ladder N : 12 wafer : 6
stripADCCHANsideN181
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

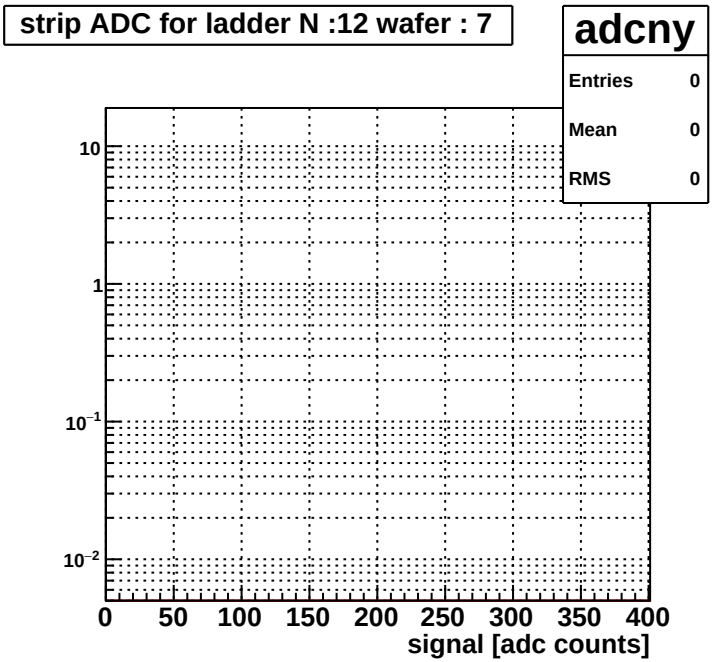

strip ADC for ladder P :12 wafer : 6
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

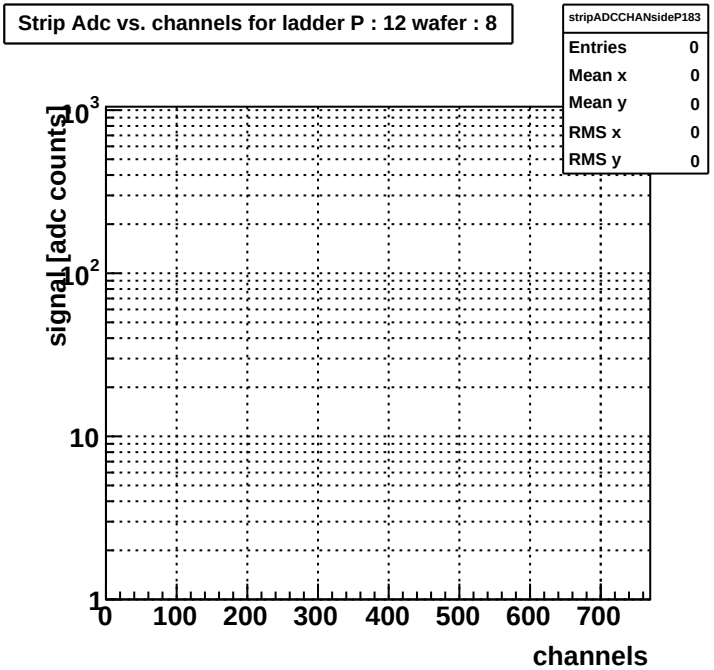

strip ADC for ladder N :12 wafer : 6
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

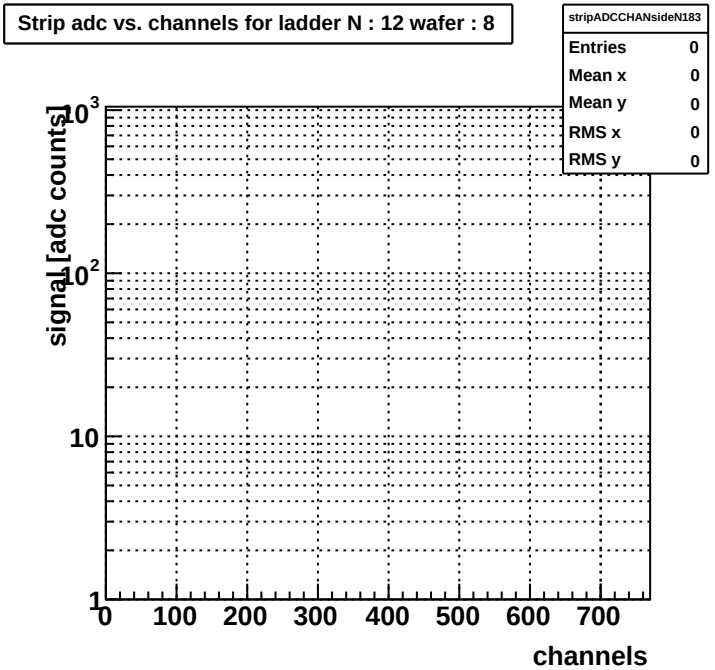

Strip Adc vs. channels for ladder P : 12 wafer : 7
stripADCCHANsideP182
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

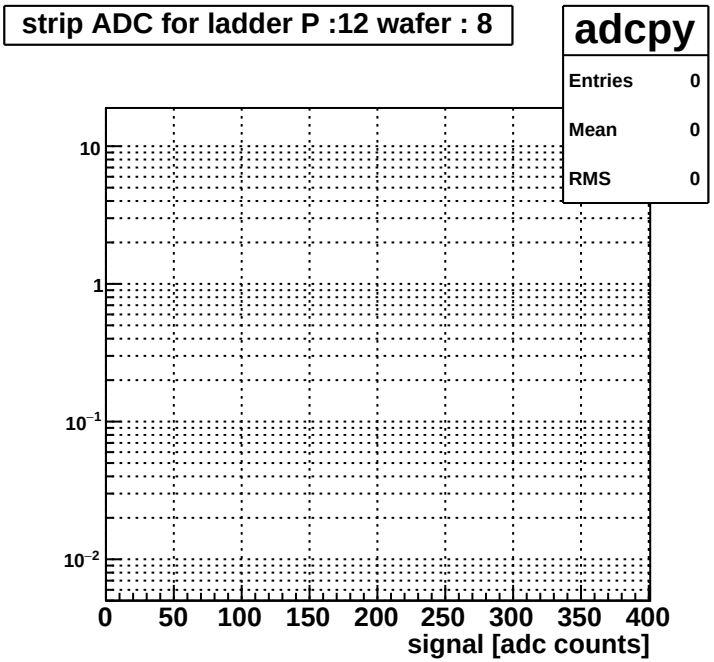

Strip adc vs. channels for ladder N : 12 wafer : 7
stripADCCHANsideN182
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

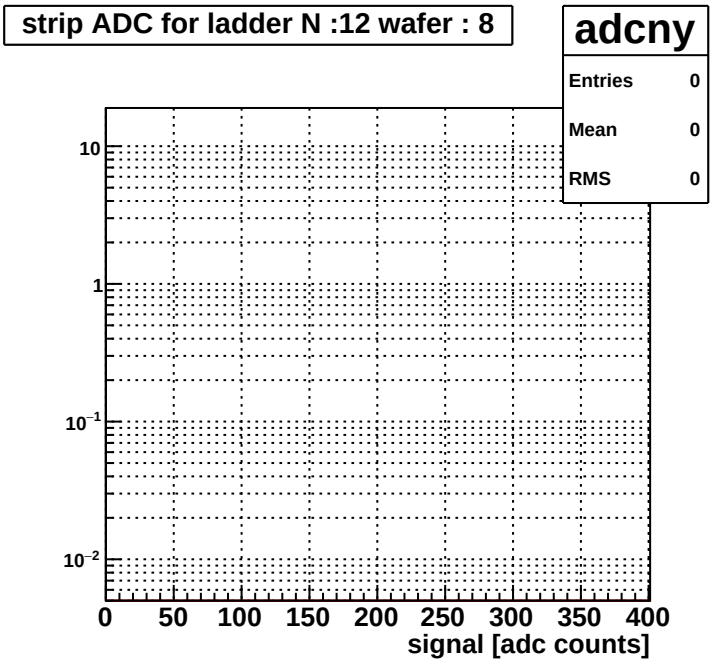

strip ADC for ladder P :12 wafer : 7
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

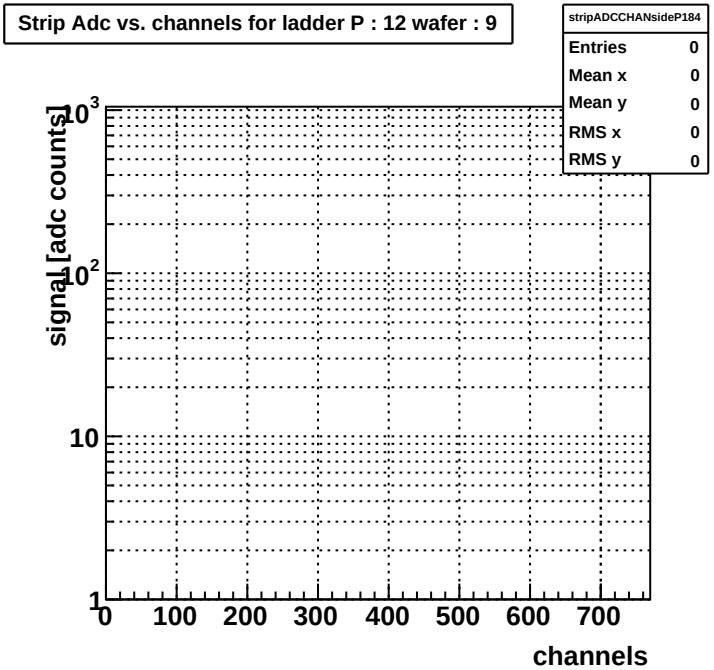

strip ADC for ladder N :12 wafer : 7
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

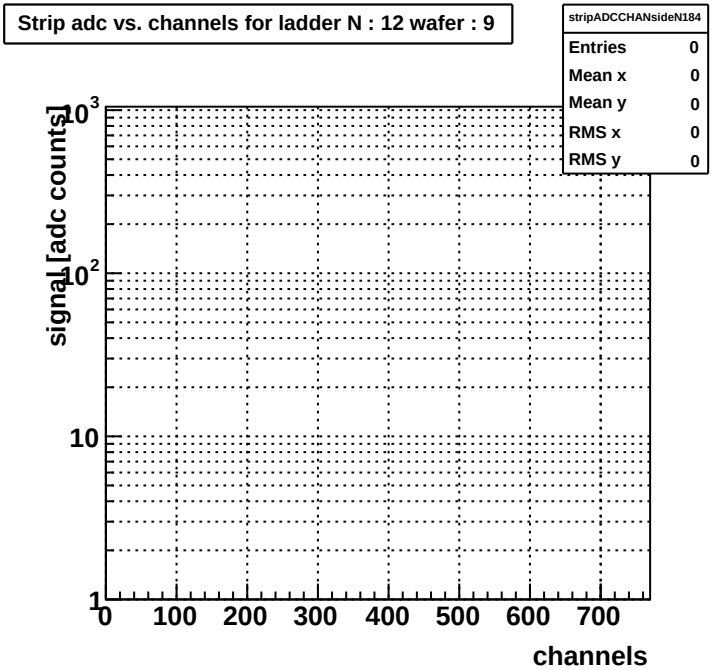

Strip Adc vs. channels for ladder P : 12 wafer : 8
stripADCCHANsideP183
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

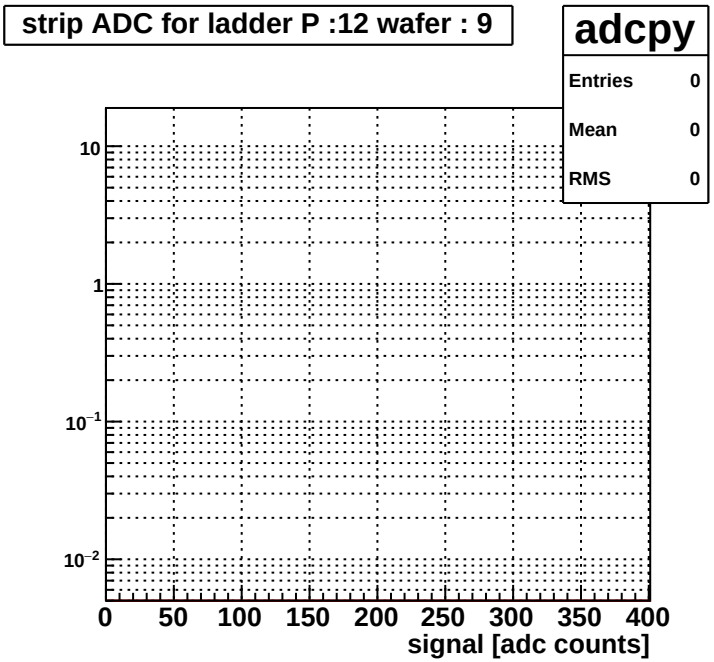

Strip adc vs. channels for ladder N : 12 wafer : 8
stripADCCHANsideN183
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

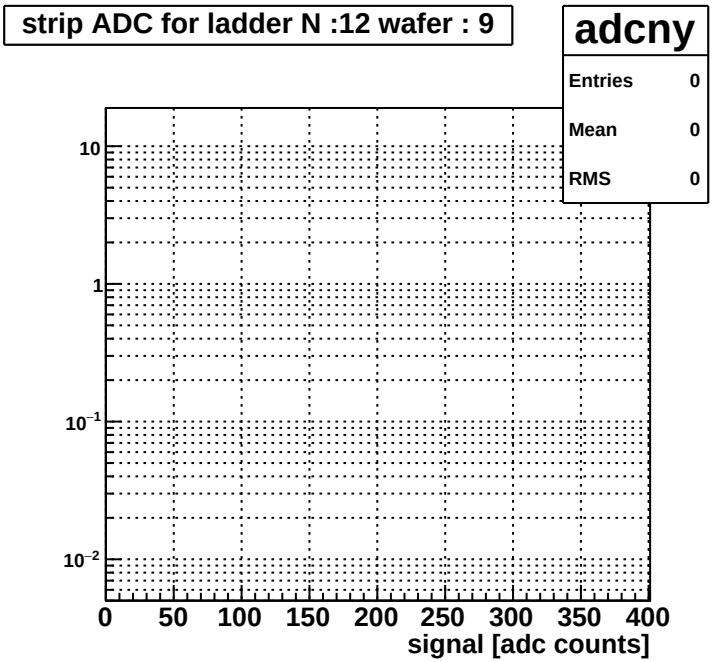

strip ADC for ladder P :12 wafer : 8
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

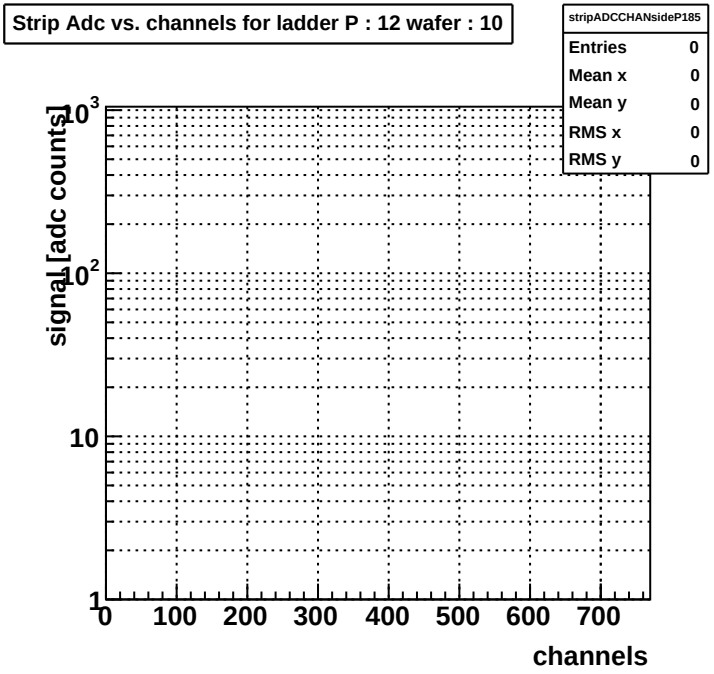

strip ADC for ladder N :12 wafer : 8
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

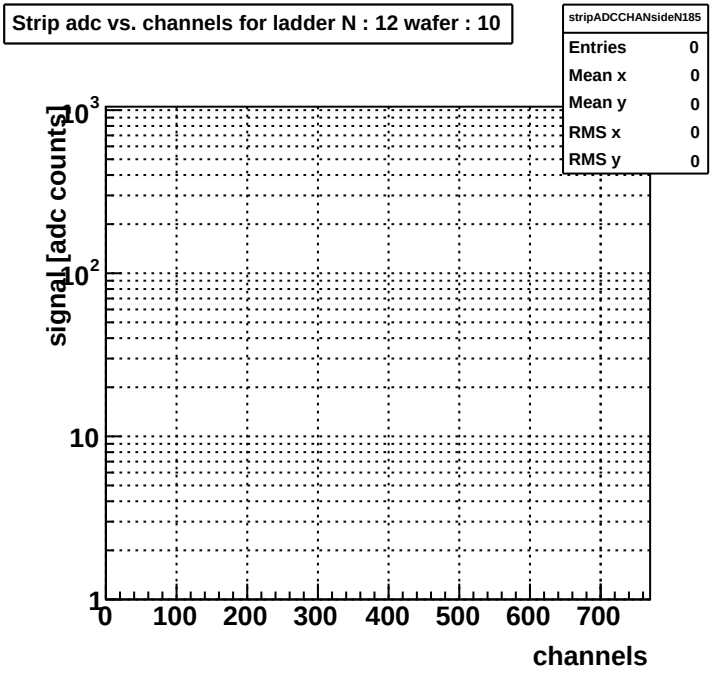

Strip Adc vs. channels for ladder P : 12 wafer : 9
stripADCCHANsideP184
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

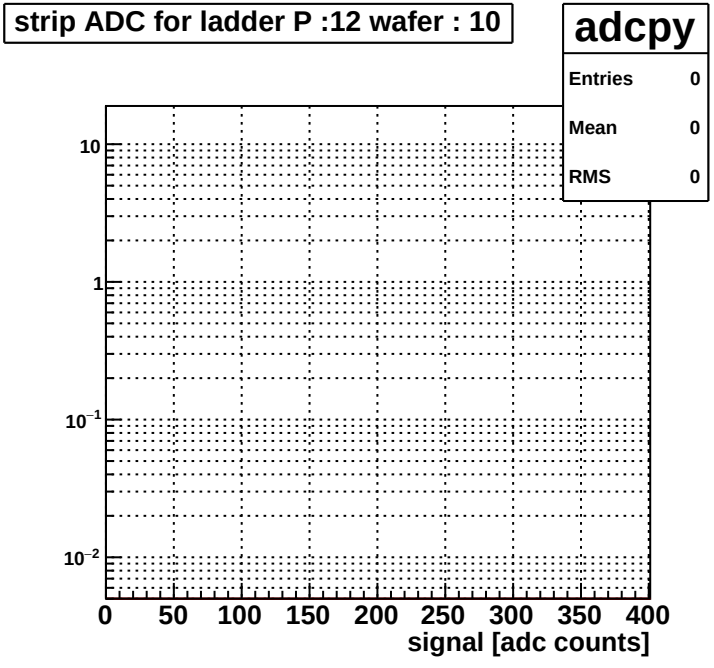

Strip adc vs. channels for ladder N : 12 wafer : 9
stripADCCHANsideN184
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

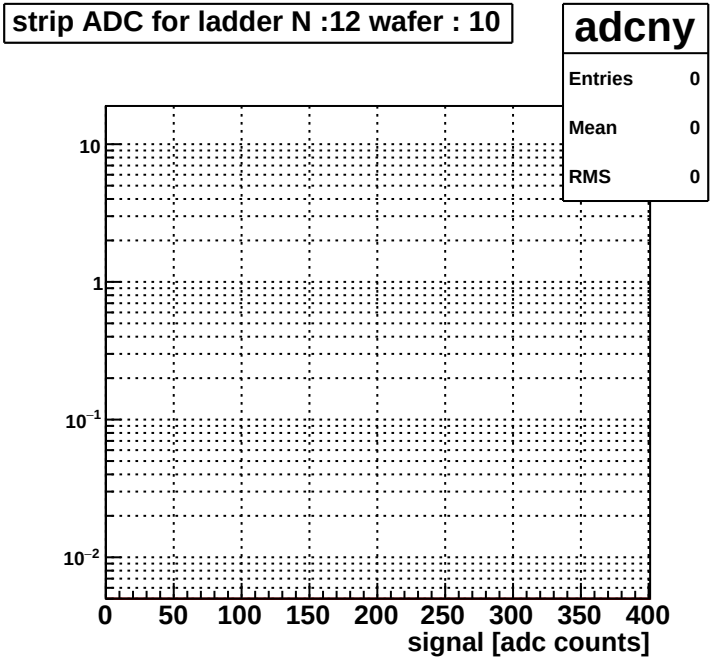

strip ADC for ladder P :12 wafer : 9
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

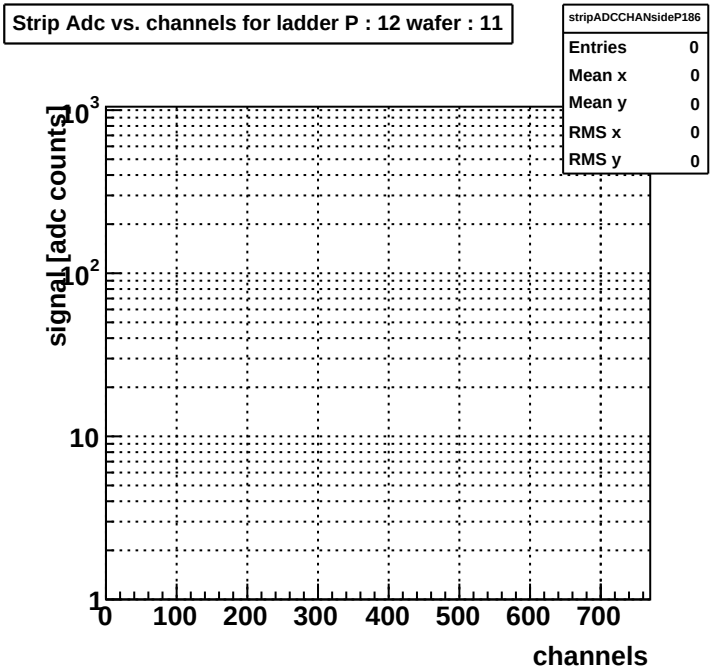

strip ADC for ladder N :12 wafer : 9
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

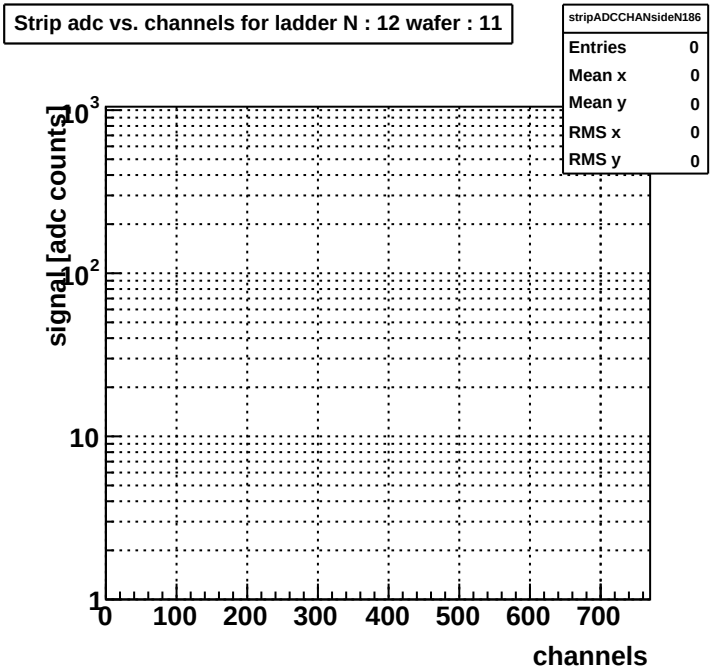

Strip Adc vs. channels for ladder P : 12 wafer : 10
stripADCCHANsideP185
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

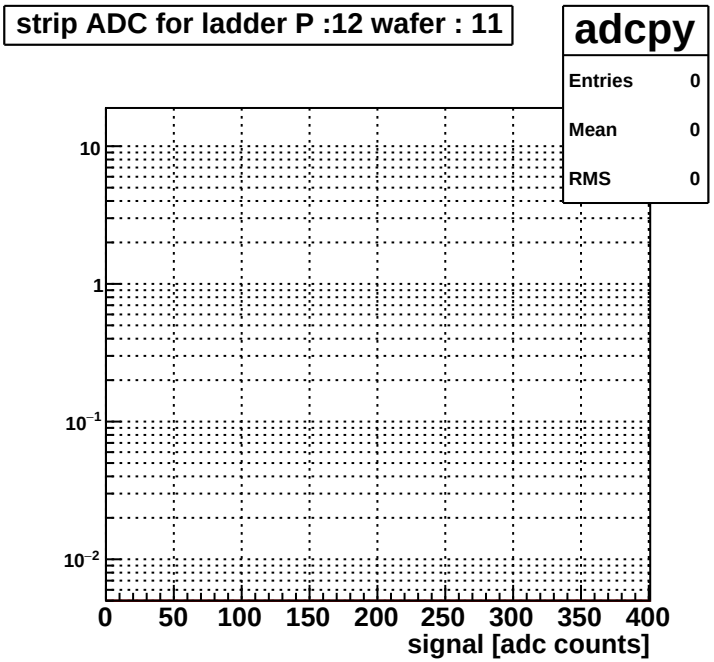

Strip adc vs. channels for ladder N : 12 wafer : 10
stripADCCHANsideN185
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

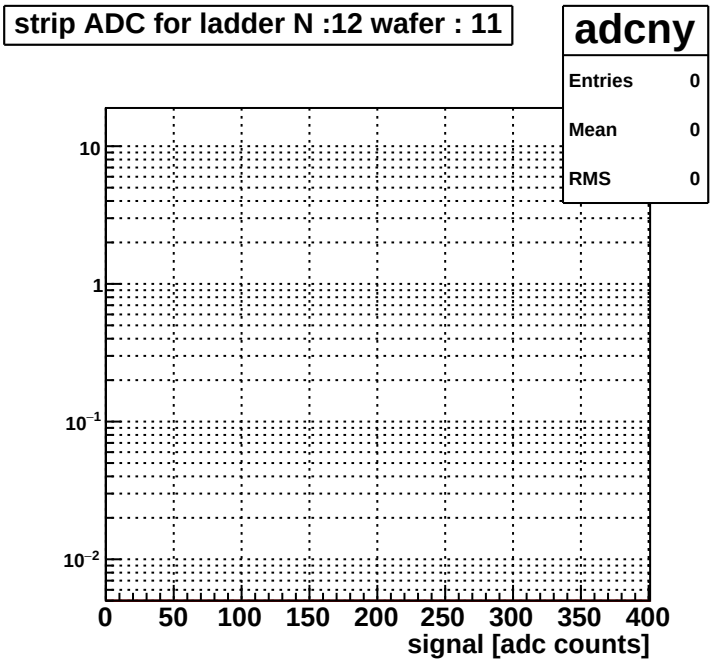

strip ADC for ladder P :12 wafer : 10
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

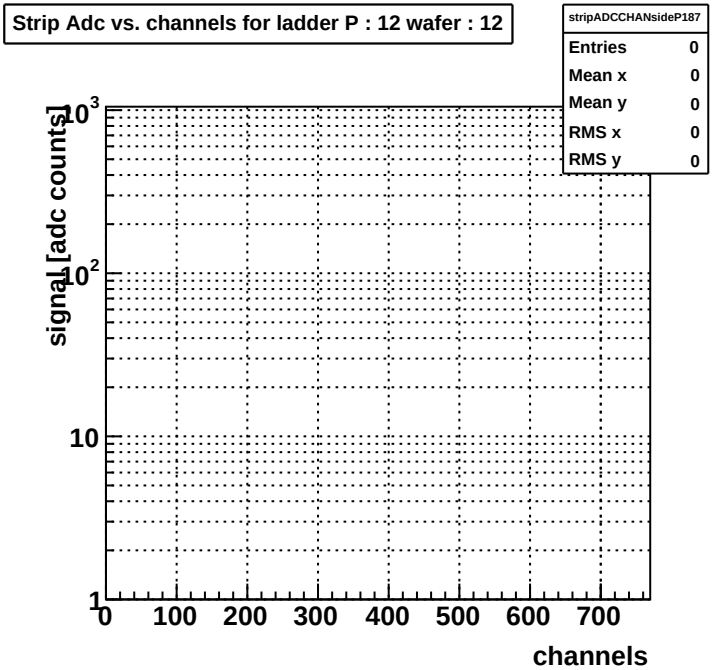

strip ADC for ladder N :12 wafer : 10
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

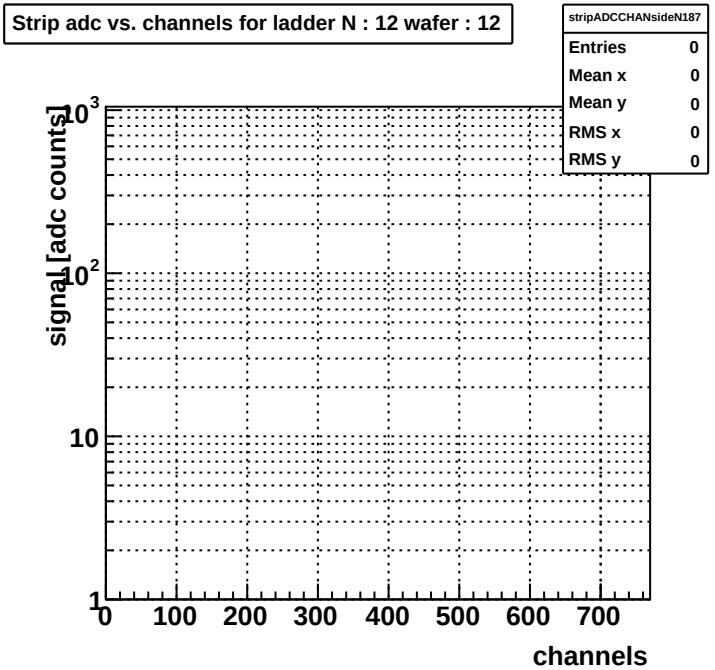

Strip Adc vs. channels for ladder P : 12 wafer : 11
stripADCCHANsideP186
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

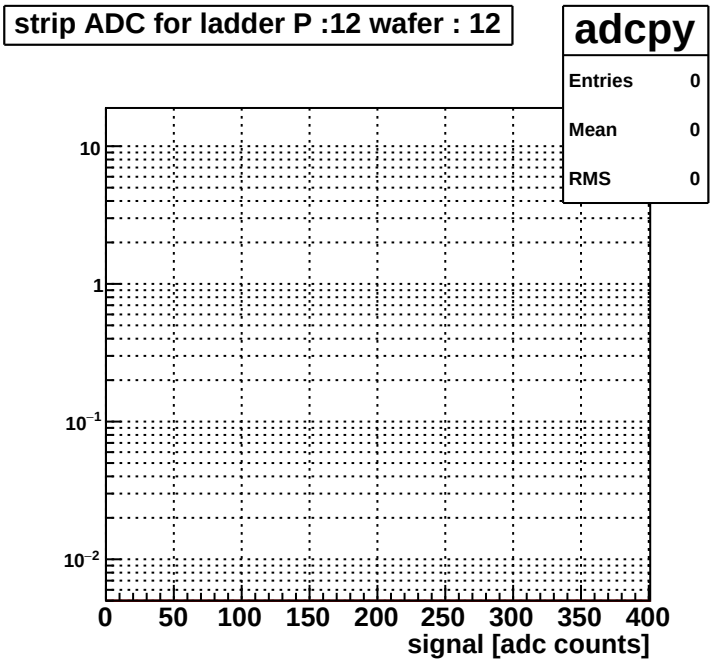

Strip adc vs. channels for ladder N : 12 wafer : 11
stripADCCHANsideN186
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

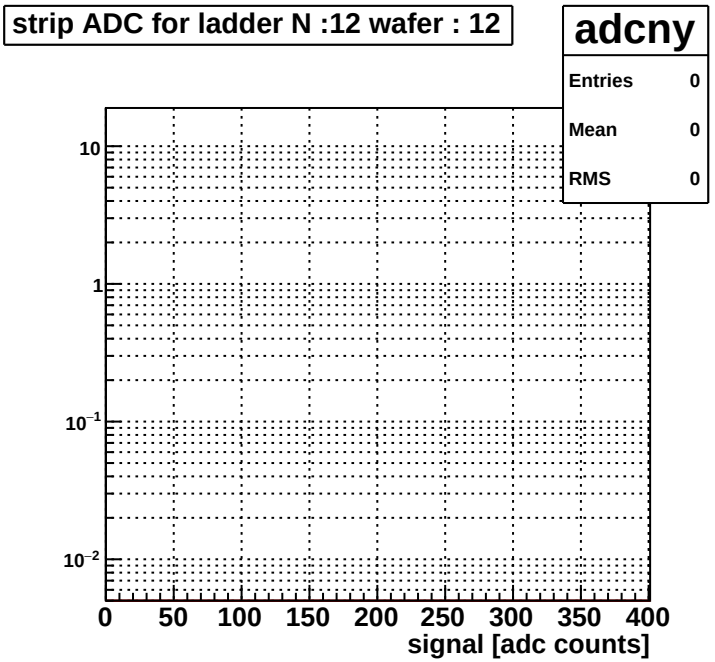

strip ADC for ladder P :12 wafer : 11
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

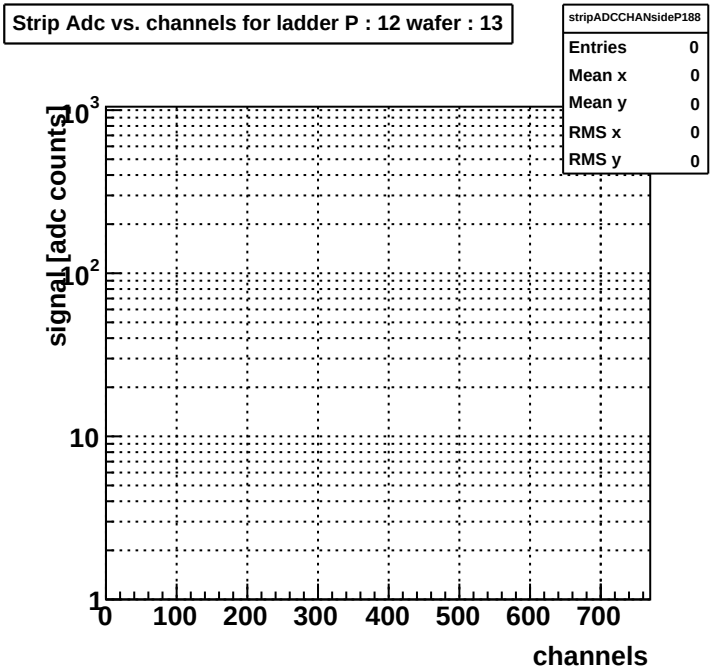

strip ADC for ladder N :12 wafer : 11
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

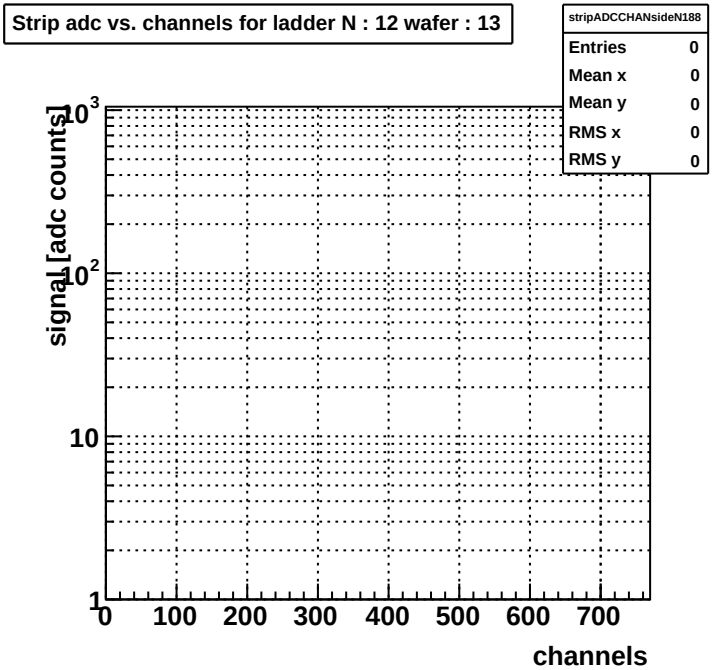

Strip Adc vs. channels for ladder P : 12 wafer : 12
stripADCCHANsideP187
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

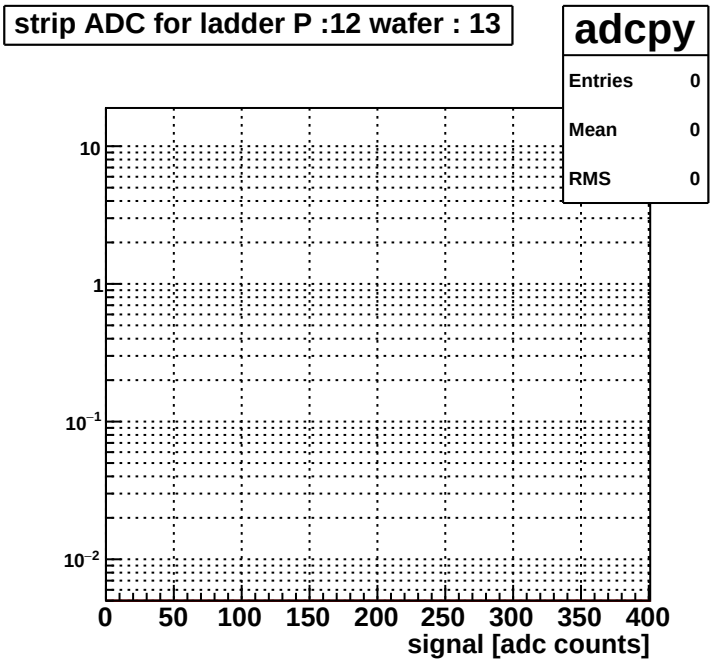

Strip adc vs. channels for ladder N : 12 wafer : 12
stripADCCHANsideN187
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

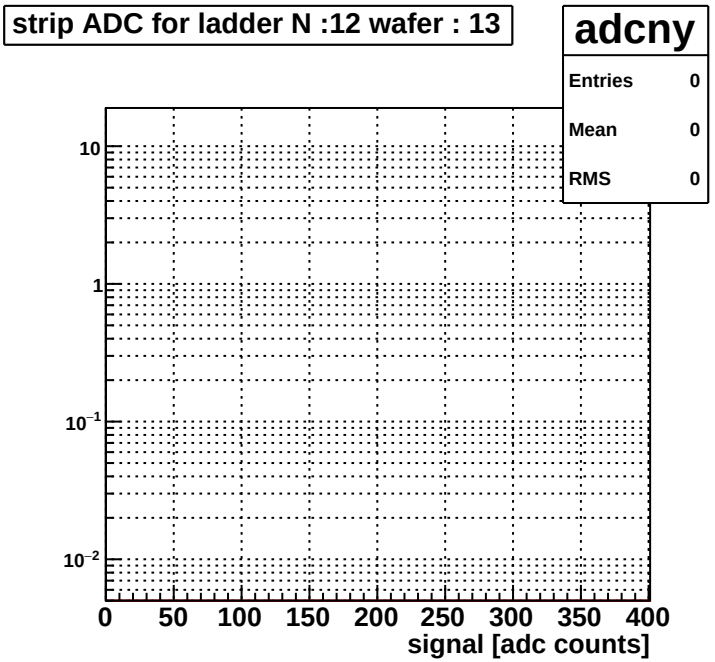

strip ADC for ladder P :12 wafer : 12
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

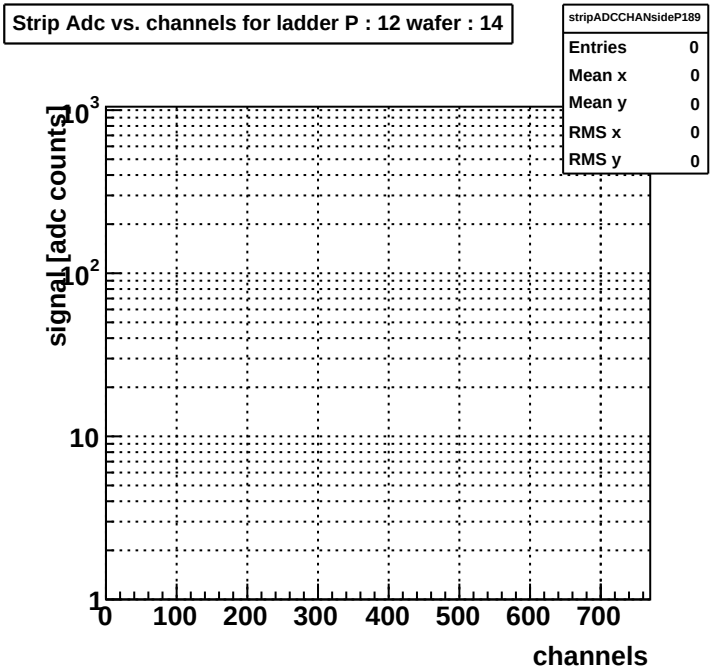

strip ADC for ladder N :12 wafer : 12
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

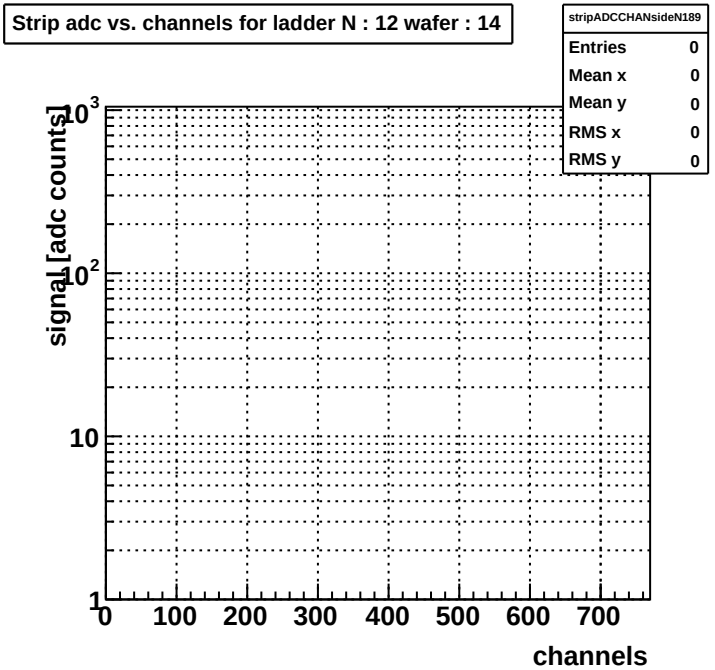

Strip Adc vs. channels for ladder P : 12 wafer : 13
stripADCCHANsideP188
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

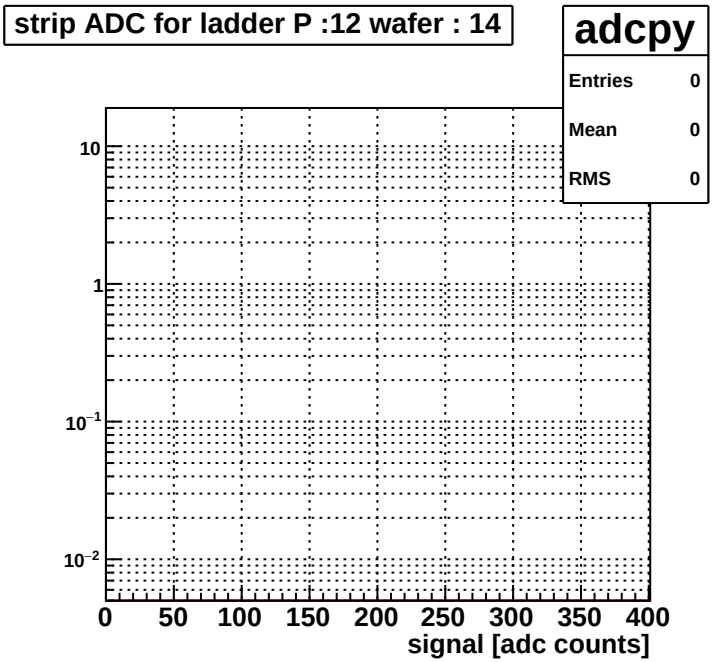

Strip adc vs. channels for ladder N : 12 wafer : 13
stripADCCHANsideN188
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

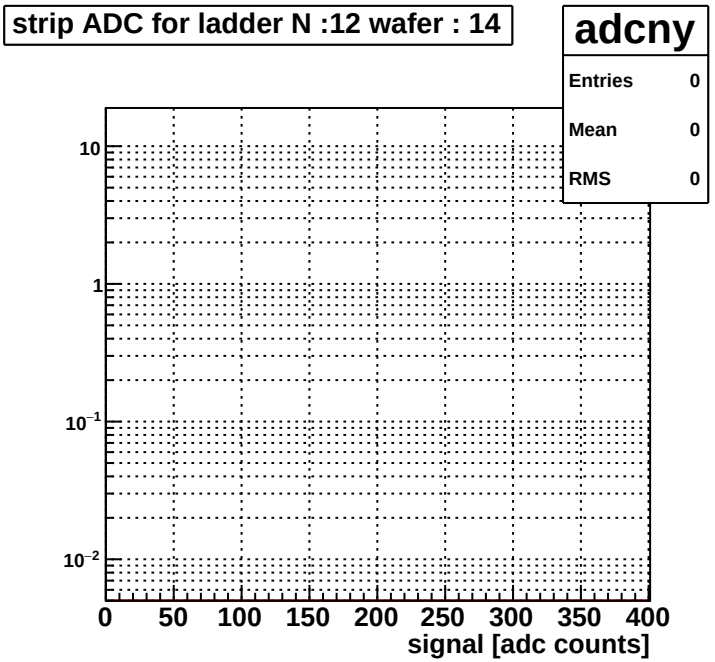

strip ADC for ladder P :12 wafer : 13
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

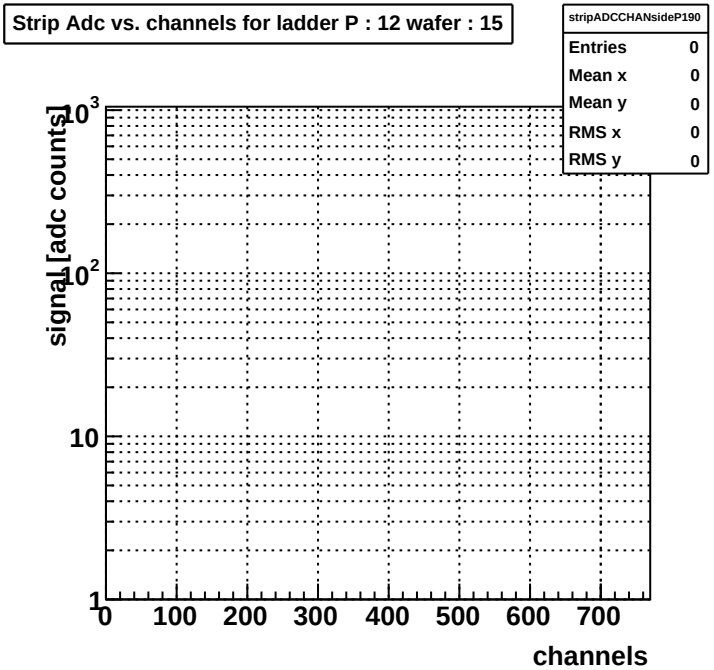

strip ADC for ladder N :12 wafer : 13
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

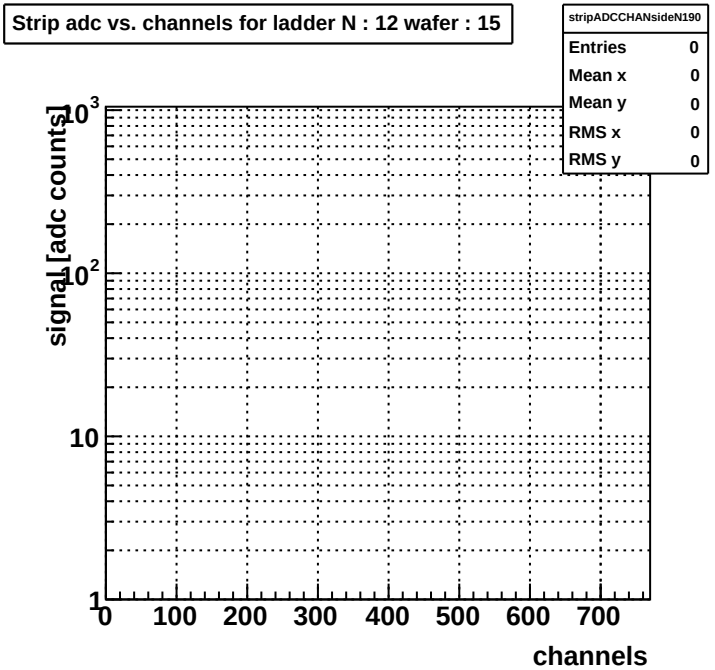

Strip Adc vs. channels for ladder P : 12 wafer : 14
stripADCCHANsideP189
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

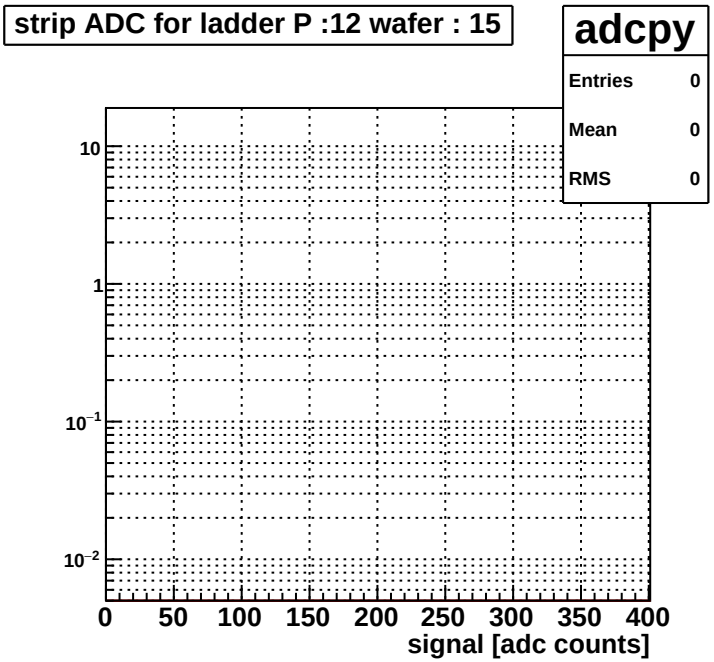

Strip adc vs. channels for ladder N : 12 wafer : 14
stripADCCHANsideN189
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

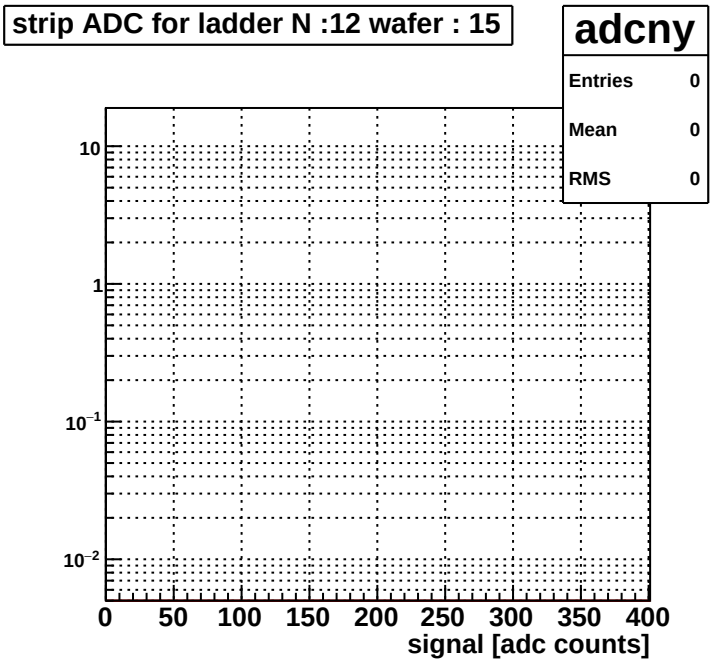

strip ADC for ladder P :12 wafer : 14
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

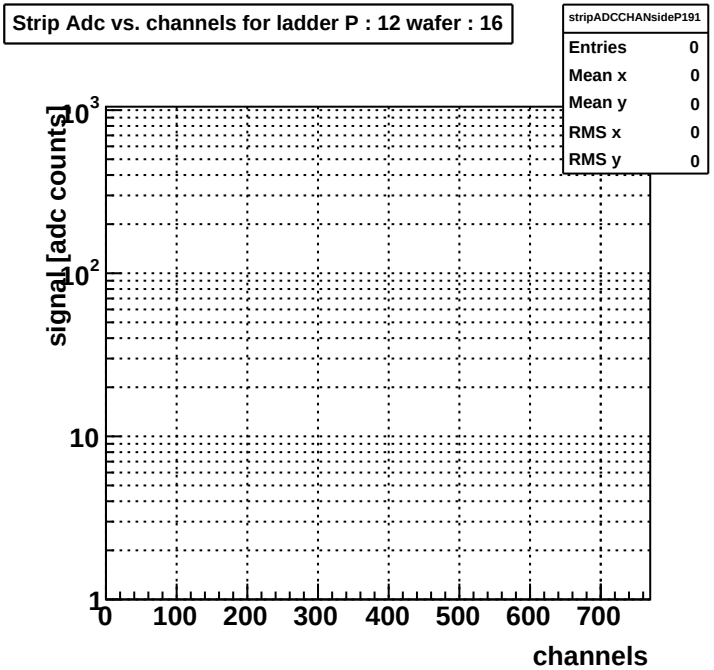

strip ADC for ladder N :12 wafer : 14
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

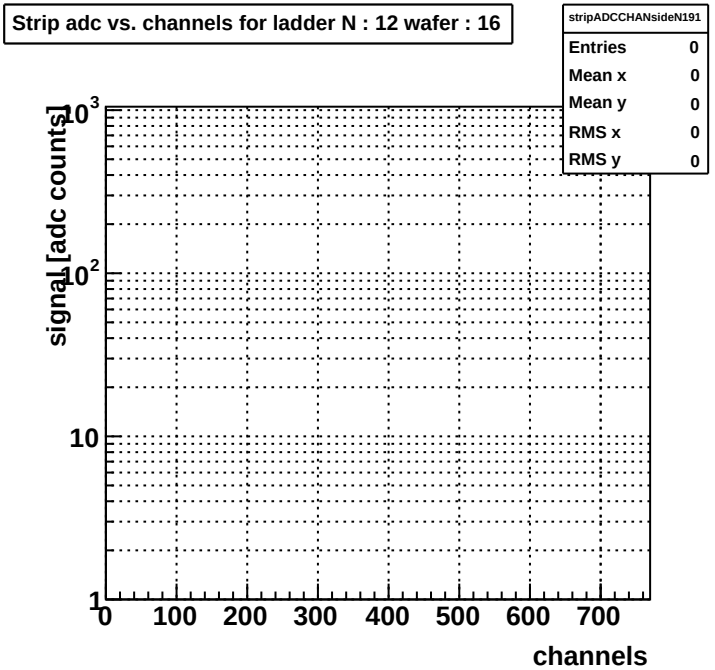

Strip Adc vs. channels for ladder P : 12 wafer : 15
stripADCCHANsideP190
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

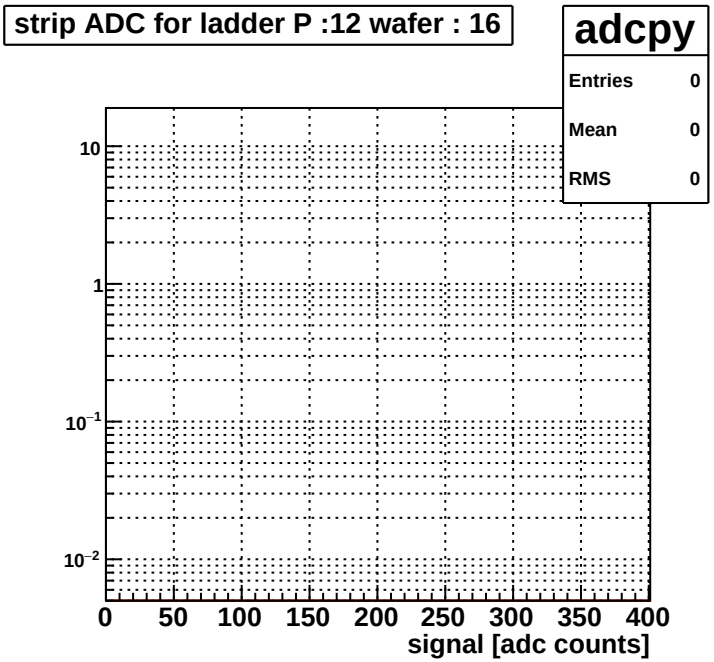

Strip adc vs. channels for ladder N : 12 wafer : 15
stripADCCHANsideN190
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

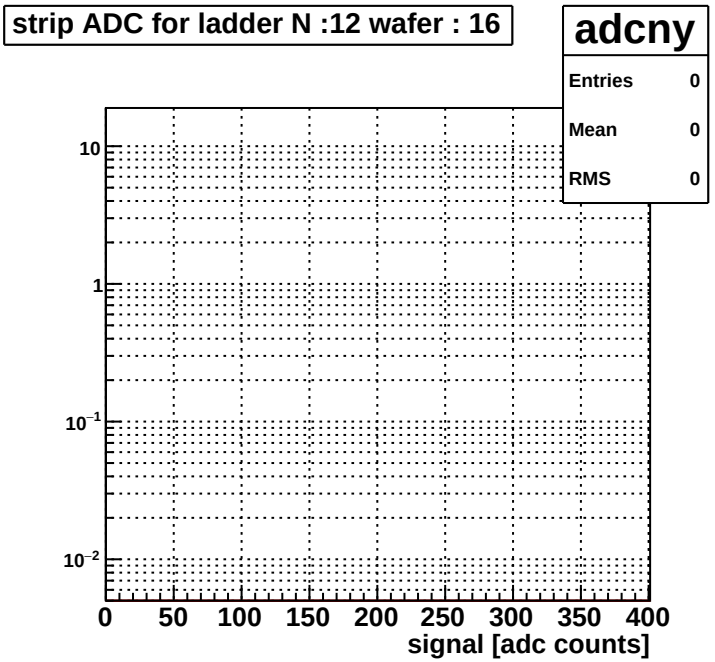

strip ADC for ladder P :12 wafer : 15
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

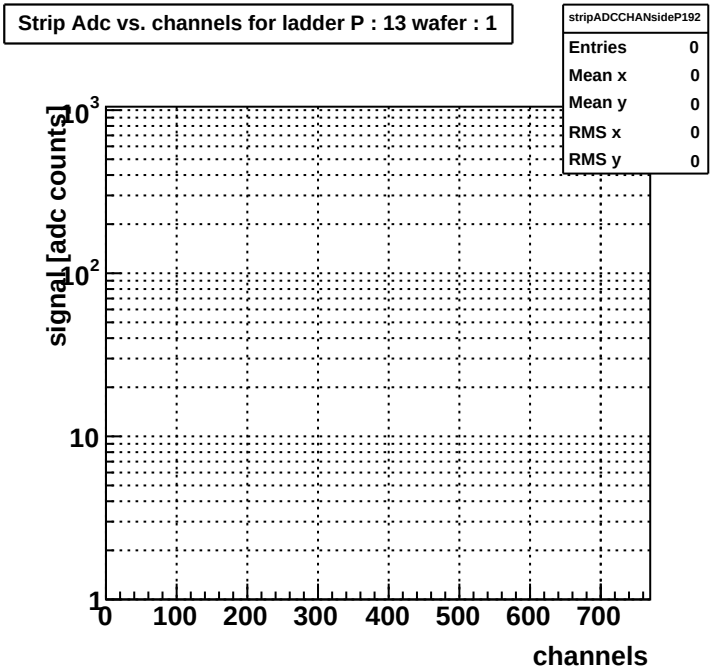

strip ADC for ladder N :12 wafer : 15
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

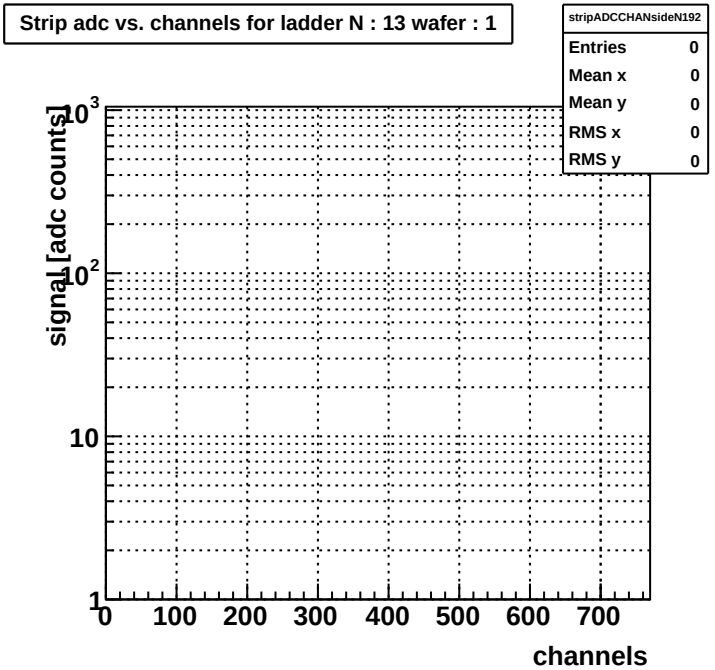

Strip Adc vs. channels for ladder P : 12 wafer : 16
stripADCCHANsideP191
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

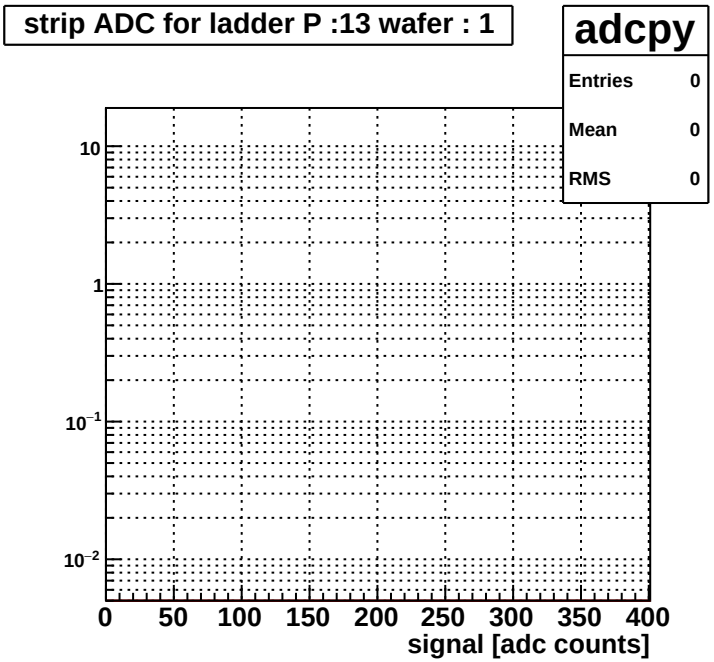

Strip adc vs. channels for ladder N : 12 wafer : 16
stripADCCHANsideN191
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

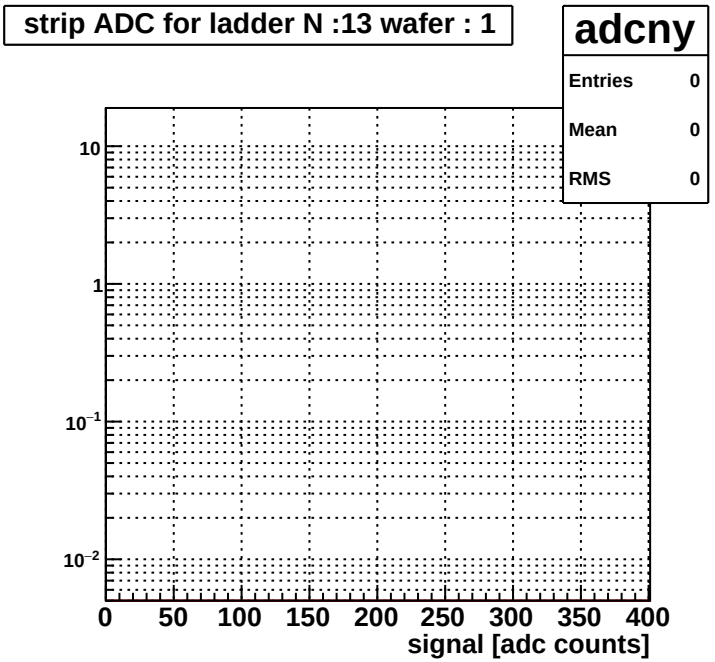

strip ADC for ladder P :12 wafer : 16
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

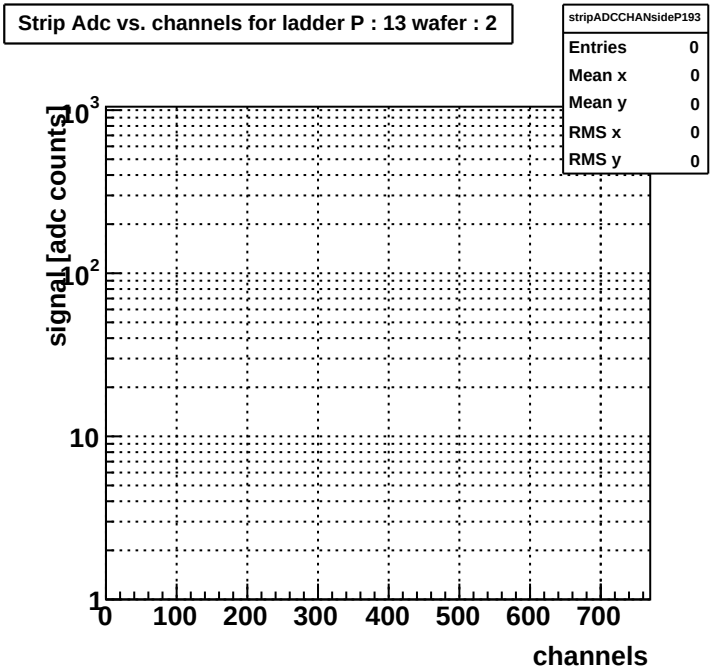

strip ADC for ladder N :12 wafer : 16
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

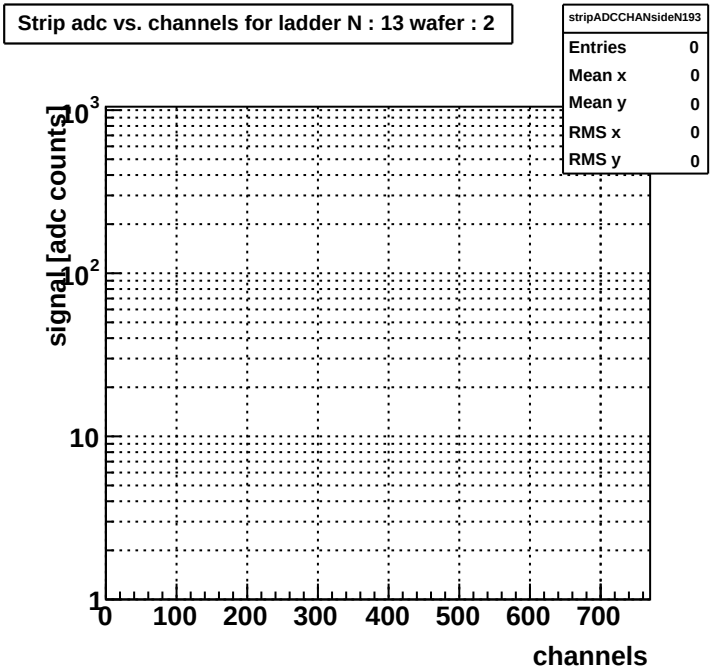

Strip Adc vs. channels for ladder P : 13 wafer : 1
stripADCCHANsideP192
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

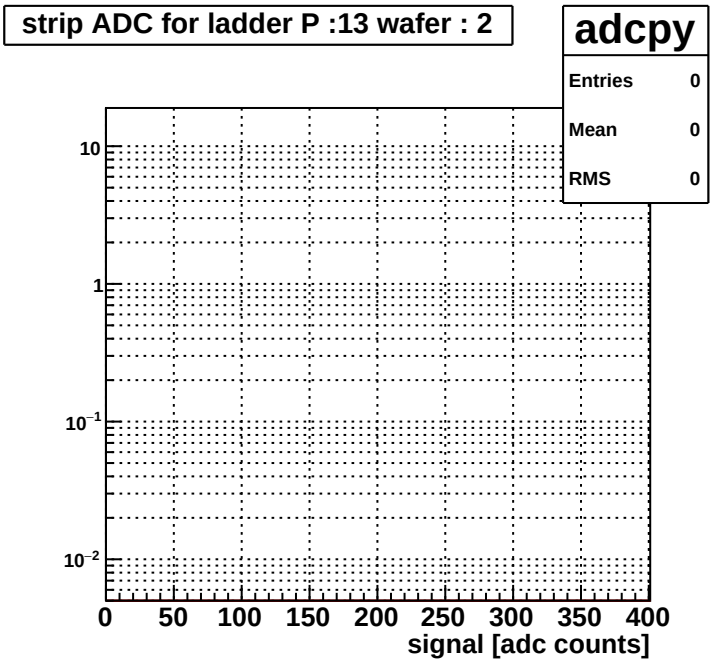

Strip adc vs. channels for ladder N : 13 wafer : 1
stripADCCHANsideN192
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

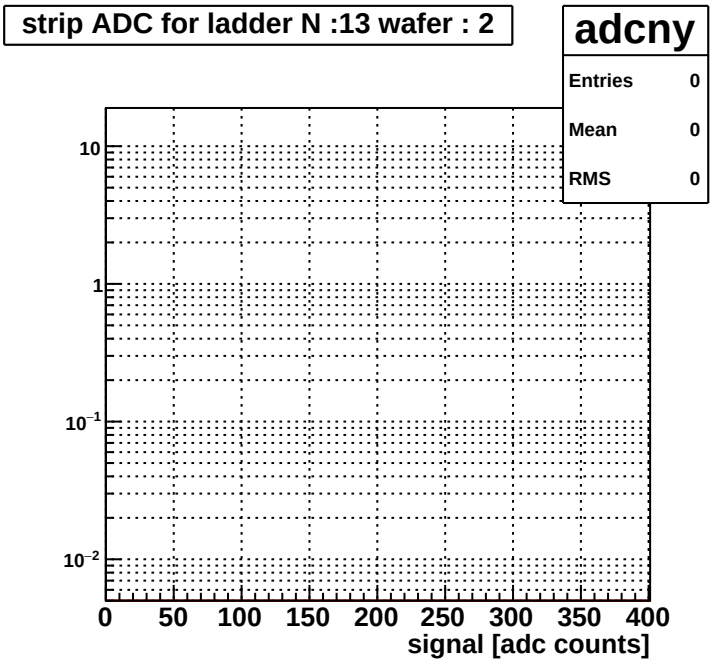

strip ADC for ladder P :13 wafer : 1
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

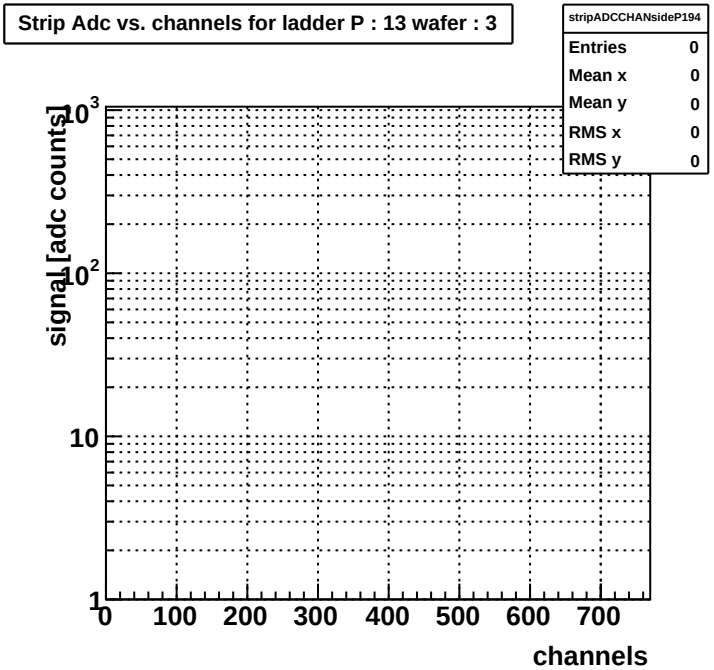

strip ADC for ladder N :13 wafer : 1
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

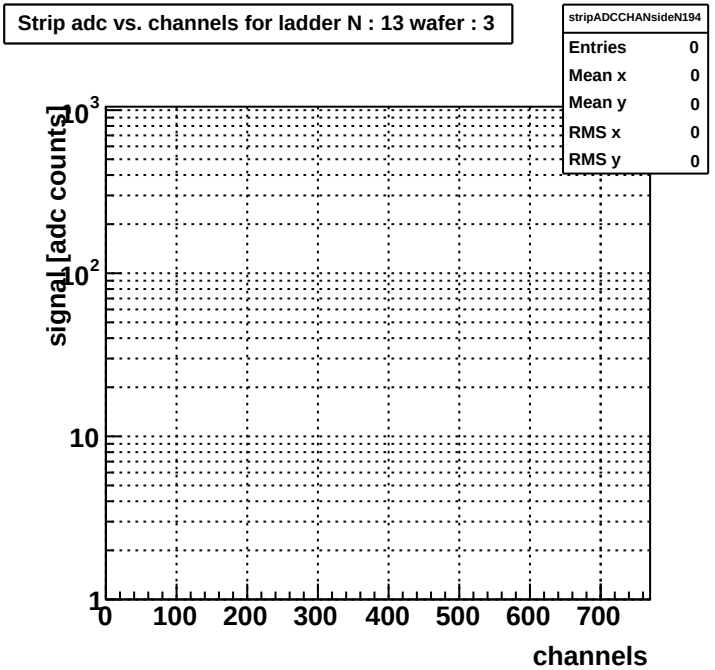

Strip Adc vs. channels for ladder P : 13 wafer : 2
stripADCCHANsideP193
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

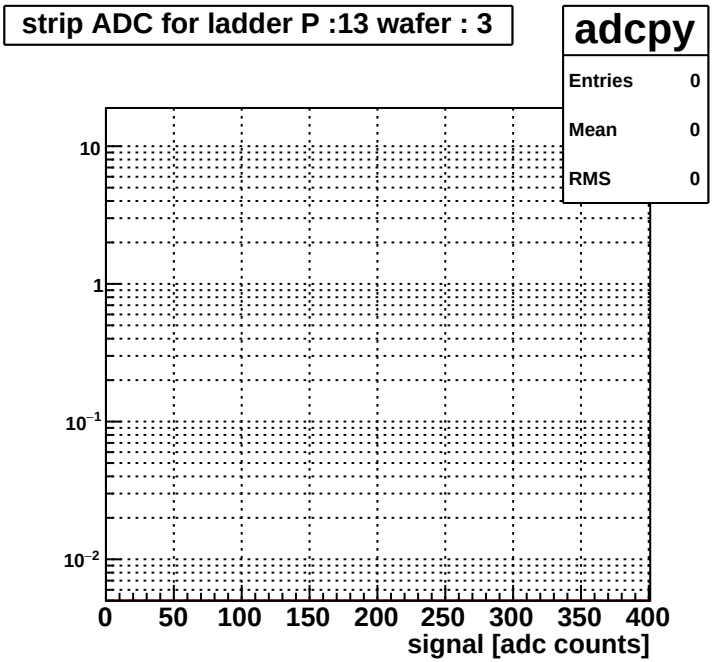

Strip adc vs. channels for ladder N : 13 wafer : 2
stripADCCHANsideN193
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

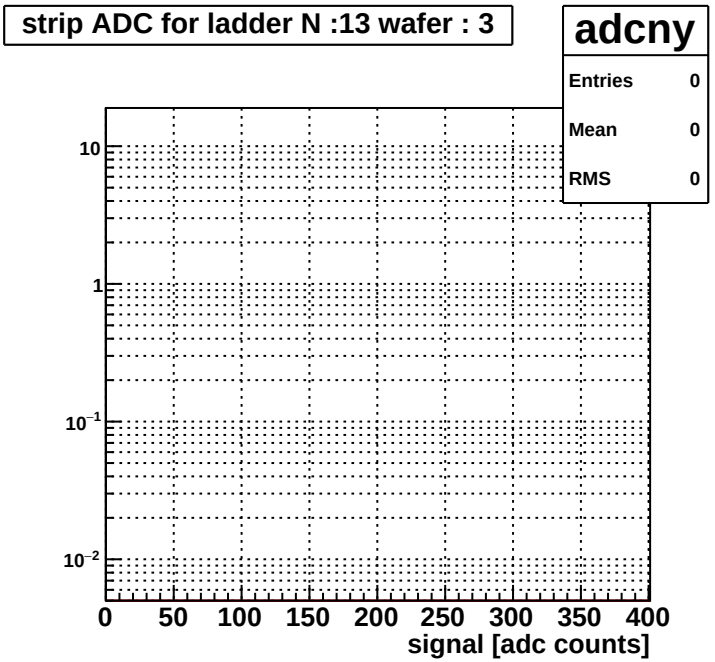

strip ADC for ladder P :13 wafer : 2
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

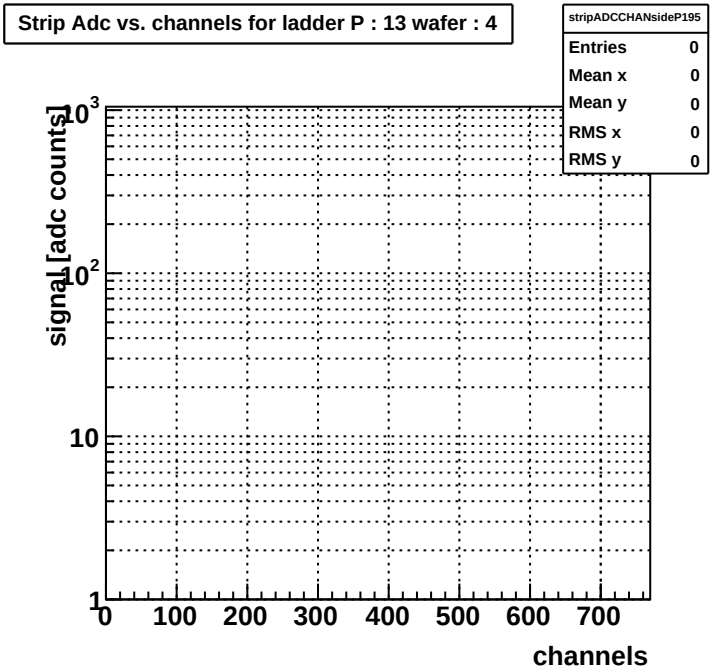

strip ADC for ladder N :13 wafer : 2
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

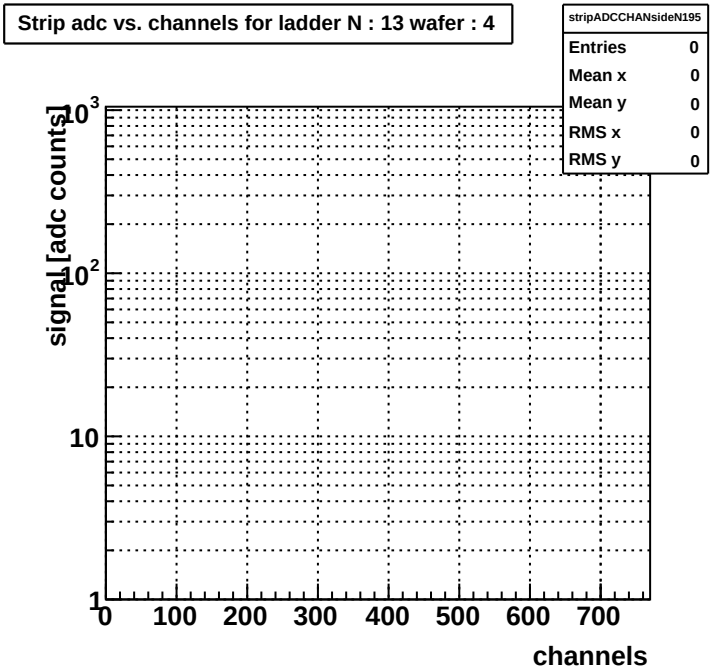

Strip Adc vs. channels for ladder P : 13 wafer : 3
stripADCCHANsideP194
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

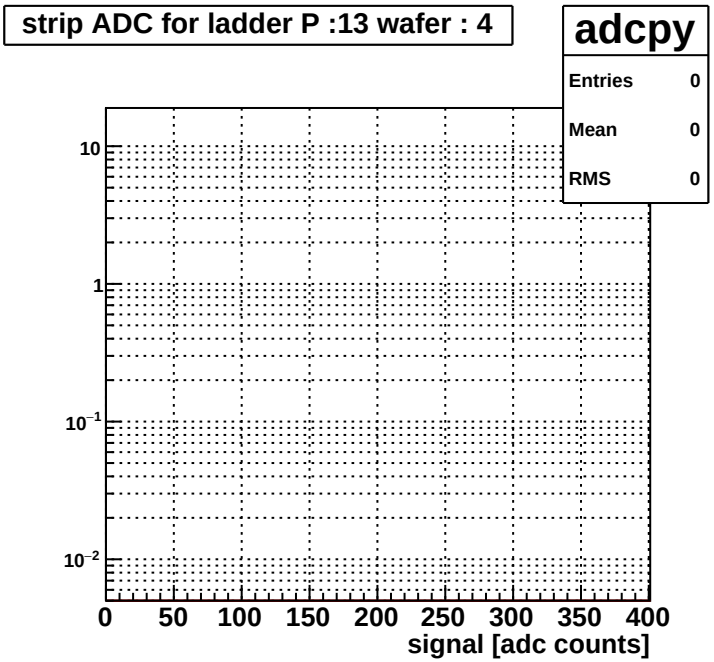

Strip adc vs. channels for ladder N : 13 wafer : 3
stripADCCHANsideN194
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

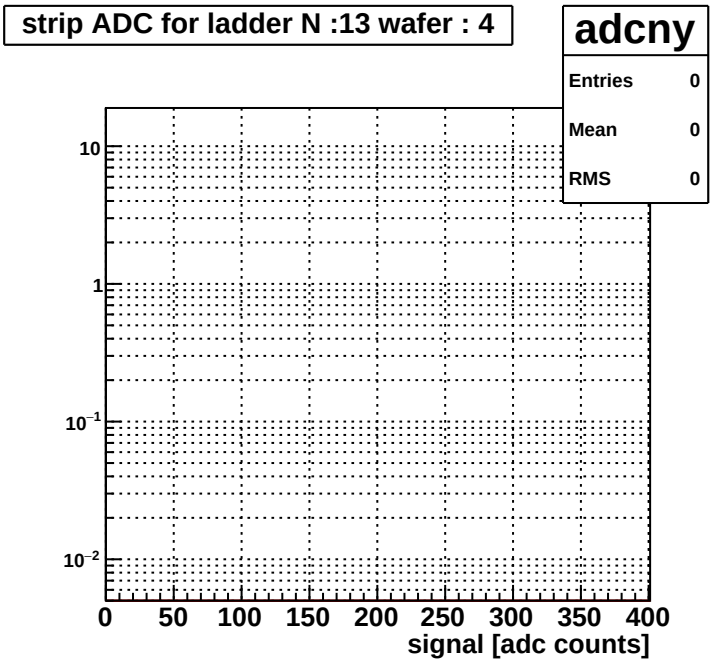

strip ADC for ladder P :13 wafer : 3
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

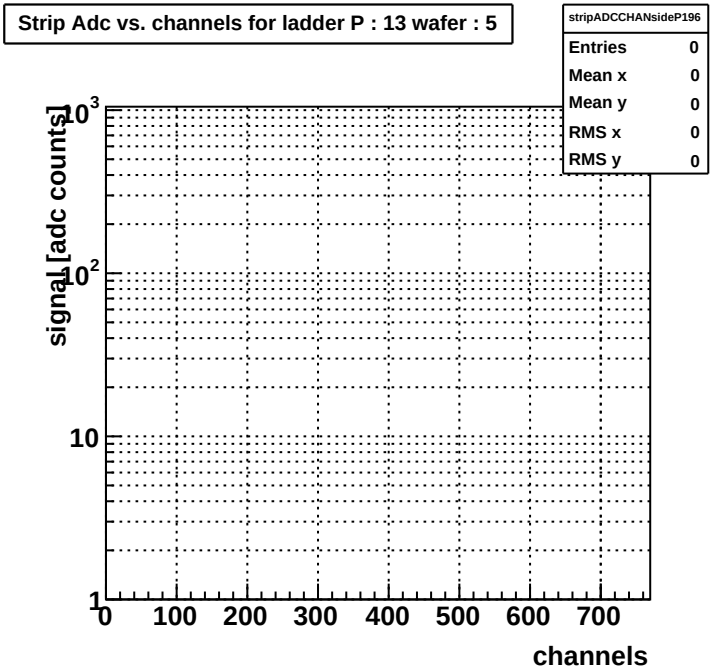

strip ADC for ladder N :13 wafer : 3
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

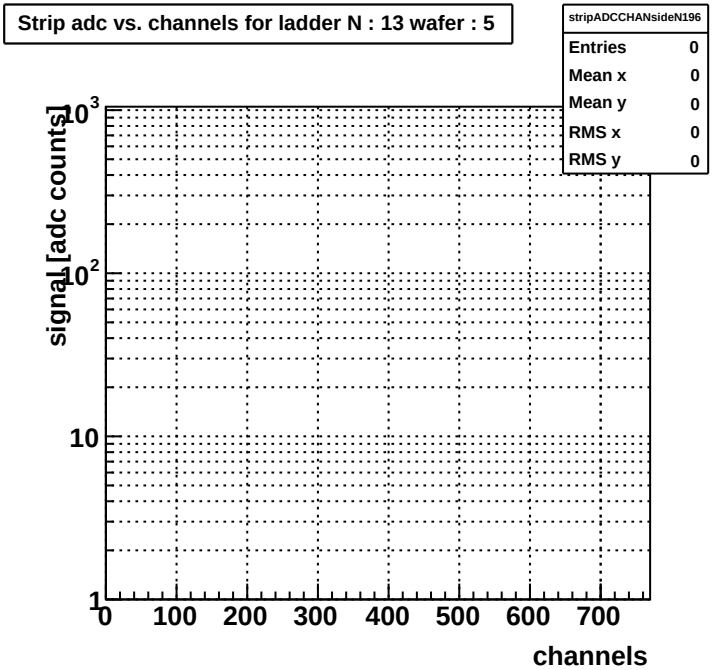

Strip Adc vs. channels for ladder P : 13 wafer : 4
stripADCCHANsideP195
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

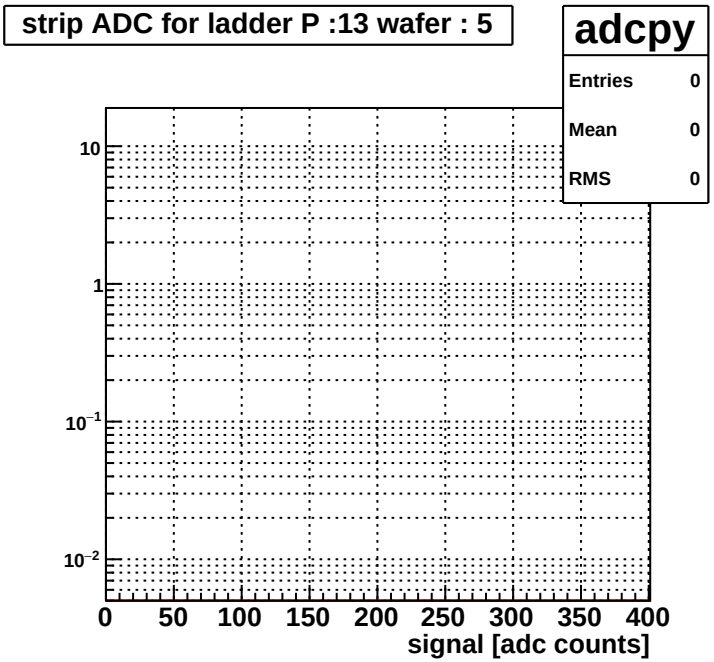

Strip adc vs. channels for ladder N : 13 wafer : 4
stripADCCHANsideN195
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

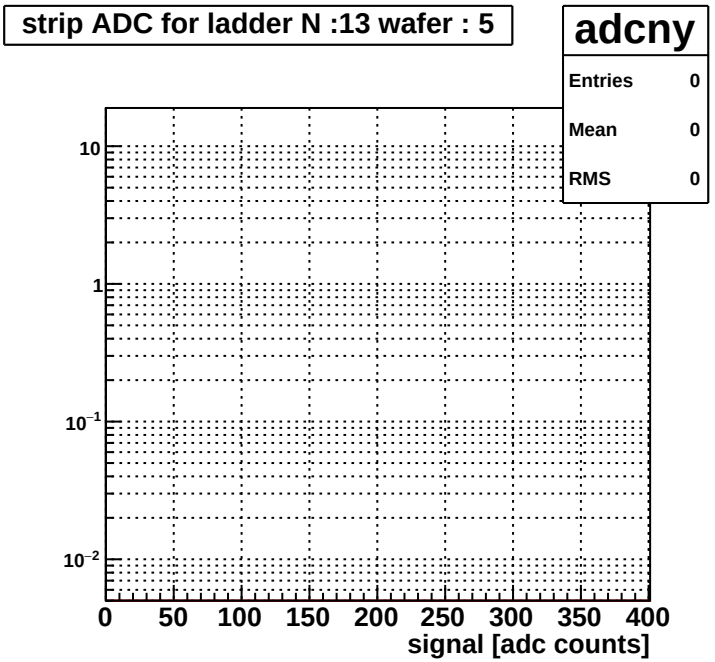

strip ADC for ladder P :13 wafer : 4
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

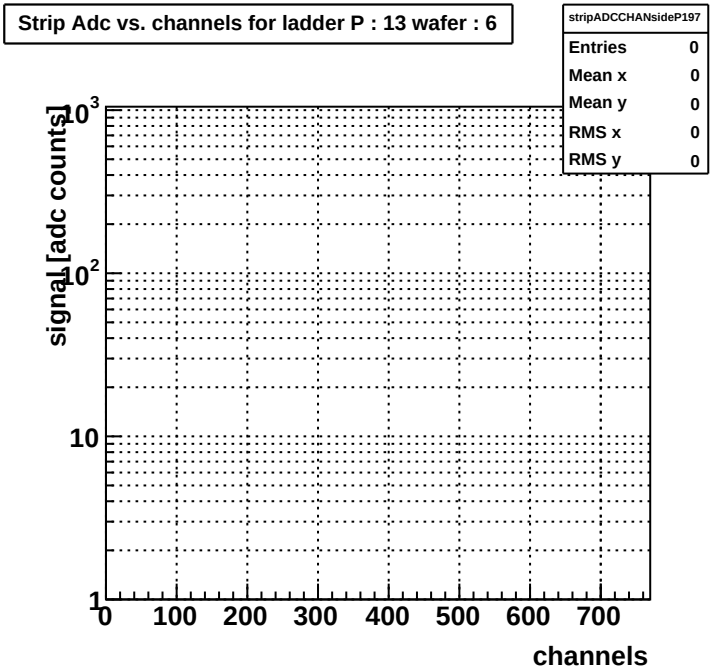

strip ADC for ladder N :13 wafer : 4
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

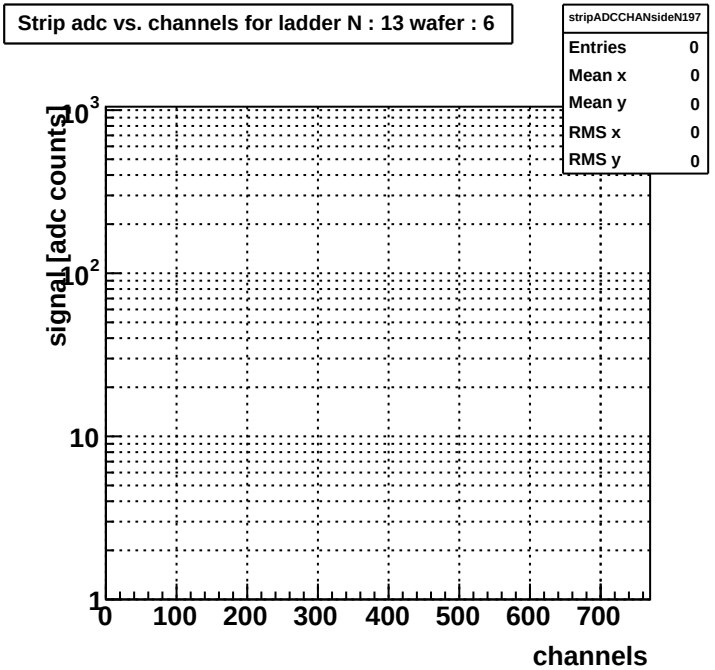

Strip Adc vs. channels for ladder P : 13 wafer : 5
stripADCCHANsideP196
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

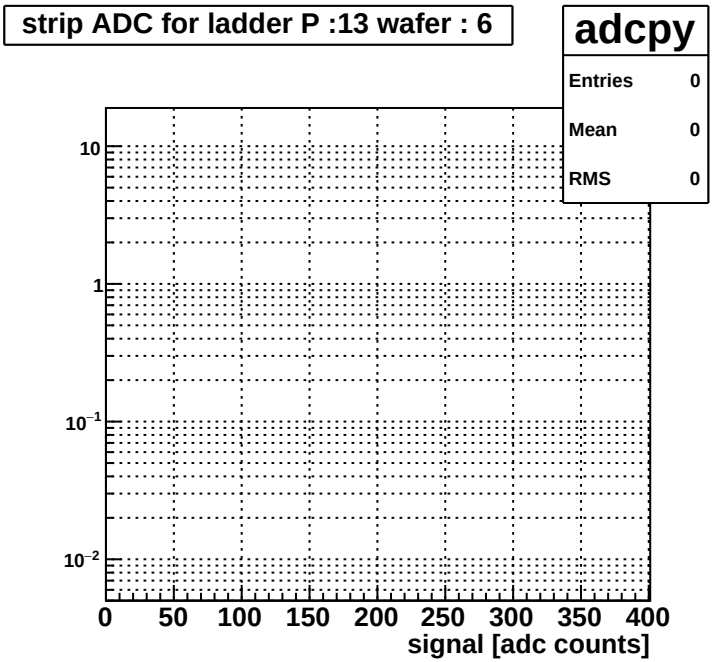

Strip adc vs. channels for ladder N : 13 wafer : 5
stripADCCHANsideN196
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

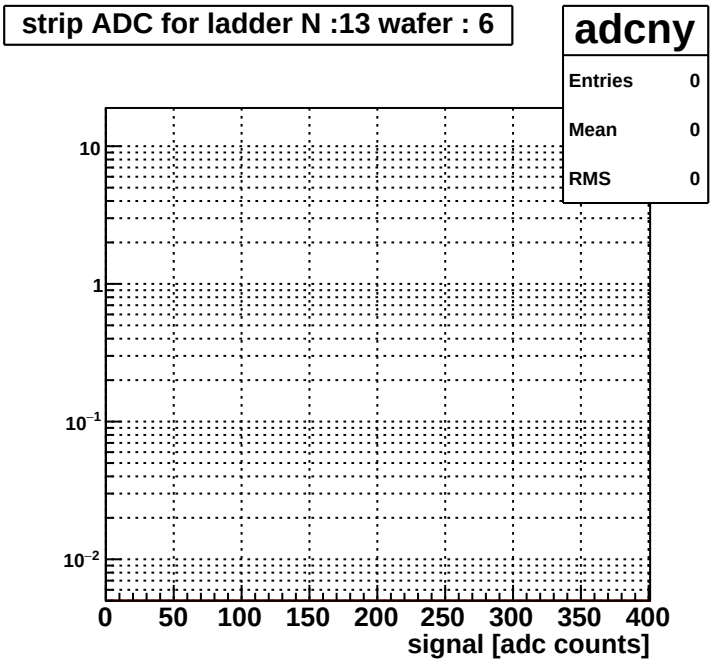

strip ADC for ladder P :13 wafer : 5
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

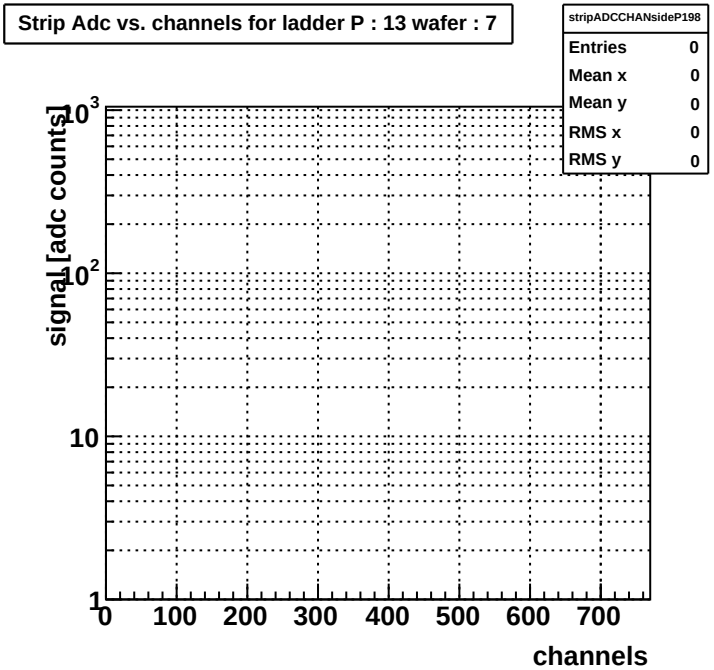

strip ADC for ladder N :13 wafer : 5
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

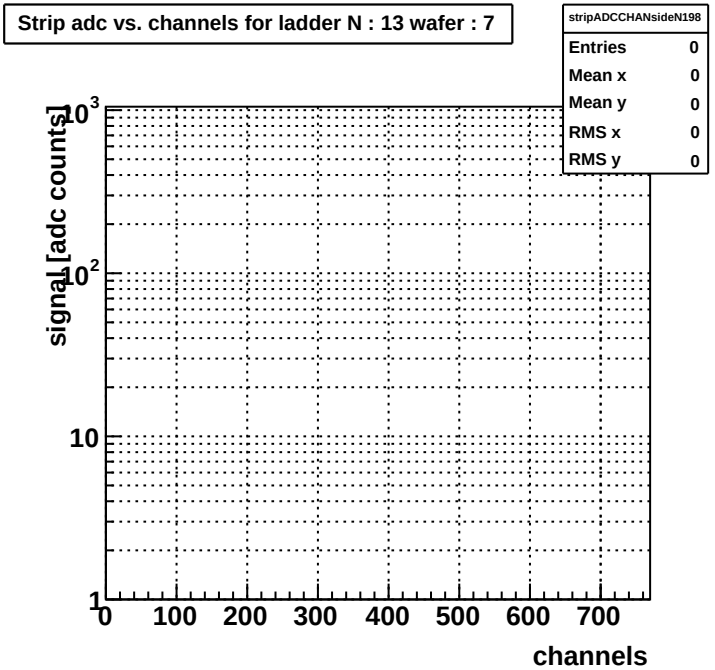

Strip Adc vs. channels for ladder P : 13 wafer : 6
stripADCCHANsideP197
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

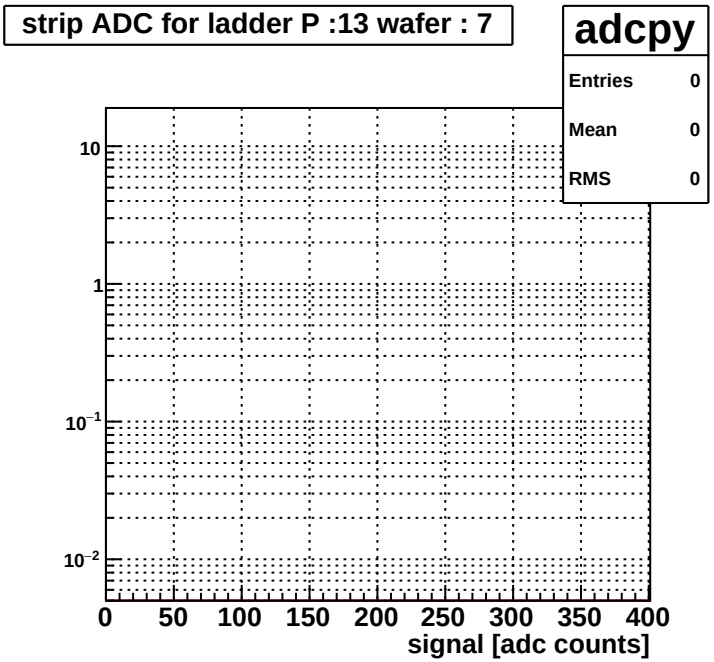

Strip adc vs. channels for ladder N : 13 wafer : 6
stripADCCHANsideN197
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

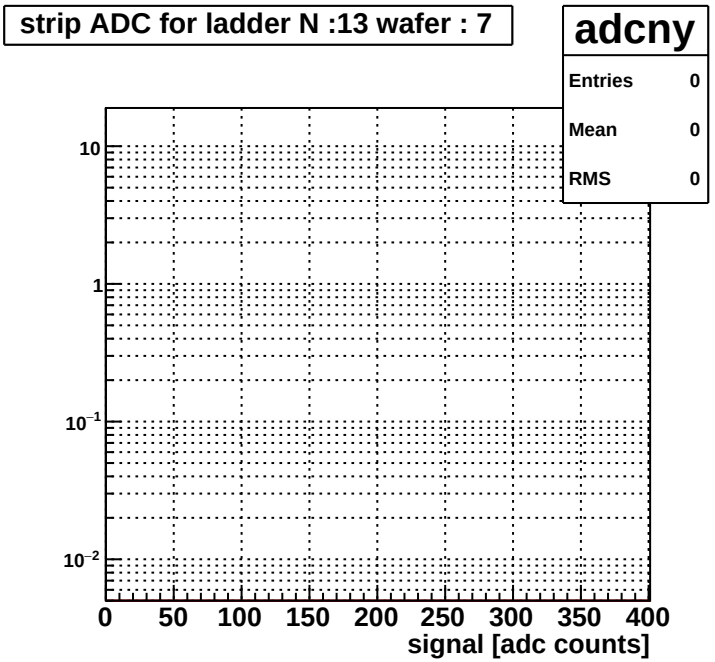

strip ADC for ladder P :13 wafer : 6
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

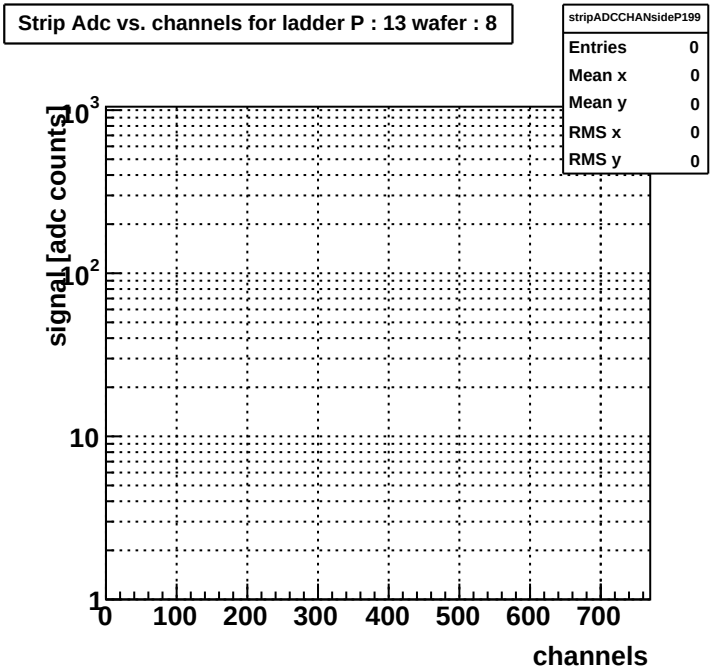

strip ADC for ladder N :13 wafer : 6
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

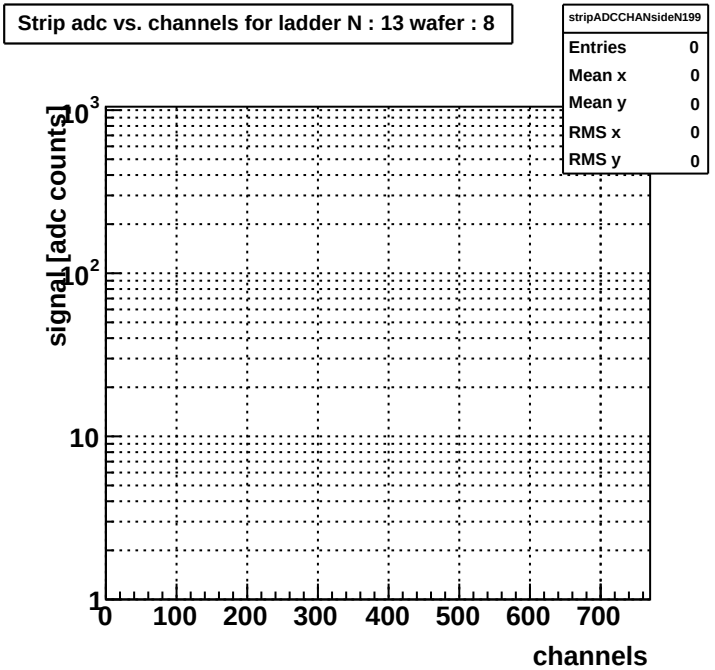

Strip Adc vs. channels for ladder P : 13 wafer : 7
stripADCCHANsideP198
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

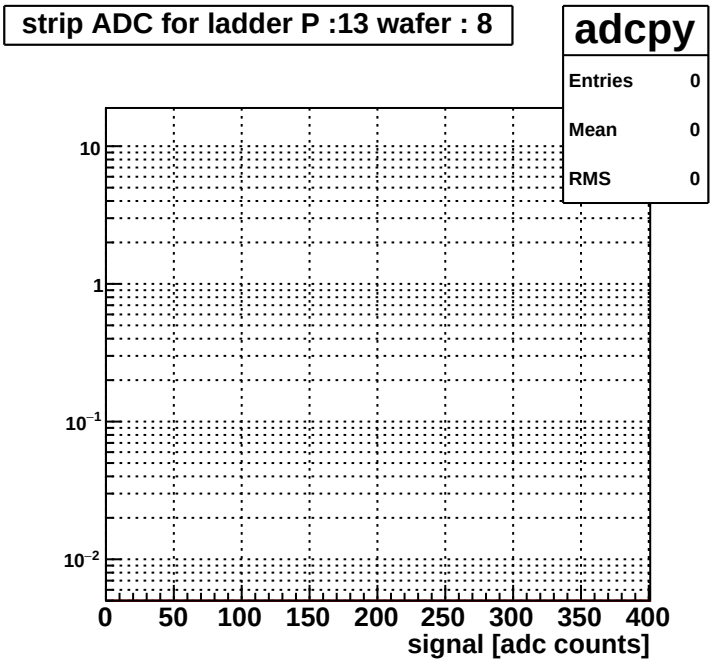

Strip adc vs. channels for ladder N : 13 wafer : 7
stripADCCHANsideN198
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

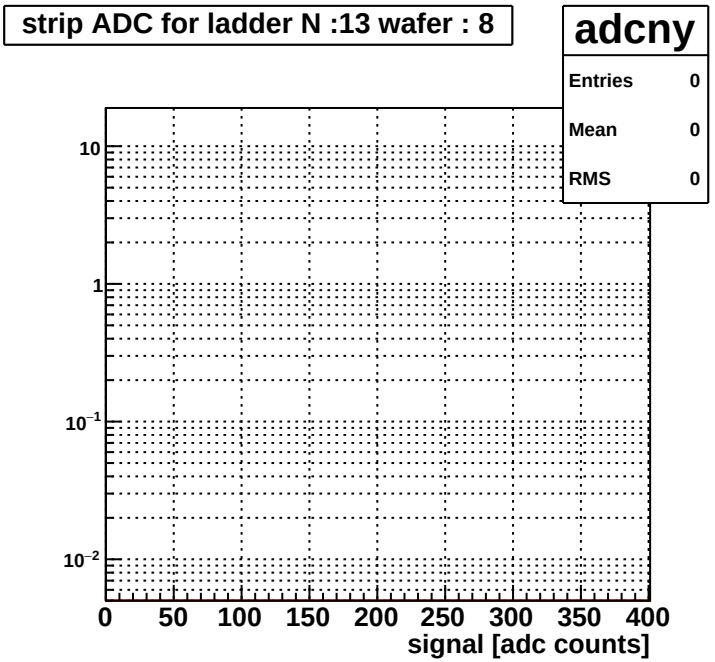

strip ADC for ladder P :13 wafer : 7
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

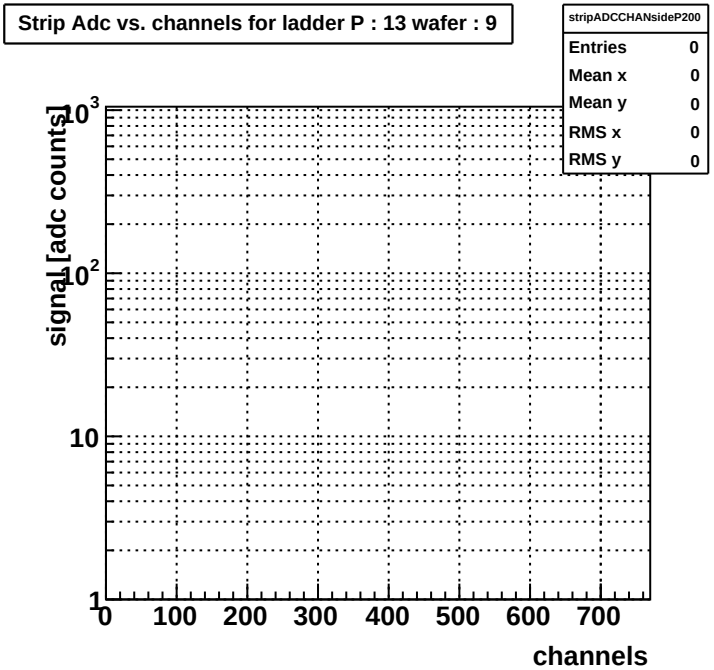

strip ADC for ladder N :13 wafer : 7
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

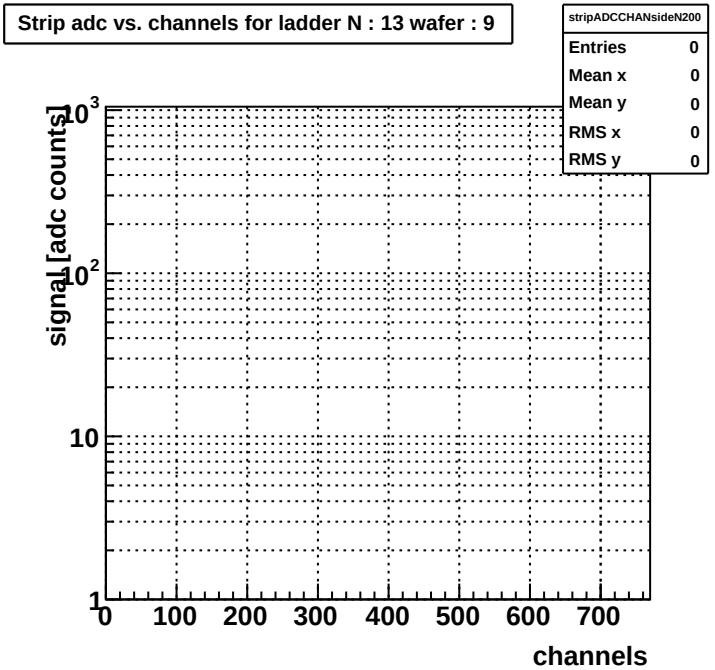

Strip Adc vs. channels for ladder P : 13 wafer : 8
stripADCCHANsideP199
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

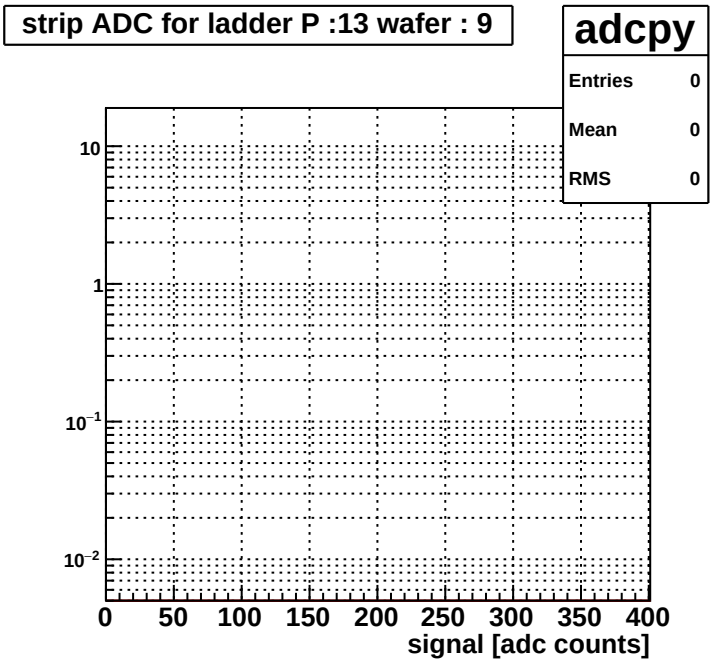

Strip adc vs. channels for ladder N : 13 wafer : 8
stripADCCHANsideN199
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

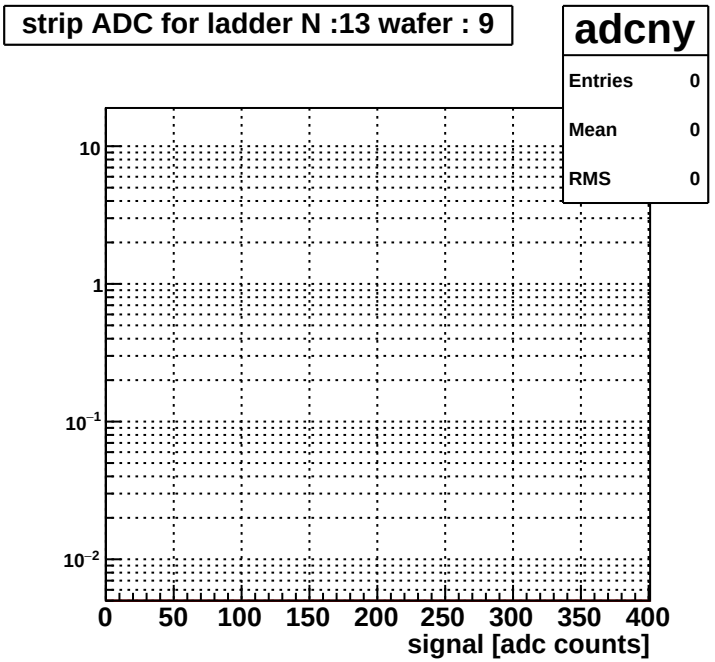

strip ADC for ladder P :13 wafer : 8
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

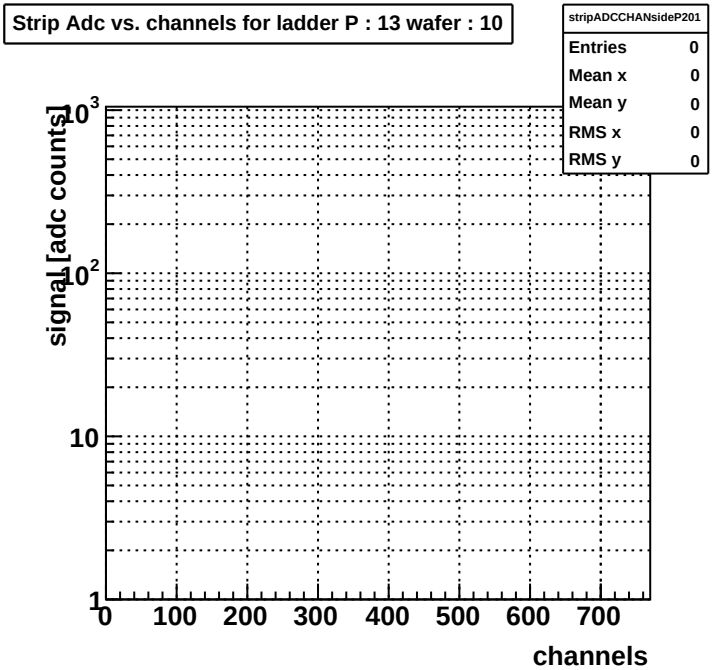

strip ADC for ladder N :13 wafer : 8
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

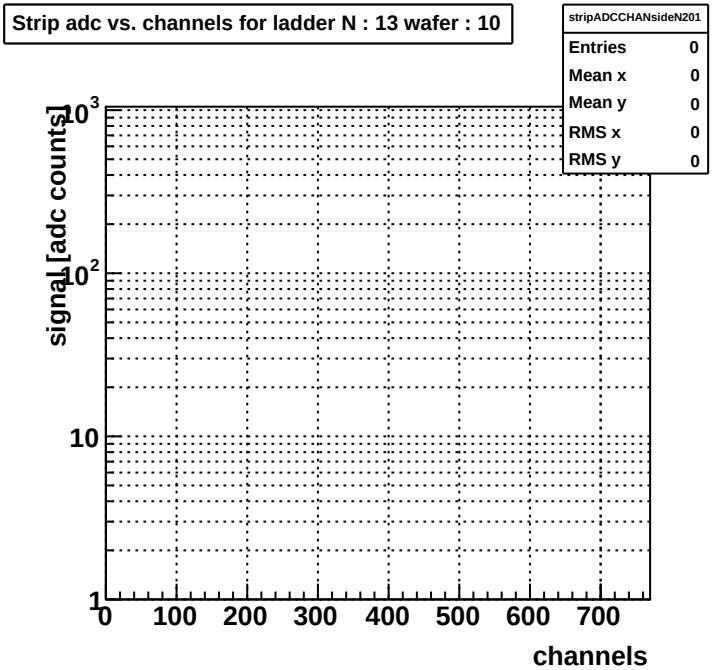

Strip Adc vs. channels for ladder P : 13 wafer : 9
stripADCCHANsideP200
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

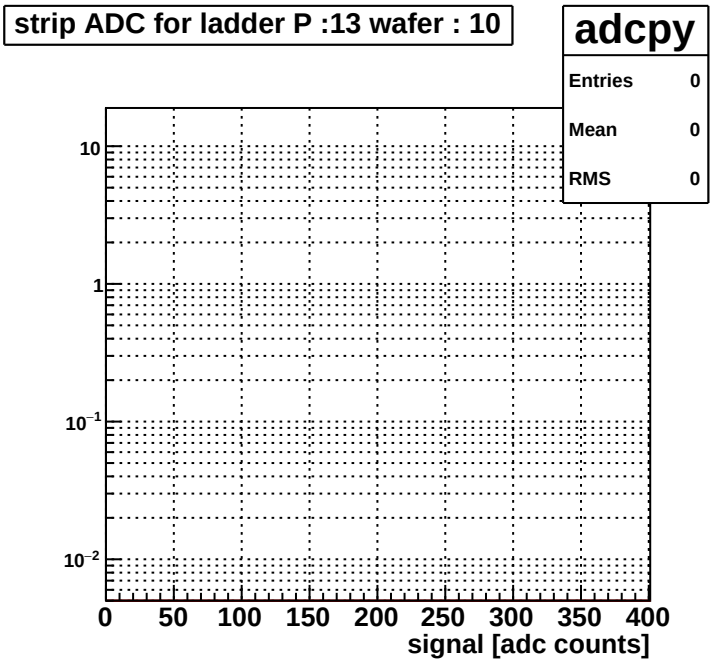

Strip adc vs. channels for ladder N : 13 wafer : 9
stripADCCHANsideN200
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

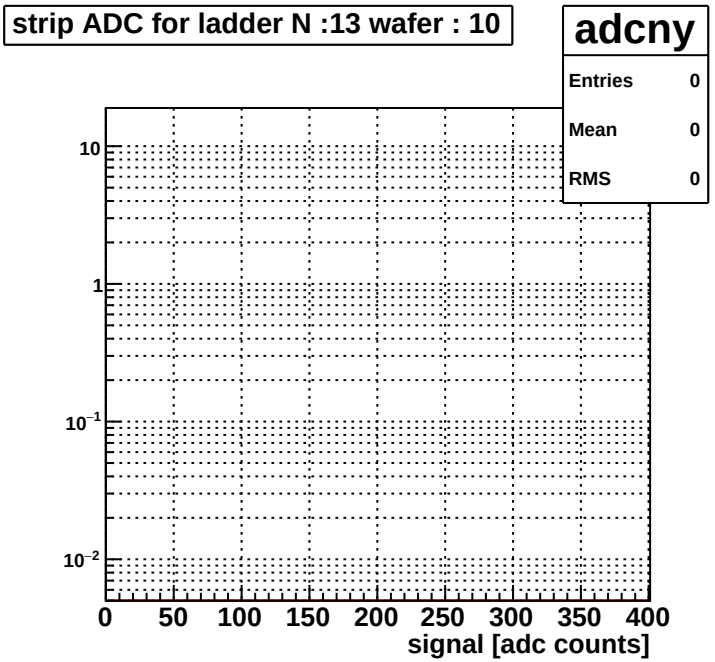

strip ADC for ladder P :13 wafer : 9
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

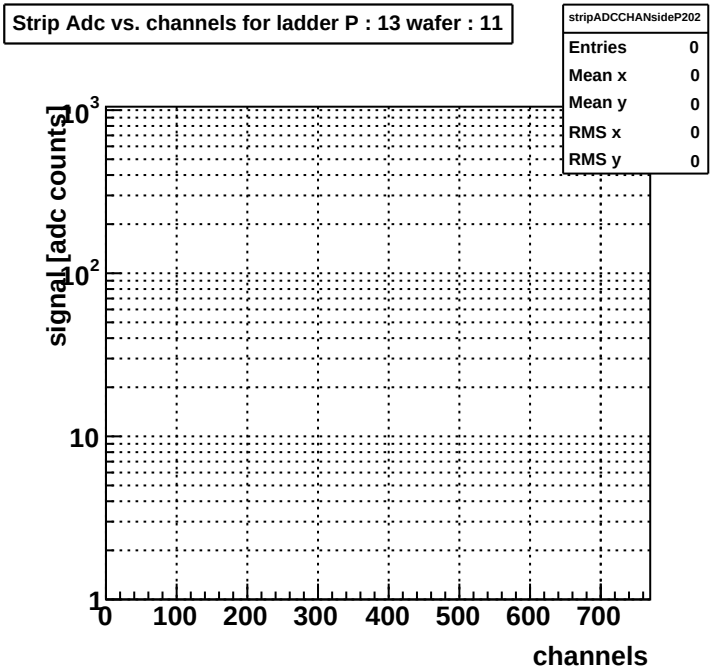

strip ADC for ladder N :13 wafer : 9
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

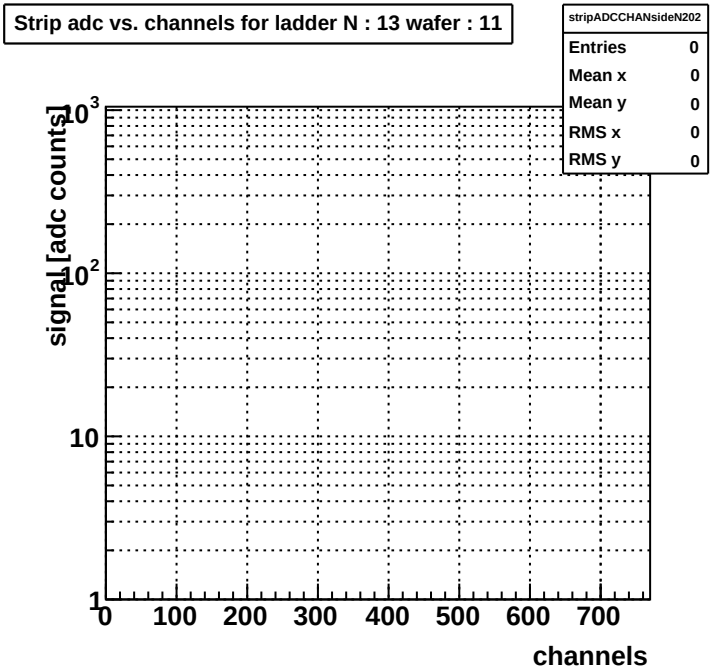

Strip Adc vs. channels for ladder P : 13 wafer : 10
stripADCCHANsideP201
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

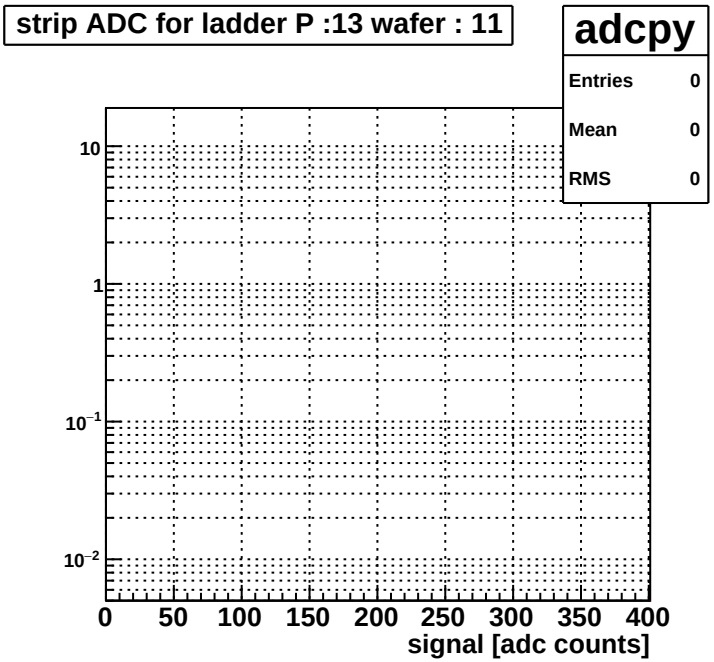

Strip adc vs. channels for ladder N : 13 wafer : 10
stripADCCHANsideN201
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

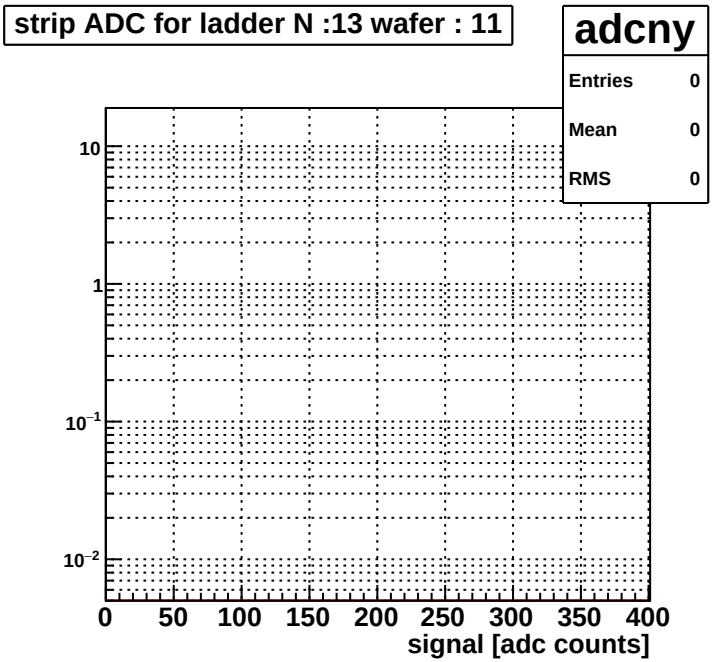

strip ADC for ladder P :13 wafer : 10
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

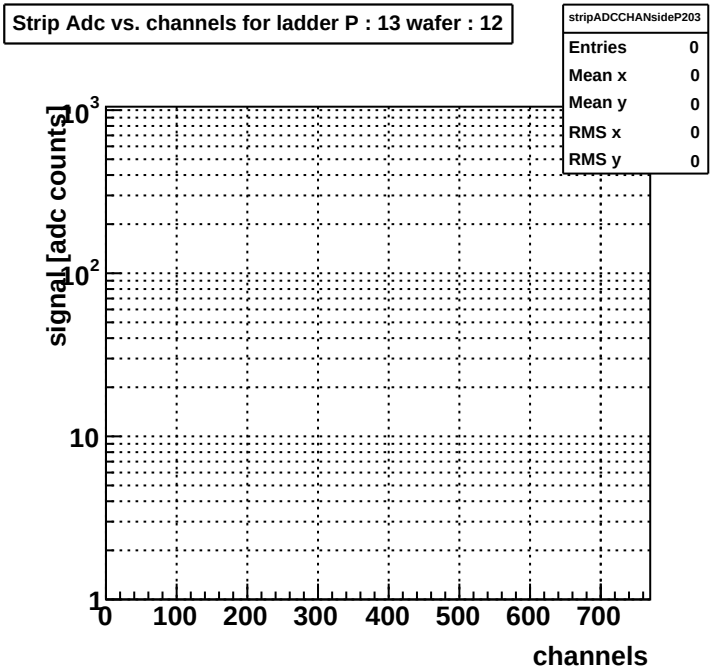

strip ADC for ladder N :13 wafer : 10
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

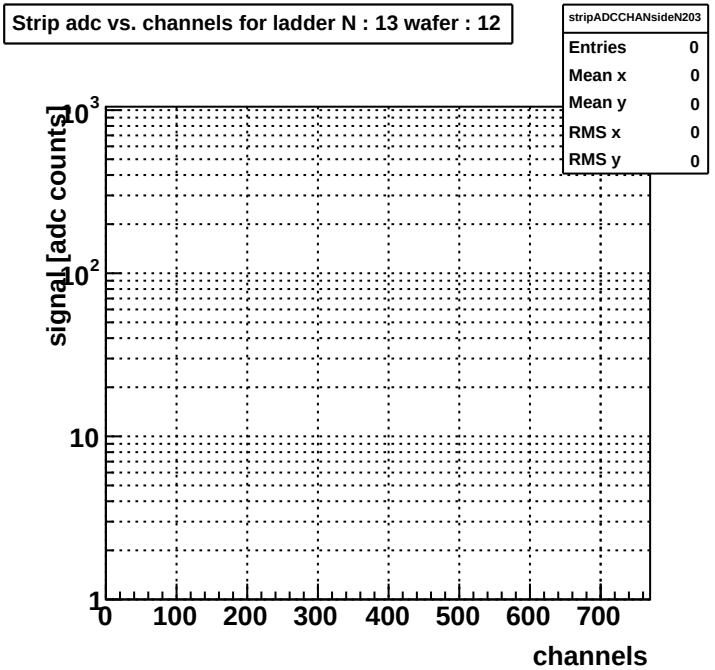

Strip Adc vs. channels for ladder P : 13 wafer : 11
stripADCCHANsideP202
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

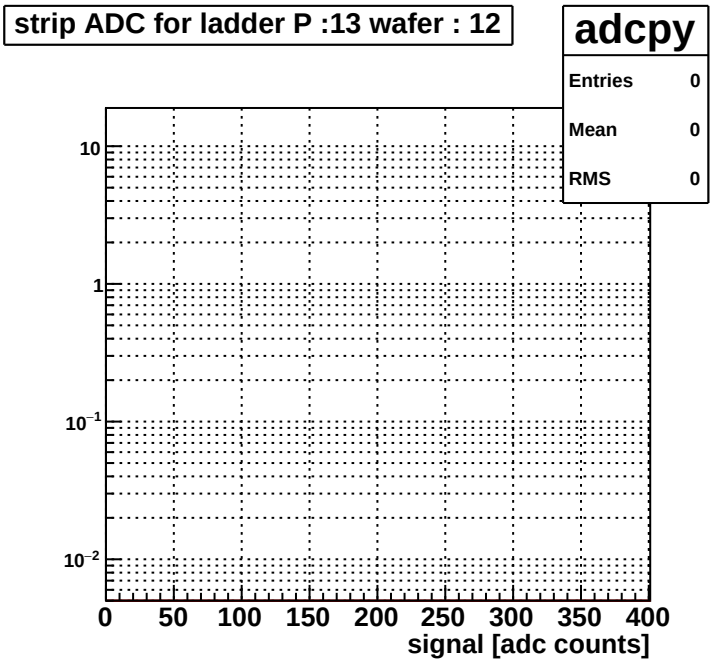

Strip adc vs. channels for ladder N : 13 wafer : 11
stripADCCHANsideN202
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

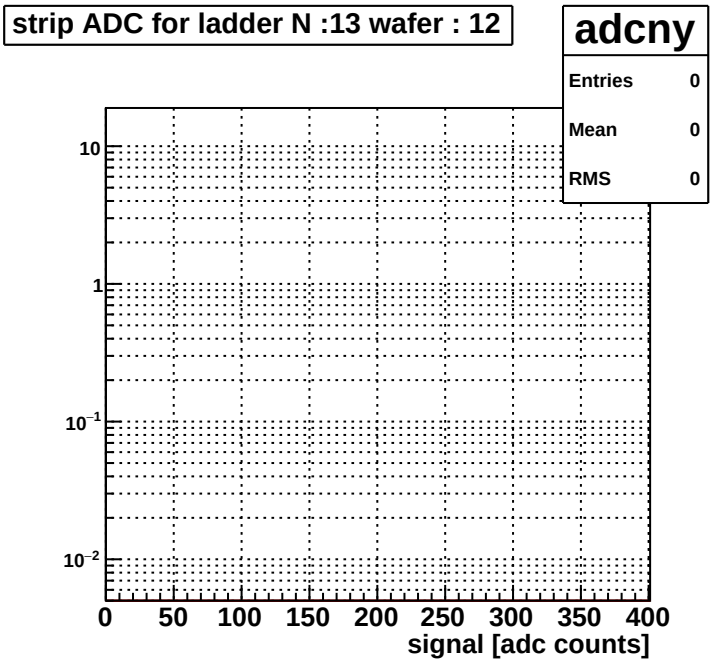

strip ADC for ladder P :13 wafer : 11
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

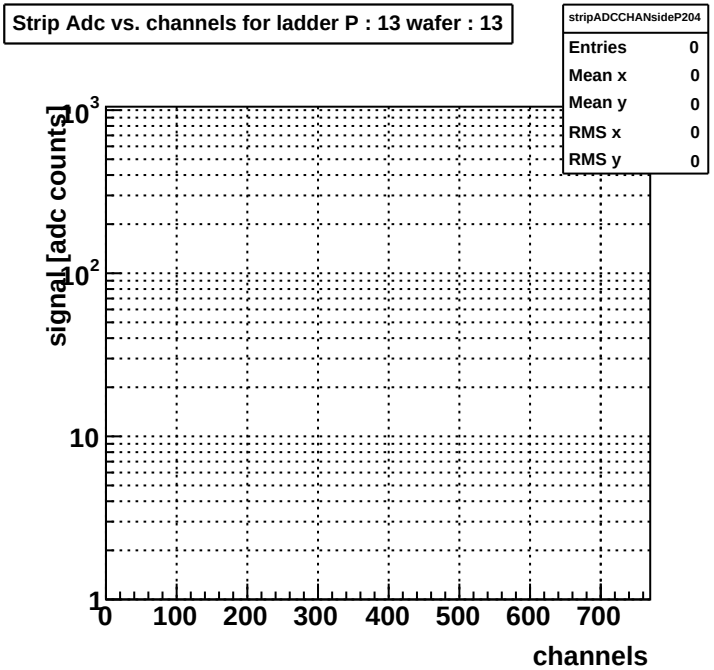

strip ADC for ladder N :13 wafer : 11
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

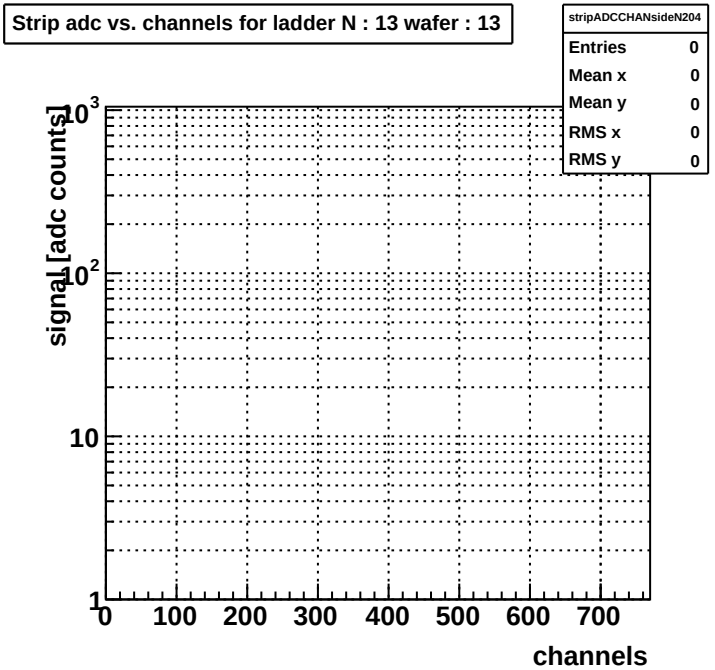

Strip Adc vs. channels for ladder P : 13 wafer : 12
stripADCCHANsideP203
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

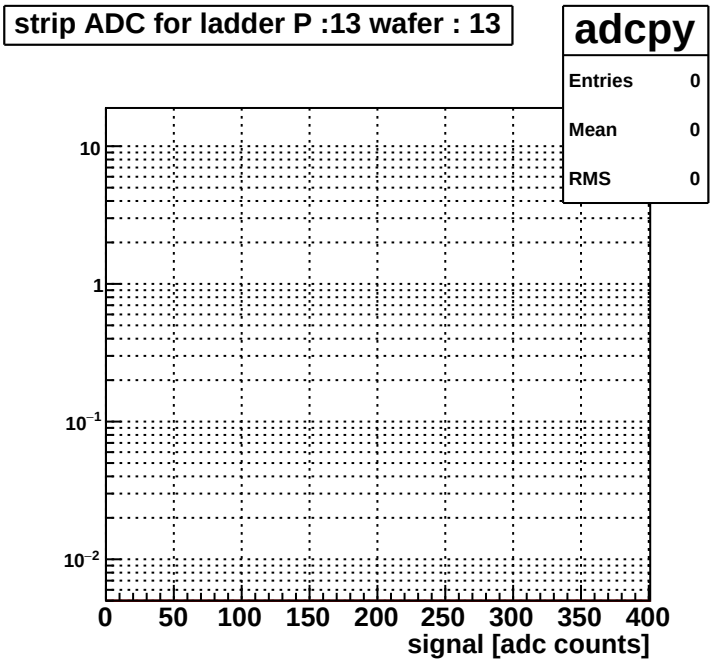

Strip adc vs. channels for ladder N : 13 wafer : 12
stripADCCHANsideN203
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

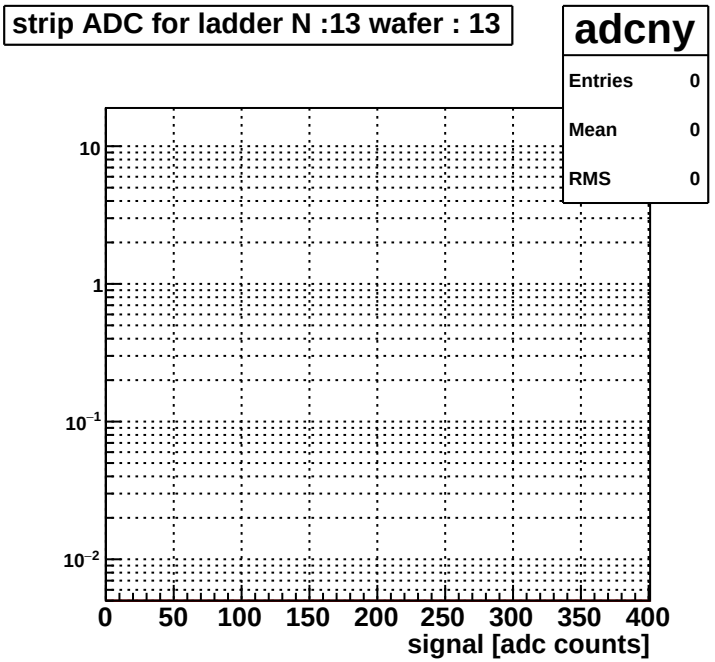

strip ADC for ladder P :13 wafer : 12
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

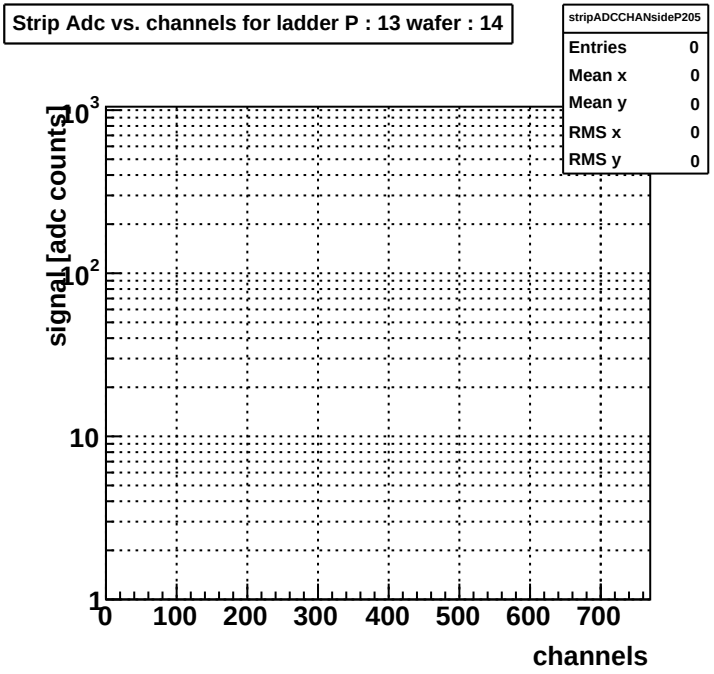

strip ADC for ladder N :13 wafer : 12
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

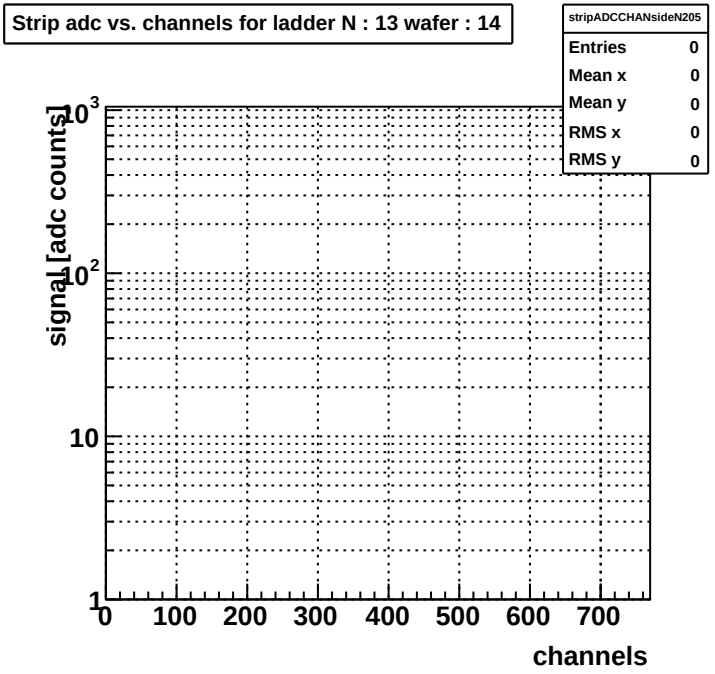

Strip Adc vs. channels for ladder P : 13 wafer : 13
stripADCCHANsideP204
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

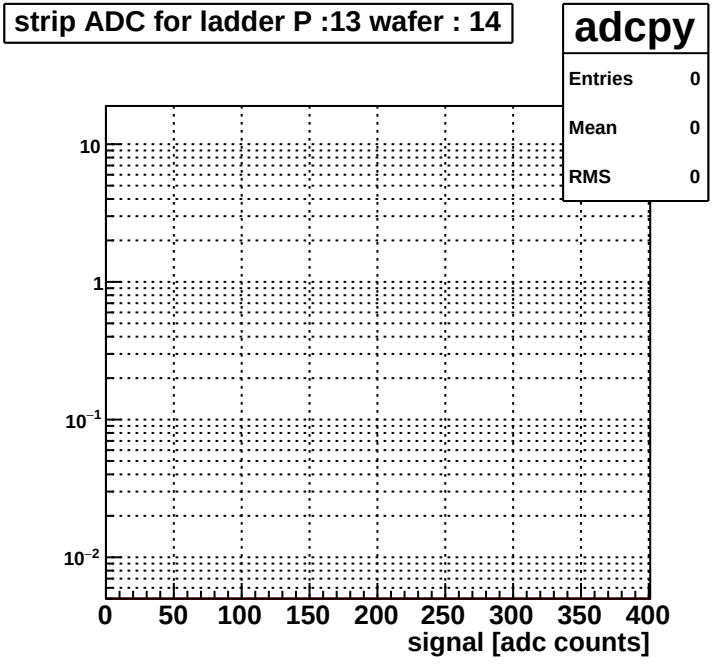

Strip adc vs. channels for ladder N : 13 wafer : 13
stripADCCHANsideN204
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

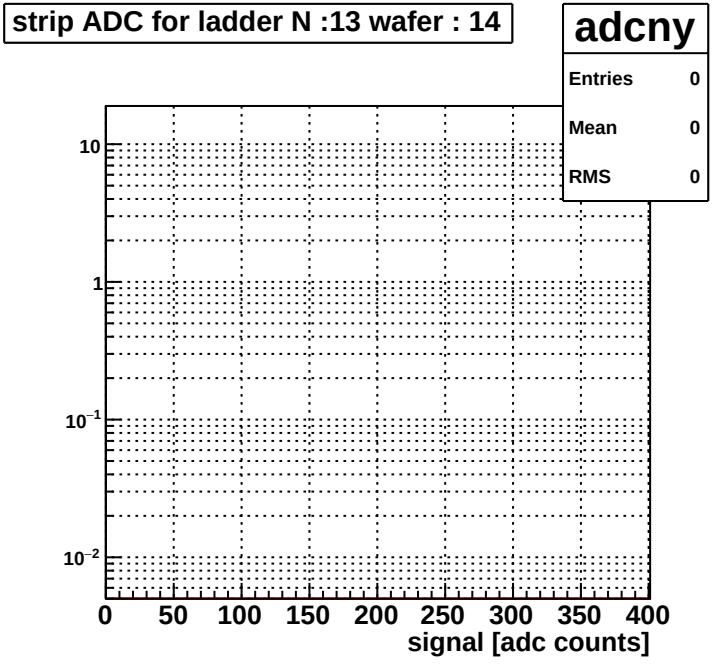

strip ADC for ladder P :13 wafer : 13
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

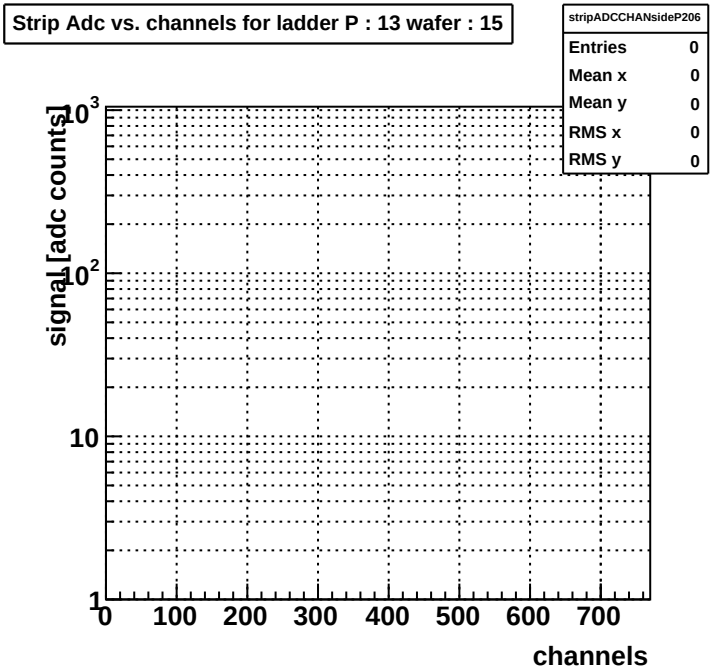

strip ADC for ladder N :13 wafer : 13
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

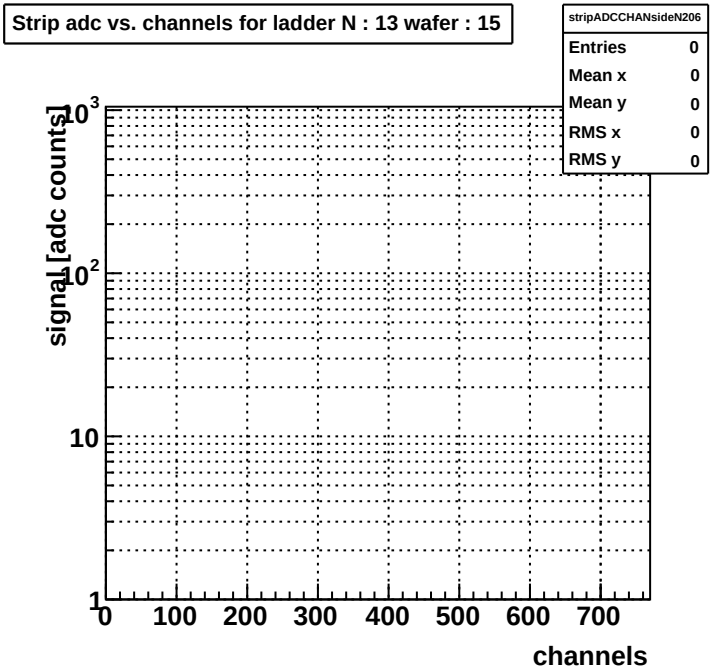

Strip Adc vs. channels for ladder P : 13 wafer : 14
stripADCCHANsideP205
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

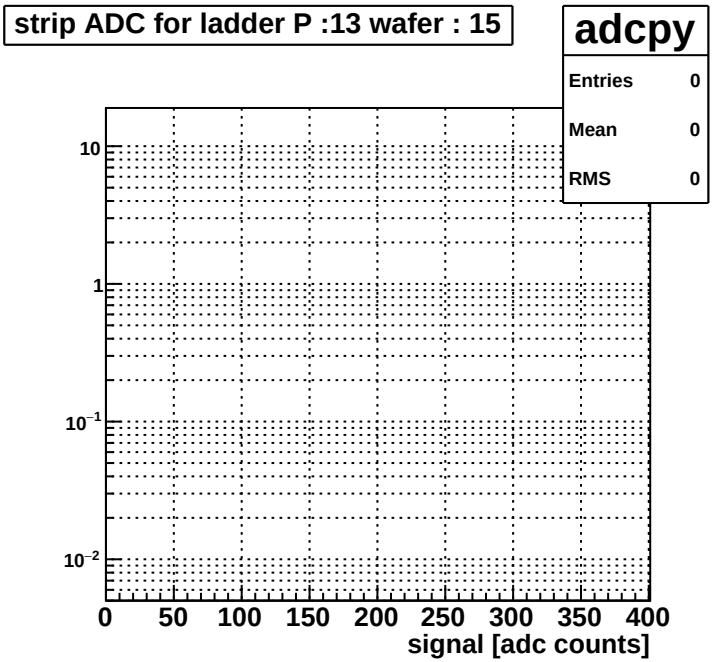

Strip adc vs. channels for ladder N : 13 wafer : 14
stripADCCHANsideN205
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

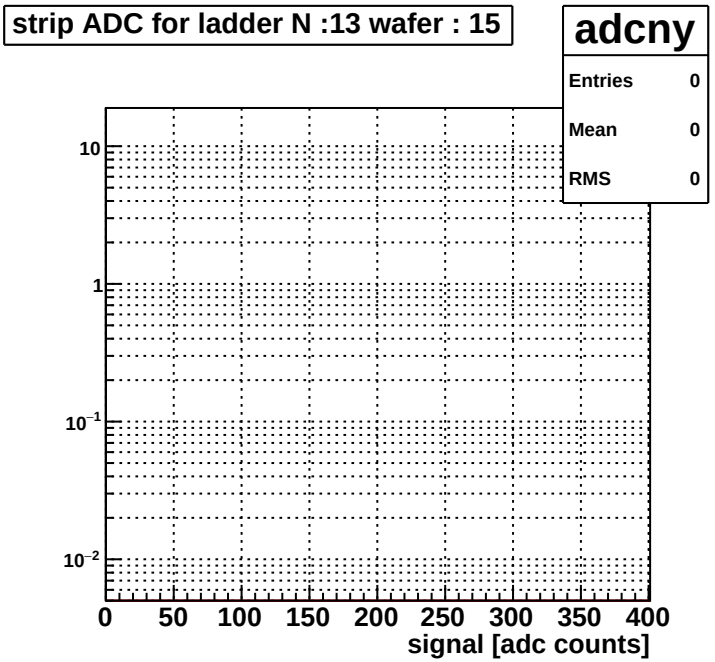

strip ADC for ladder P :13 wafer : 14
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

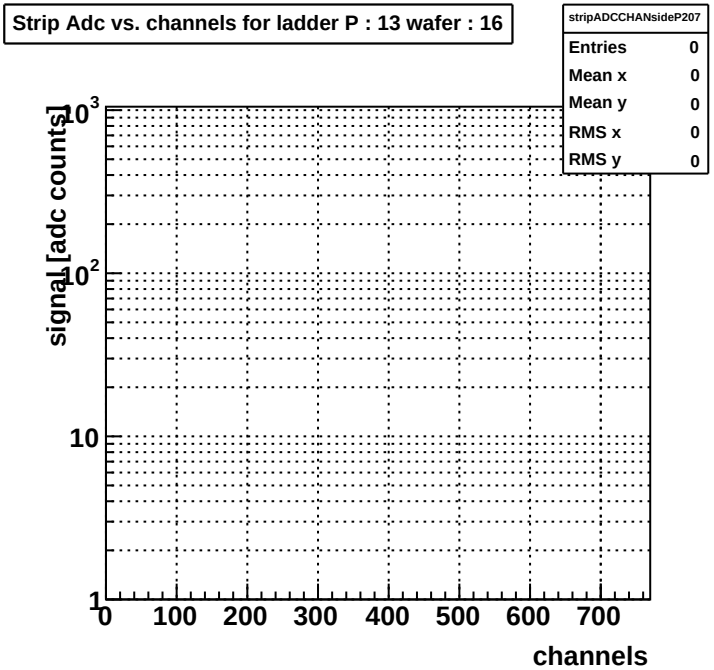

strip ADC for ladder N :13 wafer : 14
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

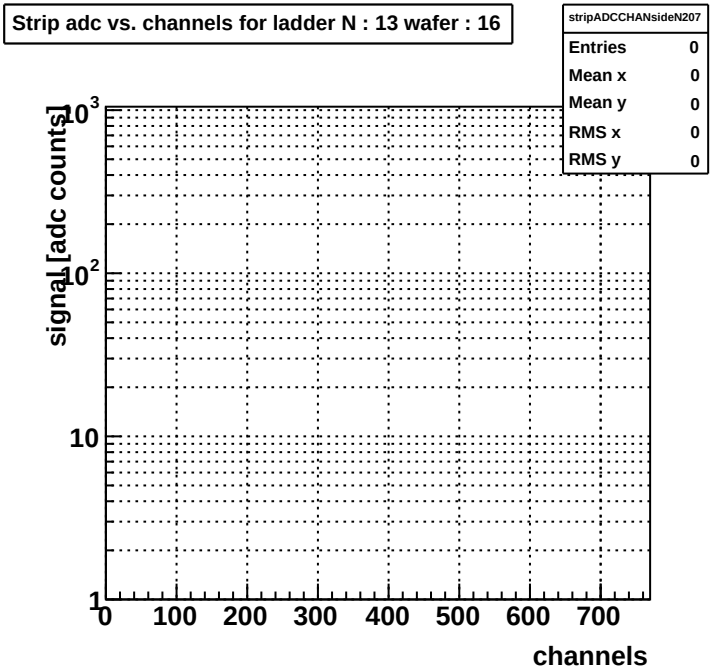

Strip Adc vs. channels for ladder P : 13 wafer : 15
stripADCCHANsideP206
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

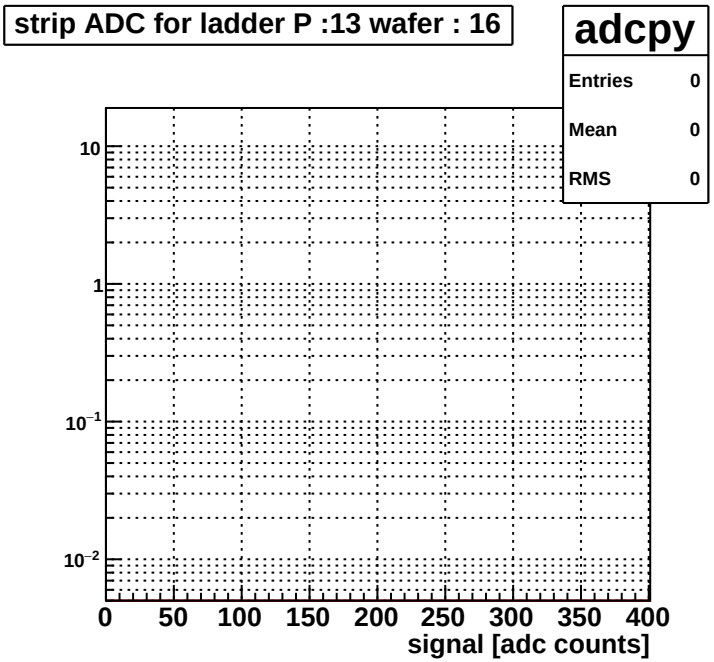

Strip adc vs. channels for ladder N : 13 wafer : 15
stripADCCHANsideN206
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

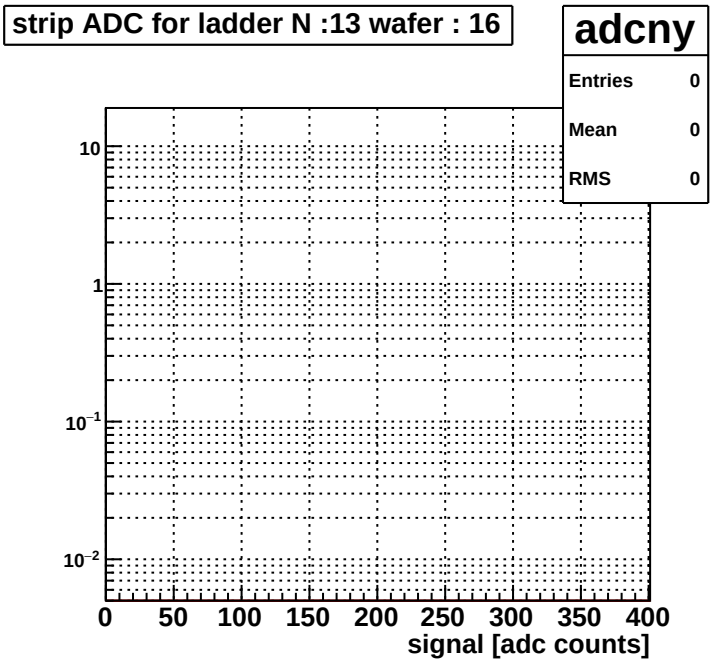

strip ADC for ladder P :13 wafer : 15
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

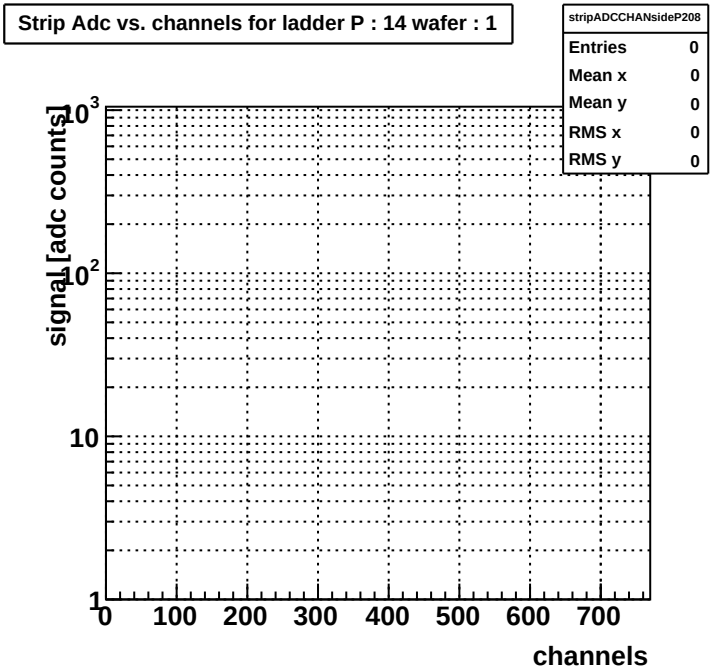

strip ADC for ladder N :13 wafer : 15
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

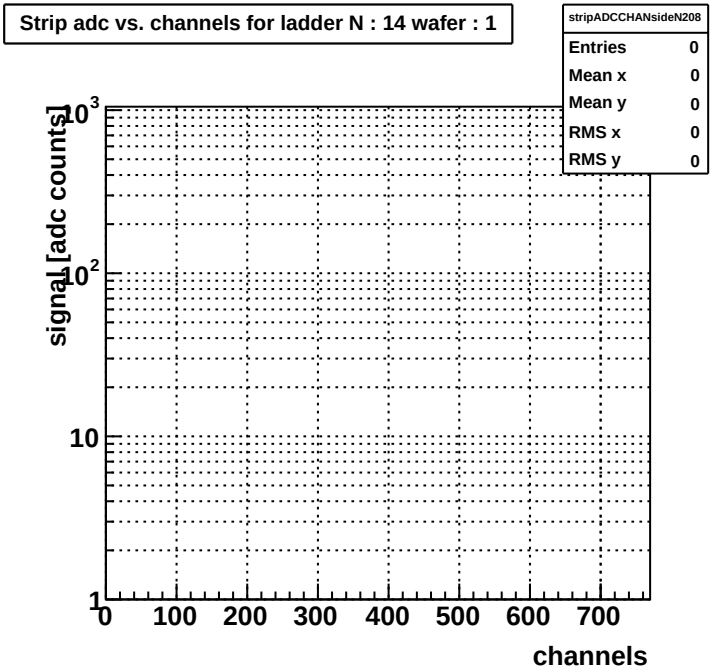

Strip Adc vs. channels for ladder P : 13 wafer : 16
stripADCCHANsideP207
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

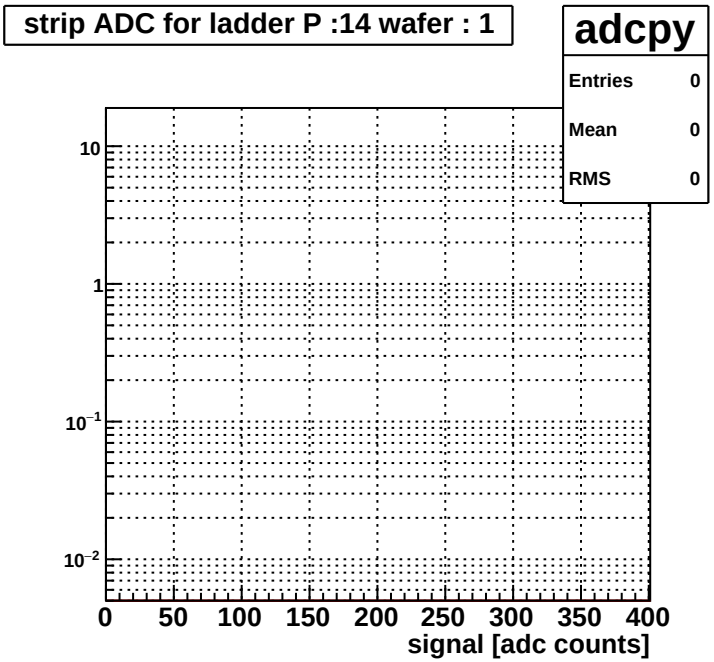

Strip adc vs. channels for ladder N : 13 wafer : 16
stripADCCHANsideN207
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

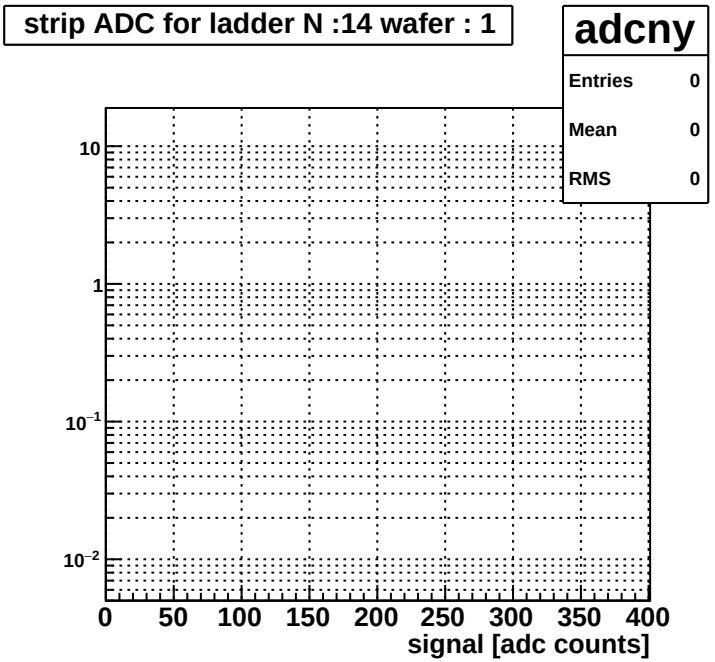

strip ADC for ladder P :13 wafer : 16
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

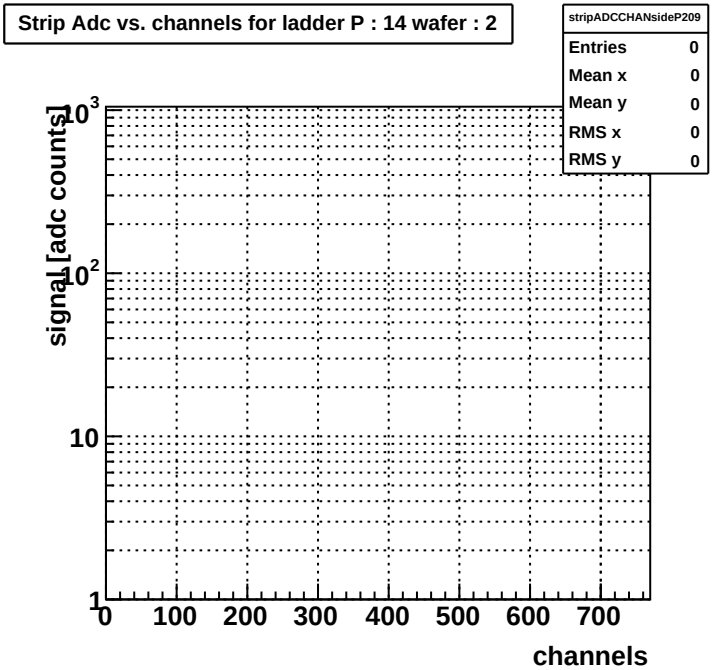

strip ADC for ladder N :13 wafer : 16
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

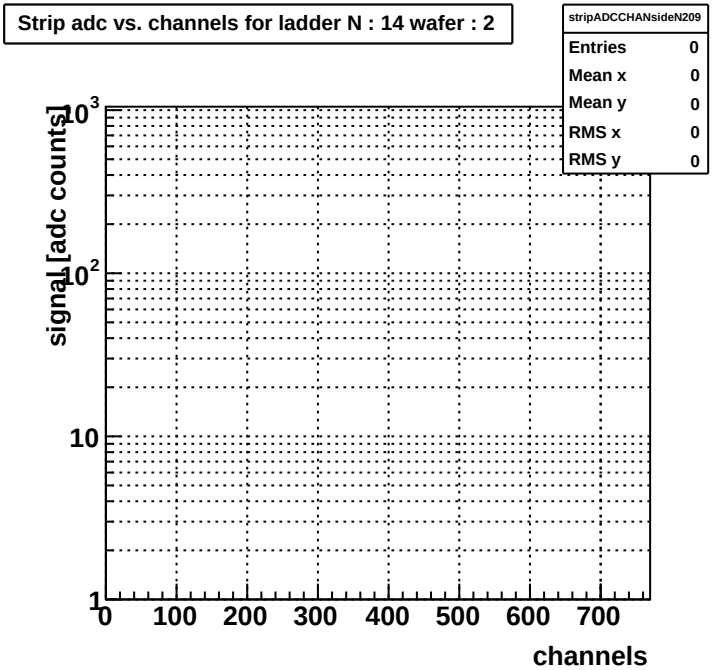

Strip Adc vs. channels for ladder P : 14 wafer : 1
stripADCCHANsideP208
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

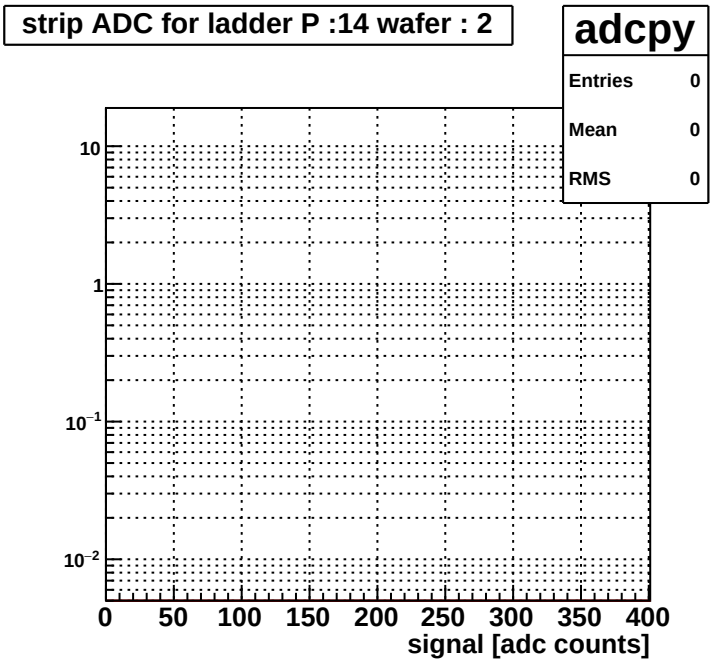

Strip adc vs. channels for ladder N : 14 wafer : 1
stripADCCHANsideN208
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

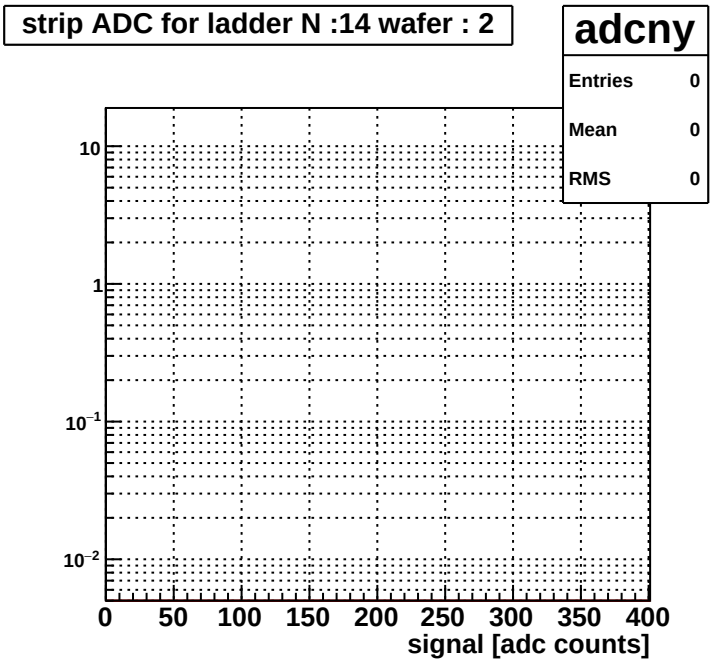

strip ADC for ladder P :14 wafer : 1
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

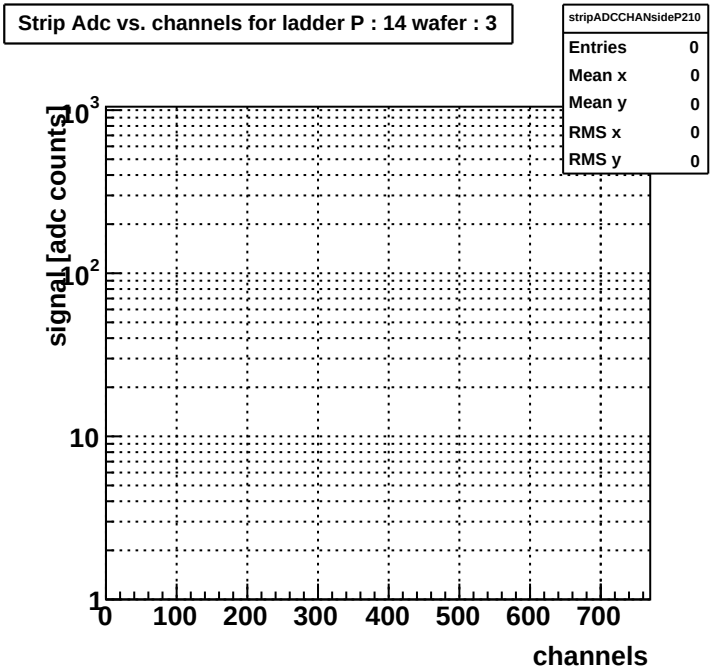

strip ADC for ladder N :14 wafer : 1
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

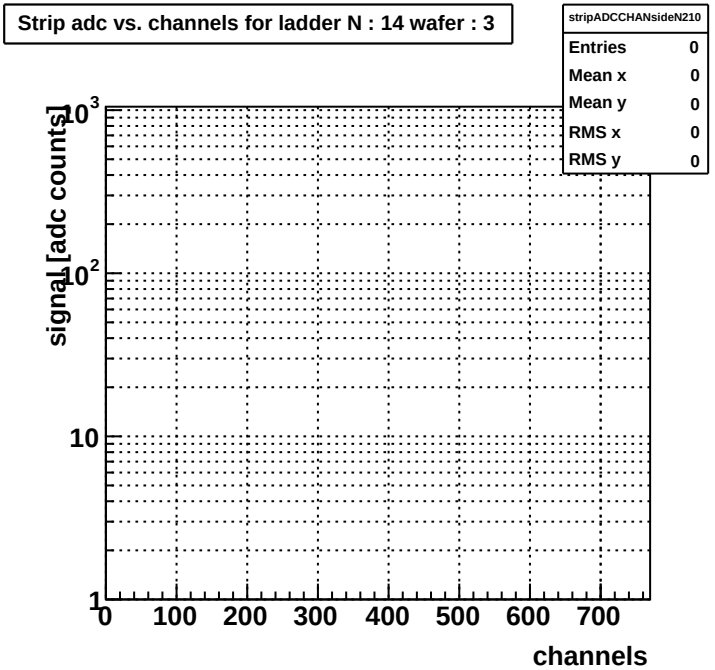

Strip Adc vs. channels for ladder P : 14 wafer : 2
stripADCCHANsideP209
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

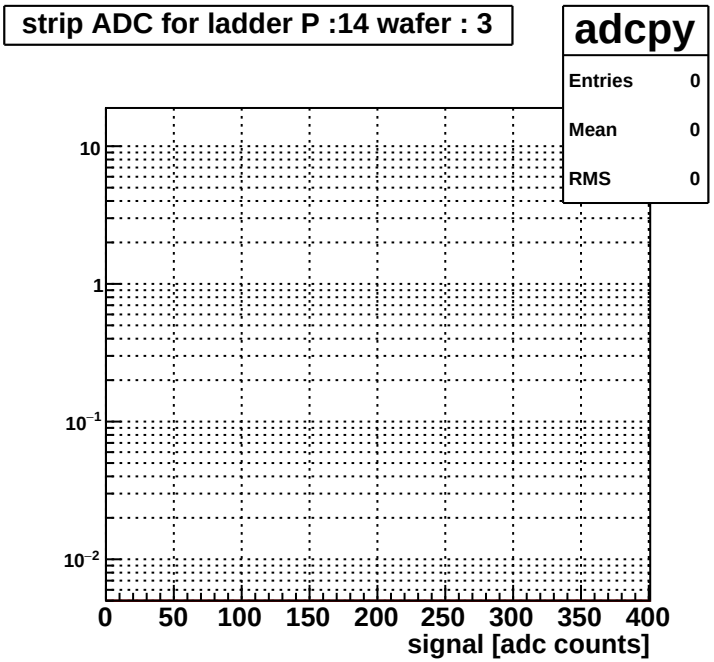

Strip adc vs. channels for ladder N : 14 wafer : 2
stripADCCHANsideN209
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

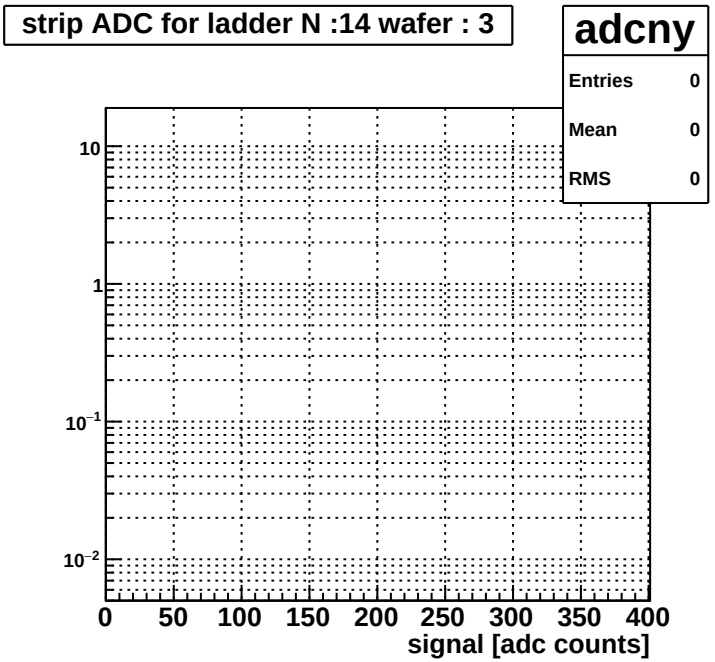

strip ADC for ladder P :14 wafer : 2
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

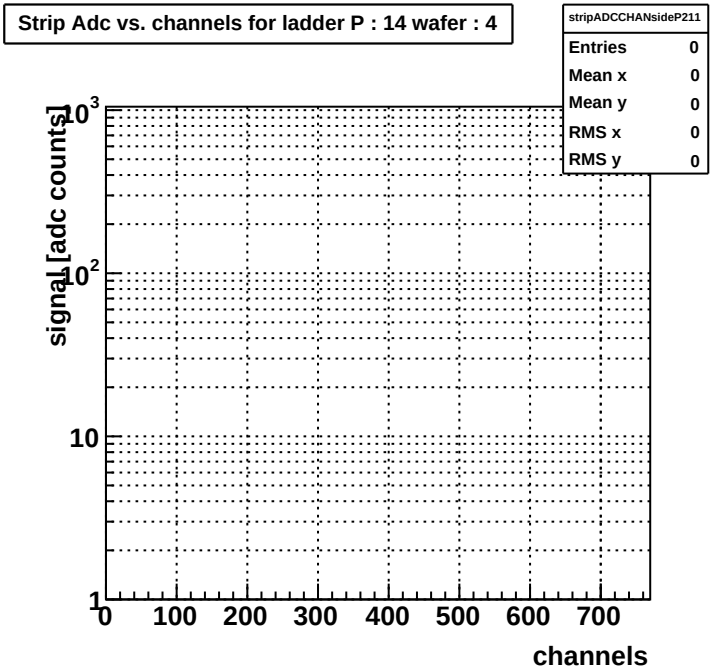

strip ADC for ladder N :14 wafer : 2
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

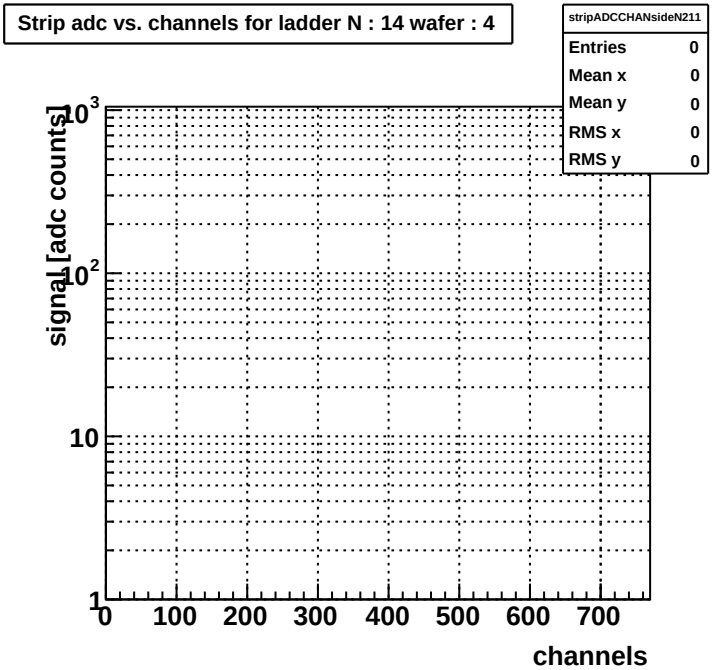

Strip Adc vs. channels for ladder P : 14 wafer : 3
stripADCCHANsideP210
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

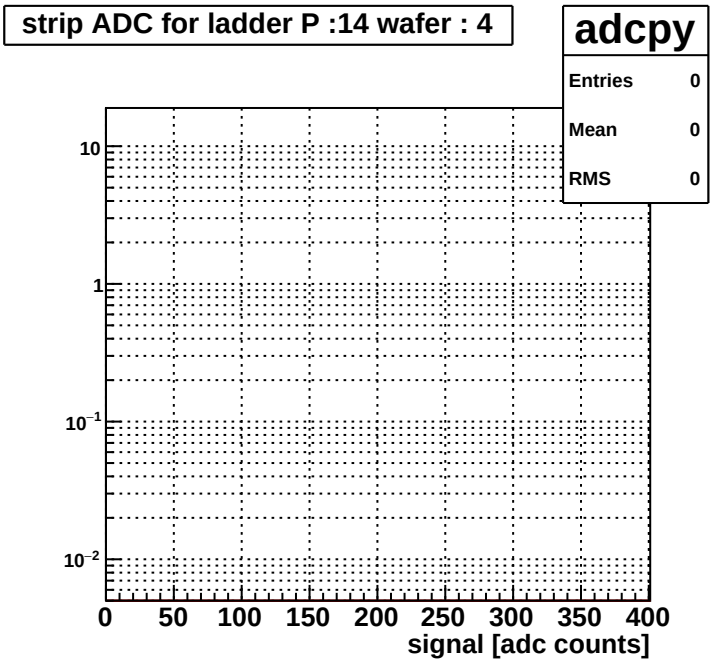

Strip adc vs. channels for ladder N : 14 wafer : 3
stripADCCHANsideN210
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

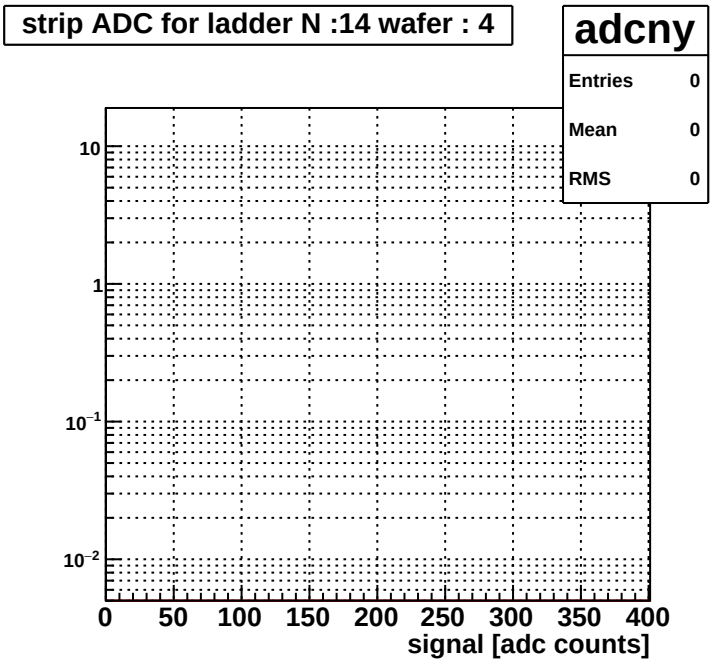

strip ADC for ladder P :14 wafer : 3
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

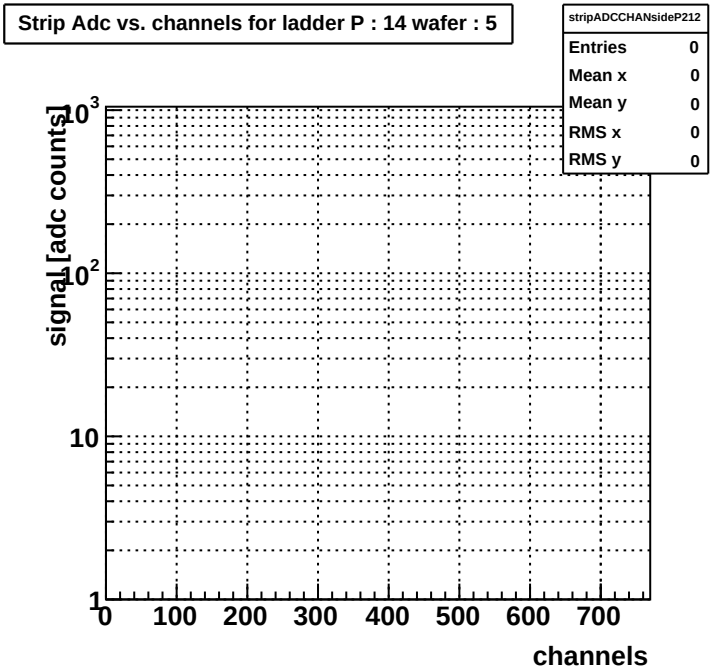

strip ADC for ladder N :14 wafer : 3
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

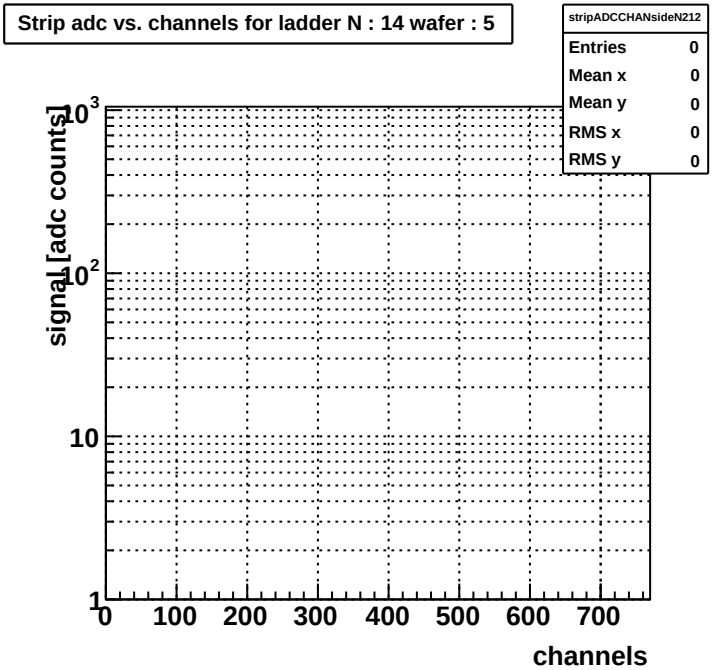

Strip Adc vs. channels for ladder P : 14 wafer : 4
stripADCCHANsideP211
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

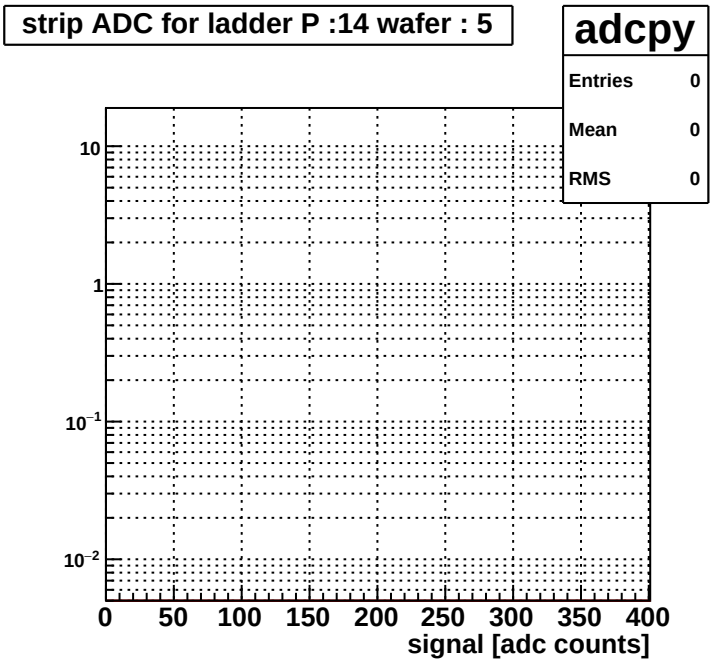

Strip adc vs. channels for ladder N : 14 wafer : 4
stripADCCHANsideN211
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

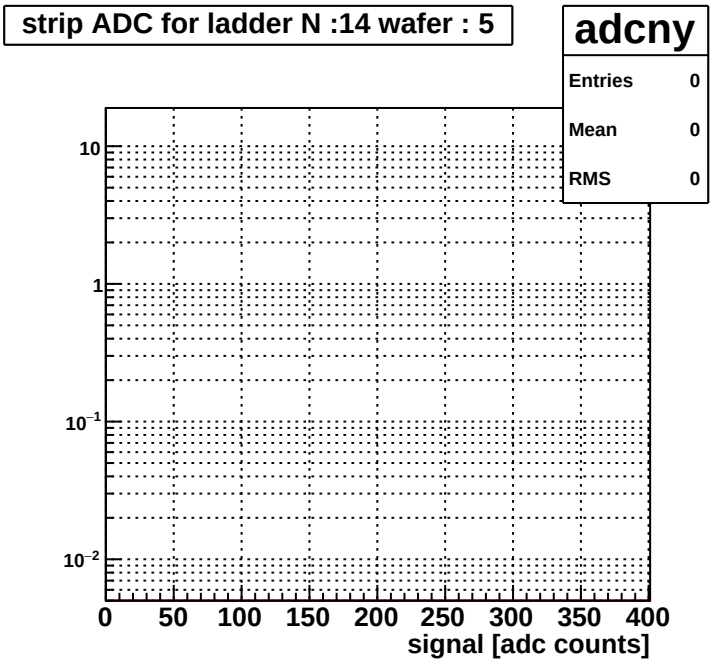

strip ADC for ladder P :14 wafer : 4
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

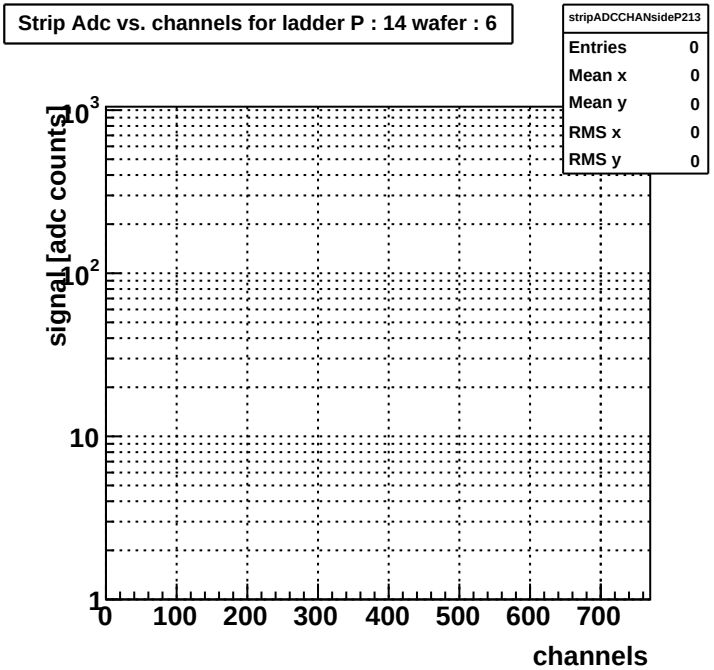

strip ADC for ladder N :14 wafer : 4
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

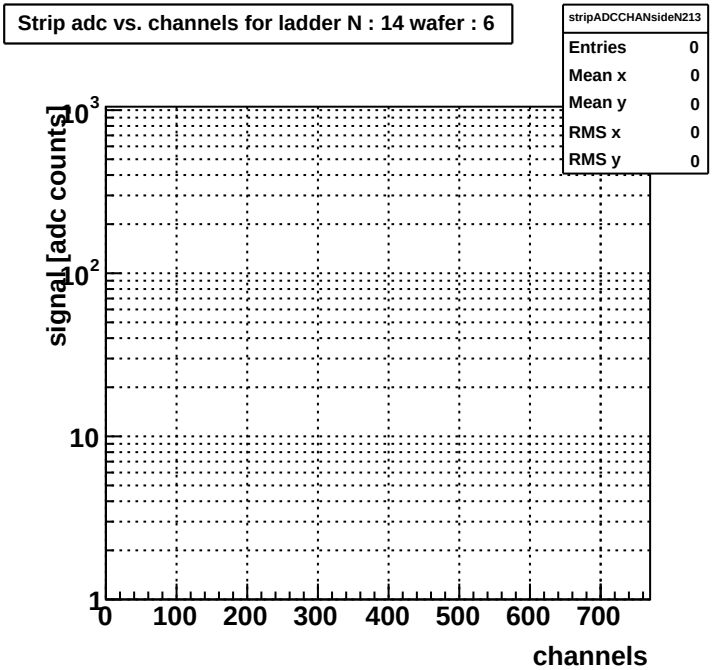

Strip Adc vs. channels for ladder P : 14 wafer : 5
stripADCCHANsideP212
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

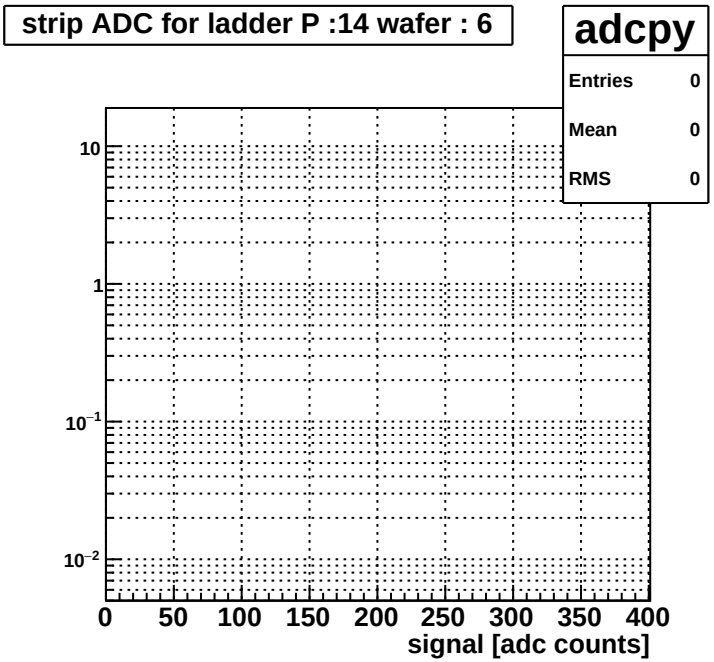

Strip adc vs. channels for ladder N : 14 wafer : 5
stripADCCHANsideN212
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

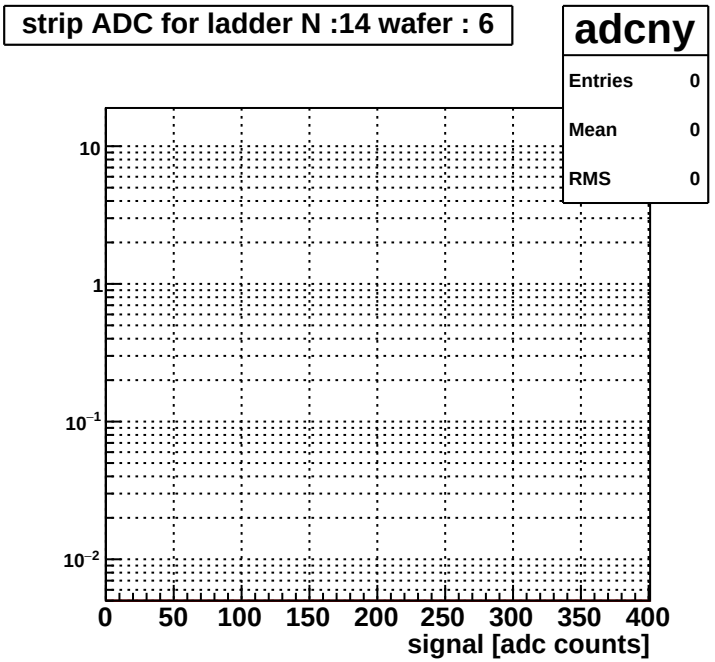

strip ADC for ladder P :14 wafer : 5
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

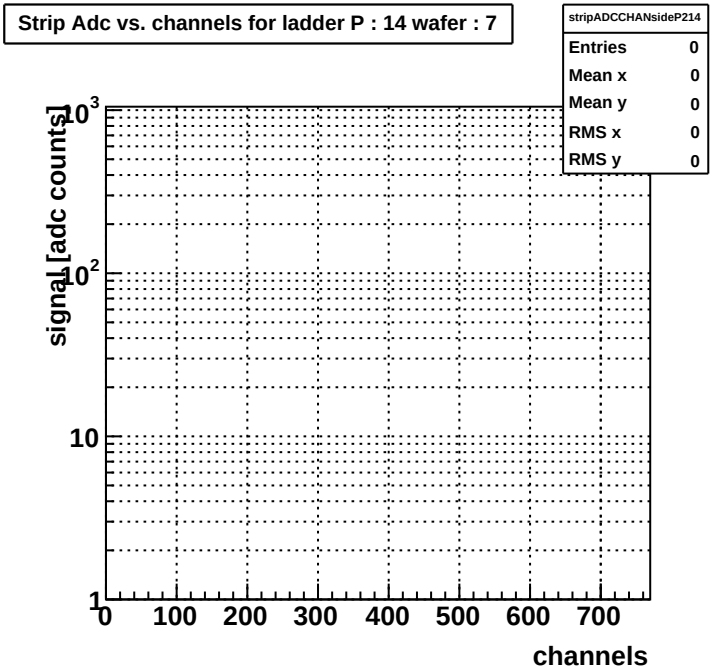

strip ADC for ladder N :14 wafer : 5
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

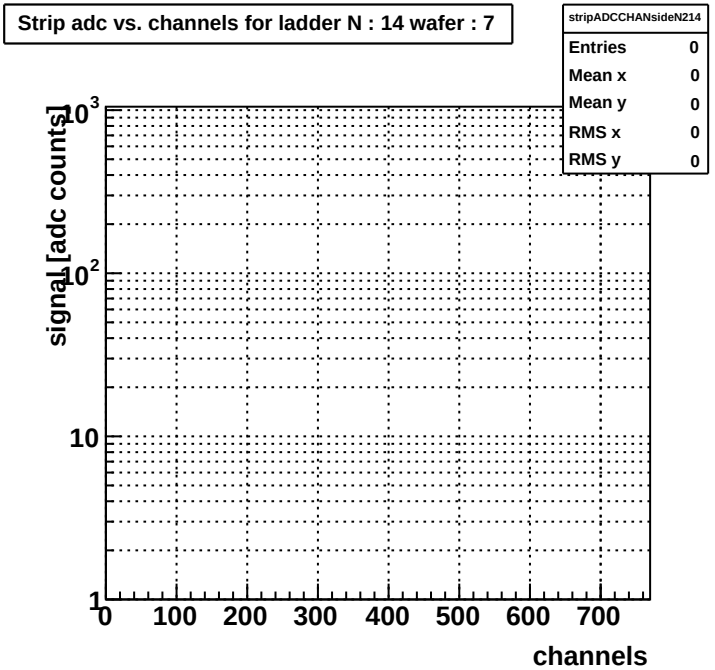

Strip Adc vs. channels for ladder P : 14 wafer : 6
stripADCCHANsideP213
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

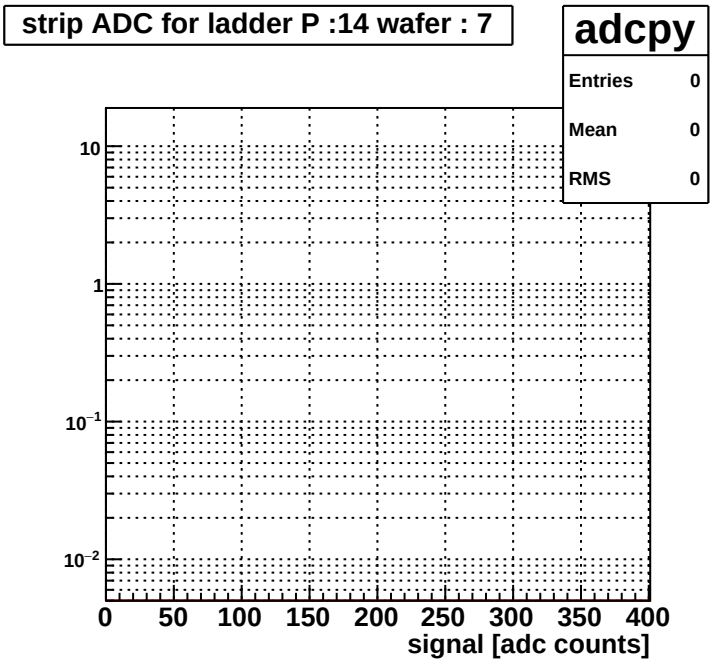

Strip adc vs. channels for ladder N : 14 wafer : 6
stripADCCHANsideN213
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

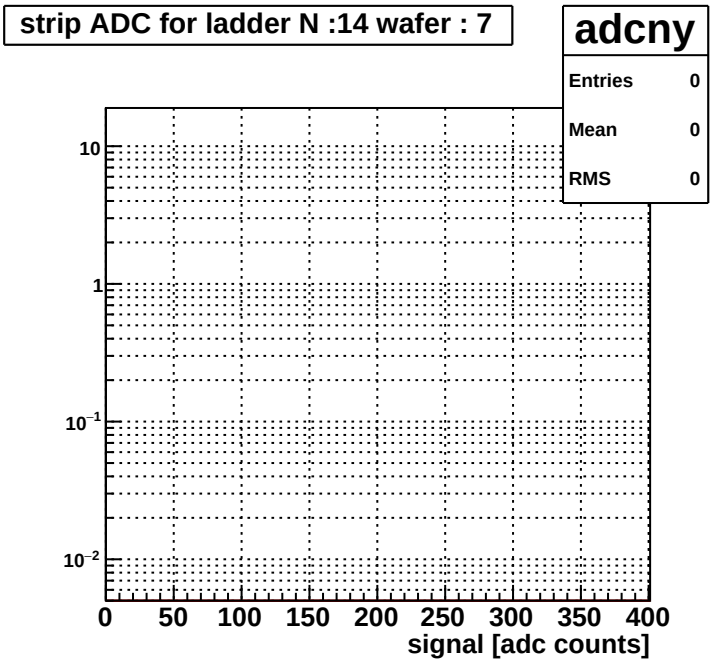

strip ADC for ladder P :14 wafer : 6
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

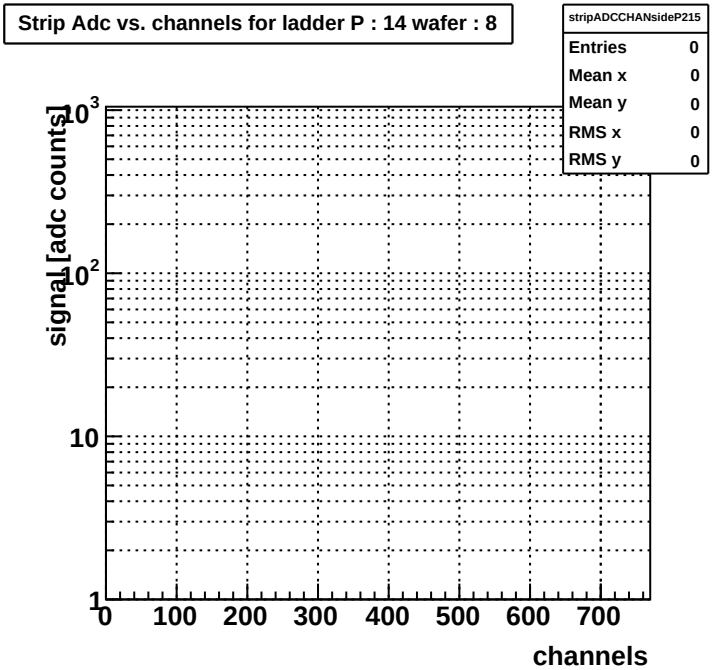

strip ADC for ladder N :14 wafer : 6
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

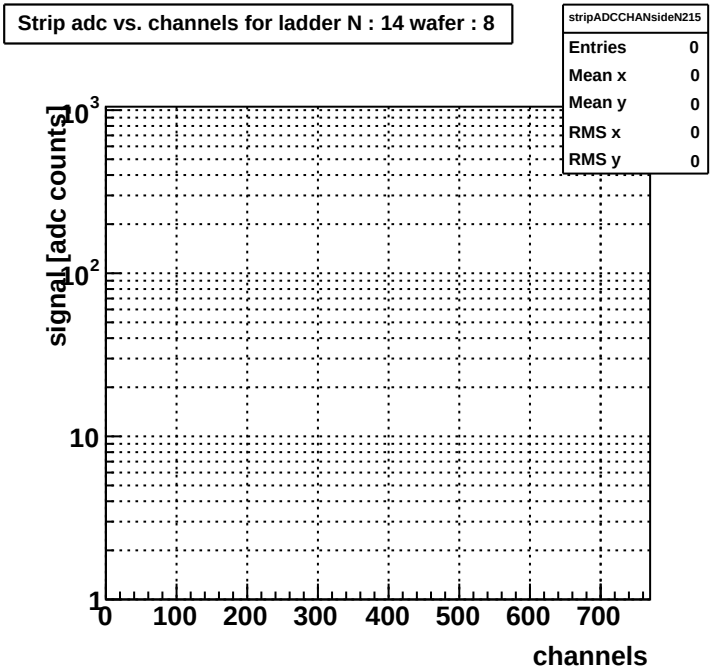

Strip Adc vs. channels for ladder P : 14 wafer : 7
stripADCCHANsideP214
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

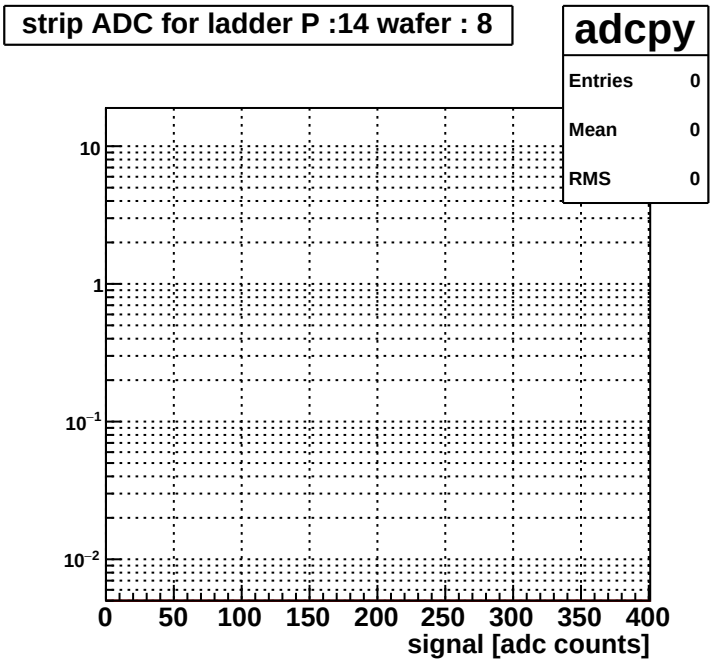

Strip adc vs. channels for ladder N : 14 wafer : 7
stripADCCHANsideN214
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

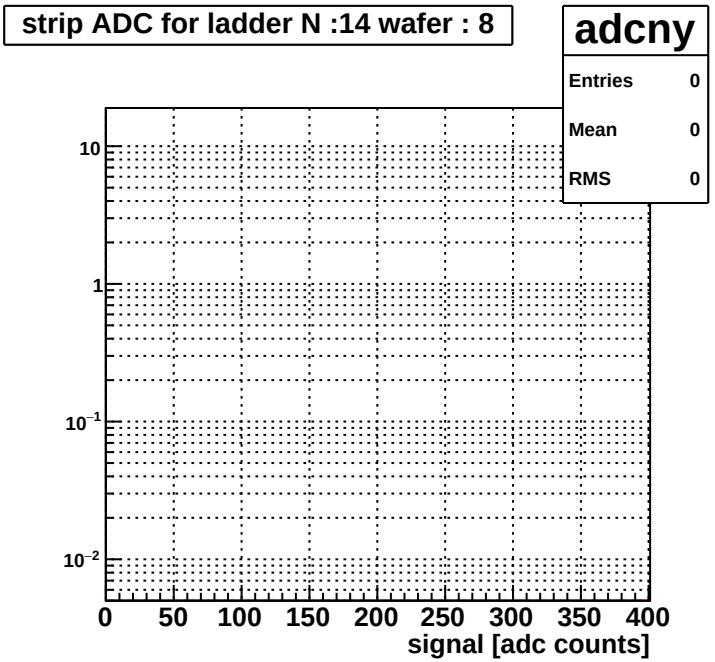

strip ADC for ladder P :14 wafer : 7
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

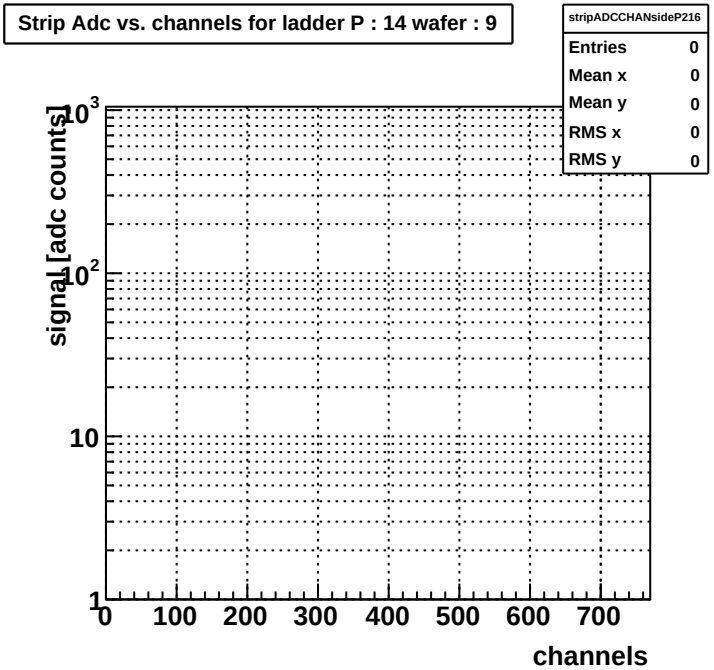

strip ADC for ladder N :14 wafer : 7
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

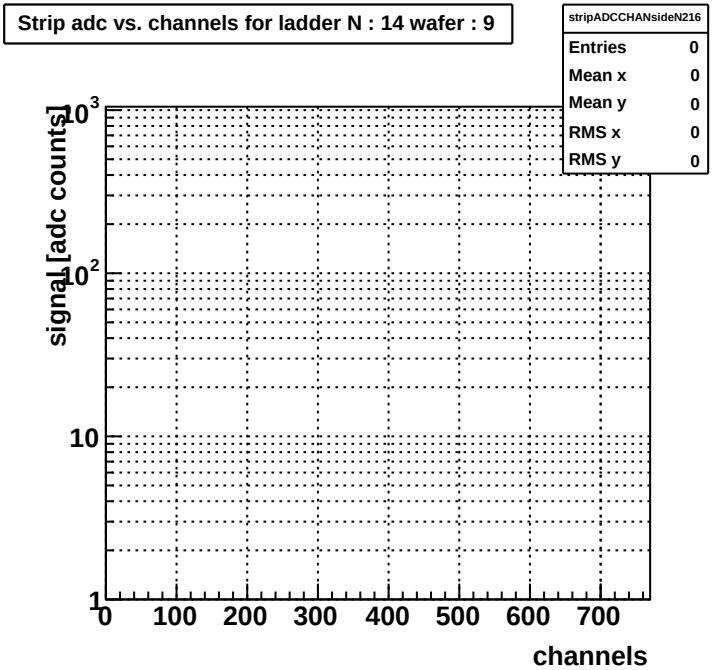

Strip Adc vs. channels for ladder P : 14 wafer : 8
stripADCCHANsideP215
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

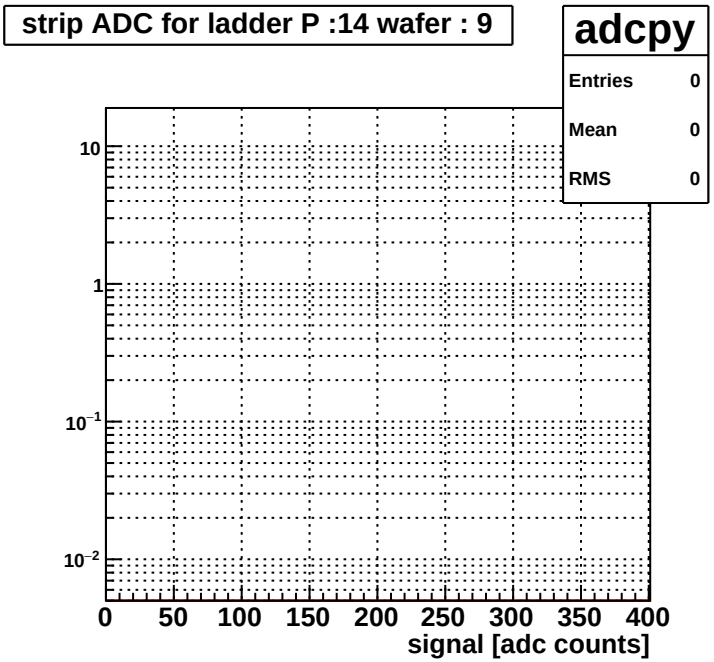

Strip adc vs. channels for ladder N : 14 wafer : 8
stripADCCHANsideN215
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

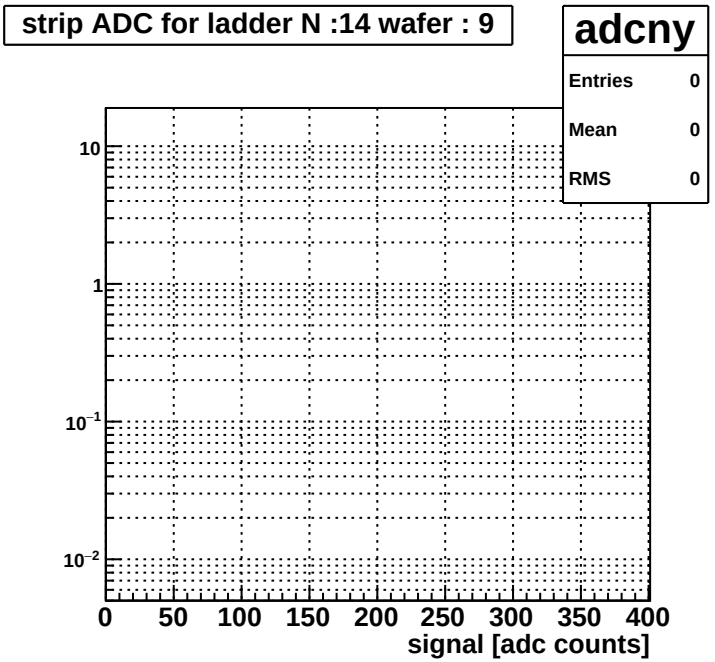

strip ADC for ladder P :14 wafer : 8
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

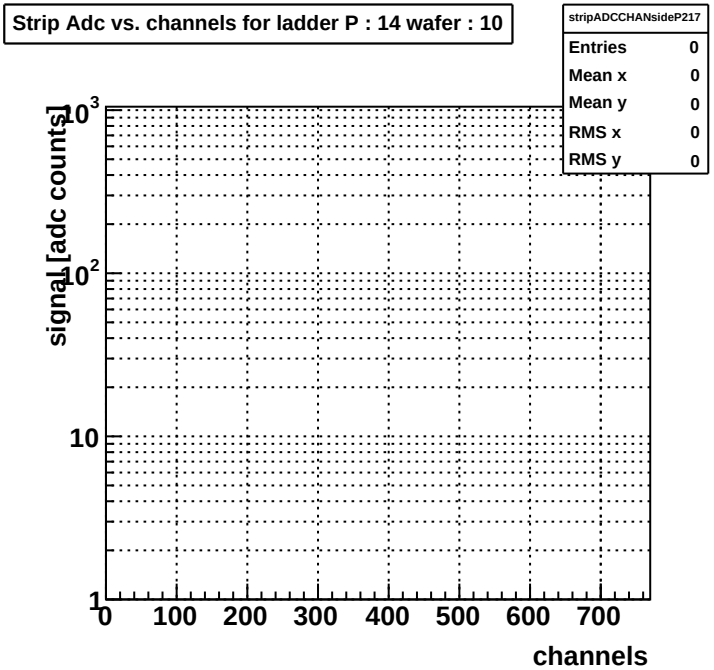

strip ADC for ladder N :14 wafer : 8
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

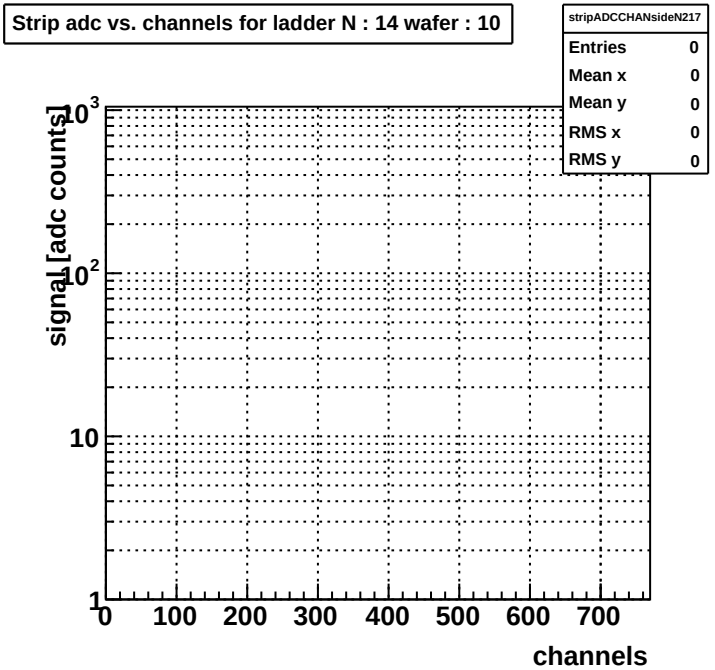

Strip Adc vs. channels for ladder P : 14 wafer : 9
stripADCCHANsideP216
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

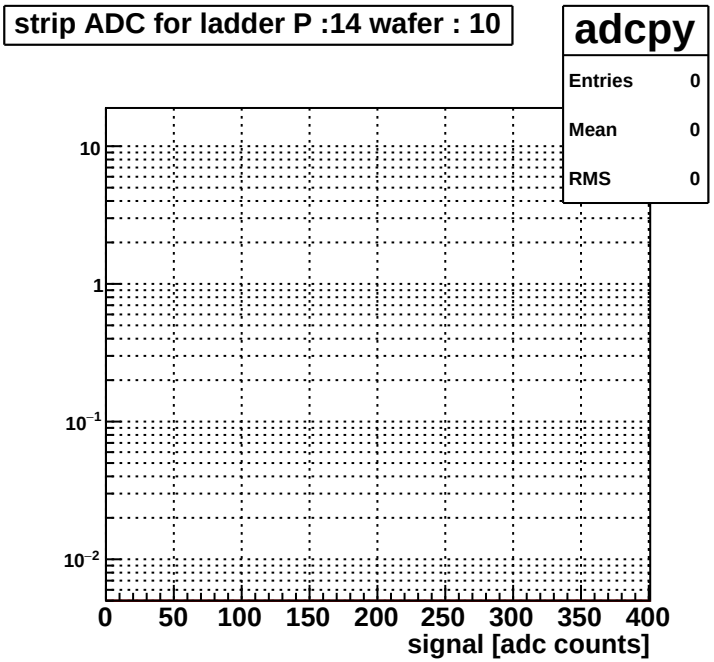

Strip adc vs. channels for ladder N : 14 wafer : 9
stripADCCHANsideN216
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

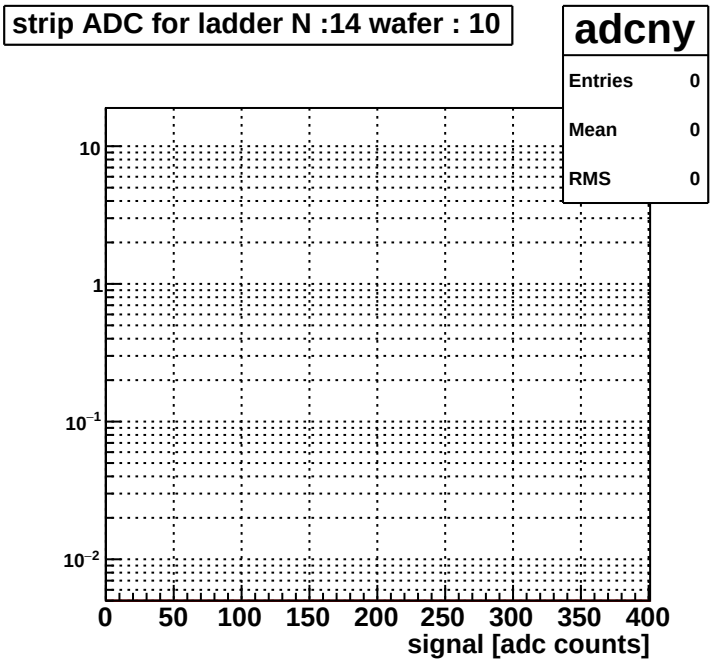

strip ADC for ladder P :14 wafer : 9
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

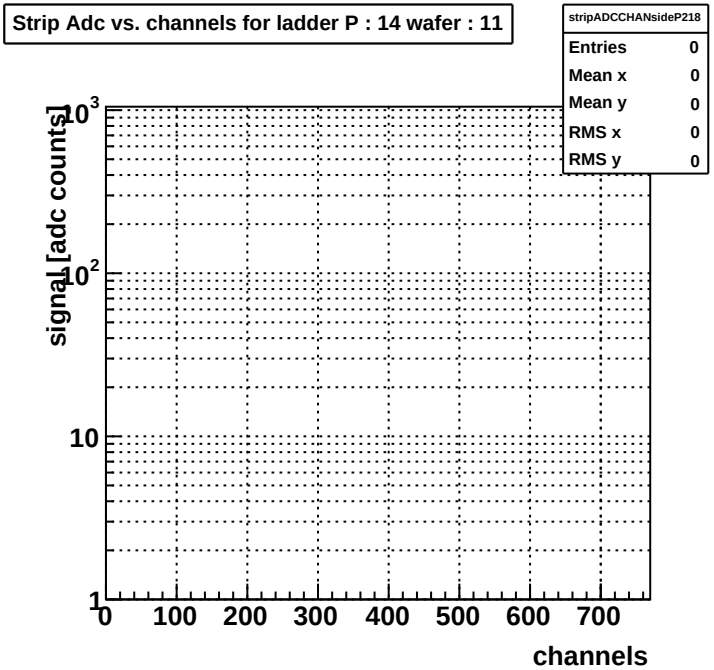

strip ADC for ladder N :14 wafer : 9
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

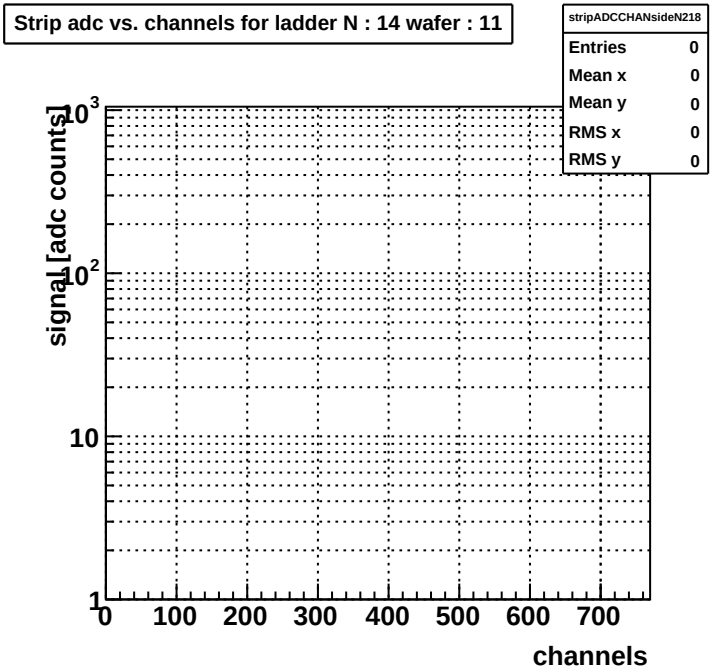

Strip Adc vs. channels for ladder P : 14 wafer : 10
stripADCCHANsideP217
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

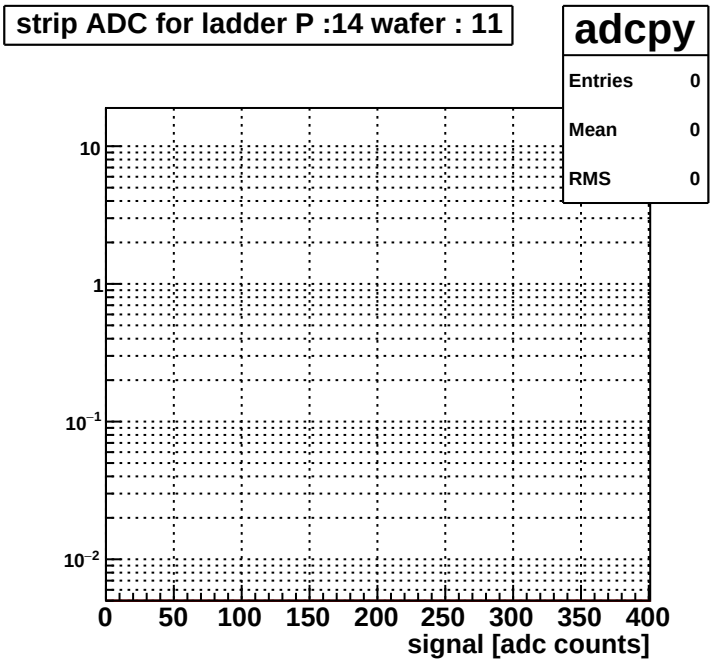

Strip adc vs. channels for ladder N : 14 wafer : 10
stripADCCHANsideN217
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

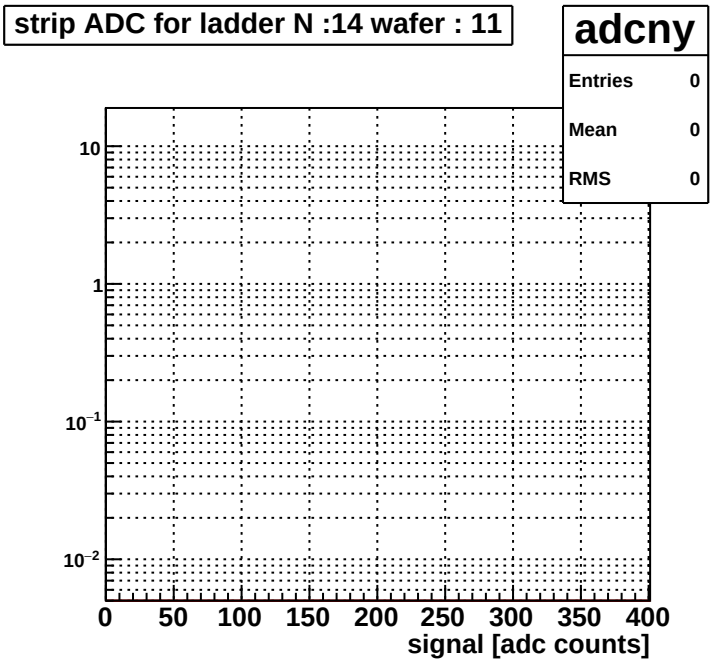

strip ADC for ladder P :14 wafer : 10
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

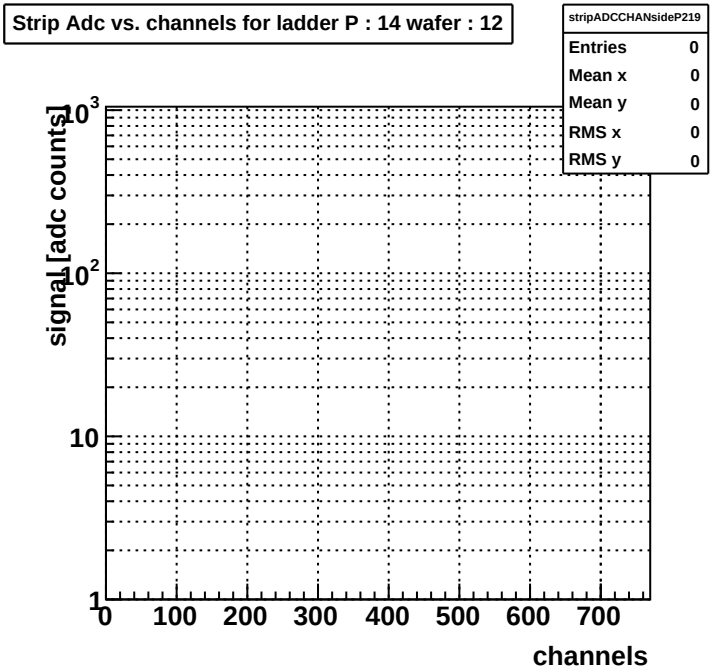

strip ADC for ladder N :14 wafer : 10
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

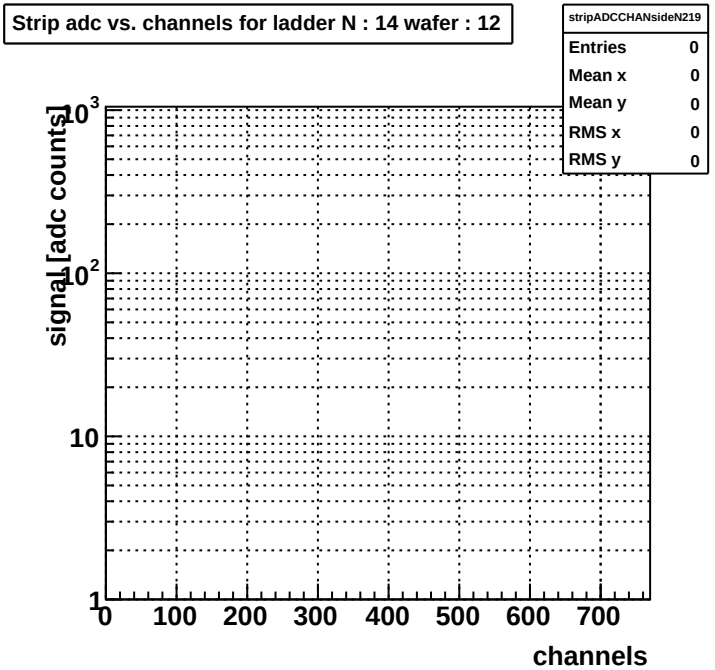

Strip Adc vs. channels for ladder P : 14 wafer : 11
stripADCCHANsideP218
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

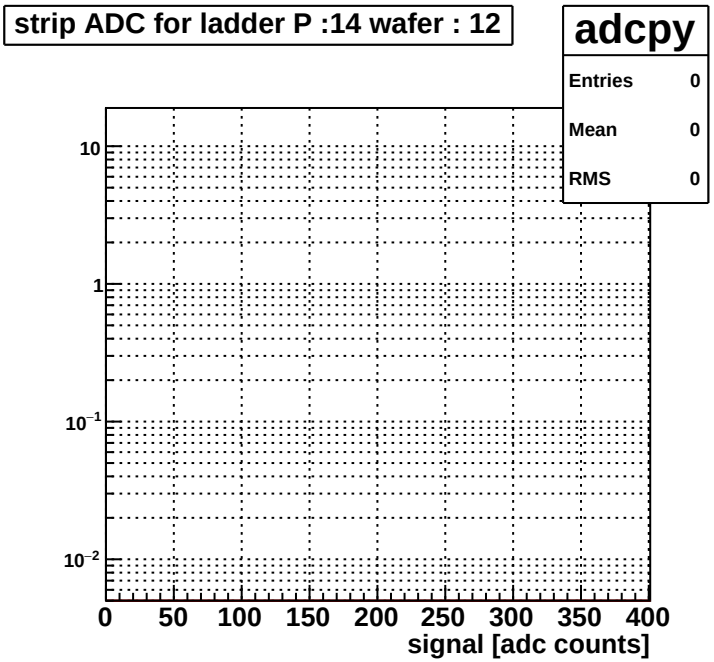

Strip adc vs. channels for ladder N : 14 wafer : 11
stripADCCHANsideN218
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

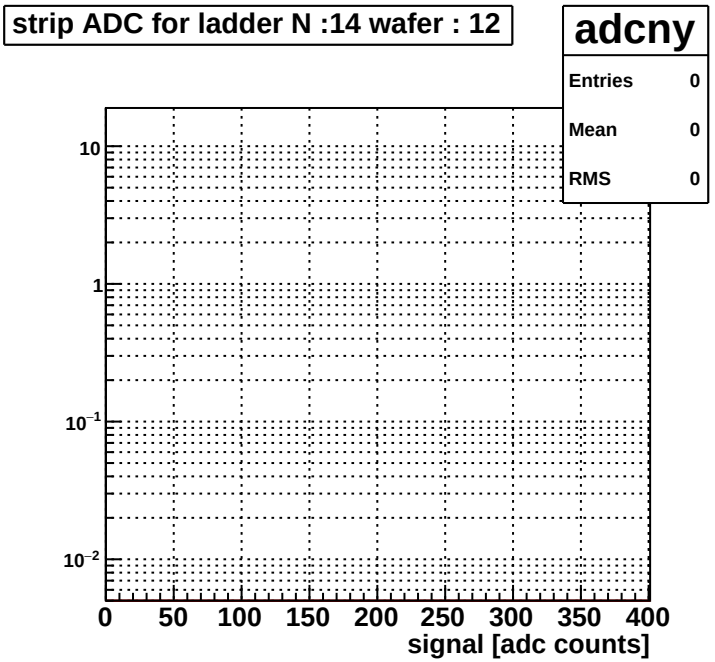

strip ADC for ladder P :14 wafer : 11
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

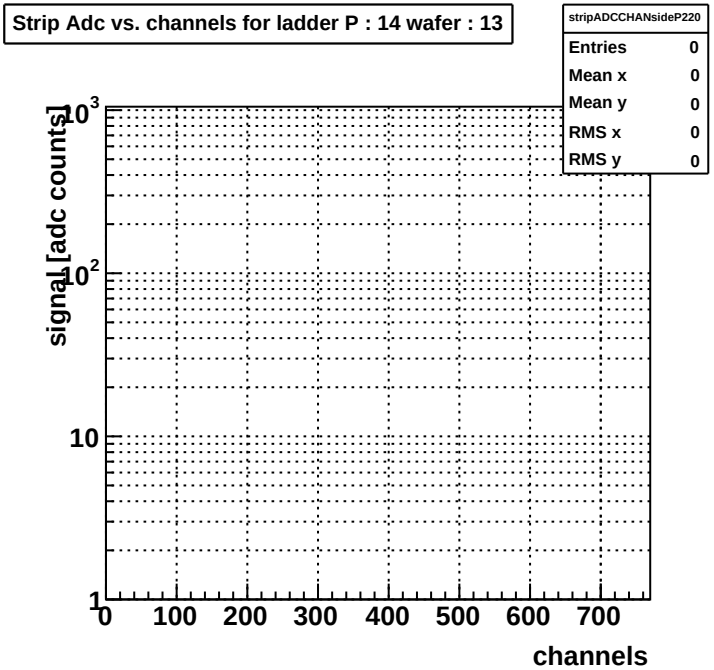

strip ADC for ladder N :14 wafer : 11
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

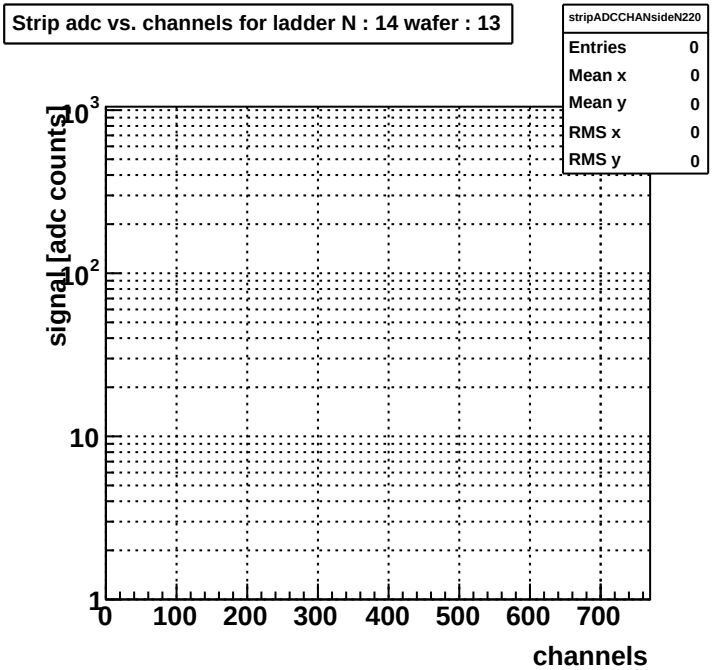

Strip Adc vs. channels for ladder P : 14 wafer : 12
stripADCCHANsideP219
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

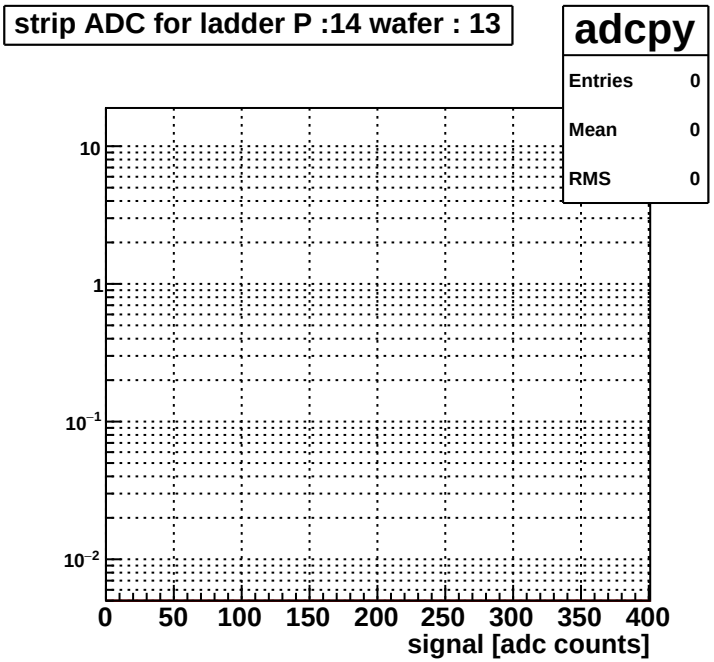

Strip adc vs. channels for ladder N : 14 wafer : 12
stripADCCHANsideN219
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

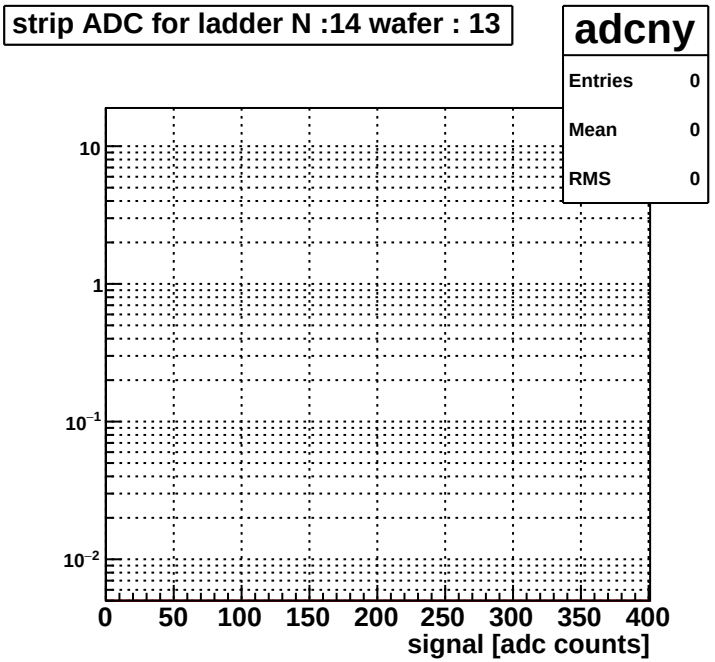

strip ADC for ladder P :14 wafer : 12
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

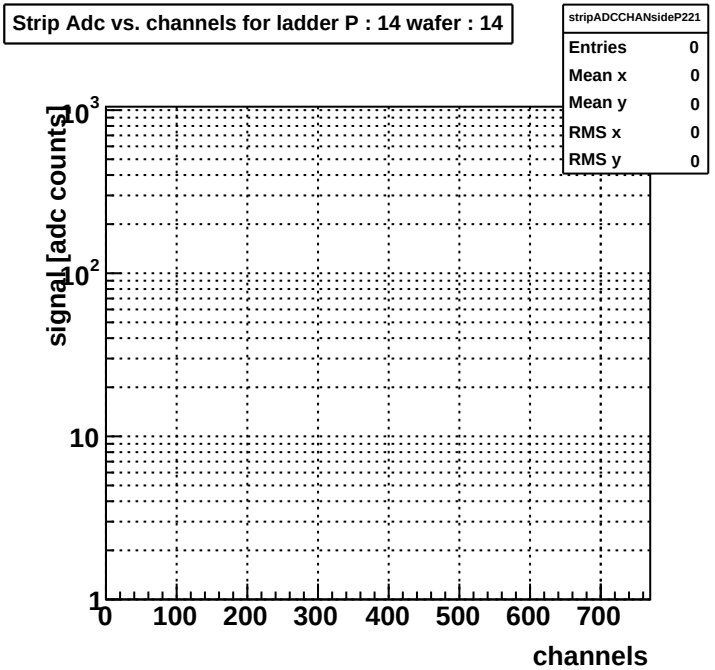

strip ADC for ladder N :14 wafer : 12
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

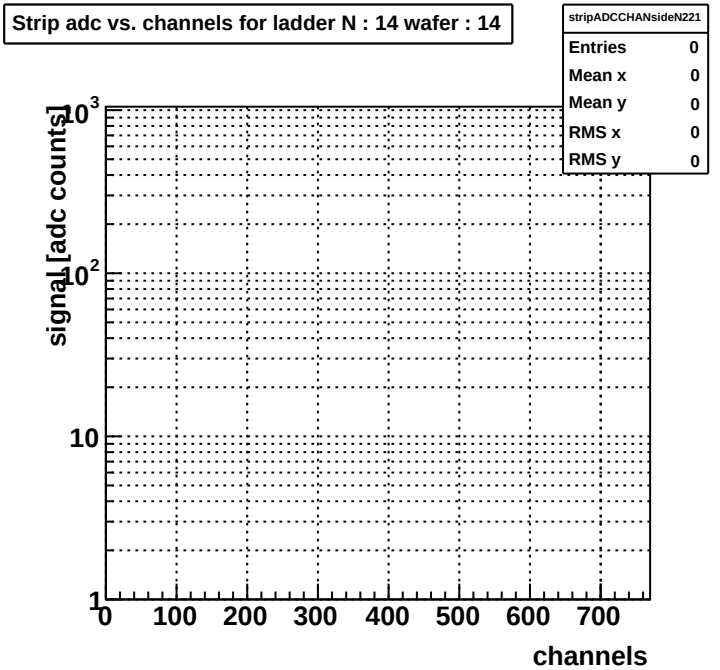

Strip Adc vs. channels for ladder P : 14 wafer : 13
stripADCCHANsideP220
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

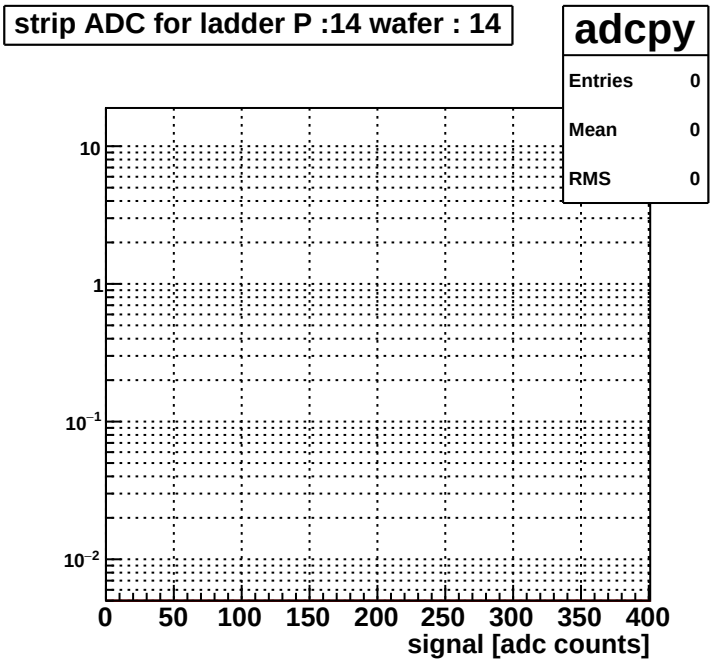

Strip adc vs. channels for ladder N : 14 wafer : 13
stripADCCHANsideN220
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

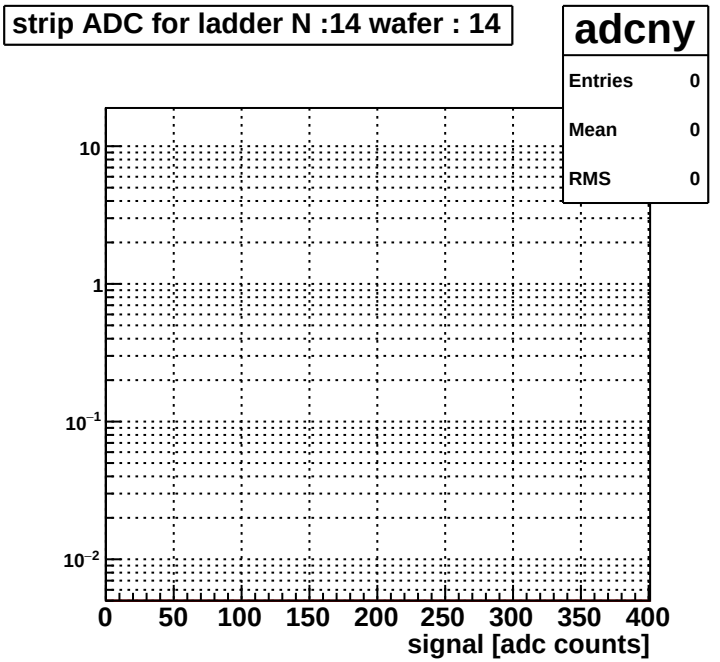

strip ADC for ladder P :14 wafer : 13
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

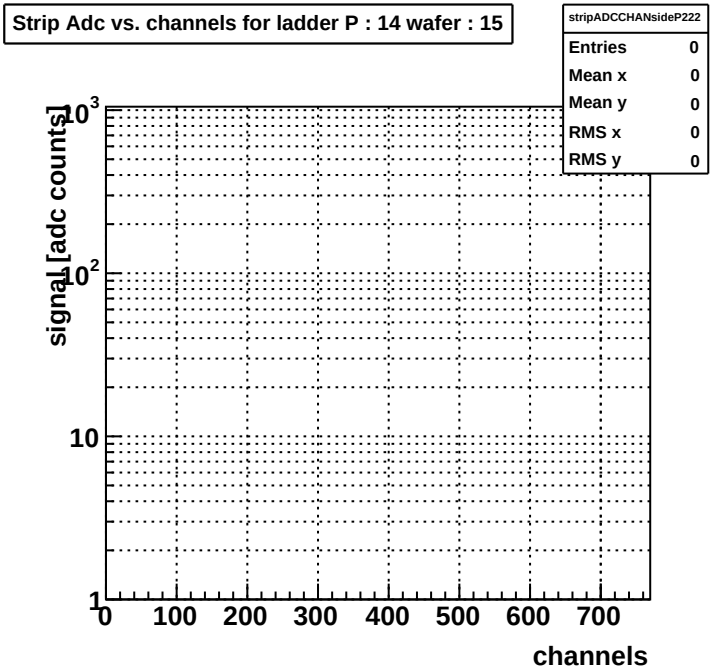

strip ADC for ladder N :14 wafer : 13
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

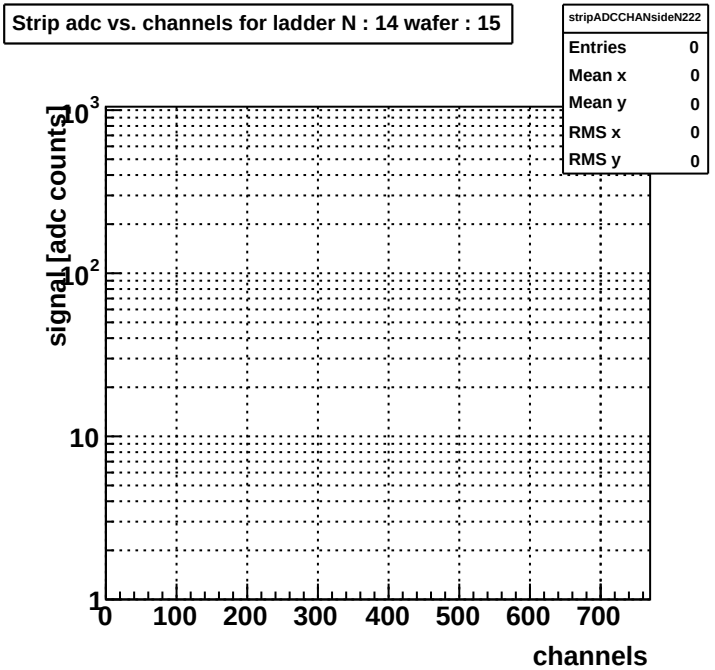

Strip Adc vs. channels for ladder P : 14 wafer : 14
stripADCCHANsideP221
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

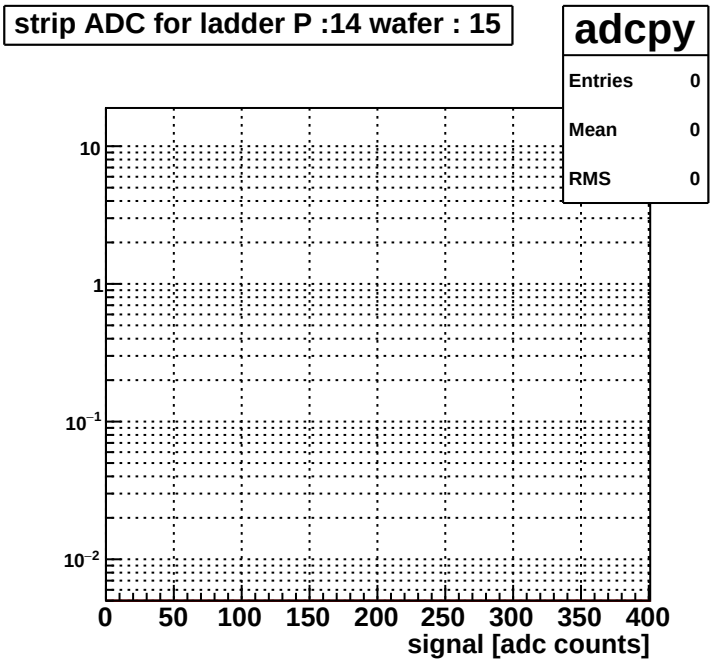

Strip adc vs. channels for ladder N : 14 wafer : 14
stripADCCHANsideN221
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

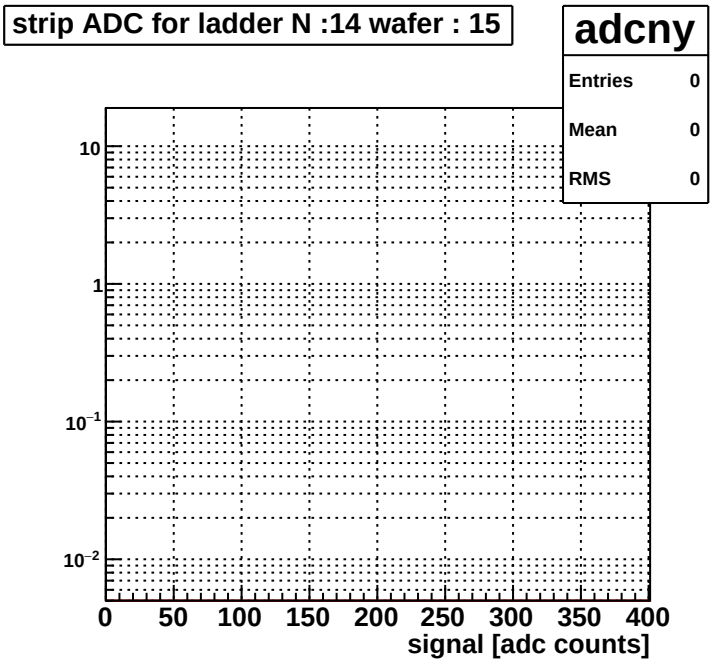

strip ADC for ladder P :14 wafer : 14
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

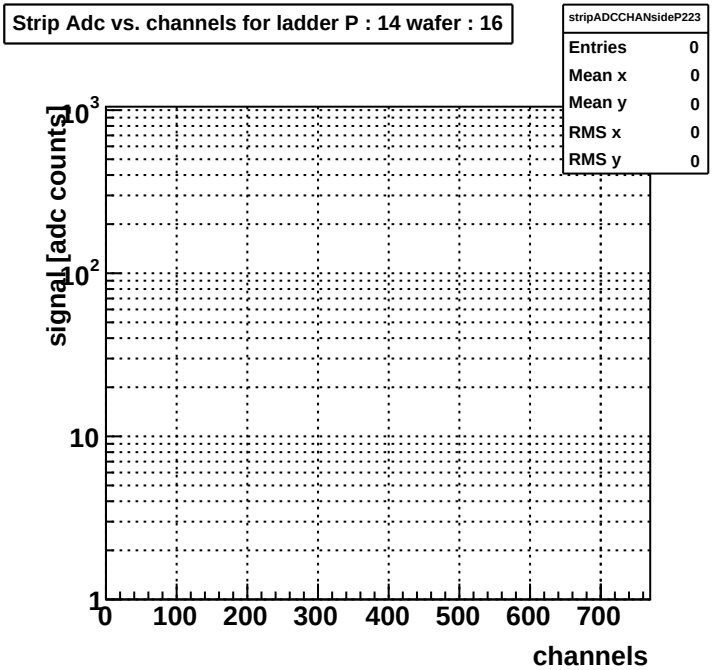

strip ADC for ladder N :14 wafer : 14
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

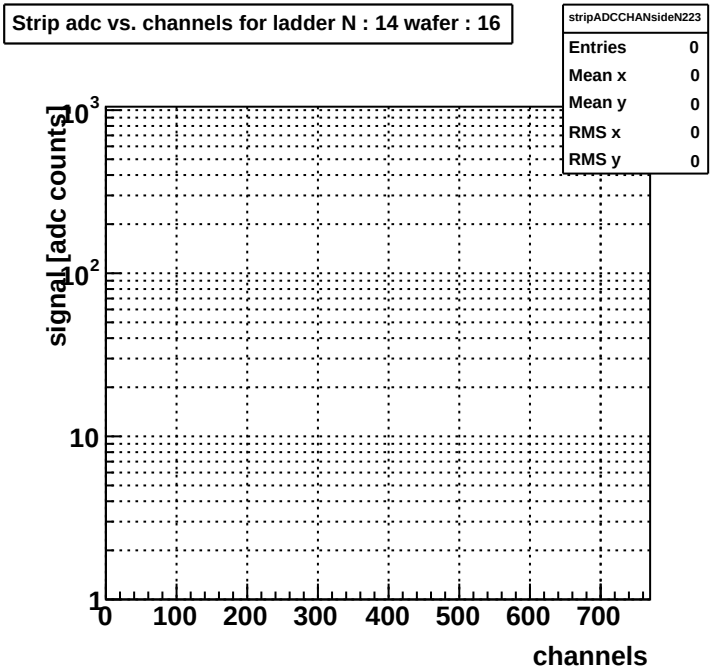

Strip Adc vs. channels for ladder P : 14 wafer : 15
stripADCCHANsideP222
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

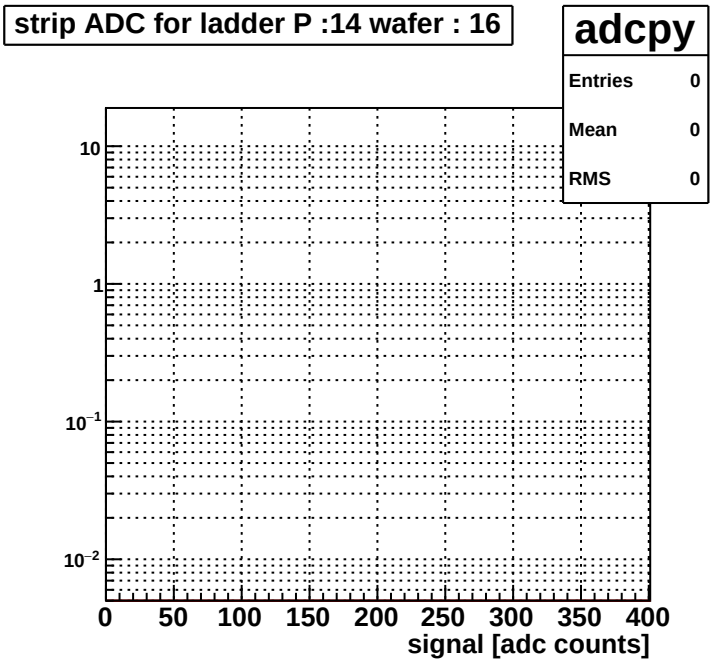

Strip adc vs. channels for ladder N : 14 wafer : 15
stripADCCHANsideN222
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

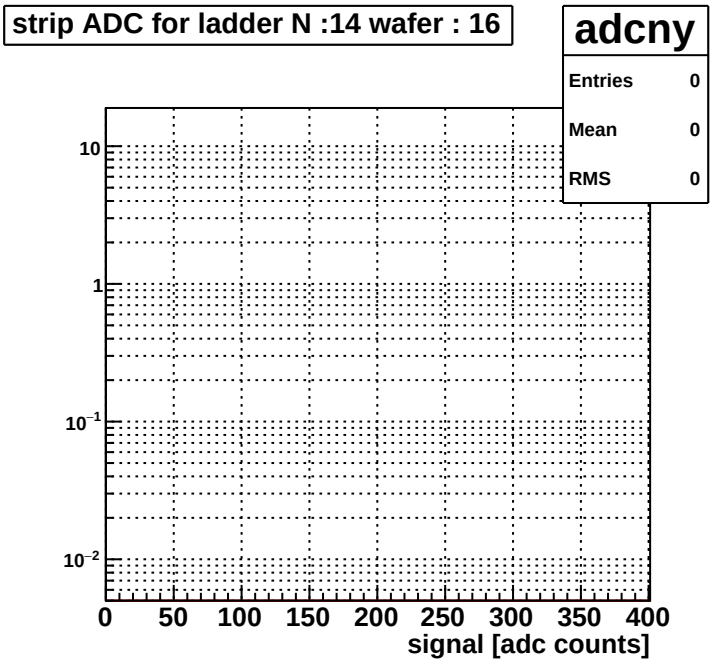

strip ADC for ladder P :14 wafer : 15
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

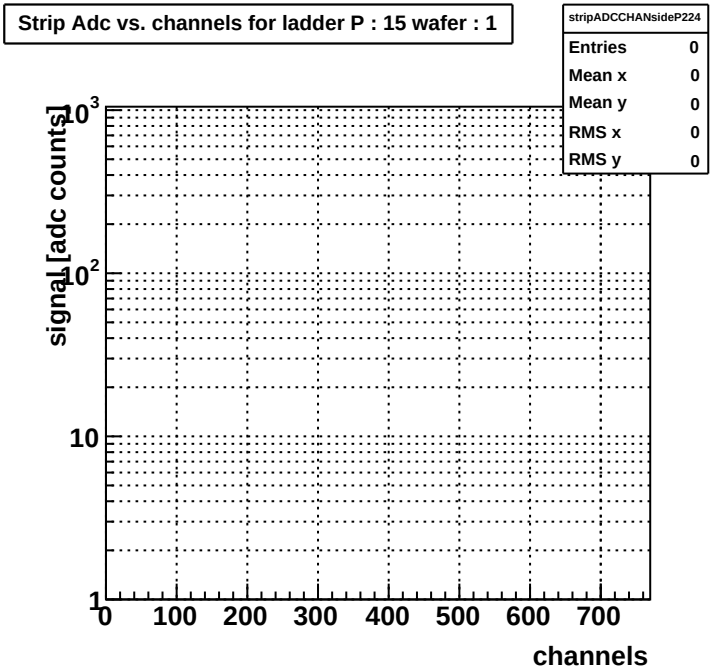

strip ADC for ladder N :14 wafer : 15
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

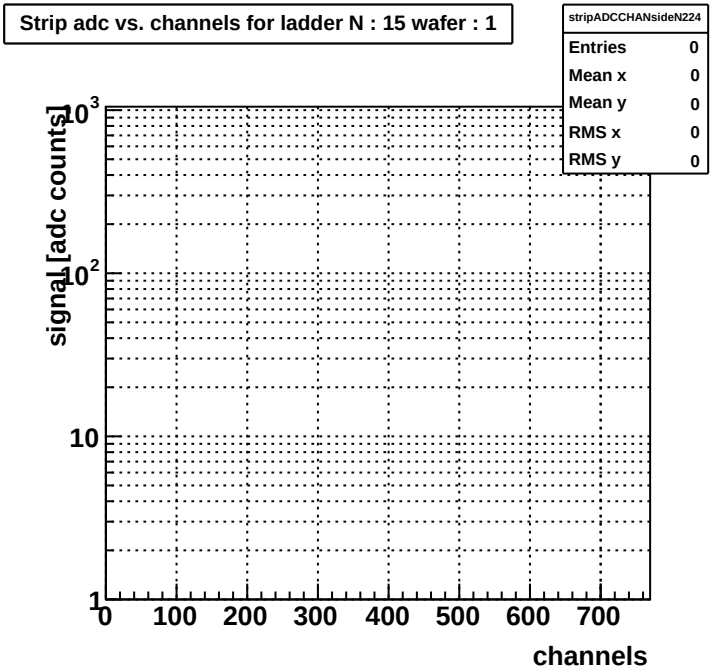

Strip Adc vs. channels for ladder P : 14 wafer : 16
stripADCCHANsideP223
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

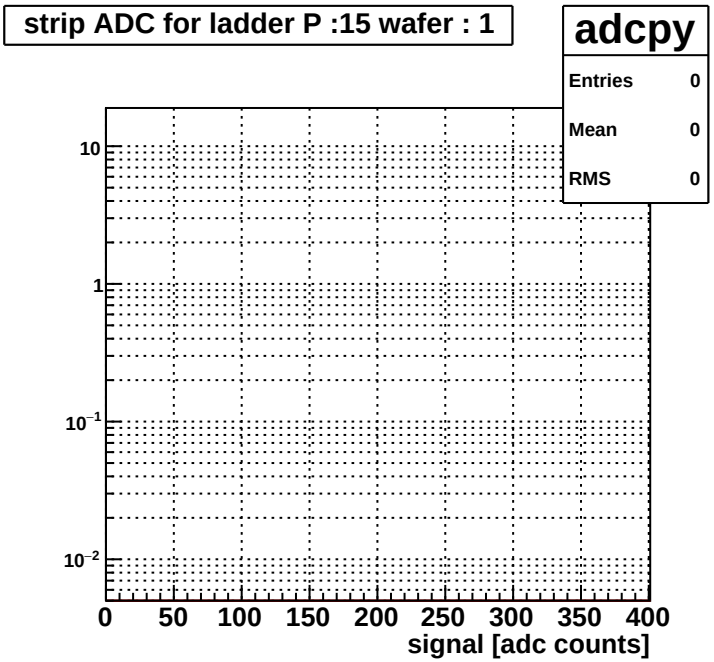

Strip adc vs. channels for ladder N : 14 wafer : 16
stripADCCHANsideN223
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

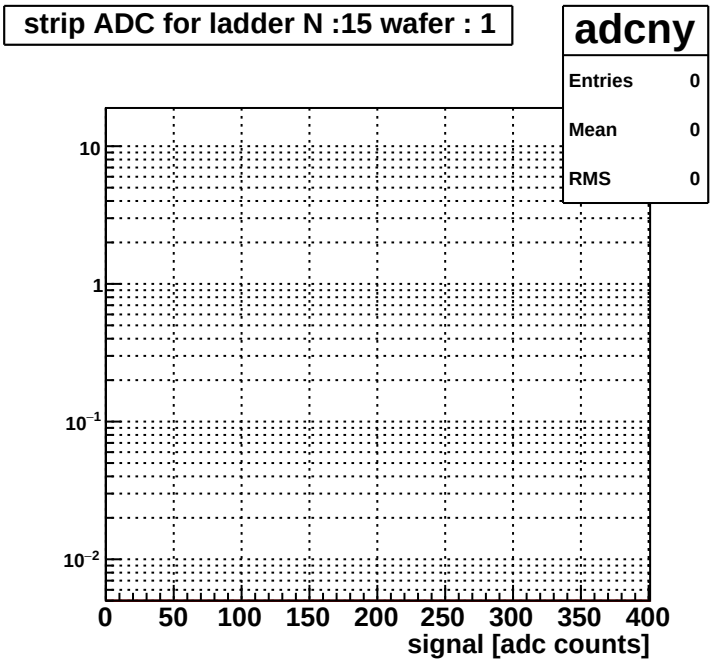

strip ADC for ladder P :14 wafer : 16
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

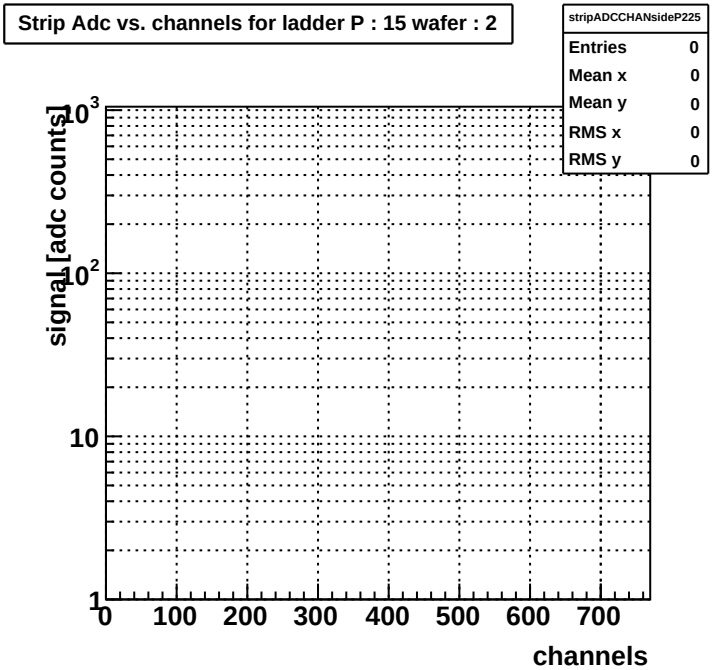

strip ADC for ladder N :14 wafer : 16
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

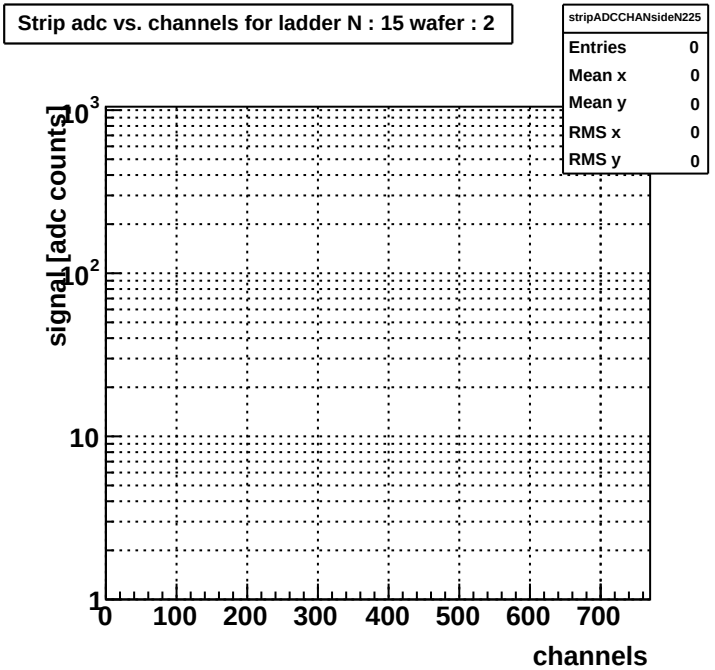

Strip Adc vs. channels for ladder P : 15 wafer : 1
stripADCCHANsideP224
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

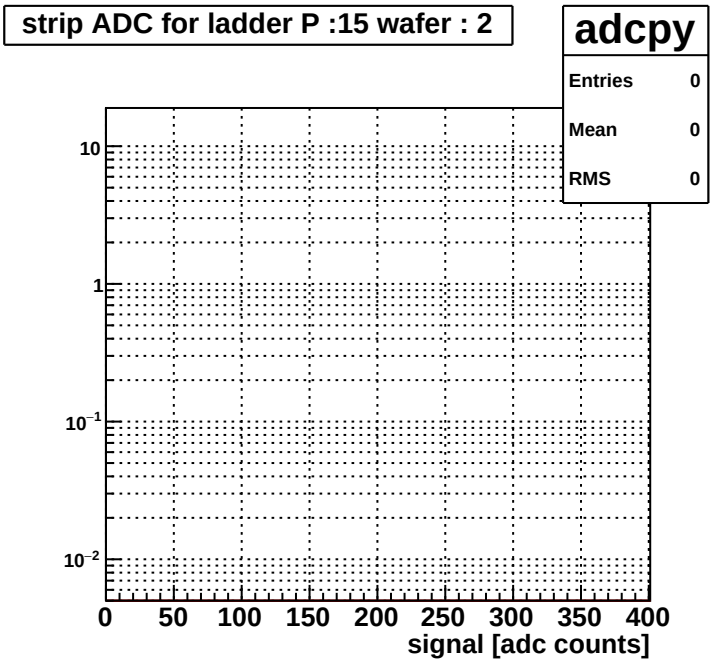

Strip adc vs. channels for ladder N : 15 wafer : 1
stripADCCHANsideN224
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

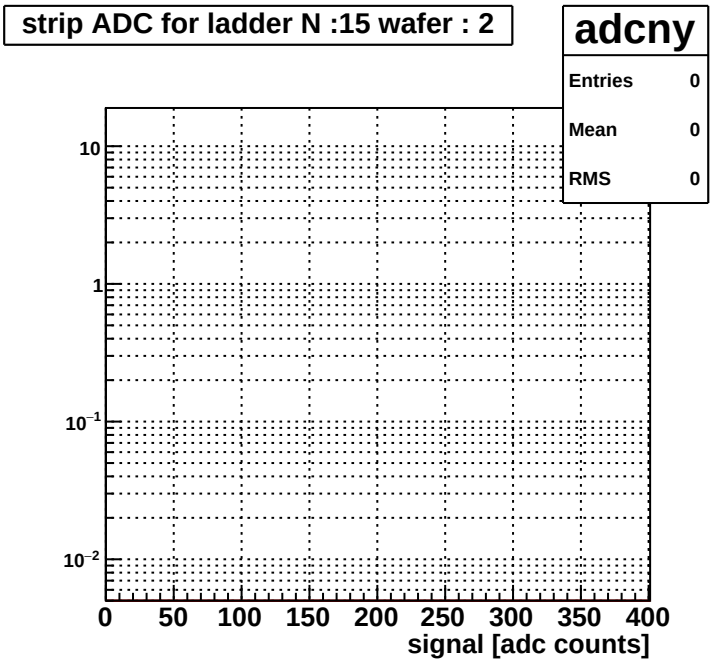

strip ADC for ladder P :15 wafer : 1
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

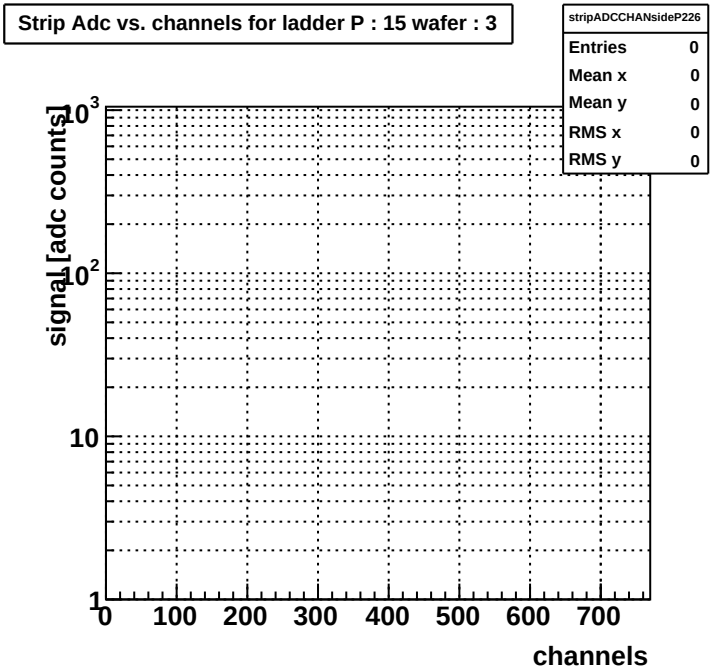

strip ADC for ladder N :15 wafer : 1
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

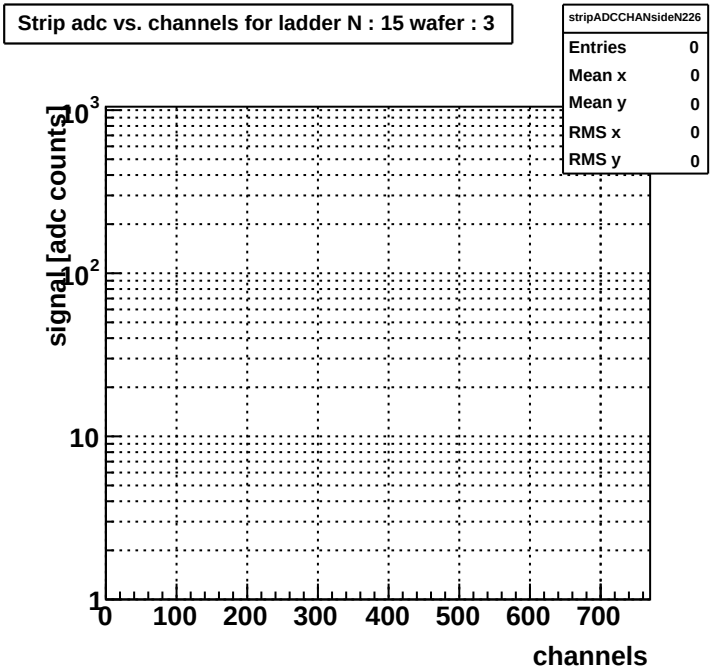

Strip Adc vs. channels for ladder P : 15 wafer : 2
stripADCCHANsideP225
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

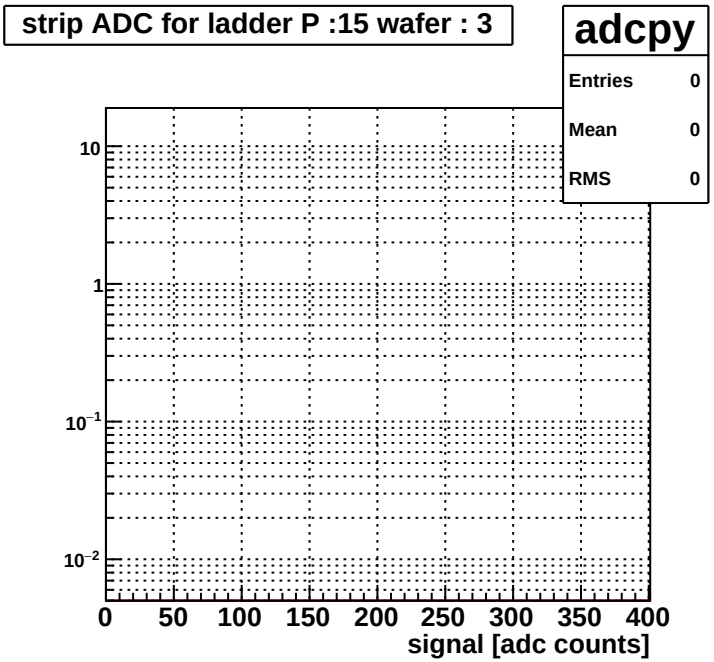

Strip adc vs. channels for ladder N : 15 wafer : 2
stripADCCHANsideN225
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

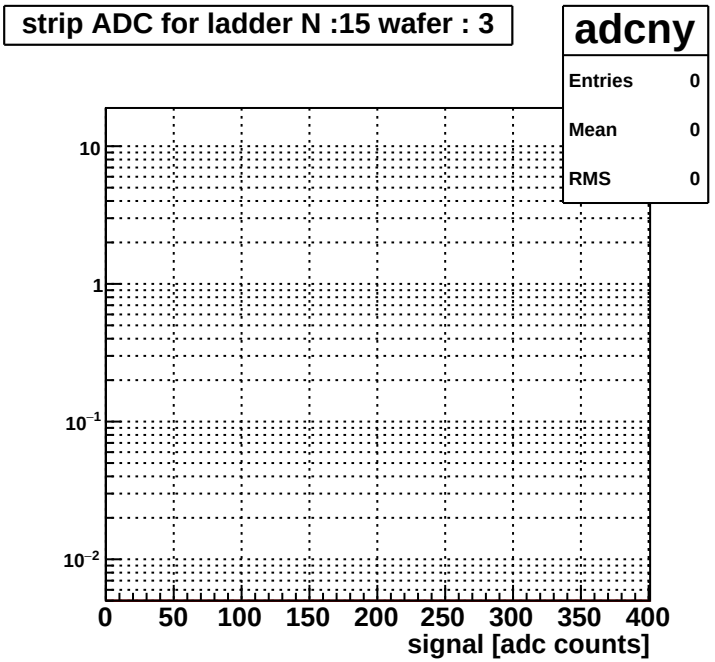

strip ADC for ladder P :15 wafer : 2
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

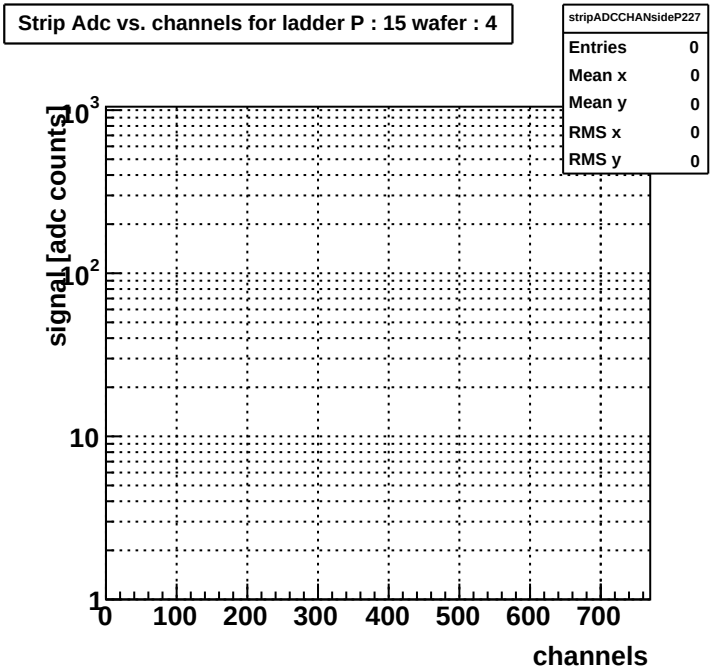

strip ADC for ladder N :15 wafer : 2
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

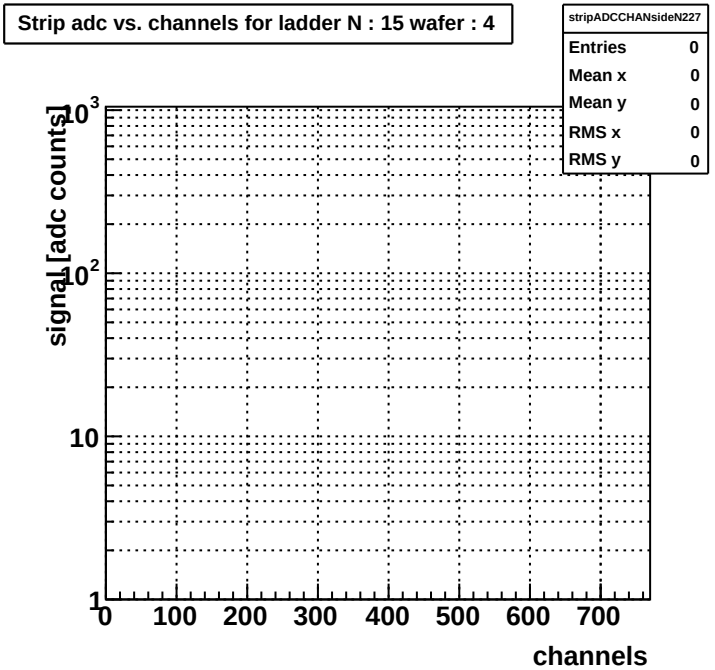

Strip Adc vs. channels for ladder P : 15 wafer : 3
stripADCCHANsideP226
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

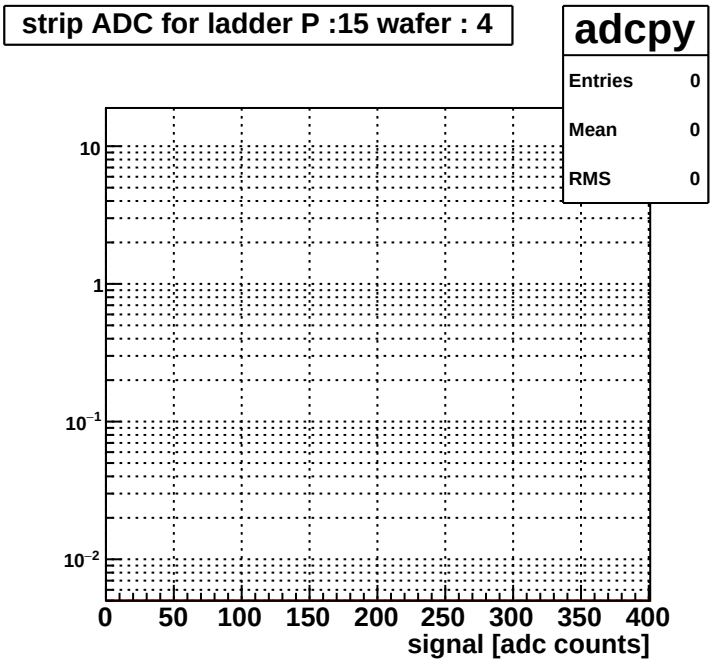

Strip adc vs. channels for ladder N : 15 wafer : 3
stripADCCHANsideN226
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

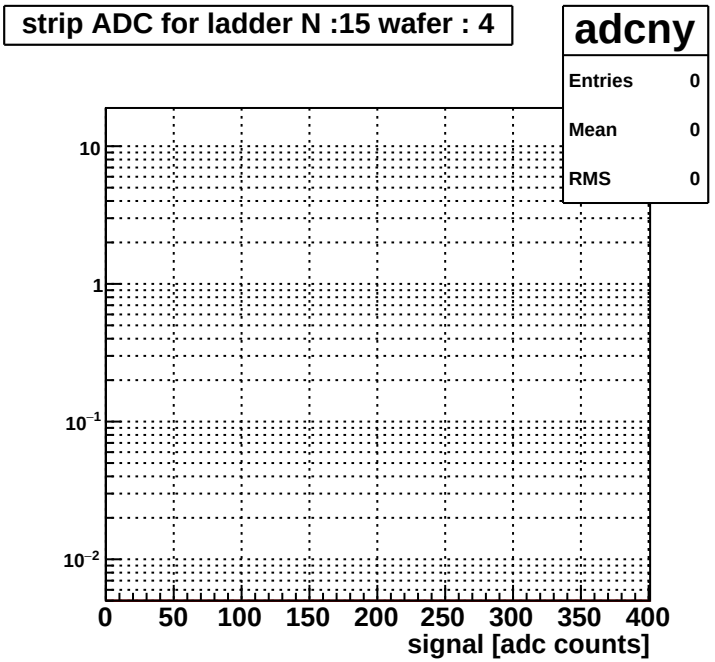

strip ADC for ladder P :15 wafer : 3
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

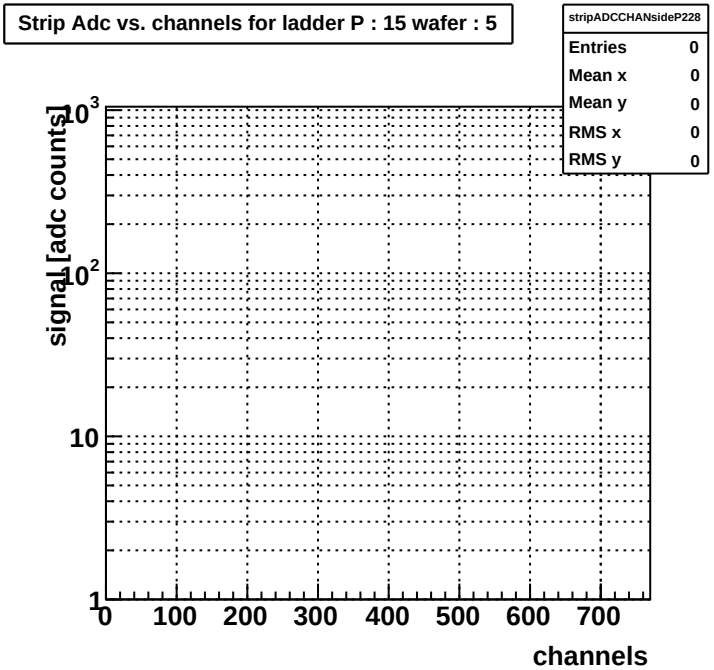

strip ADC for ladder N :15 wafer : 3
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

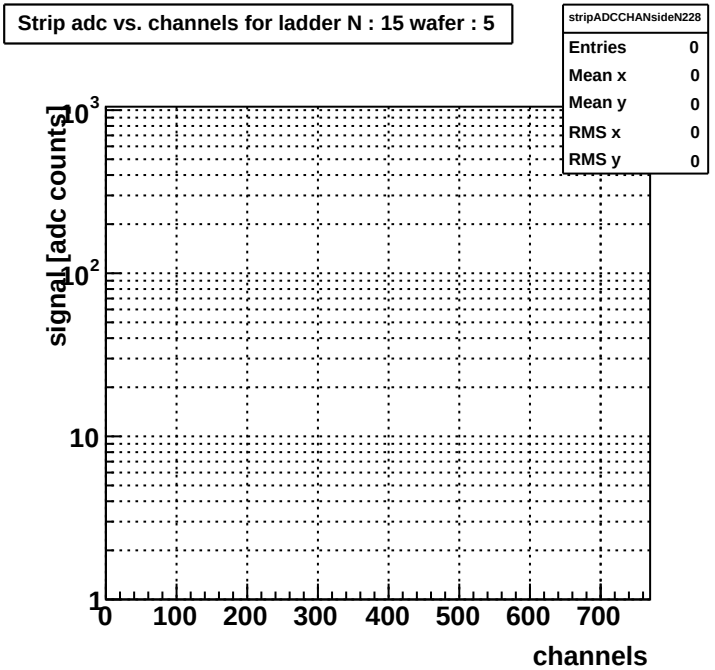

Strip Adc vs. channels for ladder P : 15 wafer : 4
stripADCCHANsideP227
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

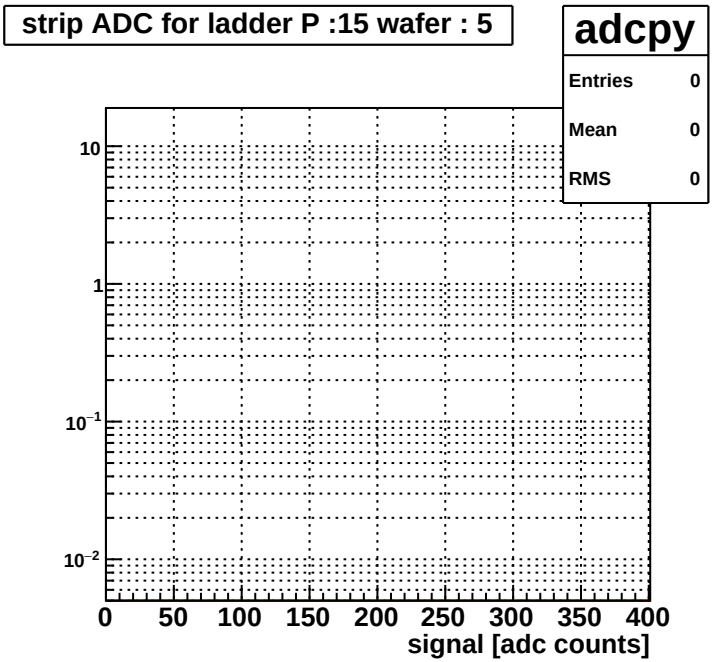

Strip adc vs. channels for ladder N : 15 wafer : 4
stripADCCHANsideN227
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

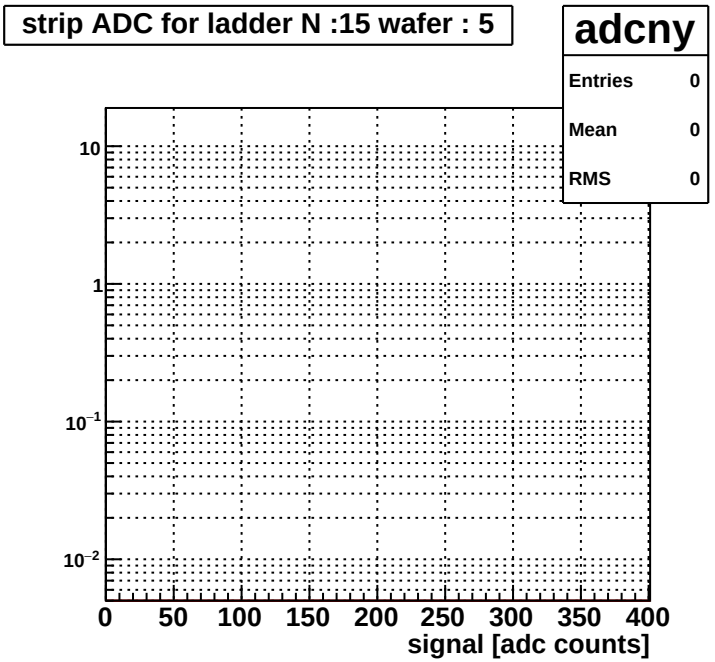

strip ADC for ladder P :15 wafer : 4
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

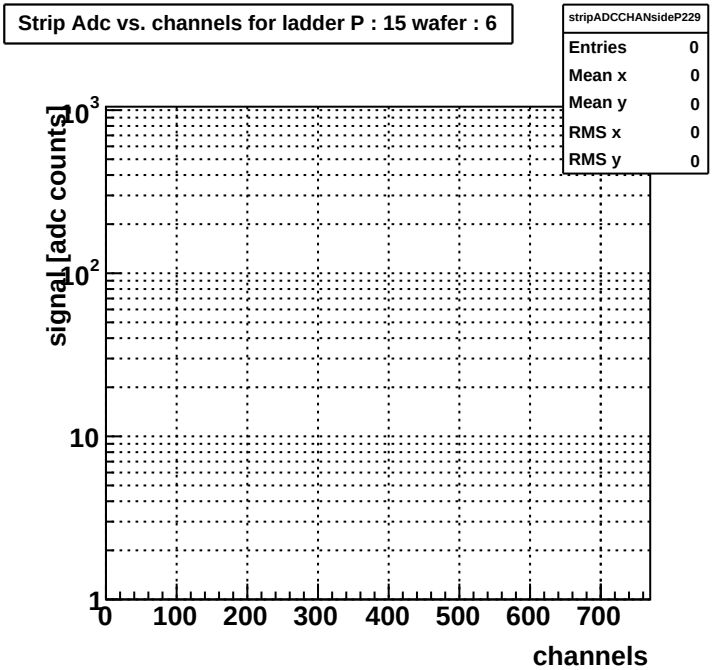

strip ADC for ladder N :15 wafer : 4
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

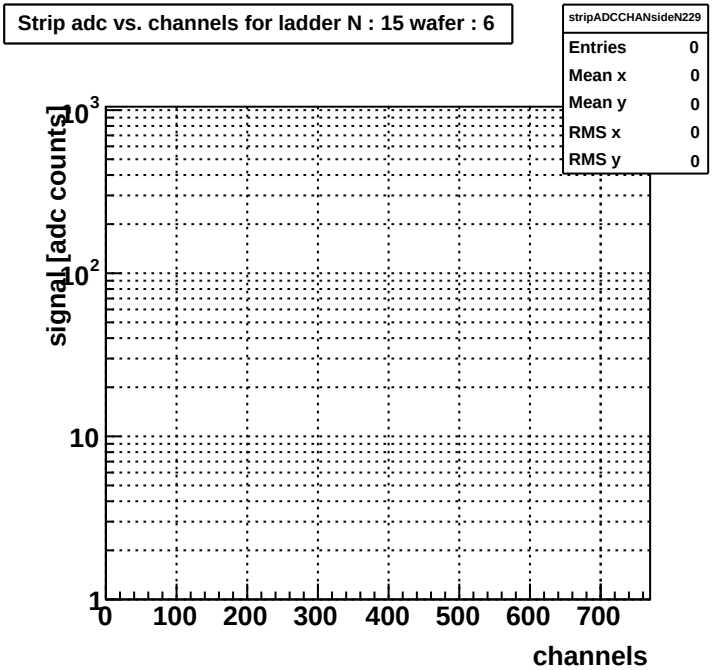

Strip Adc vs. channels for ladder P : 15 wafer : 5
stripADCCHANsideP228
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

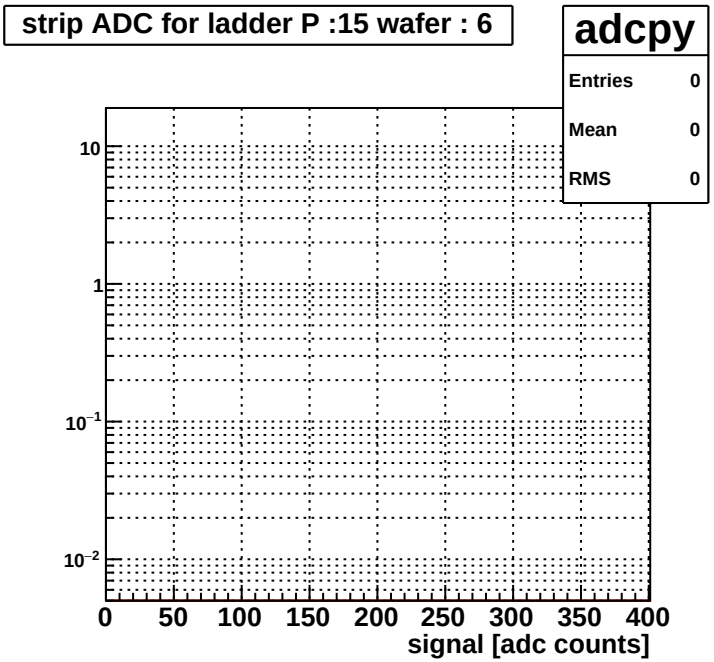

Strip adc vs. channels for ladder N : 15 wafer : 5
stripADCCHANsideN228
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

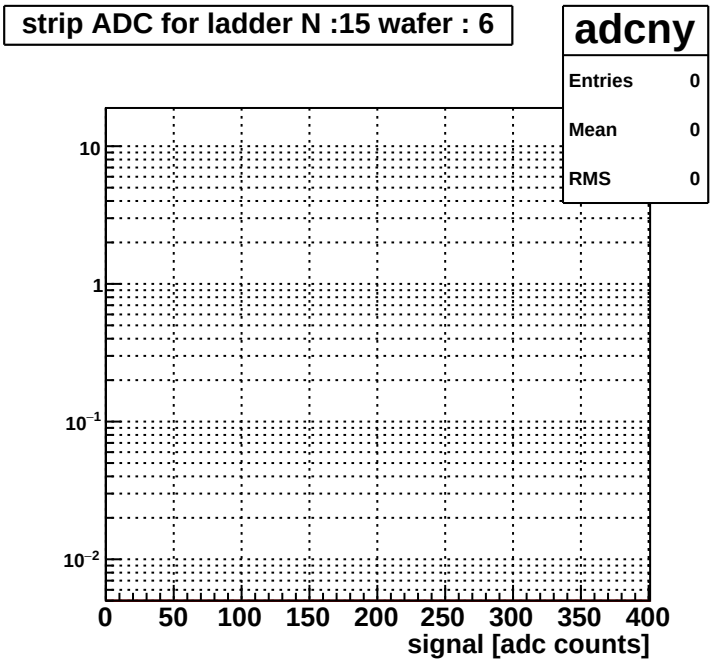

strip ADC for ladder P :15 wafer : 5
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

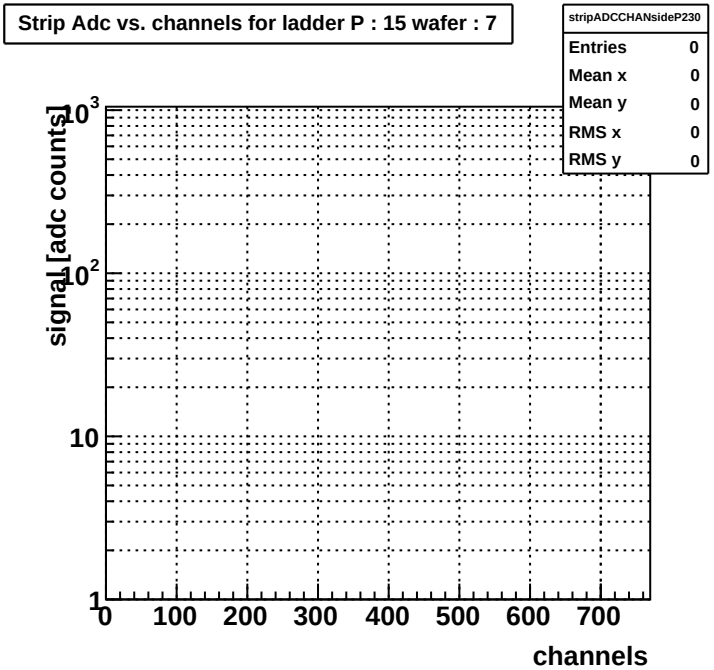

strip ADC for ladder N :15 wafer : 5
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

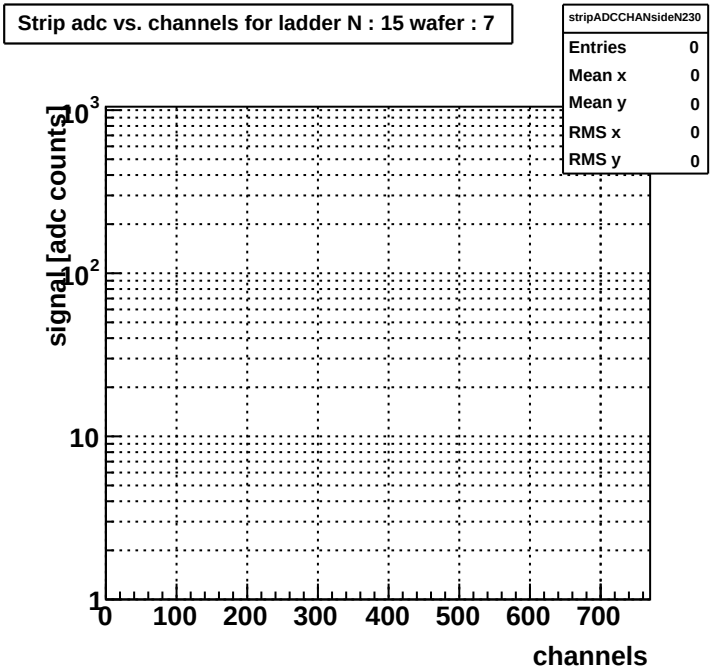

Strip Adc vs. channels for ladder P : 15 wafer : 6
stripADCCHANsideP229
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

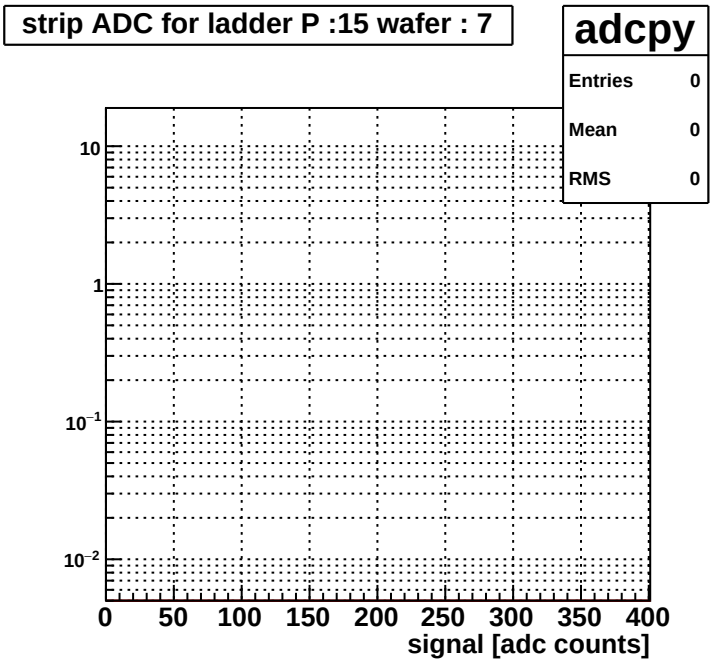

Strip adc vs. channels for ladder N : 15 wafer : 6
stripADCCHANsideN229
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

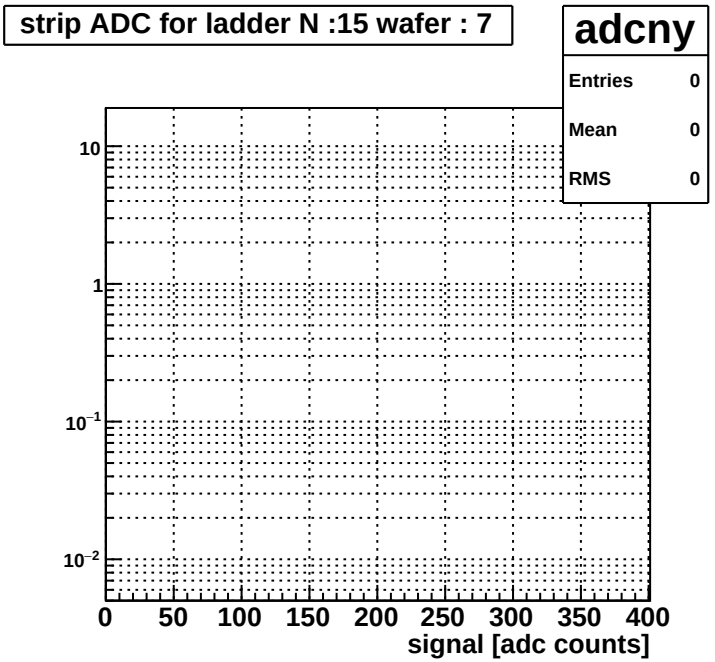

strip ADC for ladder P :15 wafer : 6
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

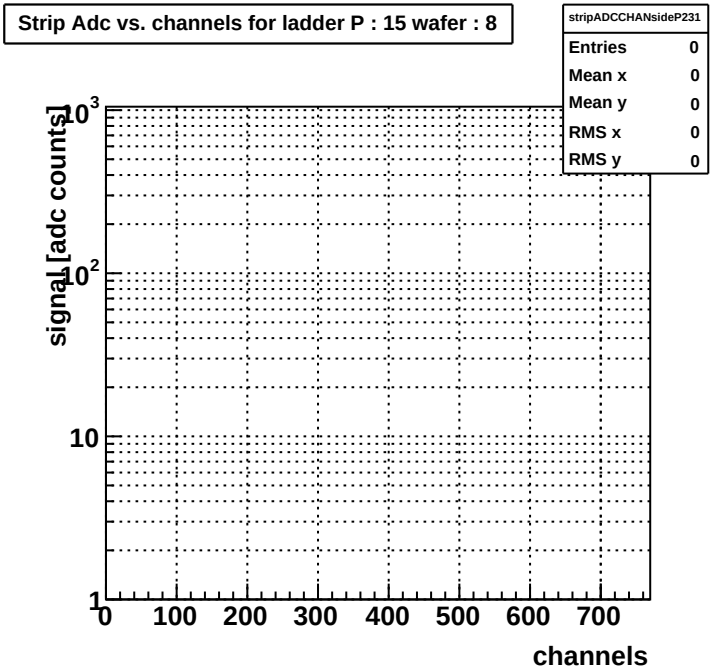

strip ADC for ladder N :15 wafer : 6
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

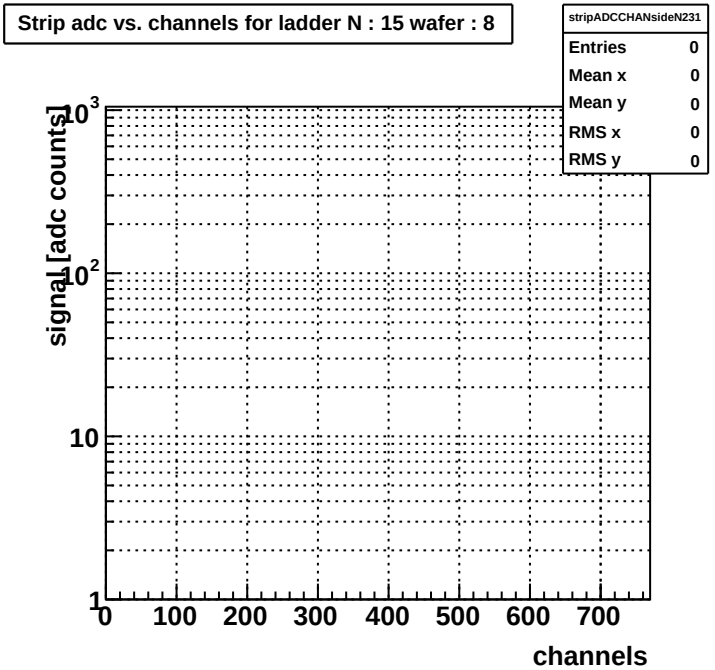

Strip Adc vs. channels for ladder P : 15 wafer : 7
stripADCCHANsideP230
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

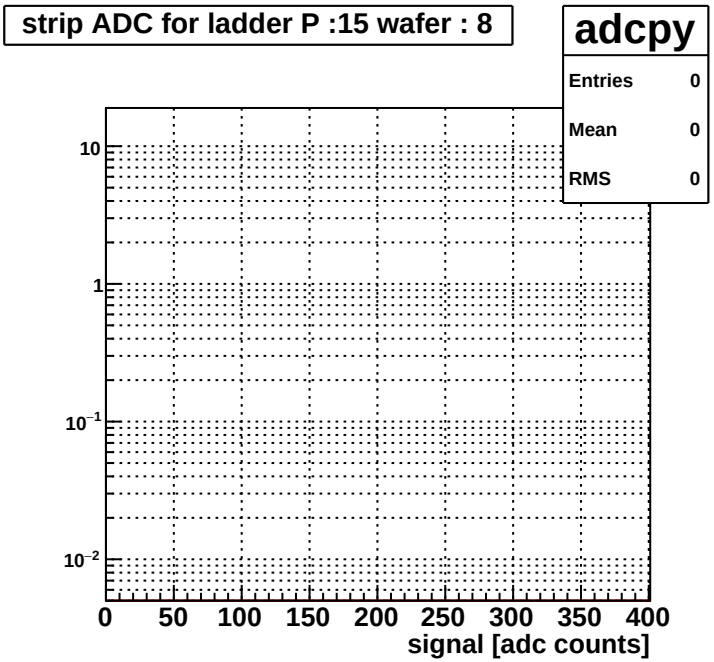

Strip adc vs. channels for ladder N : 15 wafer : 7
stripADCCHANsideN230
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

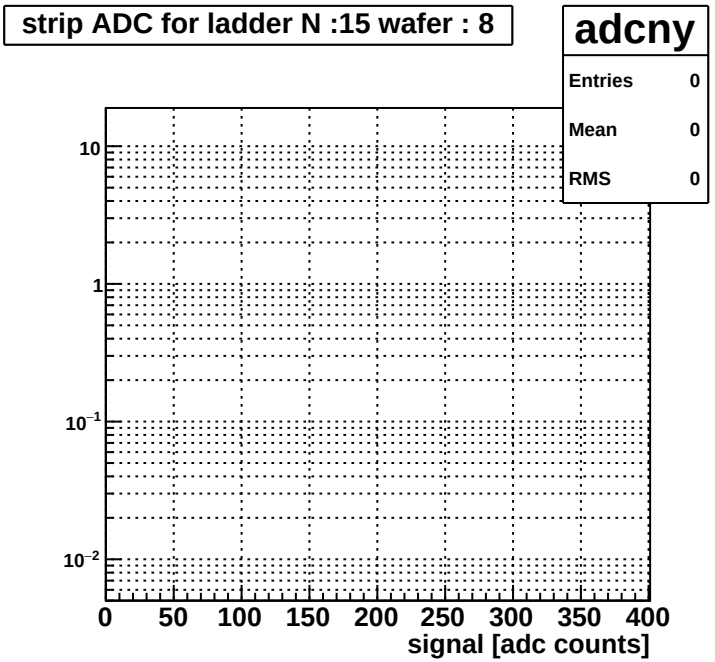

strip ADC for ladder P :15 wafer : 7
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

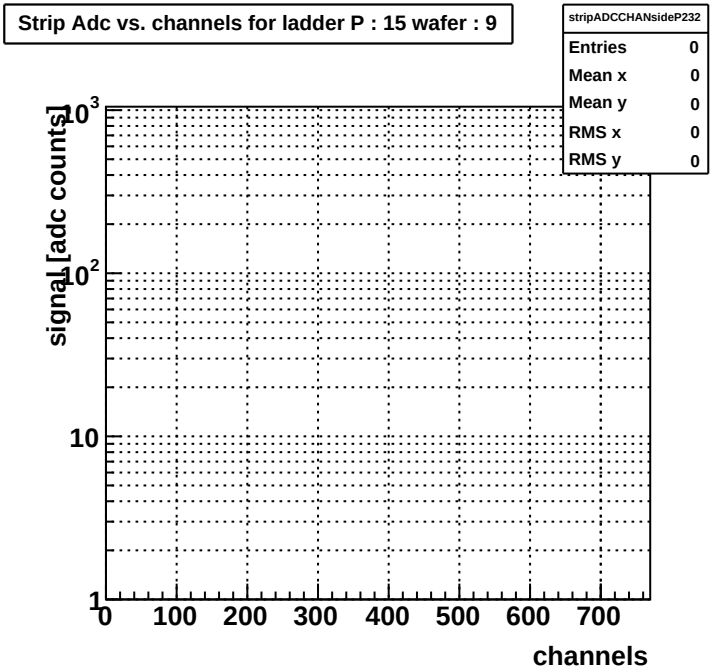

strip ADC for ladder N :15 wafer : 7
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

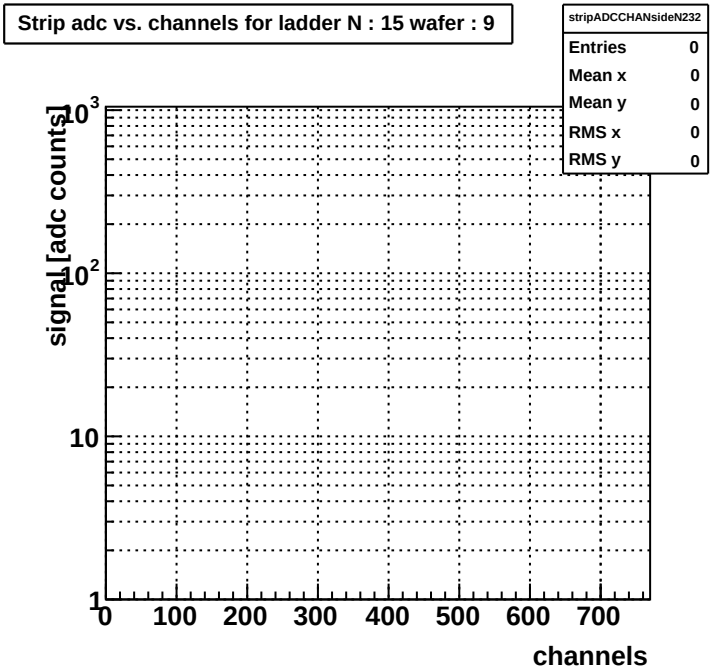

Strip Adc vs. channels for ladder P : 15 wafer : 8
stripADCCHANsideP231
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

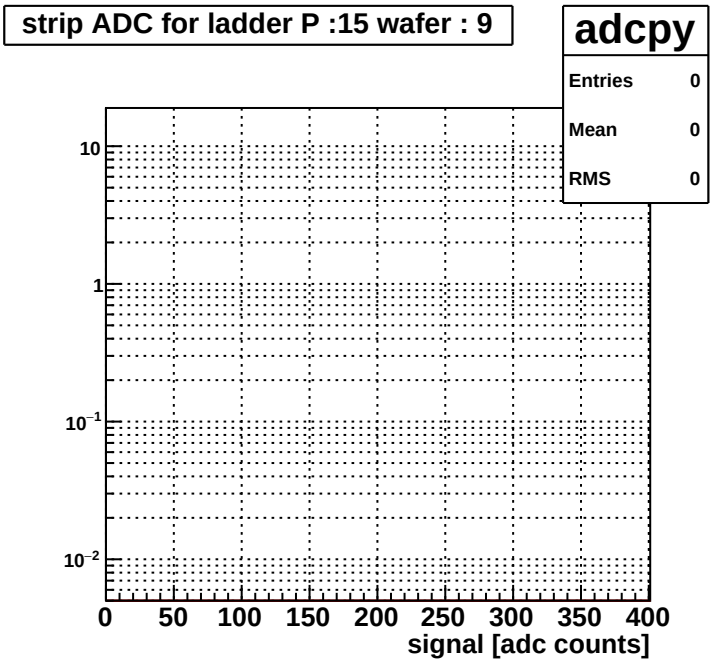

Strip adc vs. channels for ladder N : 15 wafer : 8
stripADCCHANsideN231
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

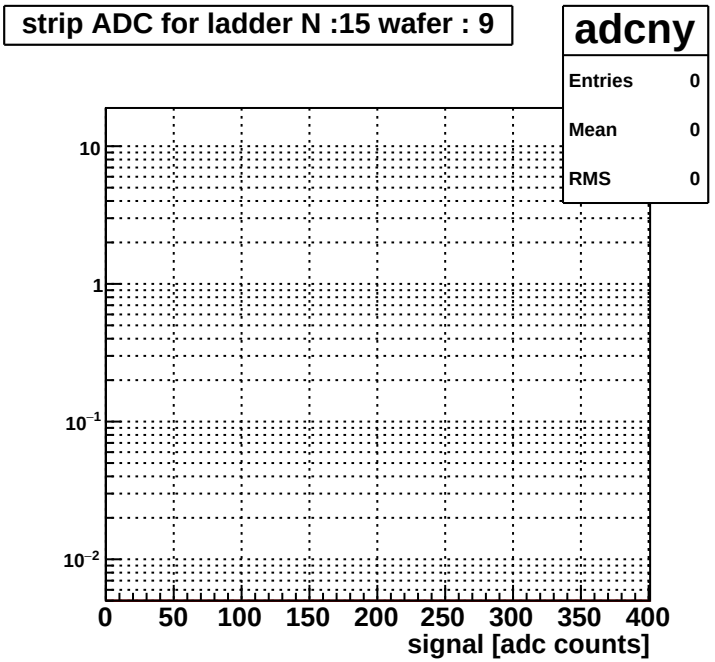

strip ADC for ladder P :15 wafer : 8
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

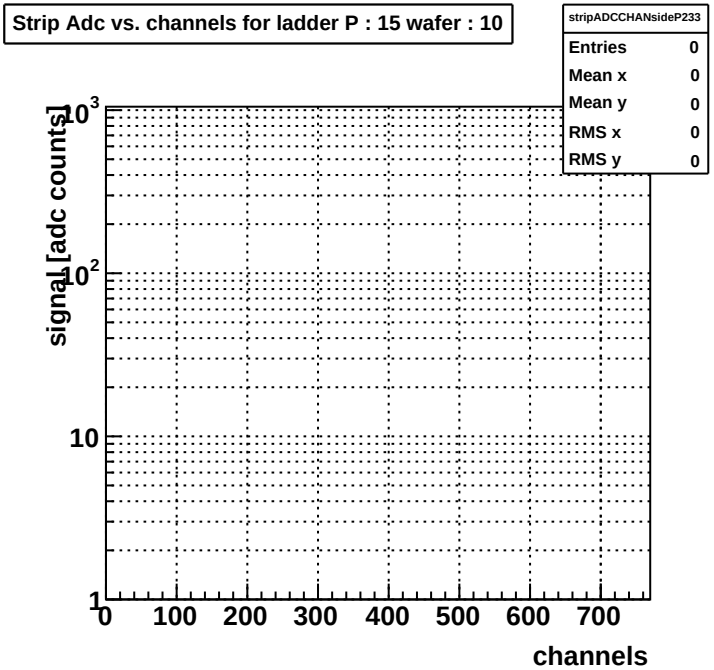

strip ADC for ladder N :15 wafer : 8
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

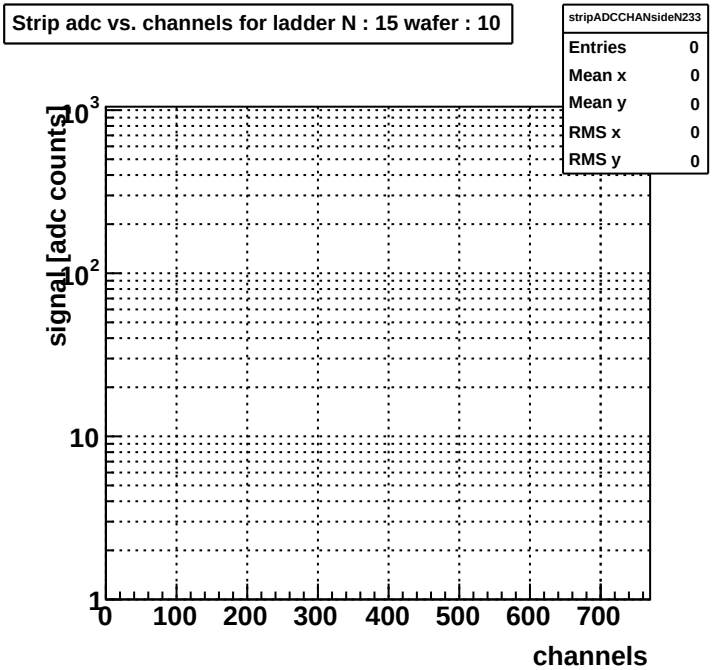

Strip Adc vs. channels for ladder P : 15 wafer : 9
stripADCCHANsideP232
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

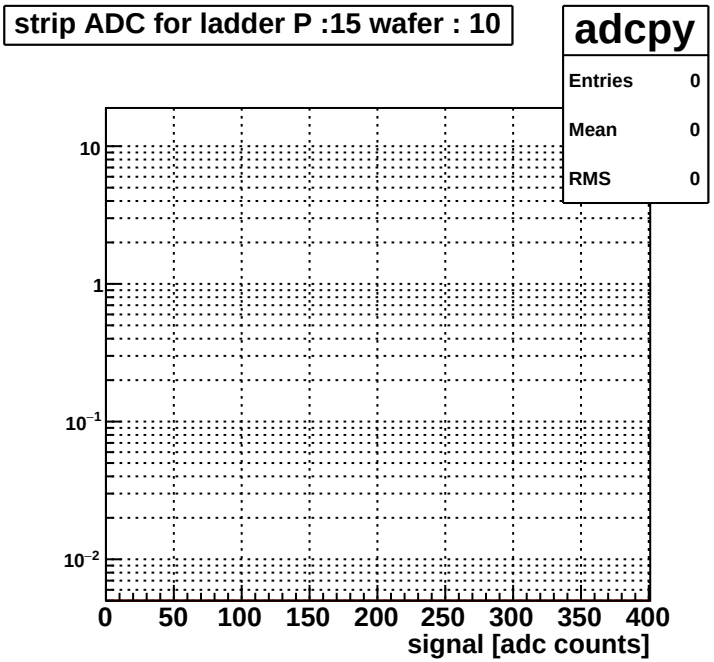

Strip adc vs. channels for ladder N : 15 wafer : 9
stripADCCHANsideN232
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

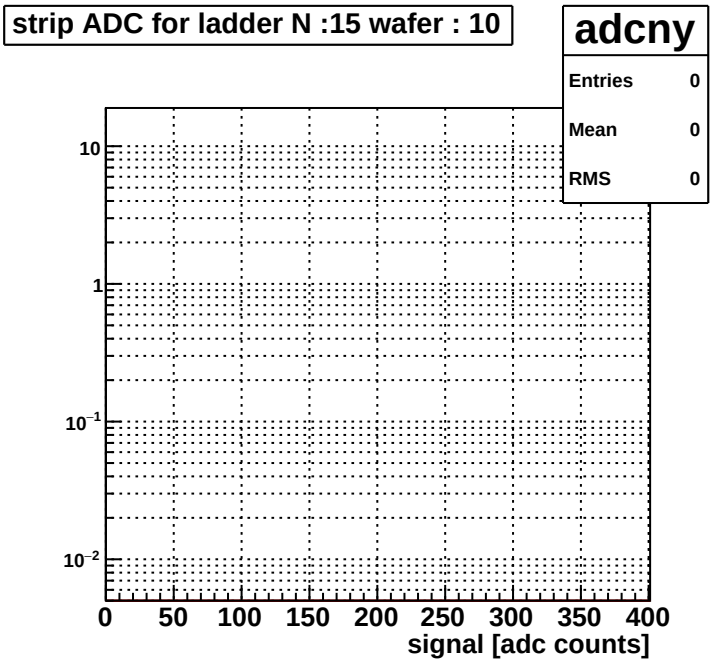

strip ADC for ladder P :15 wafer : 9
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

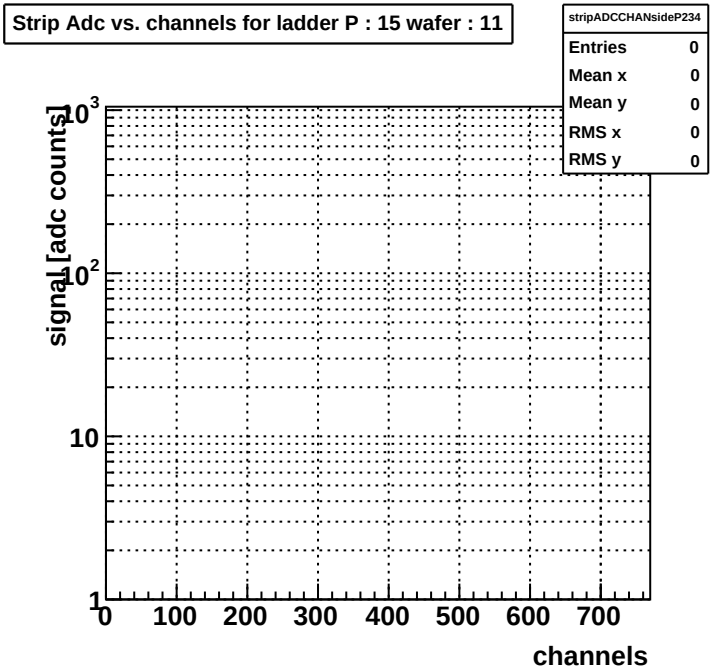

strip ADC for ladder N :15 wafer : 9
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

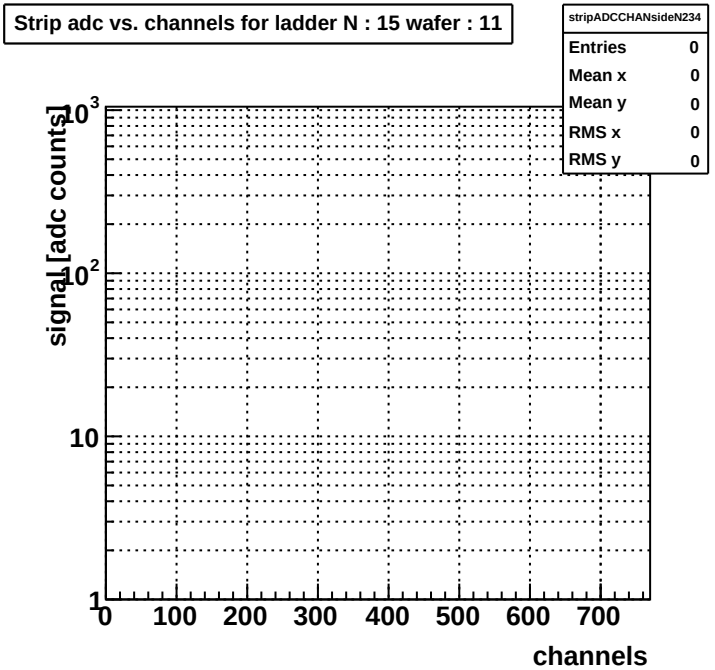

Strip Adc vs. channels for ladder P : 15 wafer : 10
stripADCCHANsideP233
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

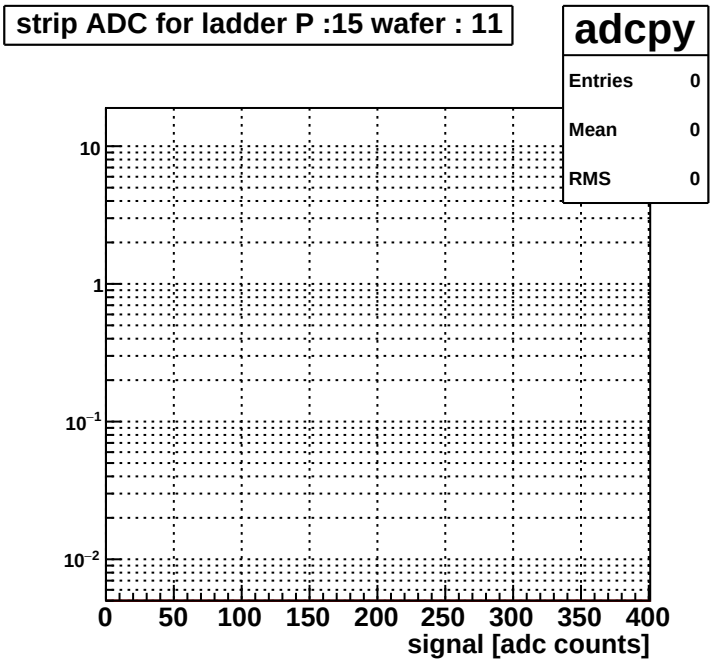

Strip adc vs. channels for ladder N : 15 wafer : 10
stripADCCHANsideN233
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

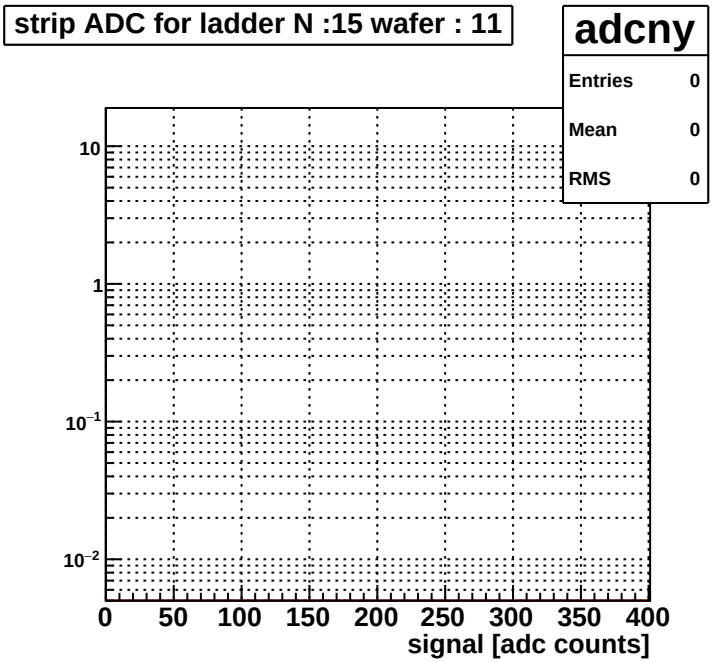

strip ADC for ladder P :15 wafer : 10
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

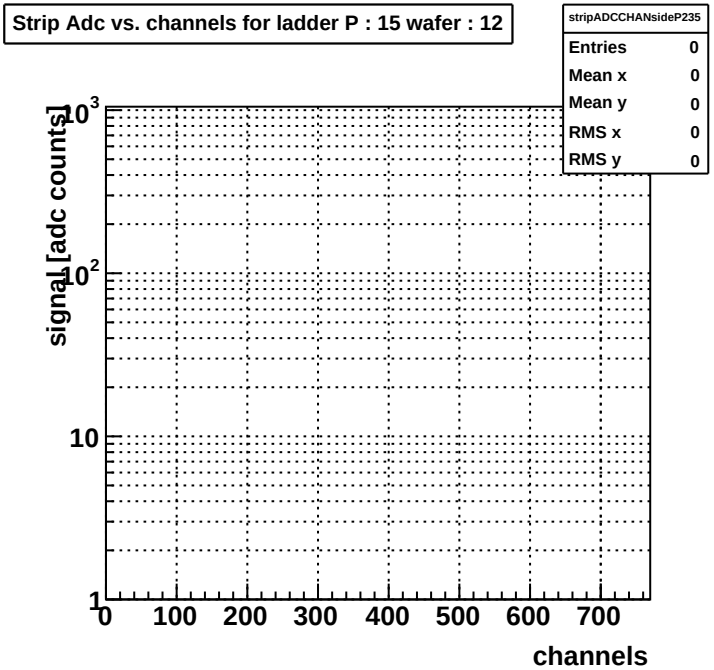

strip ADC for ladder N :15 wafer : 10
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

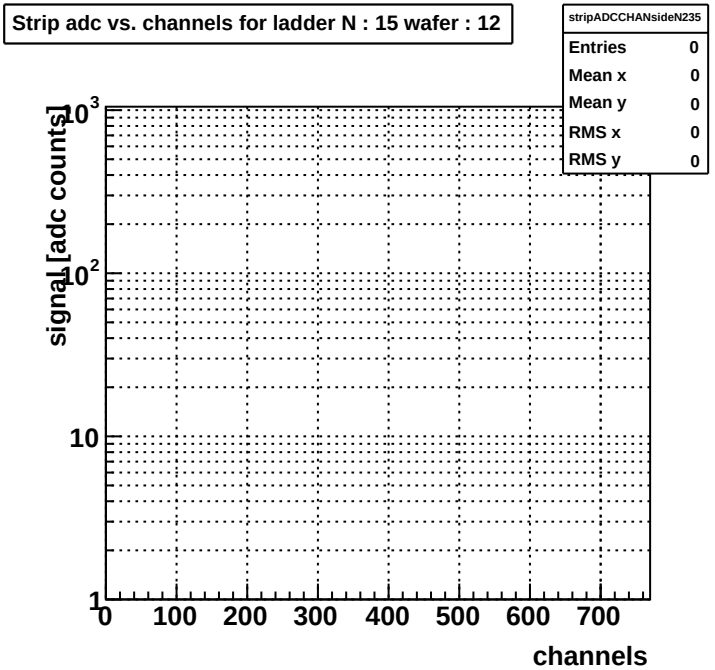

Strip Adc vs. channels for ladder P : 15 wafer : 11
stripADCCHANsideP234
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

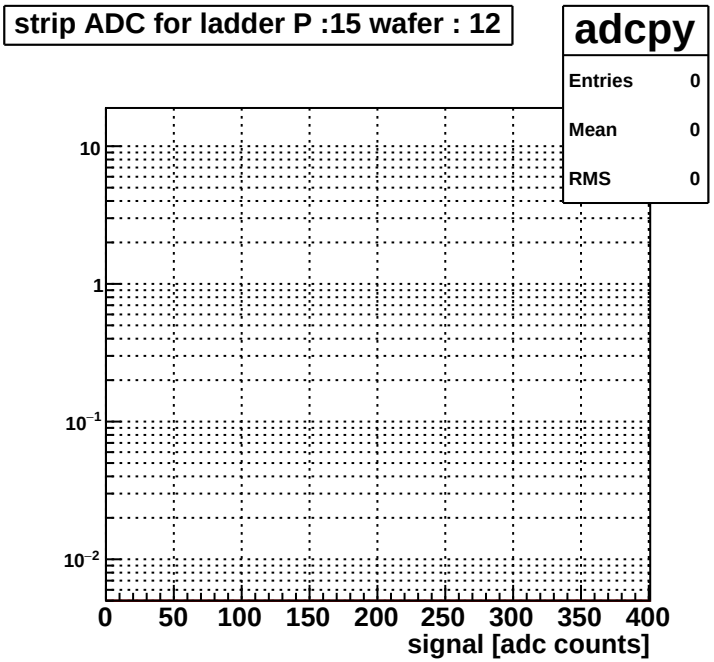

Strip adc vs. channels for ladder N : 15 wafer : 11
stripADCCHANsideN234
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

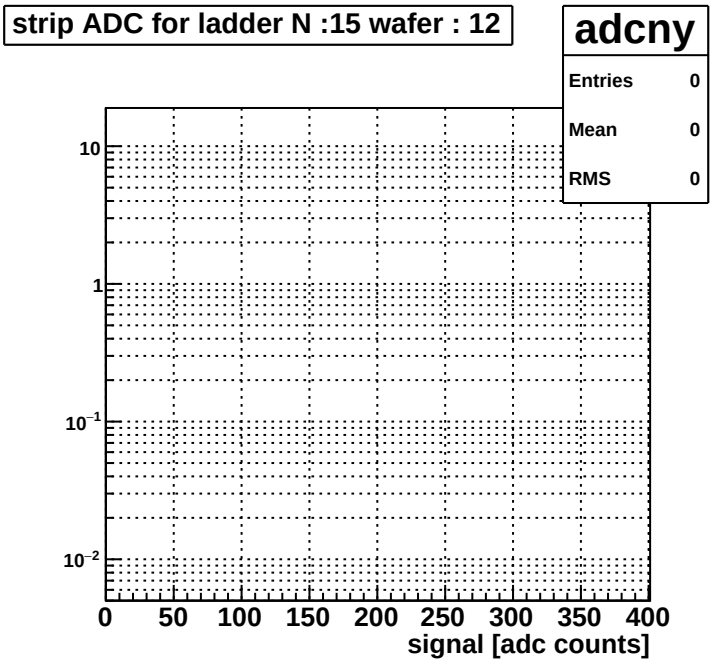

strip ADC for ladder P :15 wafer : 11
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

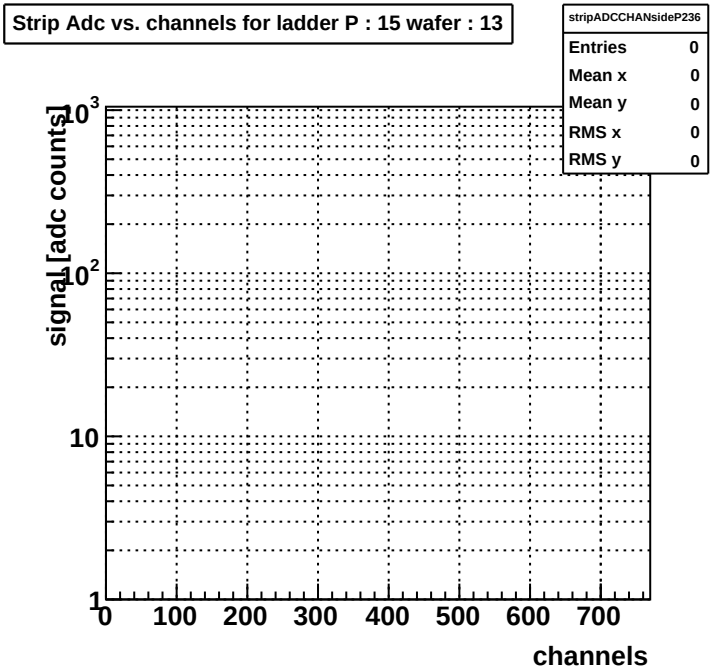

strip ADC for ladder N :15 wafer : 11
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

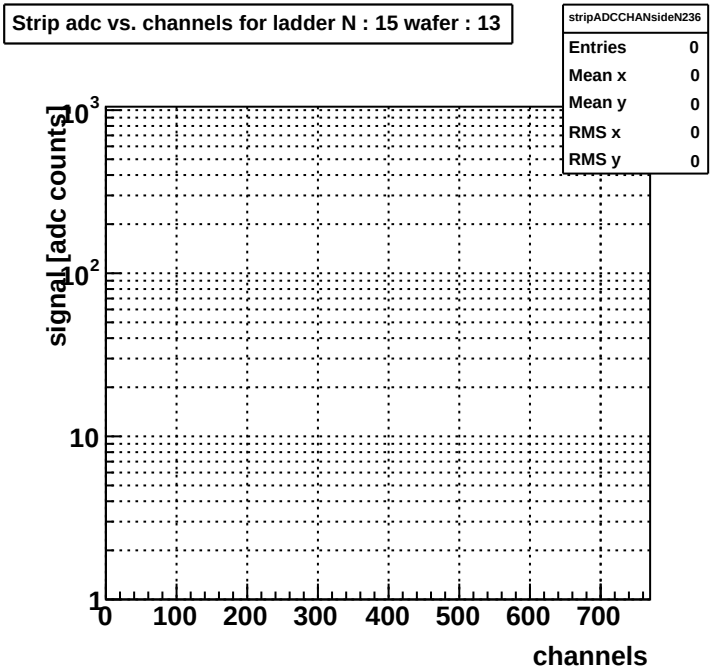

Strip Adc vs. channels for ladder P : 15 wafer : 12
stripADCCHANsideP235
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

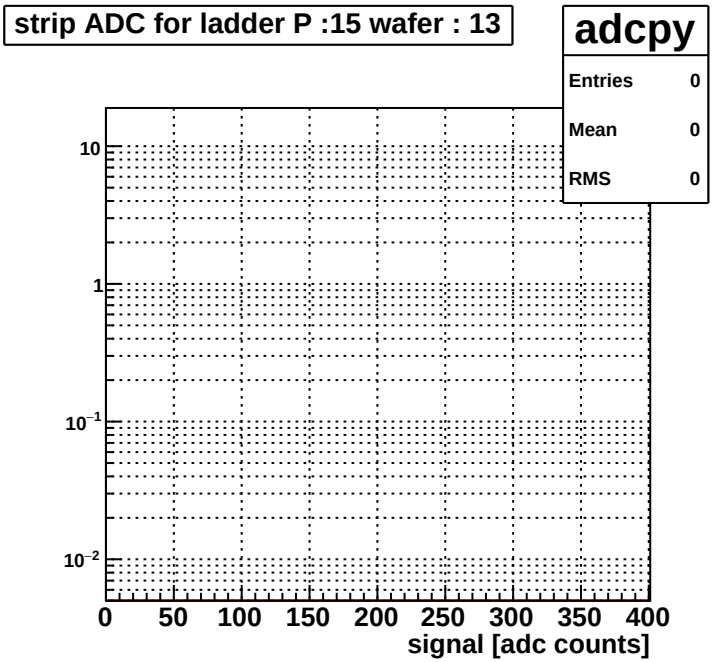

Strip adc vs. channels for ladder N : 15 wafer : 12
stripADCCHANsideN235
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

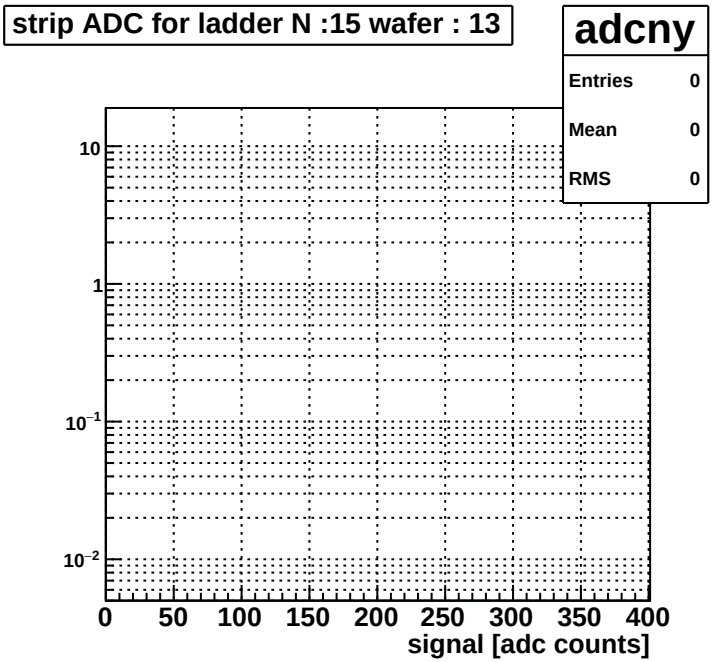

strip ADC for ladder P :15 wafer : 12
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

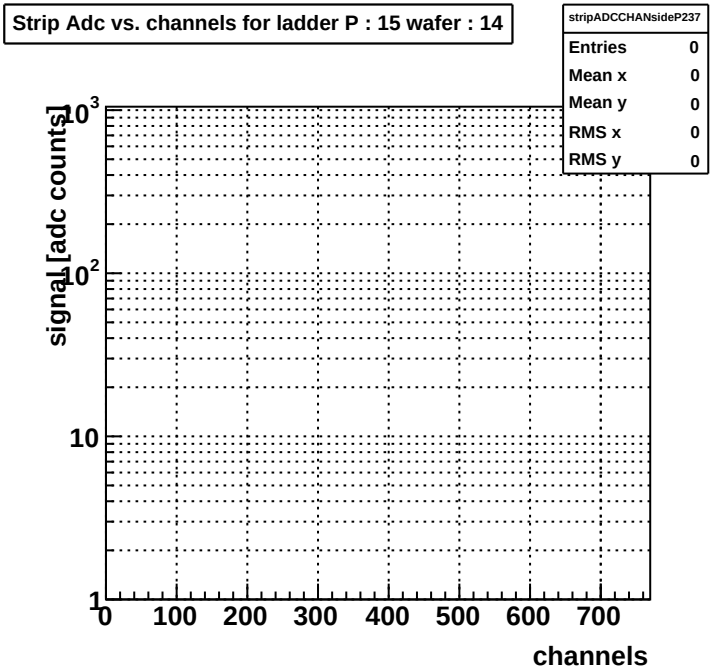

strip ADC for ladder N :15 wafer : 12
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

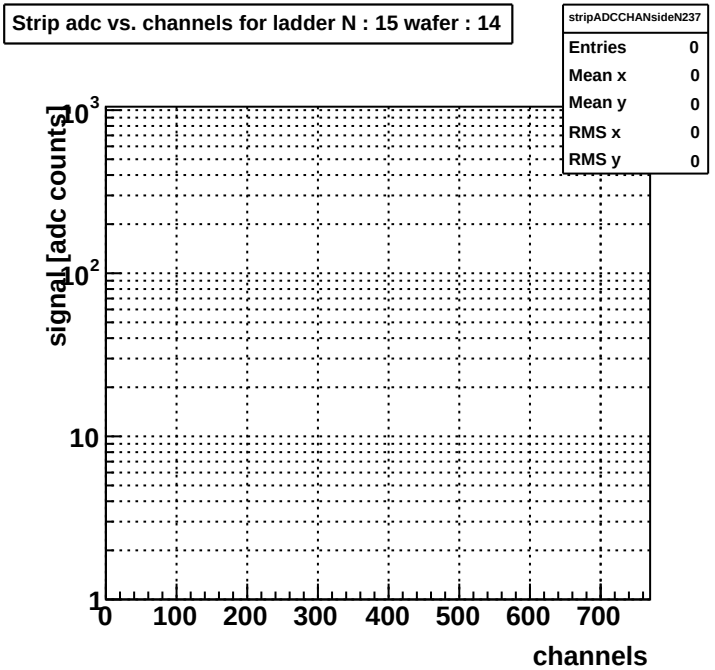

Strip Adc vs. channels for ladder P : 15 wafer : 13
stripADCCHANsideP236
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

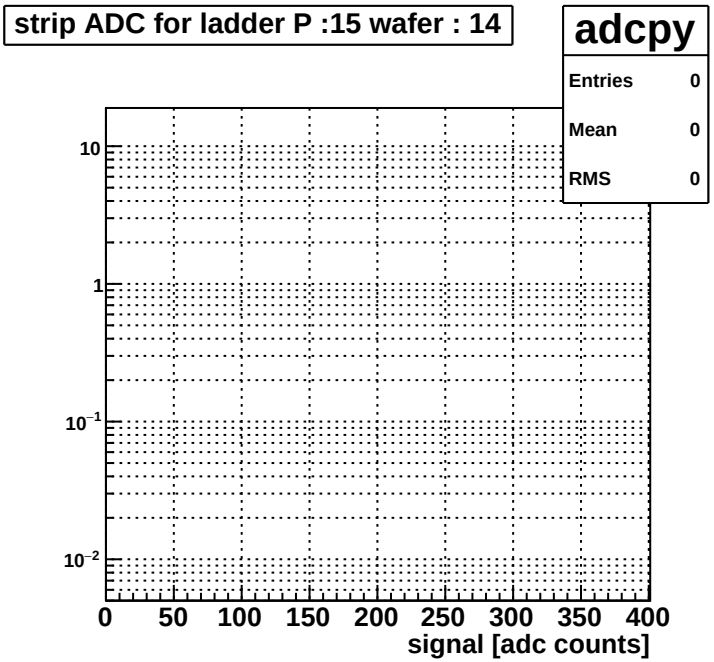

Strip adc vs. channels for ladder N : 15 wafer : 13
stripADCCHANsideN236
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

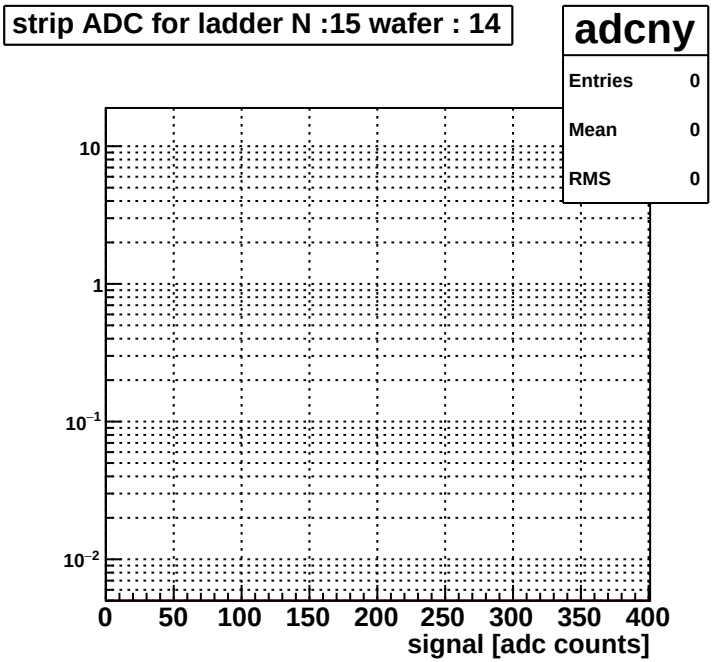

strip ADC for ladder P :15 wafer : 13
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

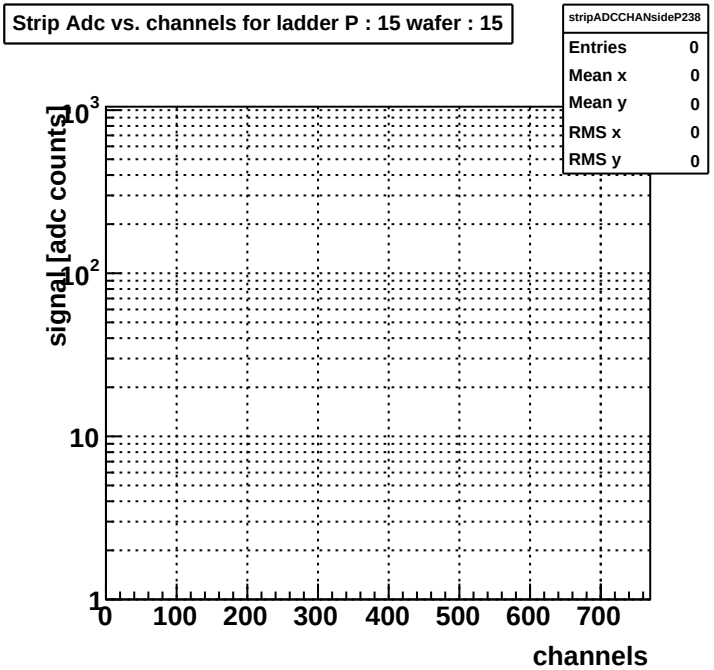

strip ADC for ladder N :15 wafer : 13
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

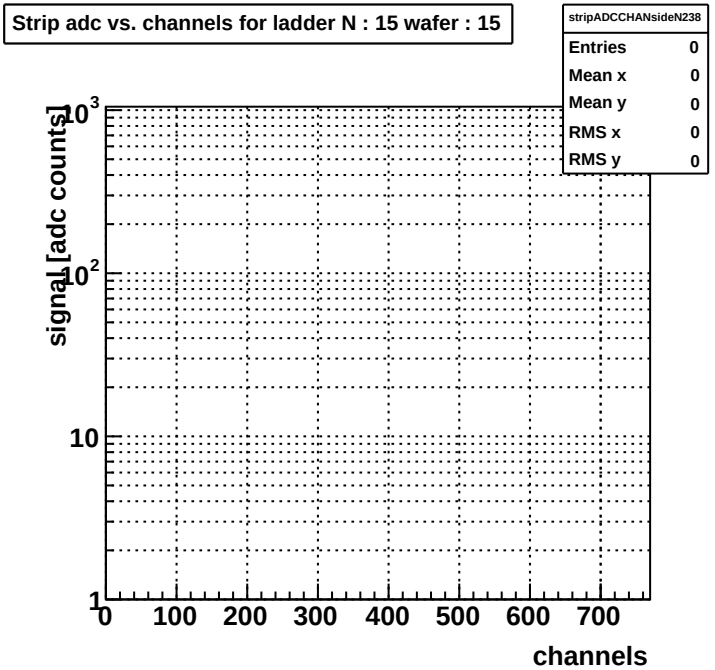

Strip Adc vs. channels for ladder P : 15 wafer : 14
stripADCCHANsideP237
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

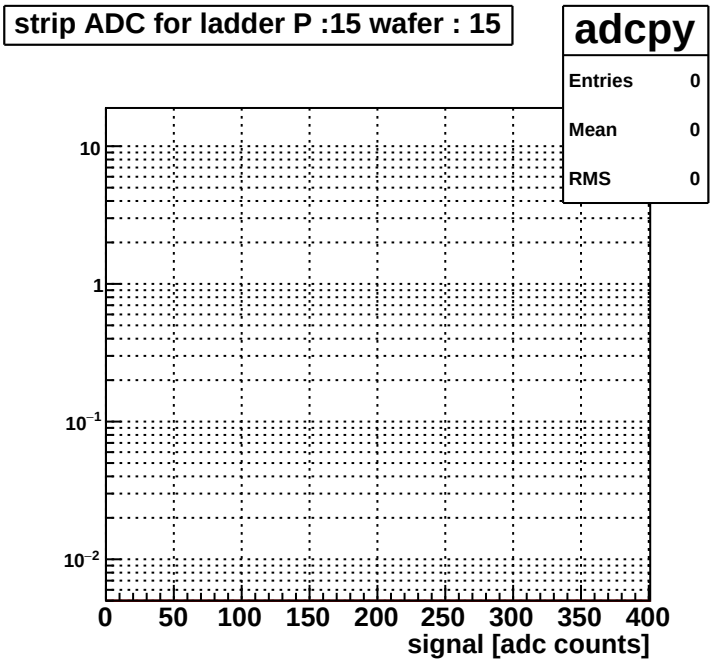

Strip adc vs. channels for ladder N : 15 wafer : 14
stripADCCHANsideN237
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

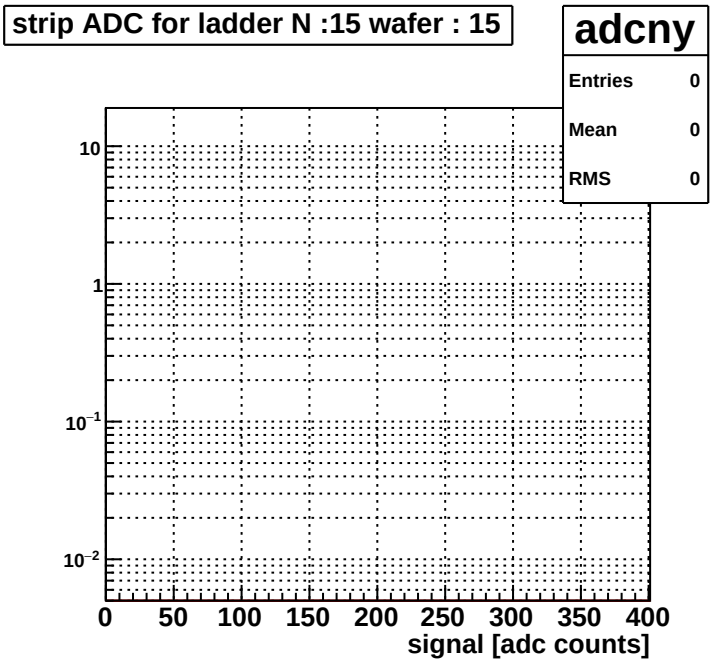

strip ADC for ladder P :15 wafer : 14
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

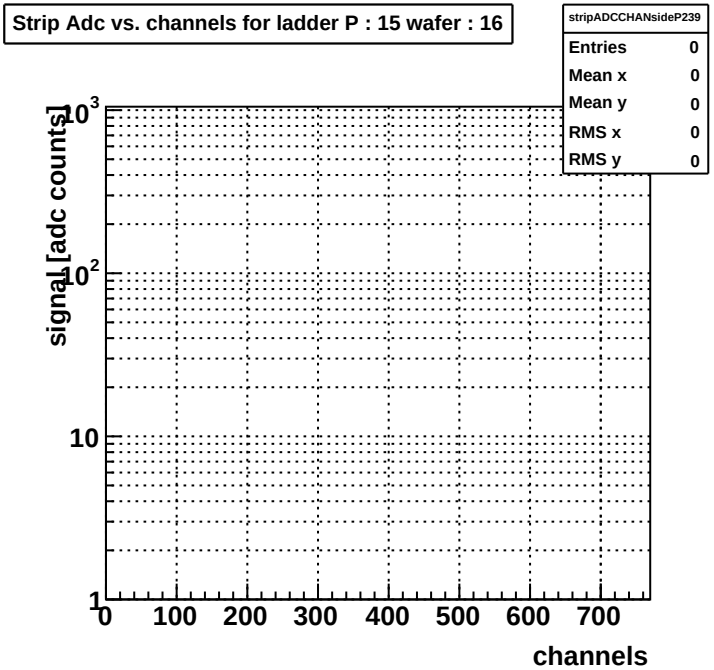

strip ADC for ladder N :15 wafer : 14
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

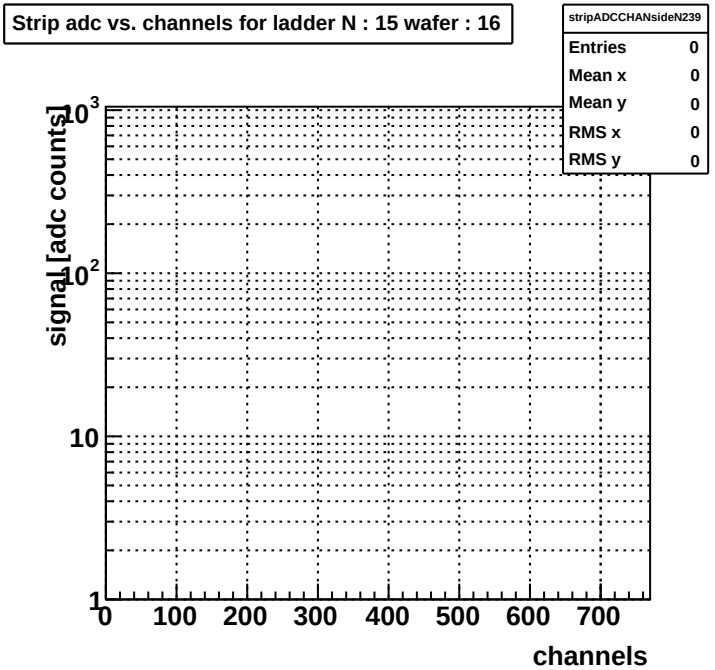

Strip Adc vs. channels for ladder P : 15 wafer : 15
stripADCCHANsideP238
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

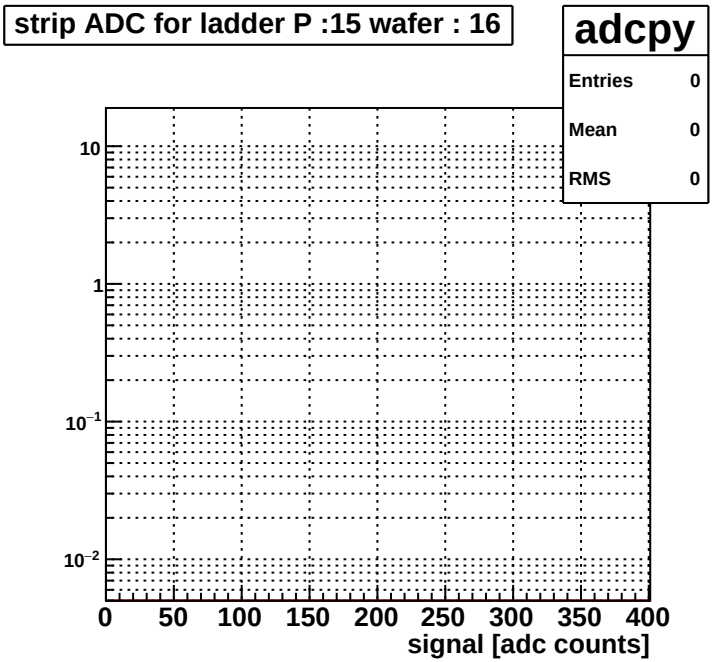

Strip adc vs. channels for ladder N : 15 wafer : 15
stripADCCHANsideN238
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

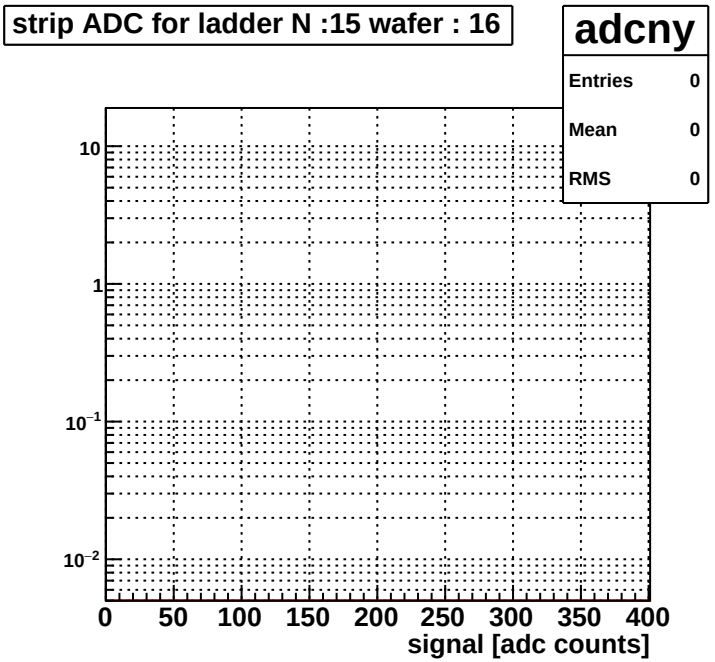

strip ADC for ladder P :15 wafer : 15
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

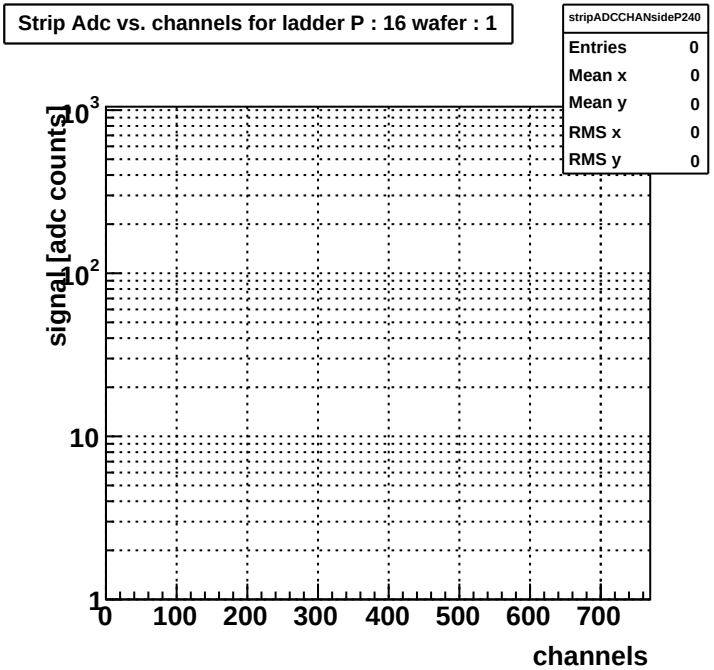

strip ADC for ladder N :15 wafer : 15
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

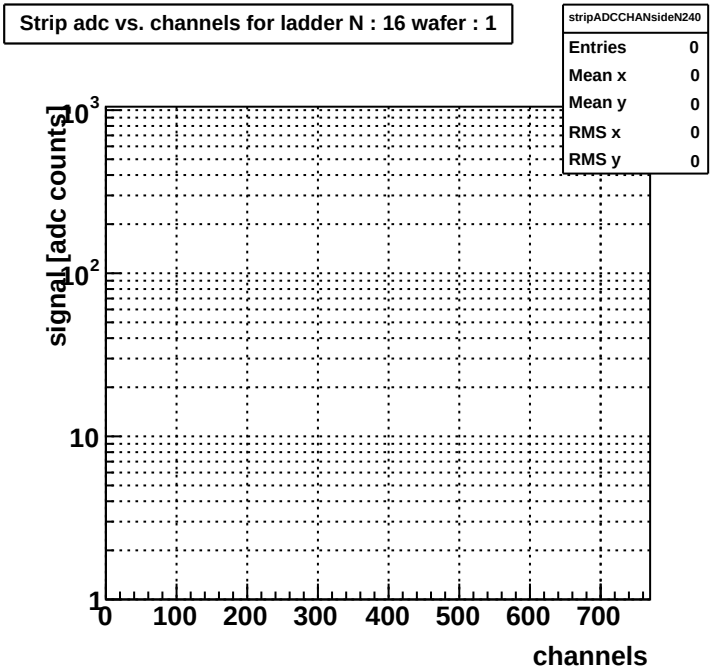

Strip Adc vs. channels for ladder P : 15 wafer : 16
stripADCCHANsideP239
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

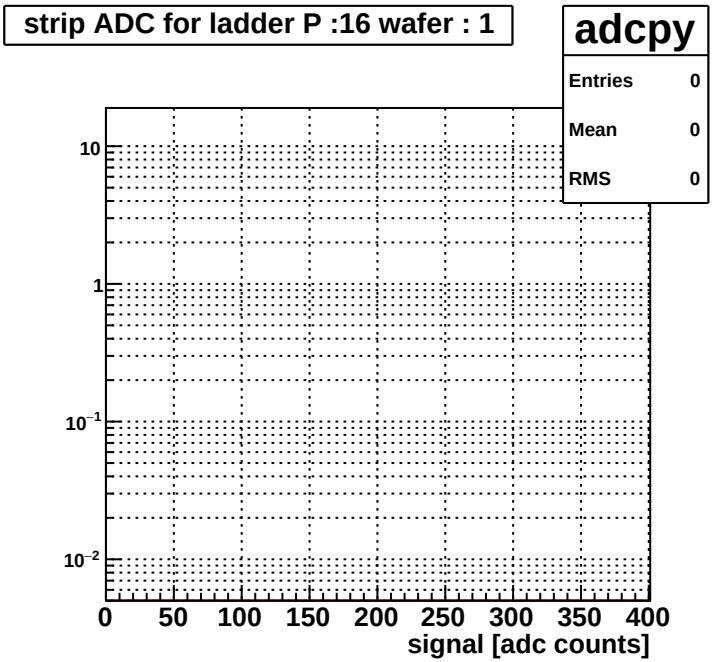

Strip adc vs. channels for ladder N : 15 wafer : 16
stripADCCHANsideN239
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

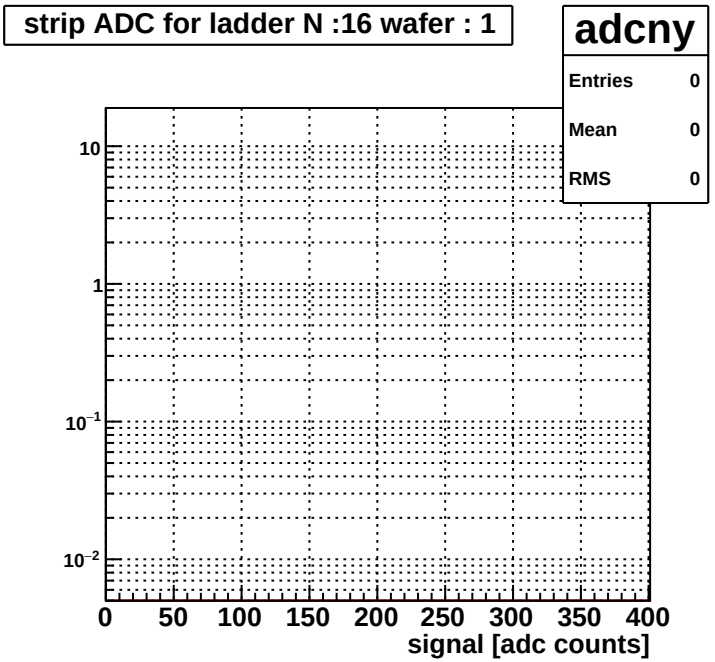

strip ADC for ladder P :15 wafer : 16
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

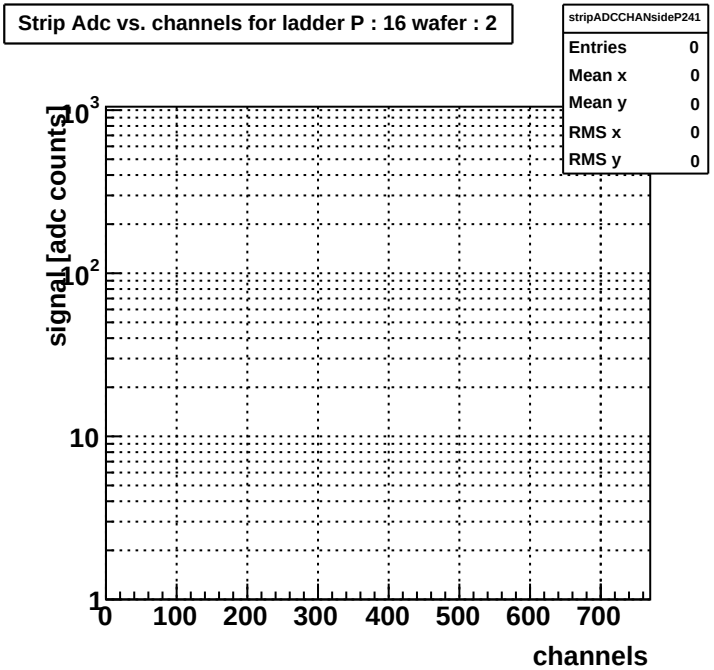

strip ADC for ladder N :15 wafer : 16
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

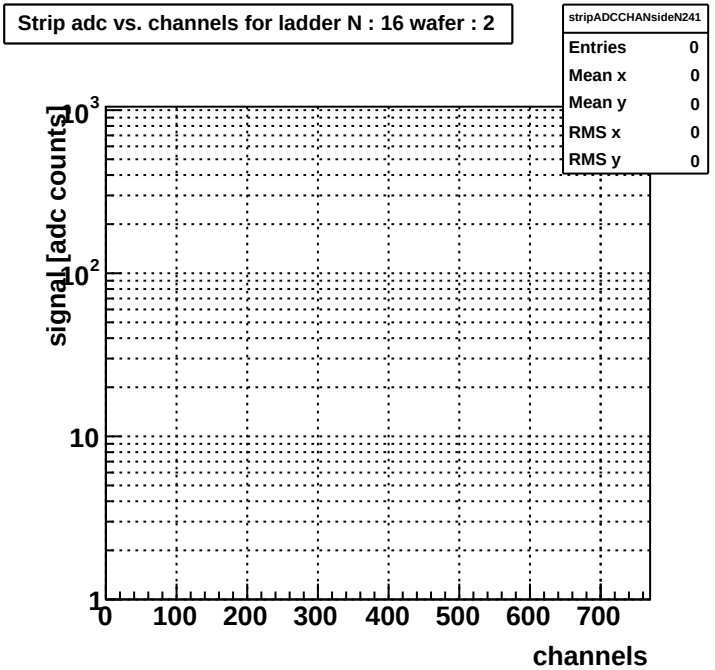

Strip Adc vs. channels for ladder P : 16 wafer : 1
stripADCCHANsideP240
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

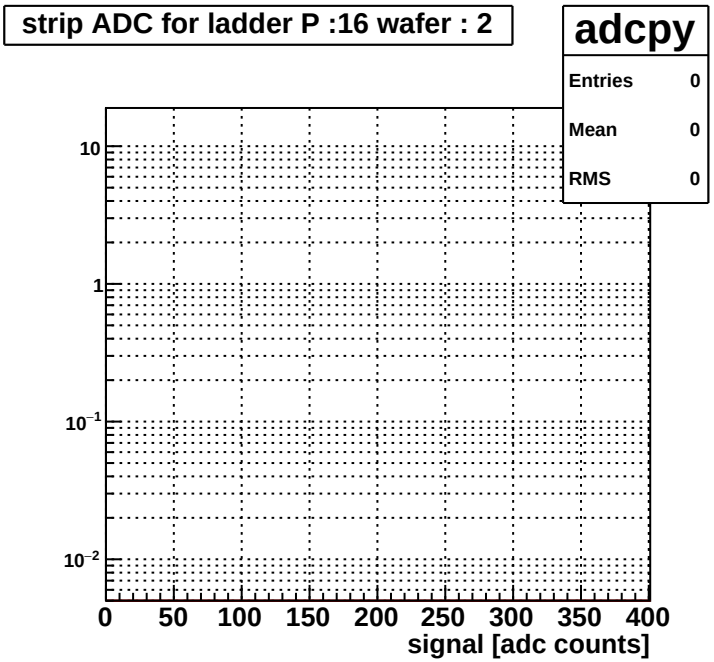

Strip adc vs. channels for ladder N : 16 wafer : 1
stripADCCHANsideN240
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

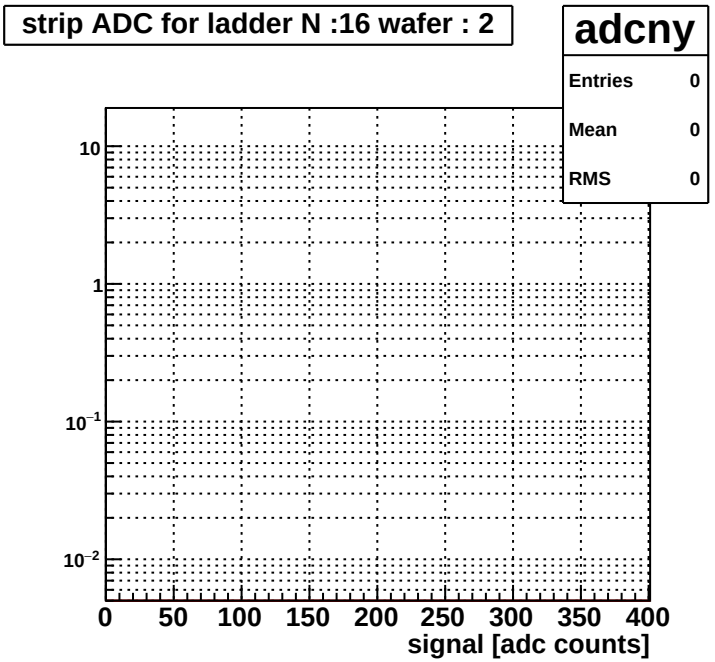

strip ADC for ladder P :16 wafer : 1
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

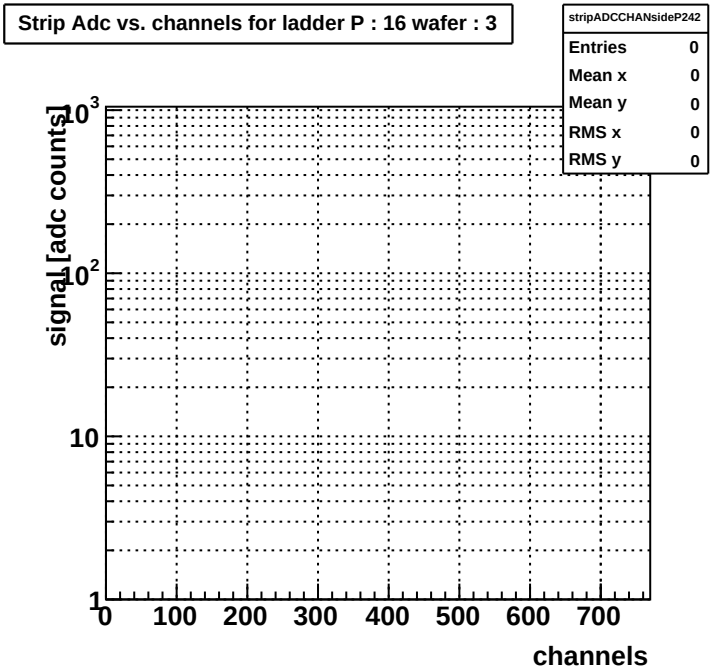

strip ADC for ladder N :16 wafer : 1
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

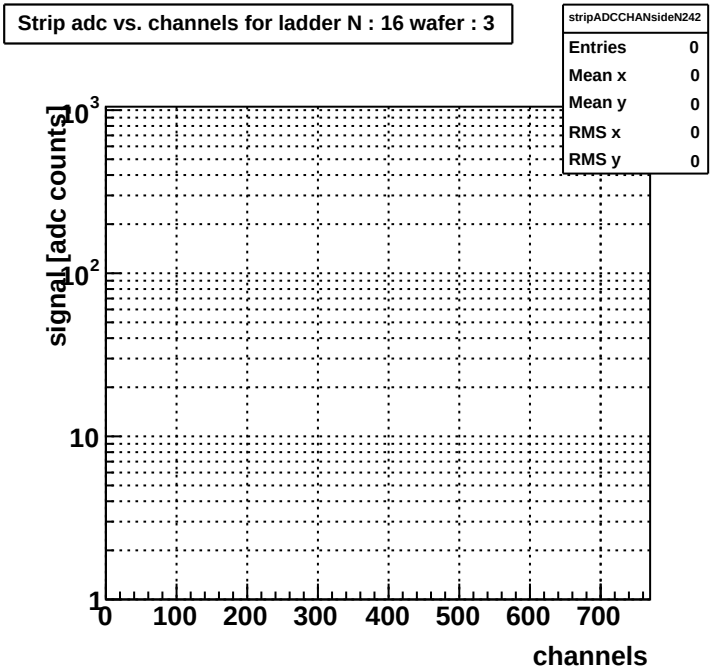

Strip Adc vs. channels for ladder P : 16 wafer : 2
stripADCCHANsideP241
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

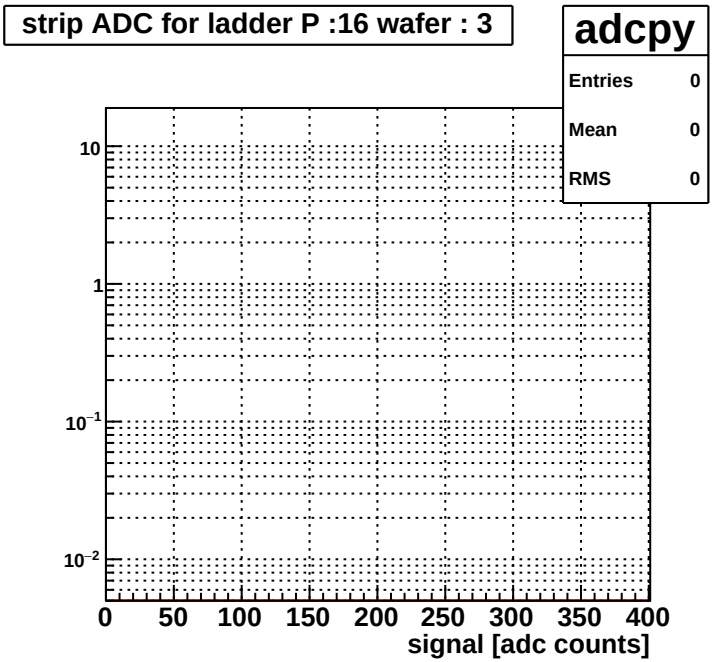

Strip adc vs. channels for ladder N : 16 wafer : 2
stripADCCHANsideN241
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

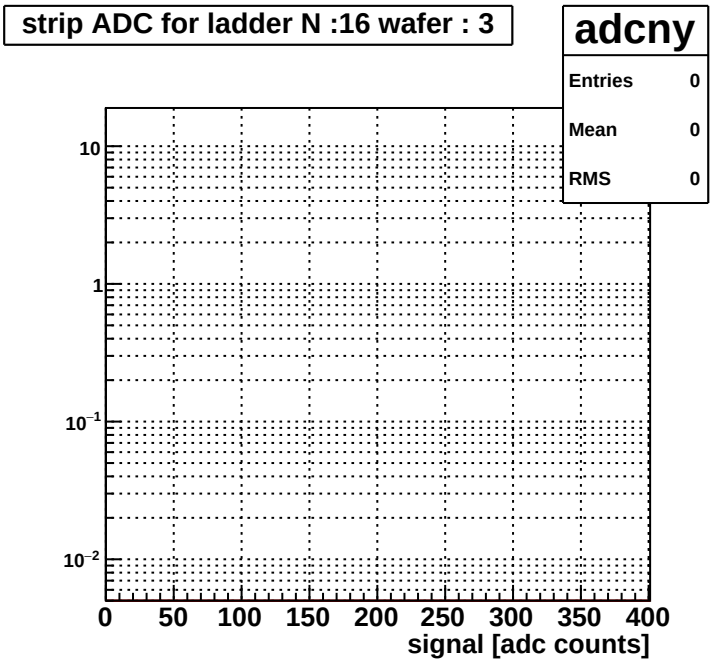

strip ADC for ladder P :16 wafer : 2
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

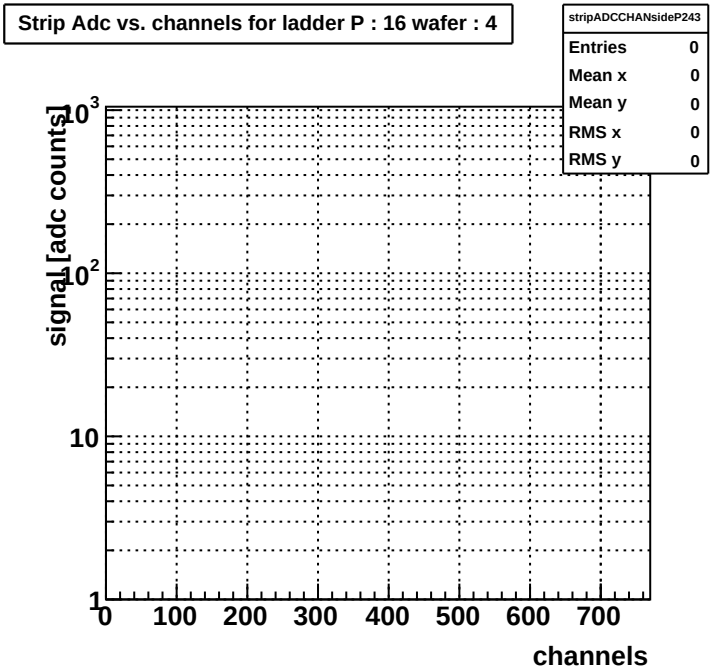

strip ADC for ladder N :16 wafer : 2
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

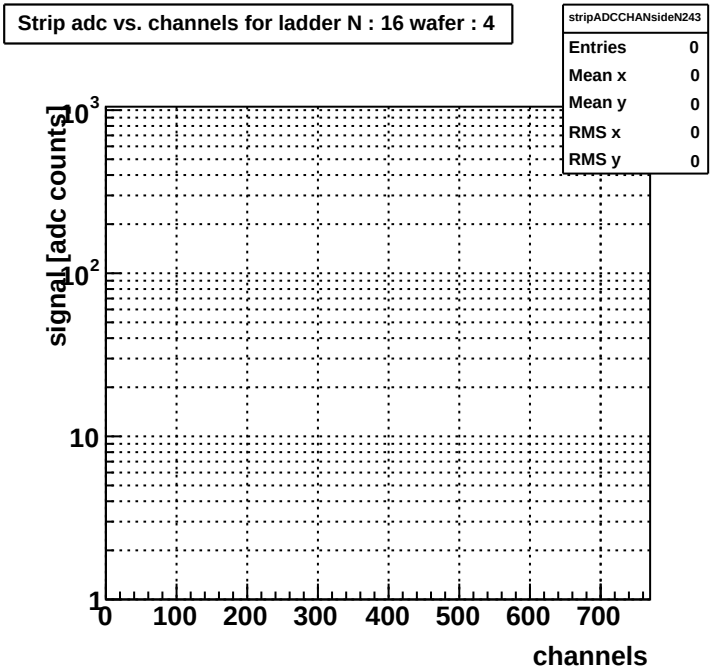

Strip Adc vs. channels for ladder P : 16 wafer : 3
stripADCCHANsideP242
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

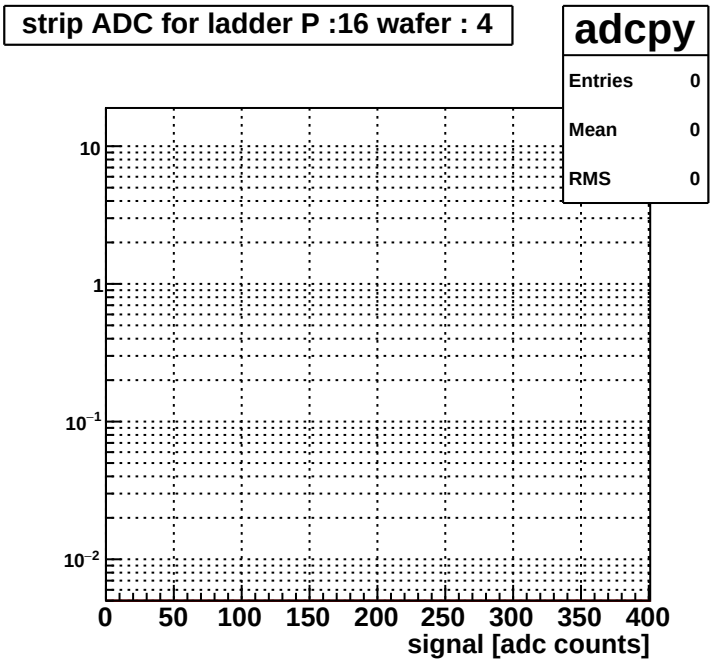

Strip adc vs. channels for ladder N : 16 wafer : 3
stripADCCHANsideN242
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

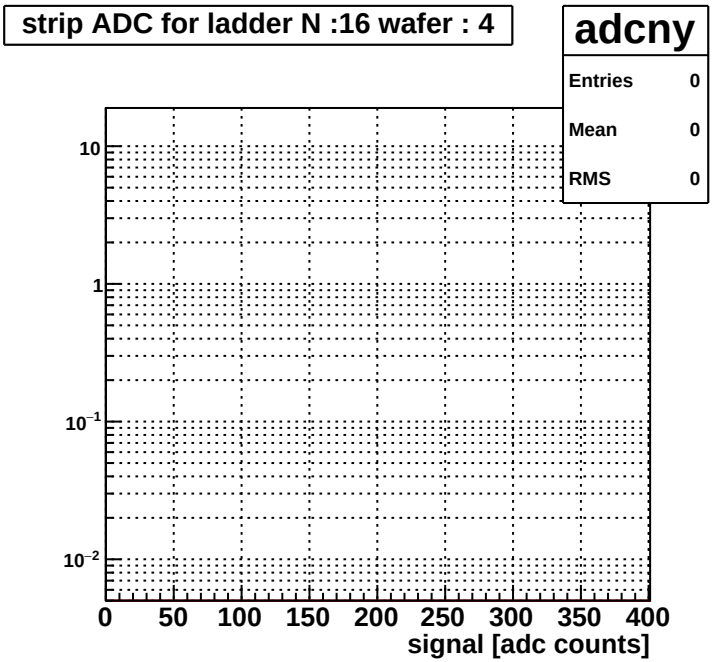

strip ADC for ladder P :16 wafer : 3
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

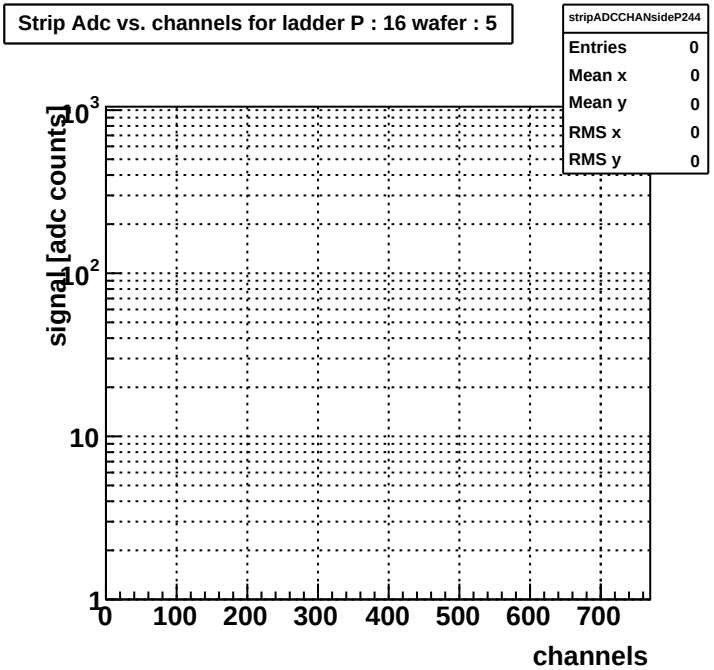

strip ADC for ladder N :16 wafer : 3
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

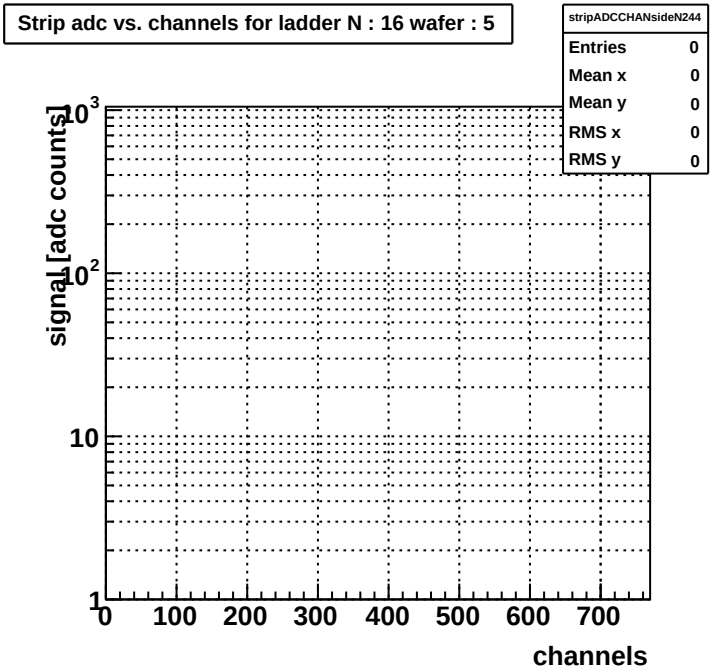

Strip Adc vs. channels for ladder P : 16 wafer : 4
stripADCCHANsideP243
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

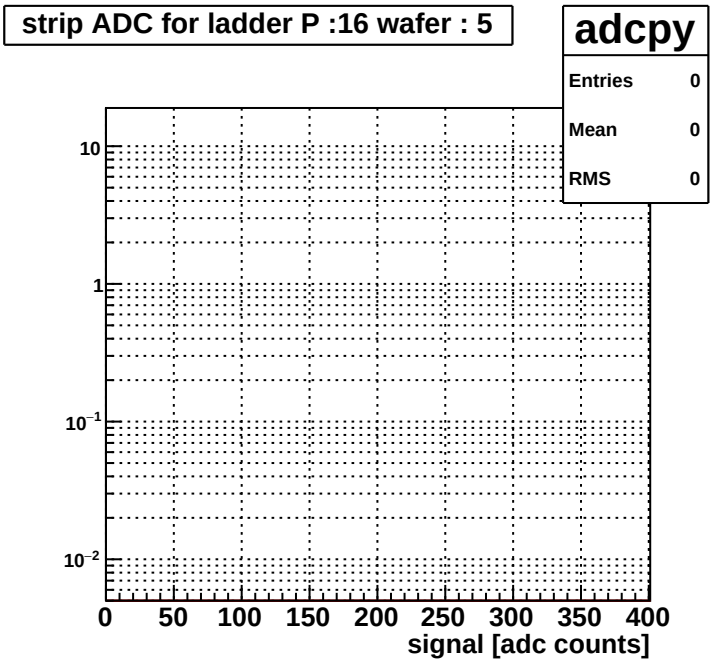

Strip adc vs. channels for ladder N : 16 wafer : 4
stripADCCHANsideN243
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

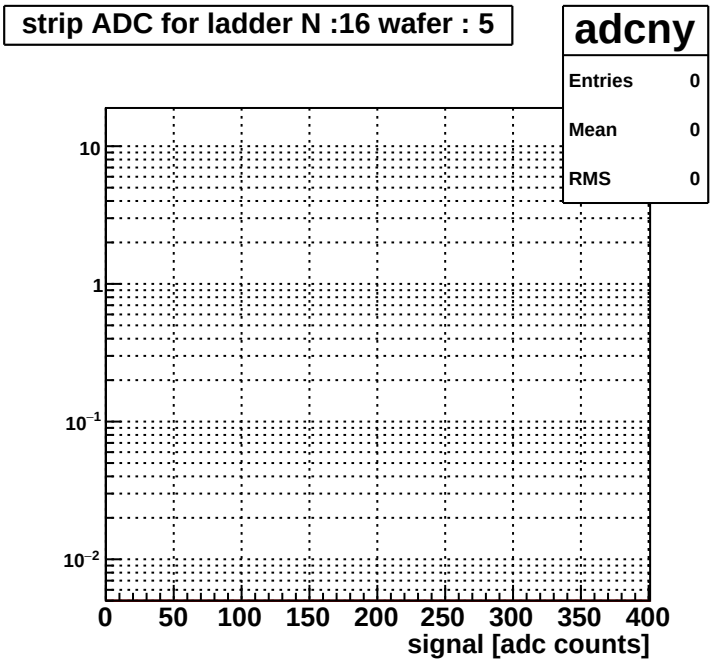

strip ADC for ladder P :16 wafer : 4
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

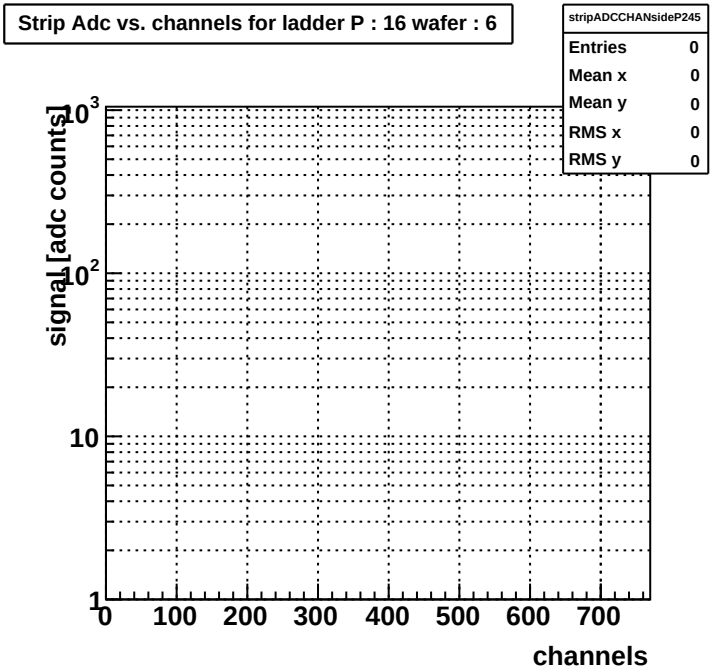

strip ADC for ladder N :16 wafer : 4
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

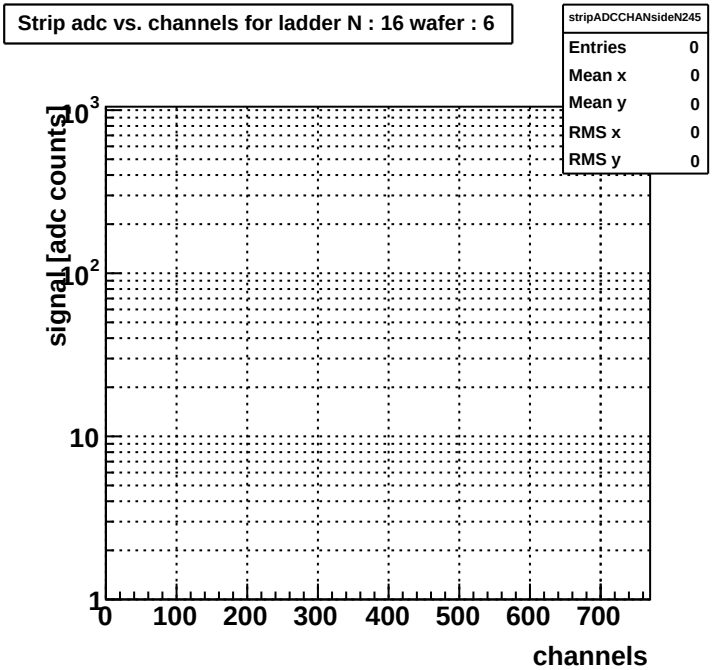

Strip Adc vs. channels for ladder P : 16 wafer : 5
stripADCCHANsideP244
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

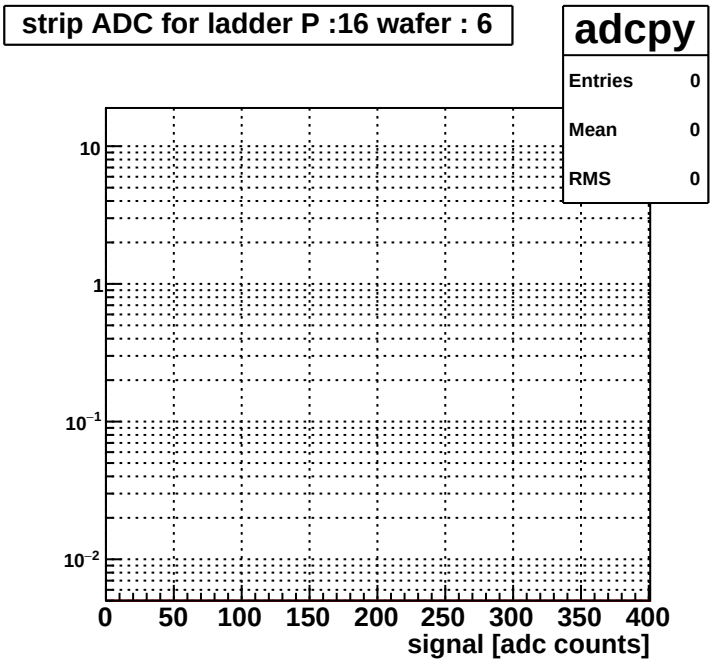

Strip adc vs. channels for ladder N : 16 wafer : 5
stripADCCHANsideN244
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

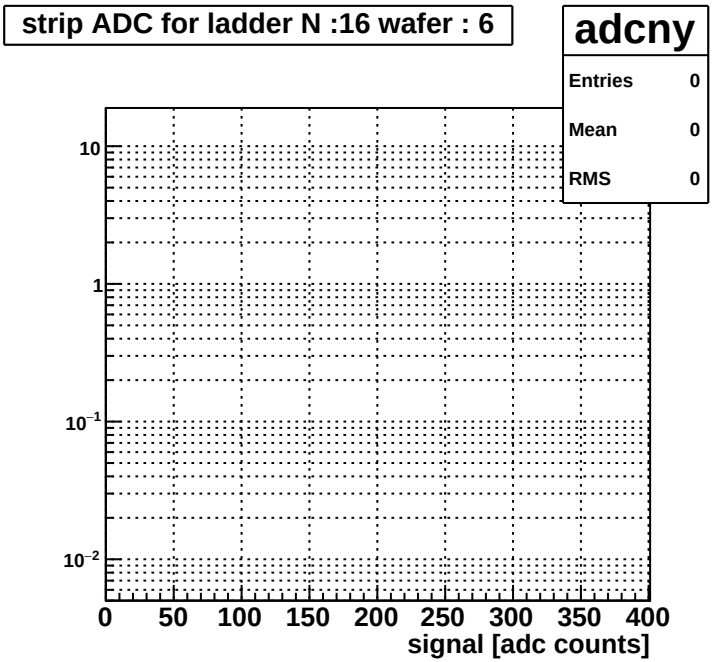

strip ADC for ladder P :16 wafer : 5
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

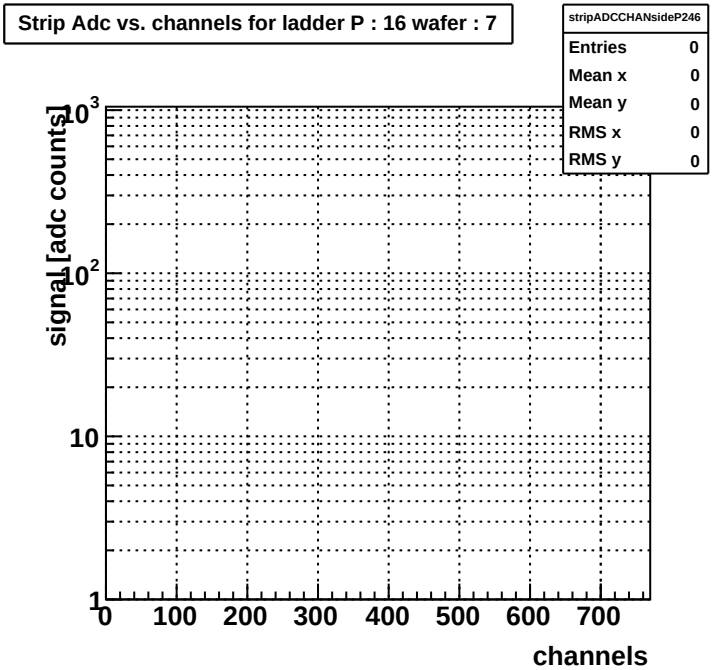

strip ADC for ladder N :16 wafer : 5
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

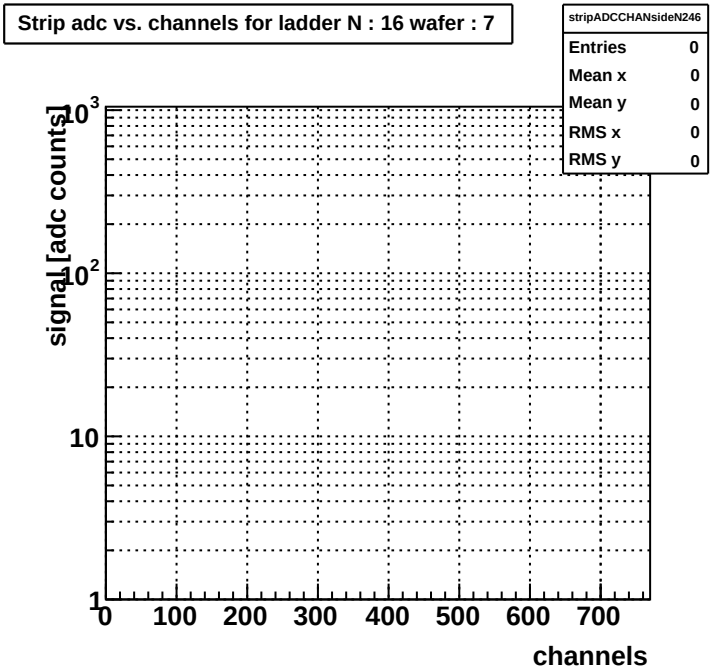

Strip Adc vs. channels for ladder P : 16 wafer : 6
stripADCCHANsideP245
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

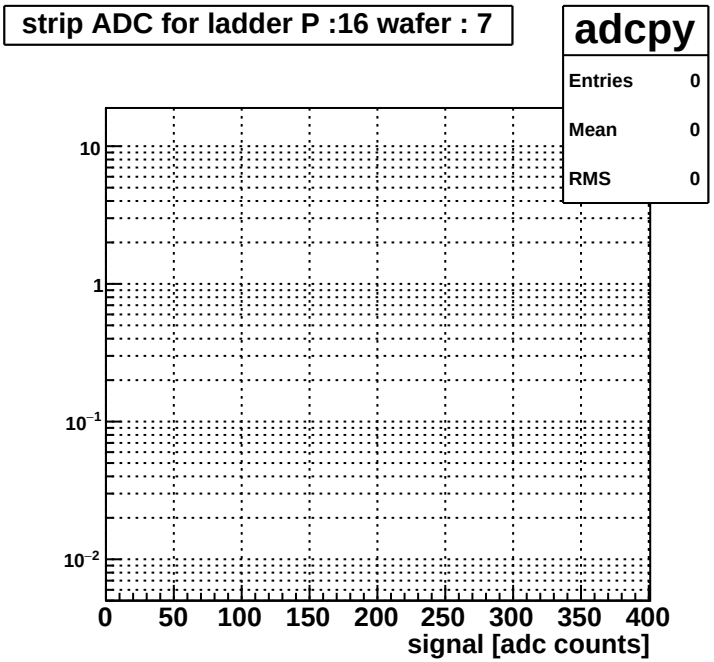

Strip adc vs. channels for ladder N : 16 wafer : 6
stripADCCHANsideN245
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

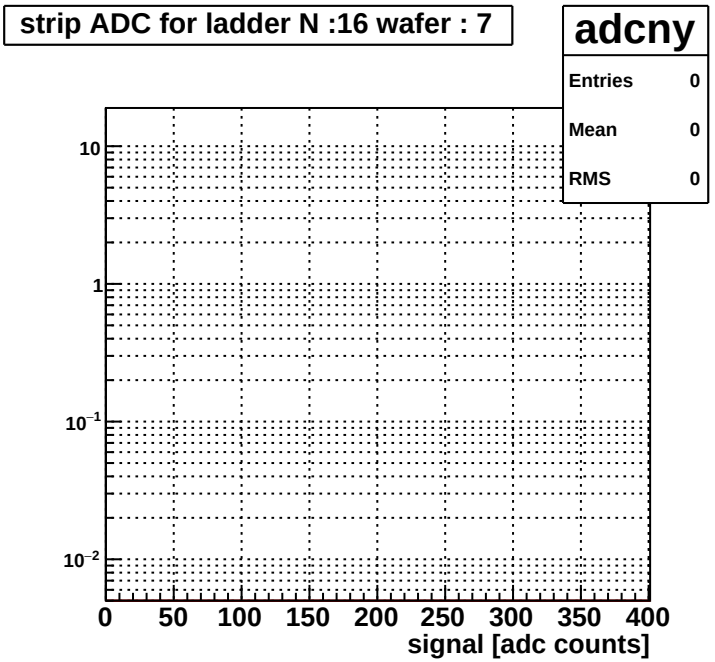

strip ADC for ladder P :16 wafer : 6
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

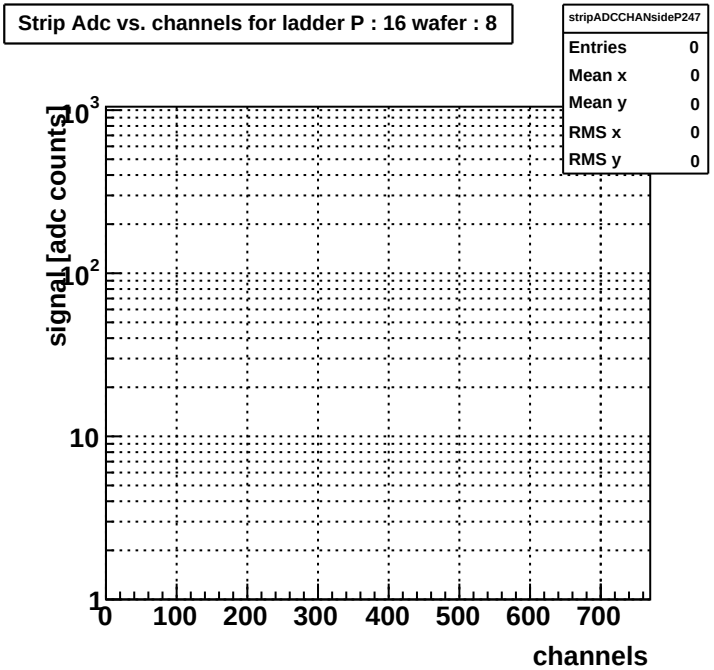

strip ADC for ladder N :16 wafer : 6
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

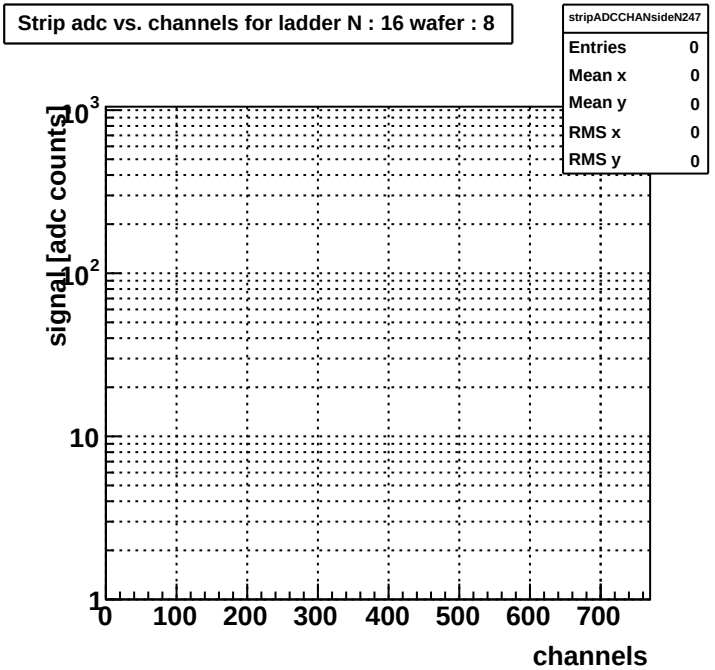

Strip Adc vs. channels for ladder P : 16 wafer : 7
stripADCCHANsideP246
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

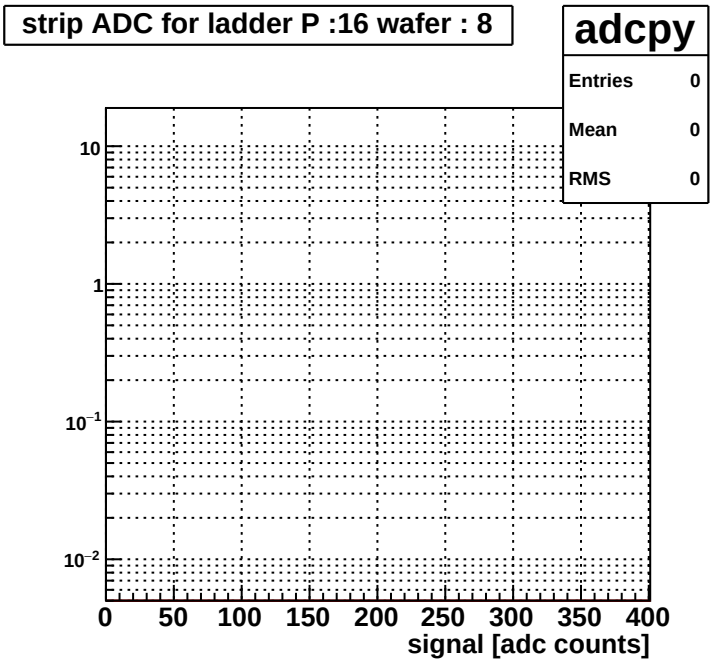

Strip adc vs. channels for ladder N : 16 wafer : 7
stripADCCHANsideN246
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

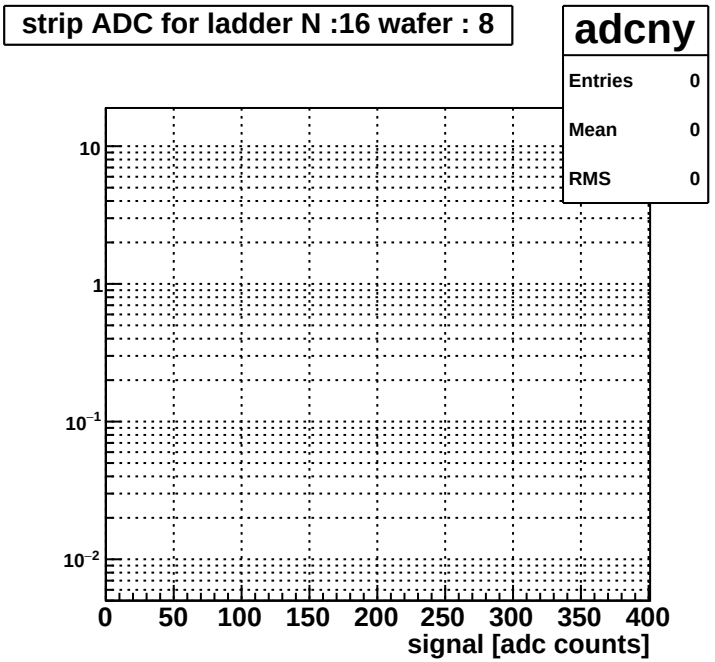

strip ADC for ladder P :16 wafer : 7
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

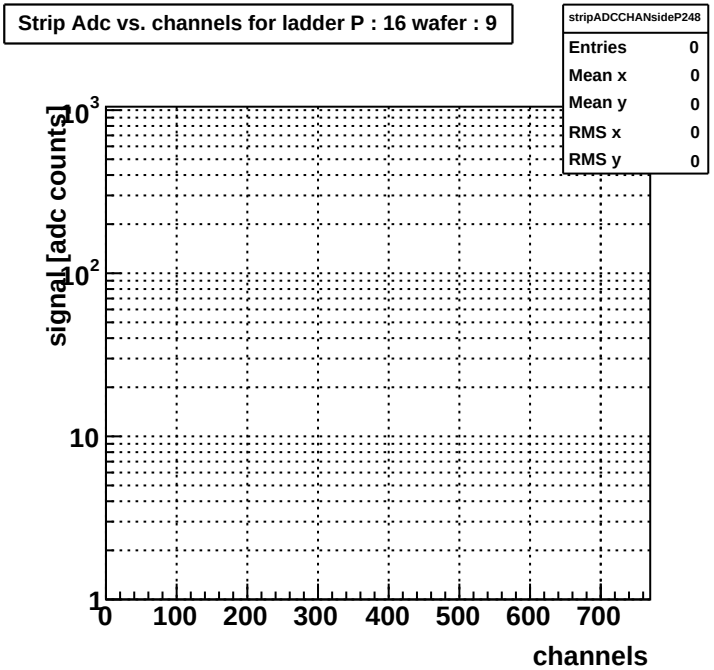

strip ADC for ladder N :16 wafer : 7
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

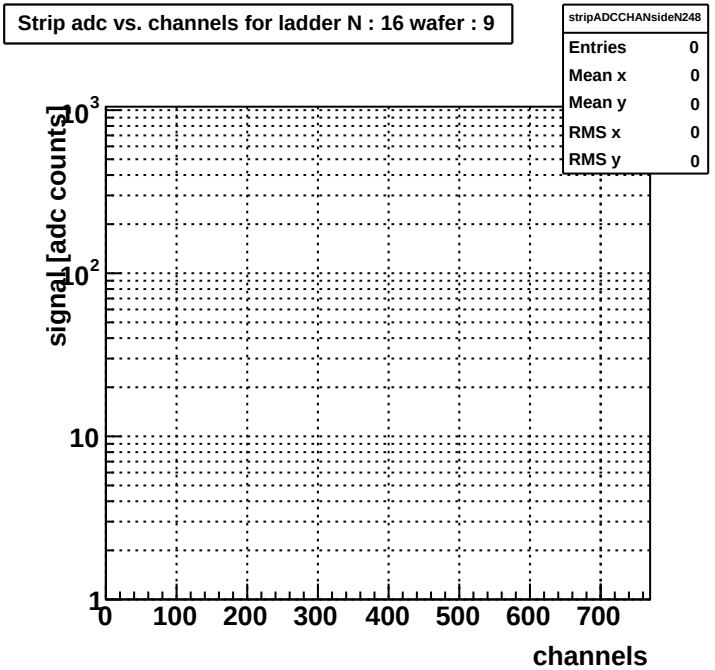

Strip Adc vs. channels for ladder P : 16 wafer : 8
stripADCCHANsideP247
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

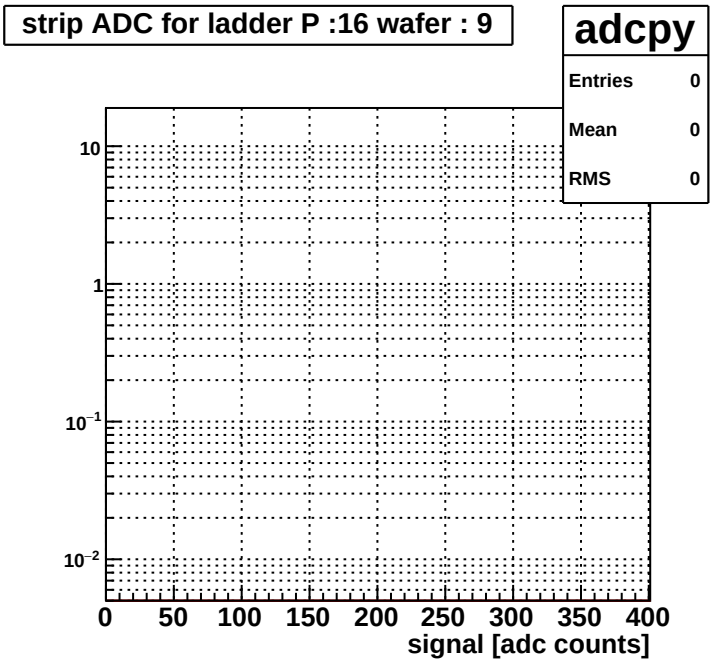

Strip adc vs. channels for ladder N : 16 wafer : 8
stripADCCHANsideN247
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

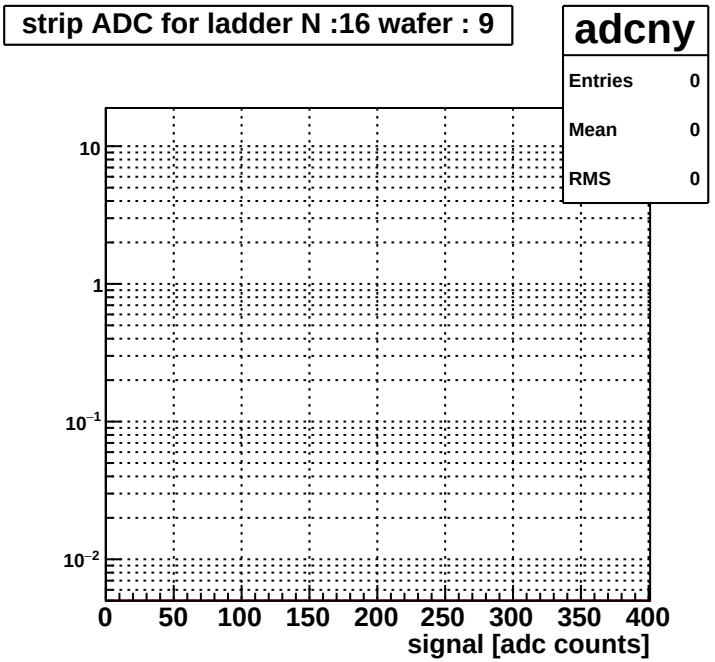

strip ADC for ladder P :16 wafer : 8
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

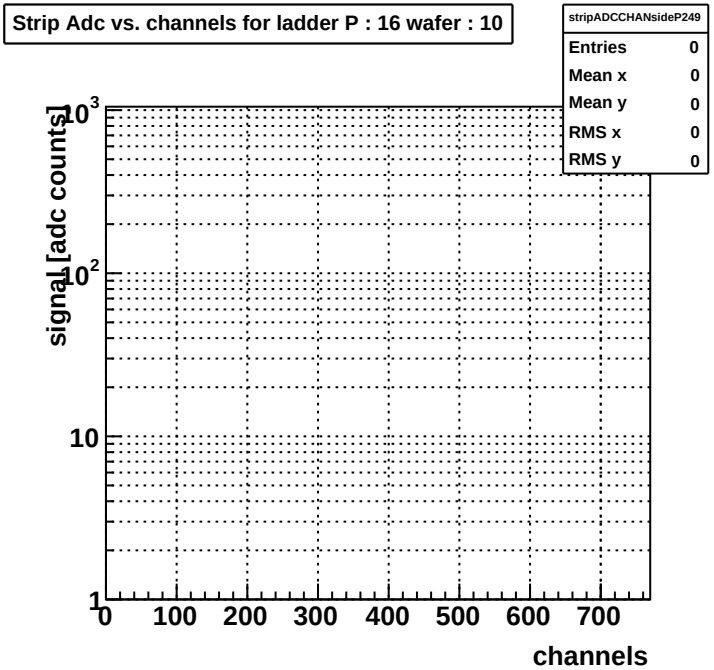

strip ADC for ladder N :16 wafer : 8
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

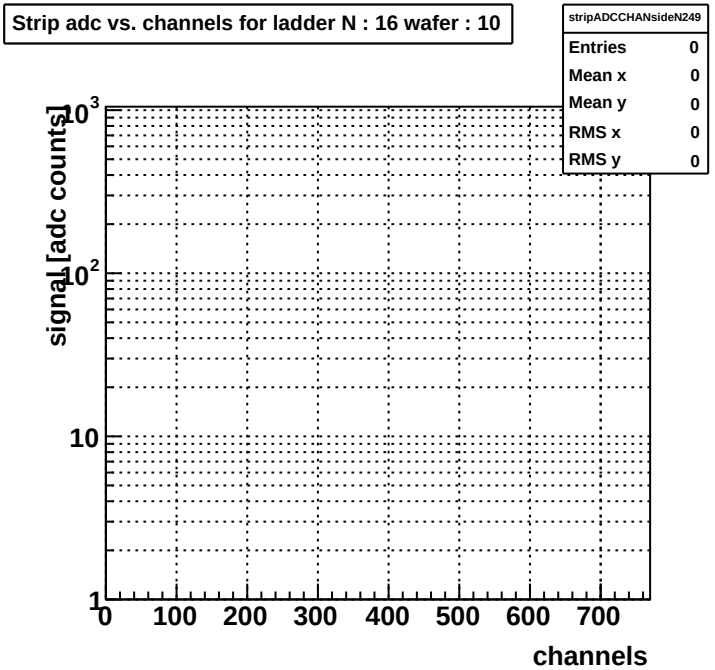

Strip Adc vs. channels for ladder P : 16 wafer : 9
stripADCCHANsideP248
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

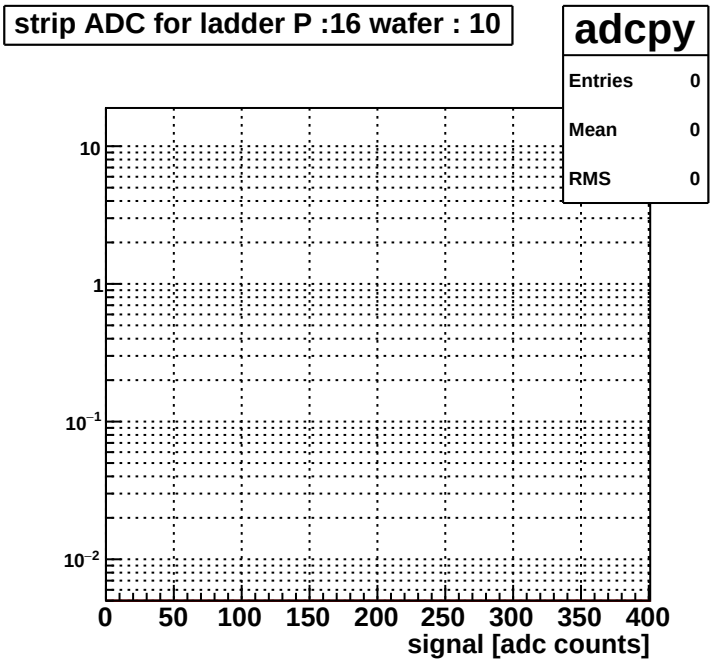

Strip adc vs. channels for ladder N : 16 wafer : 9
stripADCCHANsideN248
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

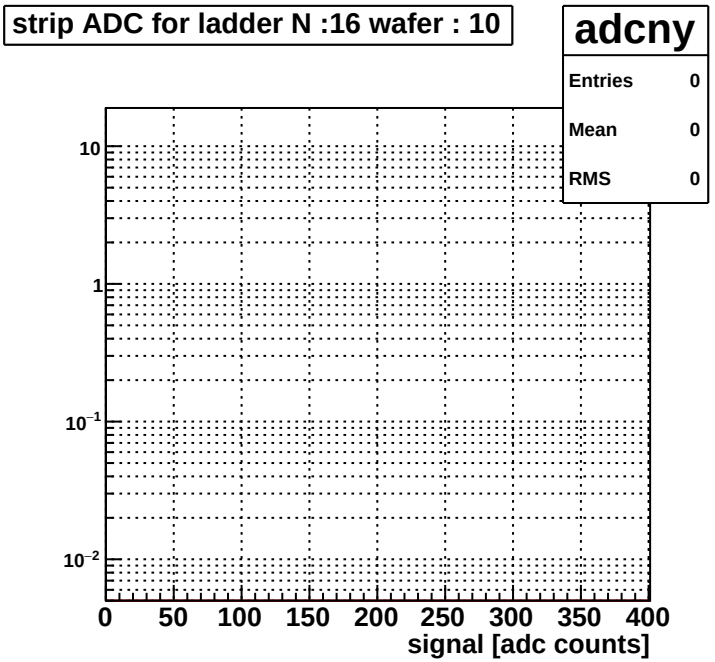

strip ADC for ladder P :16 wafer : 9
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

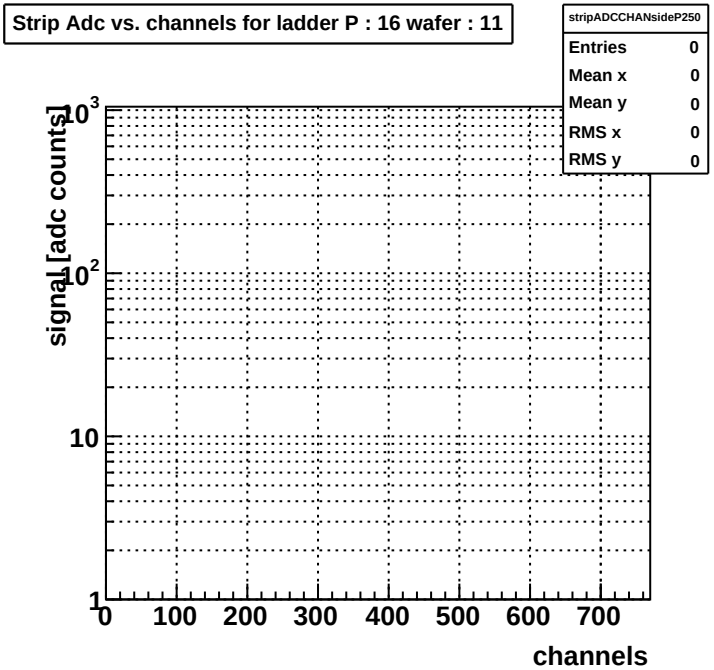

strip ADC for ladder N :16 wafer : 9
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

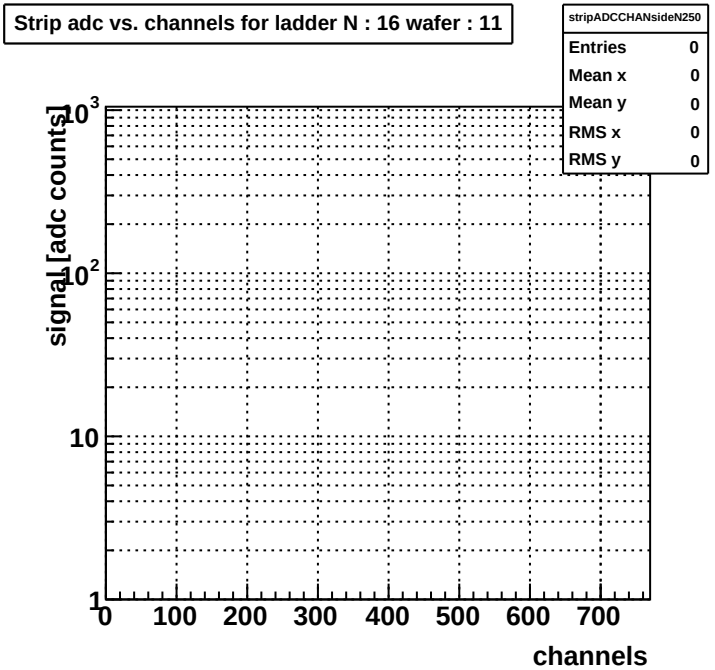

Strip Adc vs. channels for ladder P : 16 wafer : 10
stripADCCHANsideP249
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

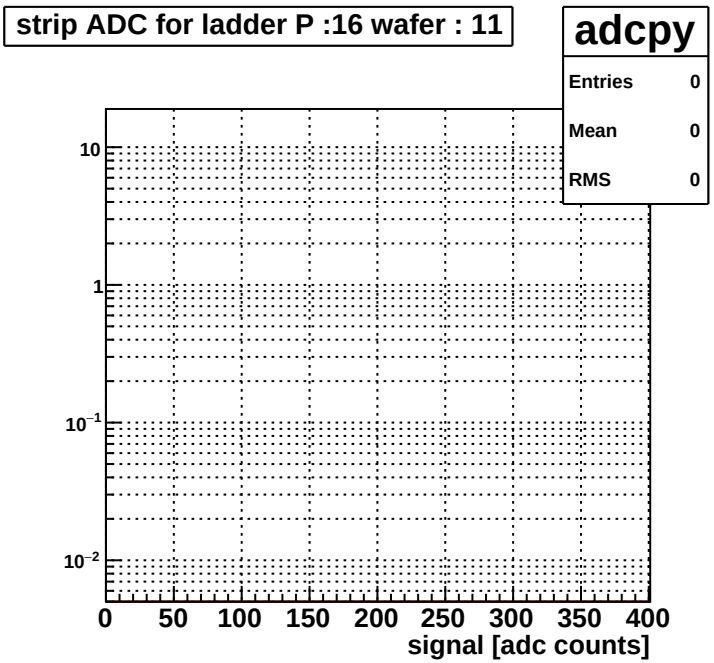

Strip adc vs. channels for ladder N : 16 wafer : 10
stripADCCHANsideN249
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

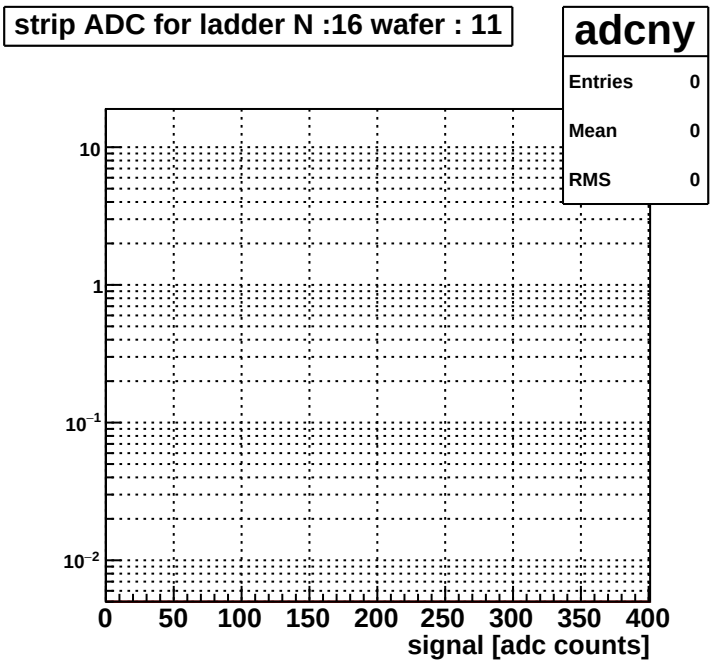

strip ADC for ladder P :16 wafer : 10
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

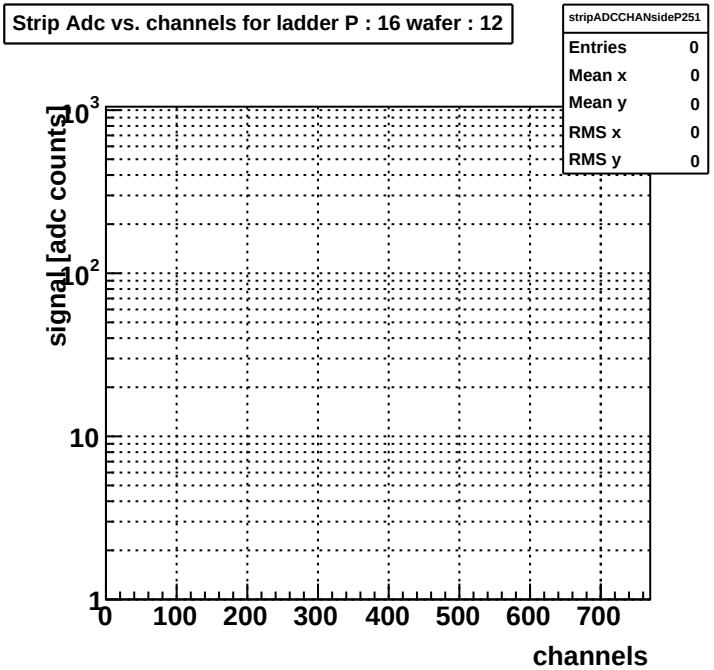

strip ADC for ladder N :16 wafer : 10
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

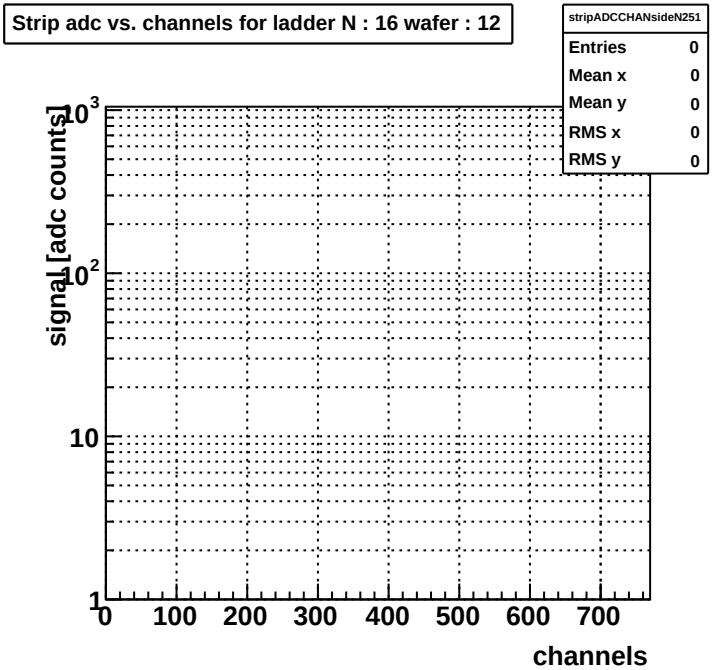

Strip Adc vs. channels for ladder P : 16 wafer : 11
stripADCCHANsideP250
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

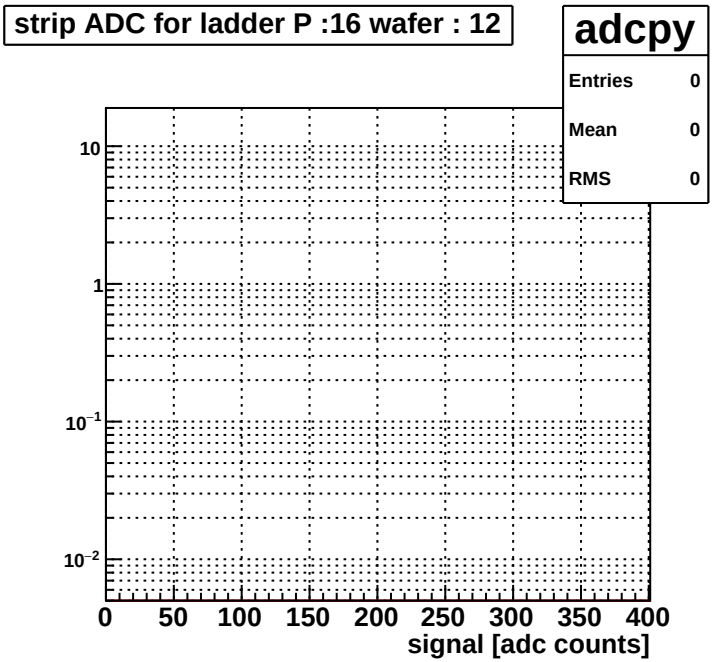

Strip adc vs. channels for ladder N : 16 wafer : 11
stripADCCHANsideN250
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

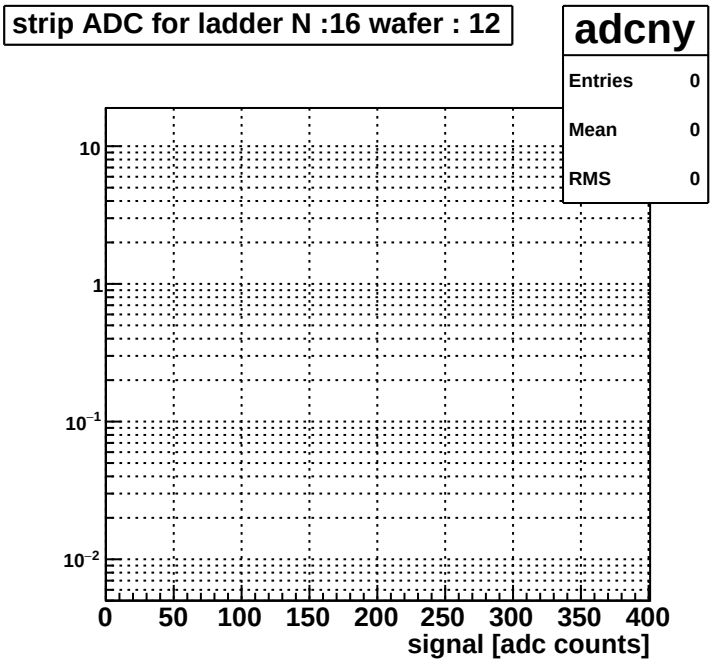

strip ADC for ladder P :16 wafer : 11
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

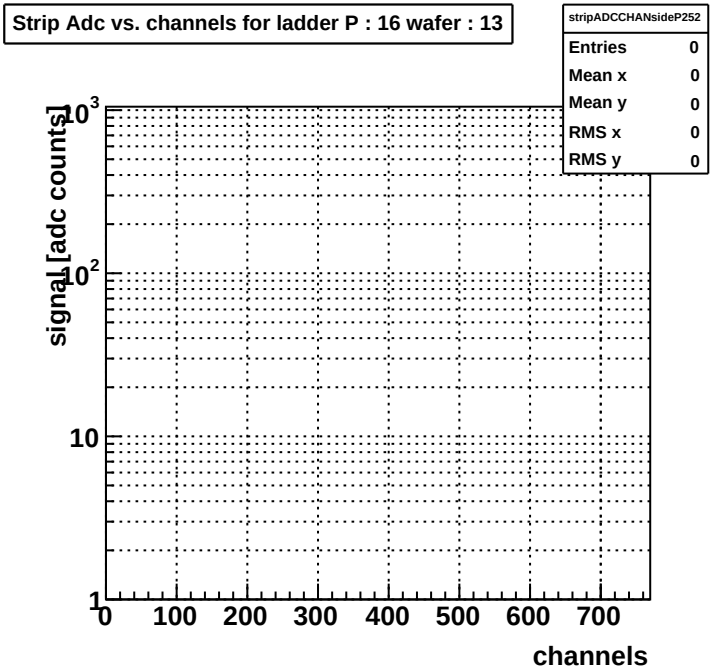

strip ADC for ladder N :16 wafer : 11
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

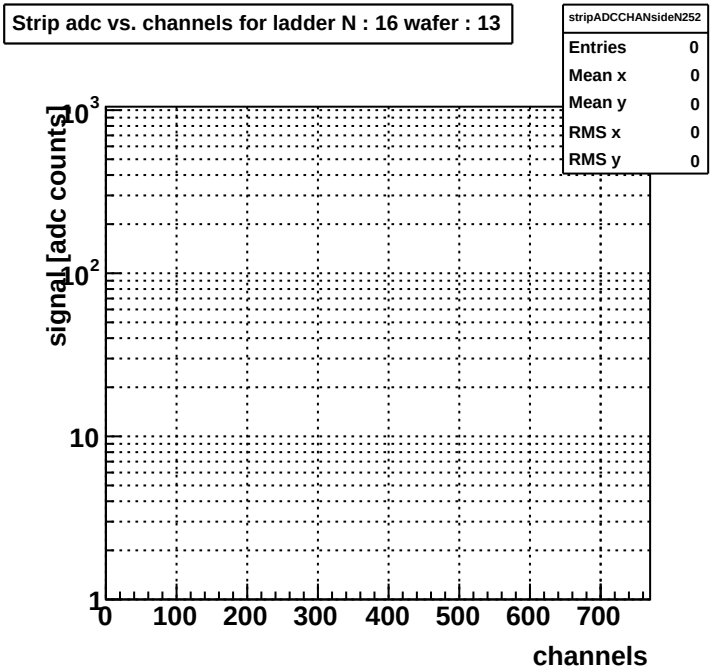

Strip Adc vs. channels for ladder P : 16 wafer : 12
stripADCCHANsideP251
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

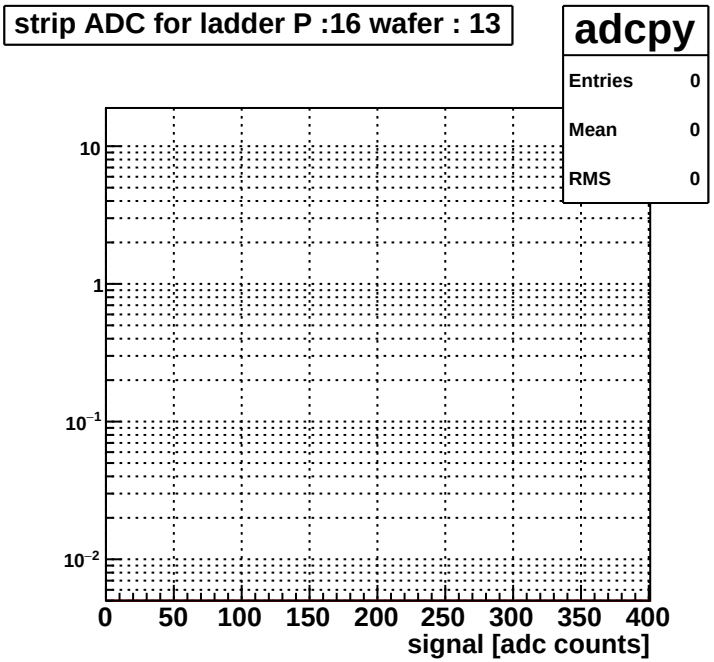

Strip adc vs. channels for ladder N : 16 wafer : 12
stripADCCHANsideN251
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

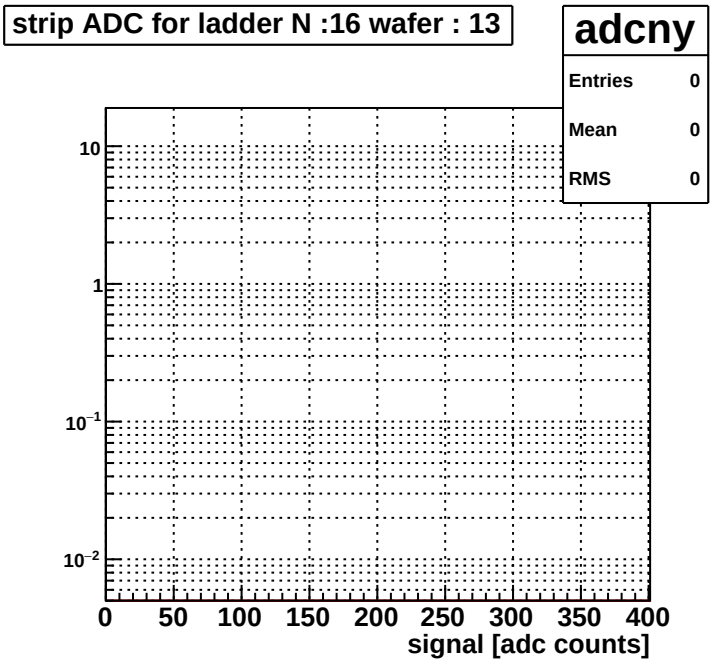

strip ADC for ladder P :16 wafer : 12
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

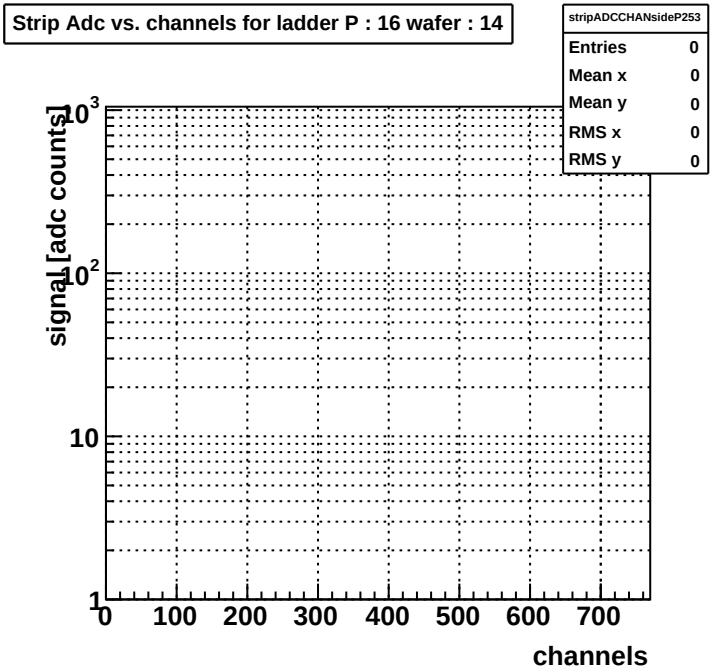

strip ADC for ladder N :16 wafer : 12
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

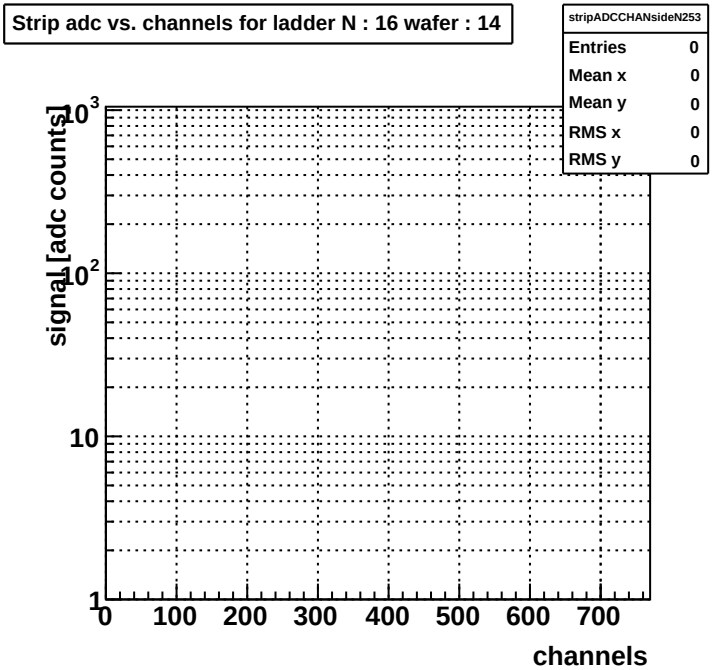

Strip Adc vs. channels for ladder P : 16 wafer : 13
stripADCCHANsideP252
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

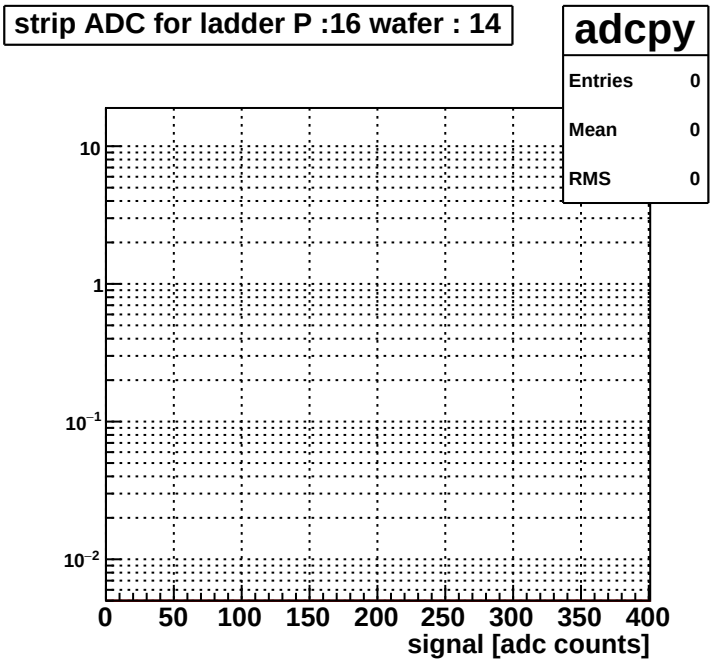

Strip adc vs. channels for ladder N : 16 wafer : 13
stripADCCHANsideN252
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

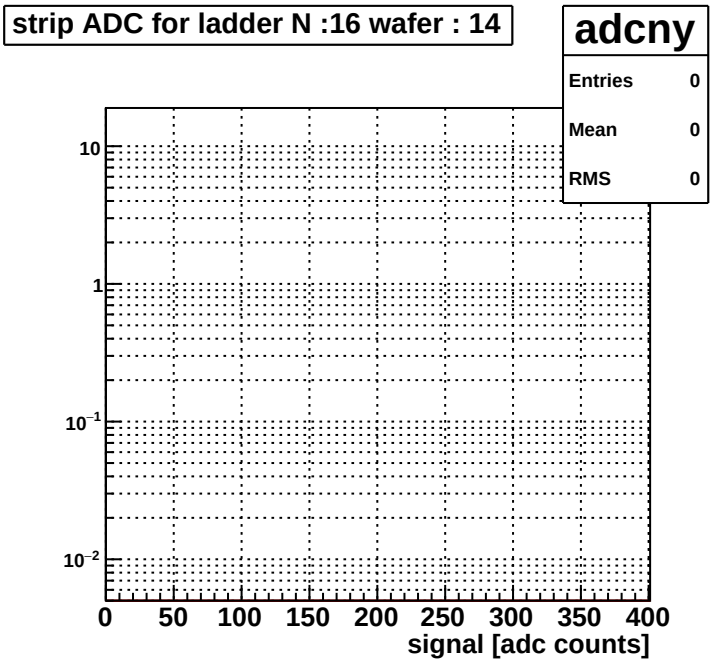

strip ADC for ladder P :16 wafer : 13
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

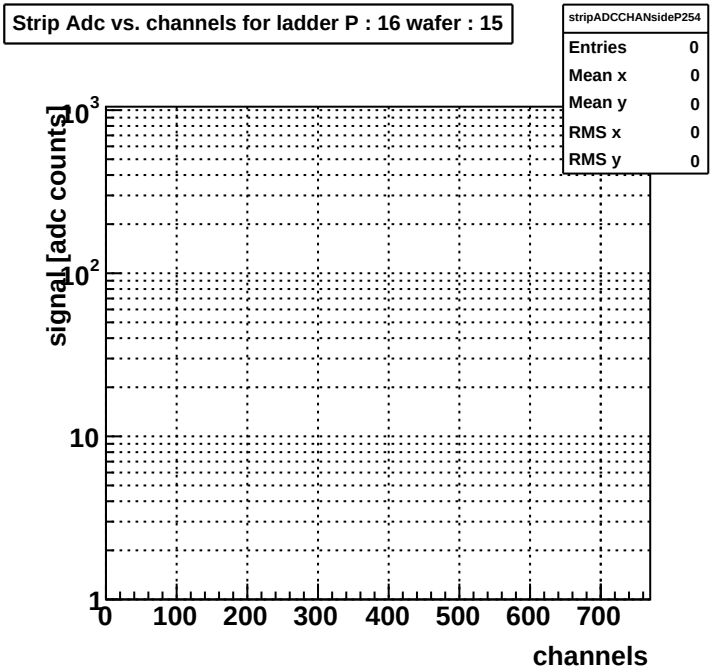

strip ADC for ladder N :16 wafer : 13
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

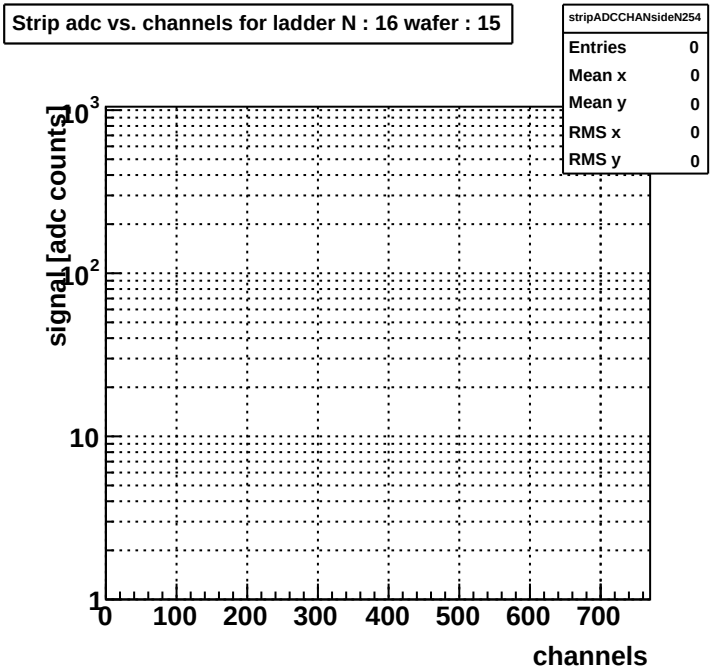

Strip Adc vs. channels for ladder P : 16 wafer : 14
stripADCCHANsideP253
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

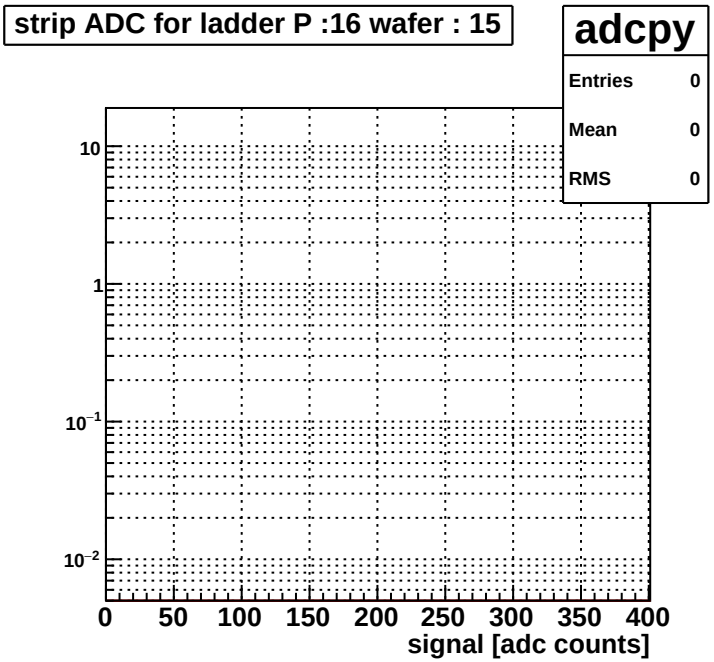

Strip adc vs. channels for ladder N : 16 wafer : 14
stripADCCHANsideN253
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

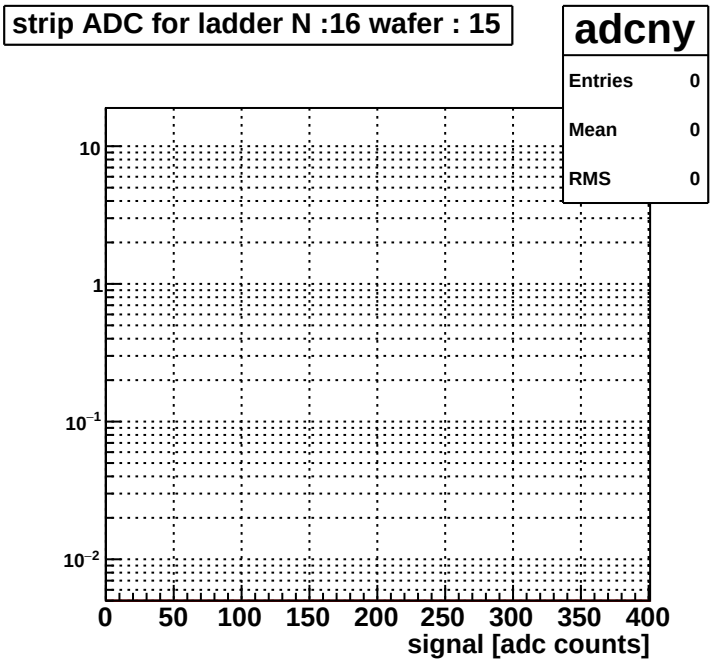

strip ADC for ladder P :16 wafer : 14
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

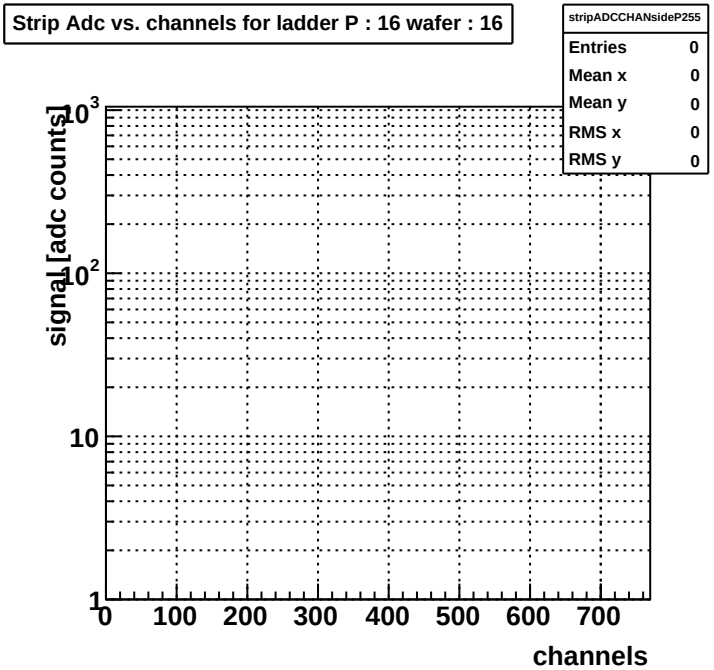

strip ADC for ladder N :16 wafer : 14
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

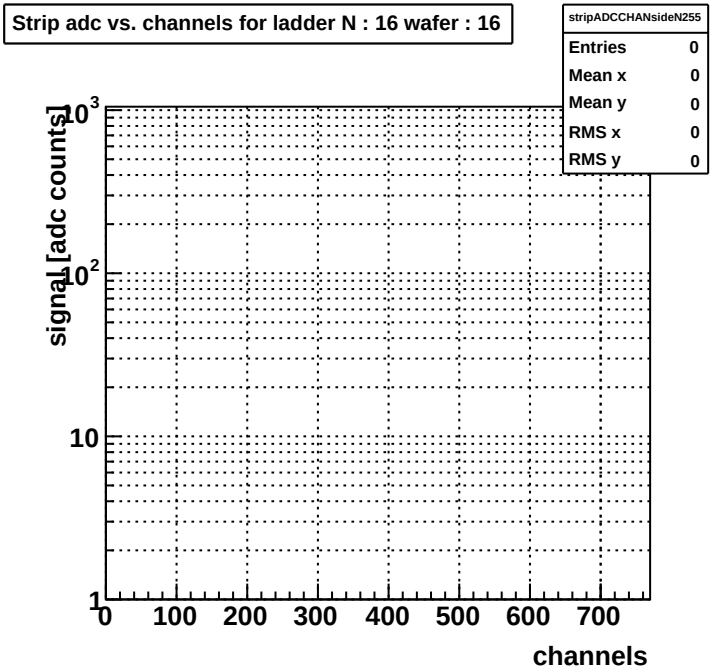

Strip Adc vs. channels for ladder P : 16 wafer : 15
stripADCCHANsideP254
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

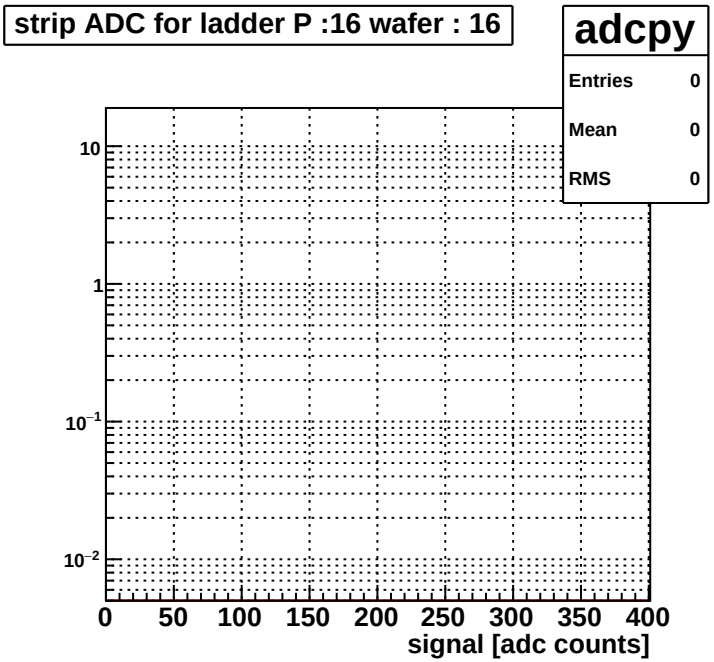

Strip adc vs. channels for ladder N : 16 wafer : 15
stripADCCHANsideN254
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

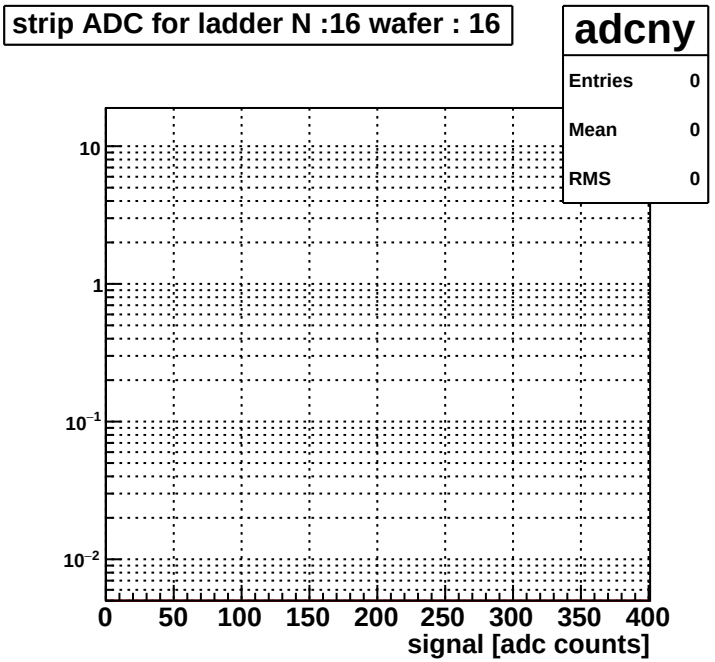

strip ADC for ladder P :16 wafer : 15
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

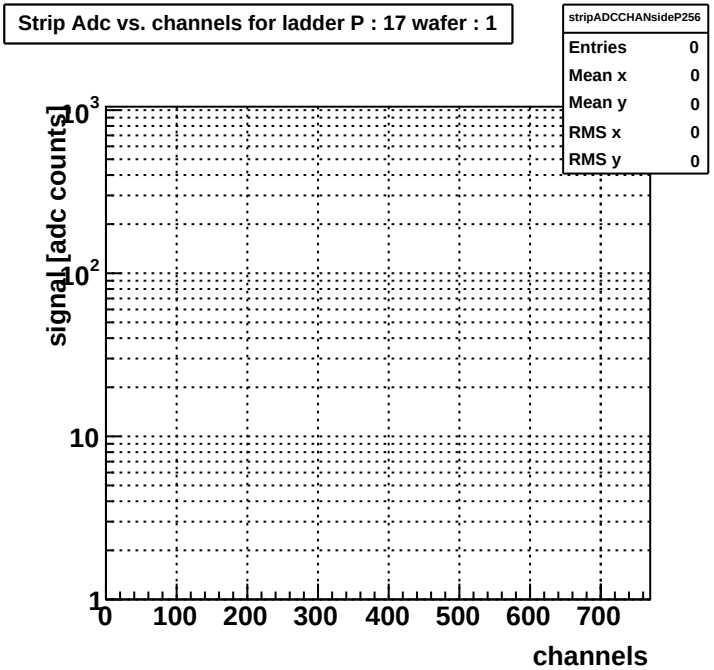

strip ADC for ladder N :16 wafer : 15
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

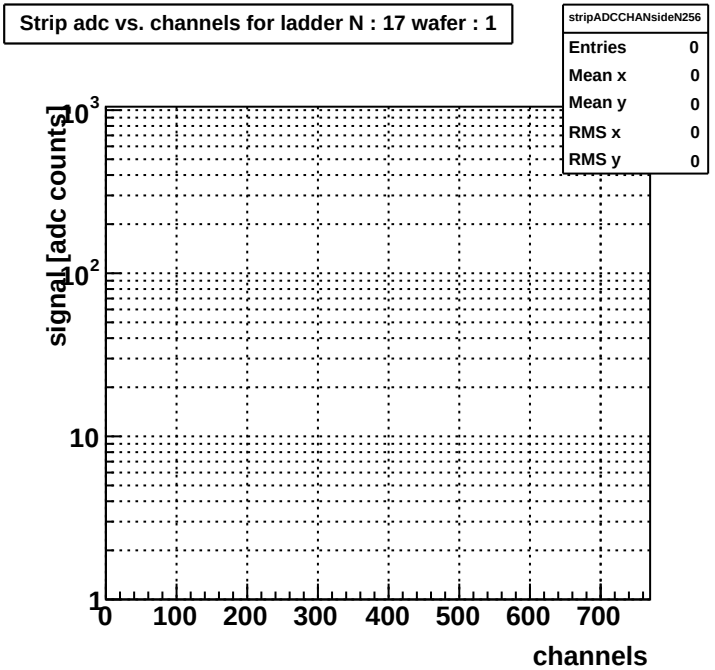

Strip Adc vs. channels for ladder P : 16 wafer : 16
stripADCCHANsideP255
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

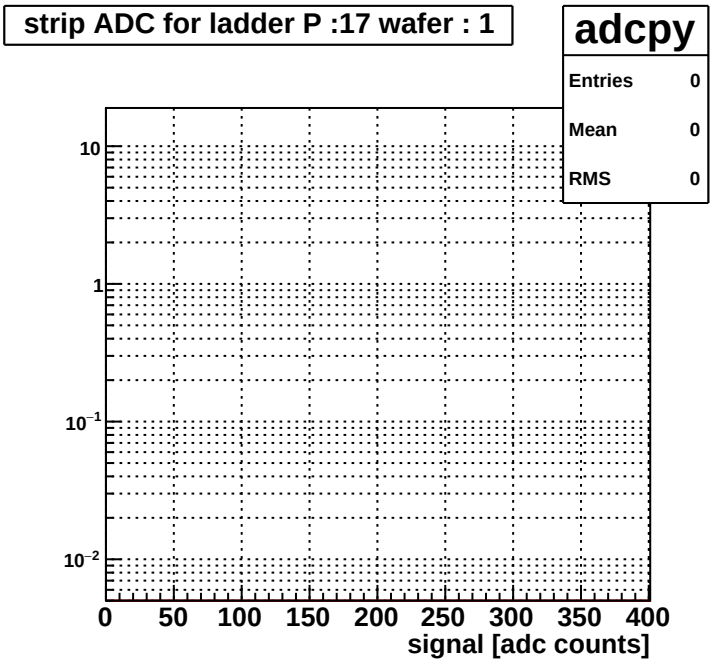

Strip adc vs. channels for ladder N : 16 wafer : 16
stripADCCHANsideN255
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

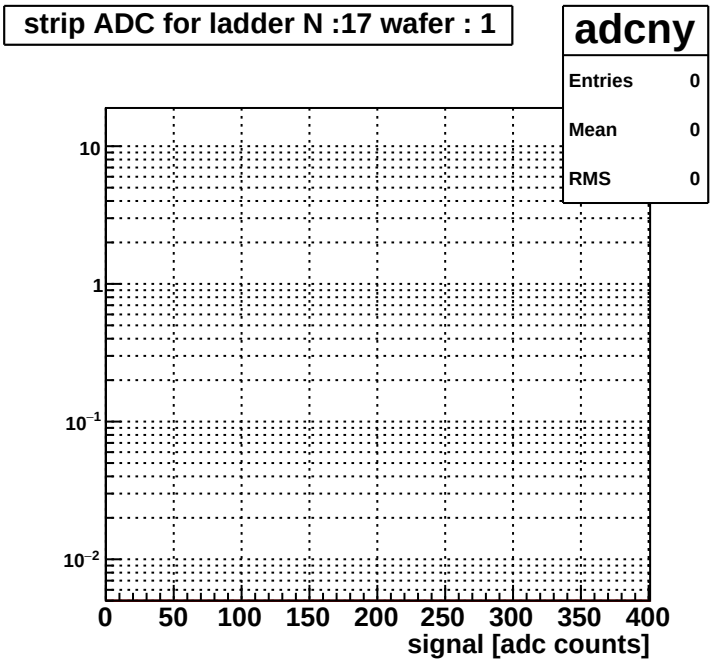

strip ADC for ladder P :16 wafer : 16
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

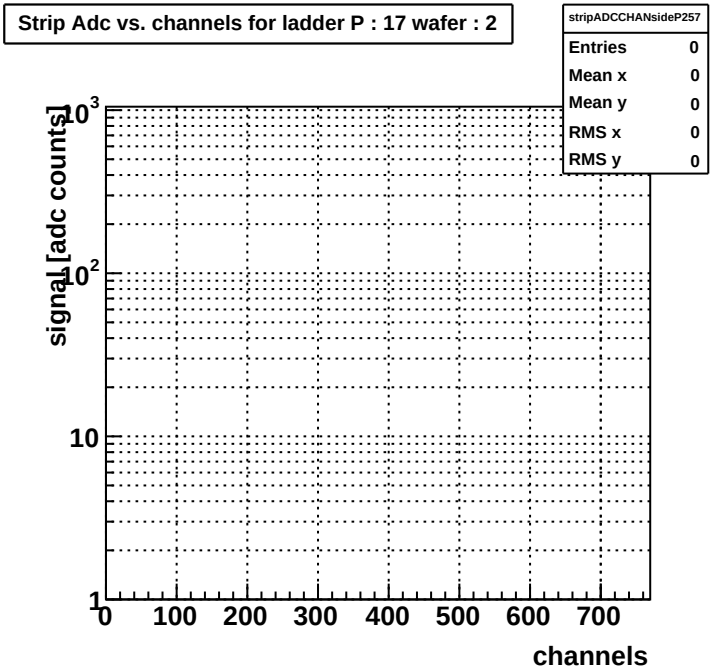

strip ADC for ladder N :16 wafer : 16
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

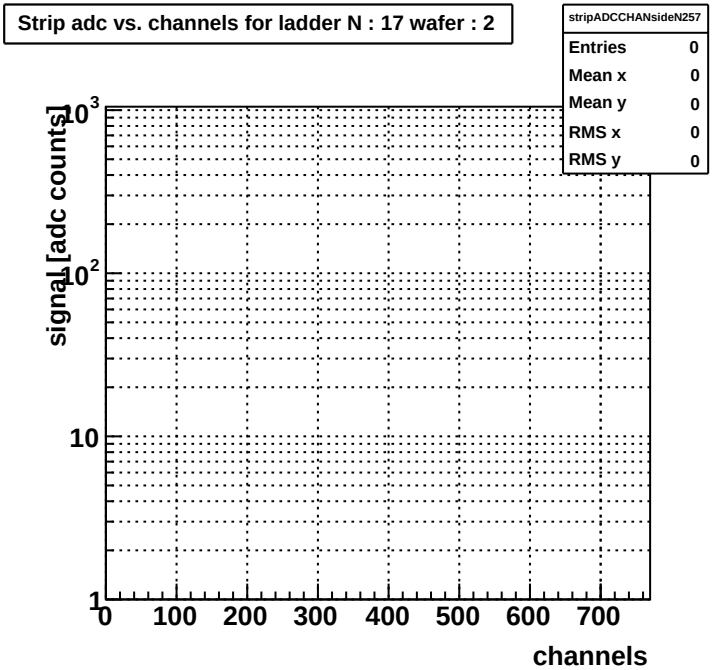

Strip Adc vs. channels for ladder P : 17 wafer : 1
stripADCCHANsideP256
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

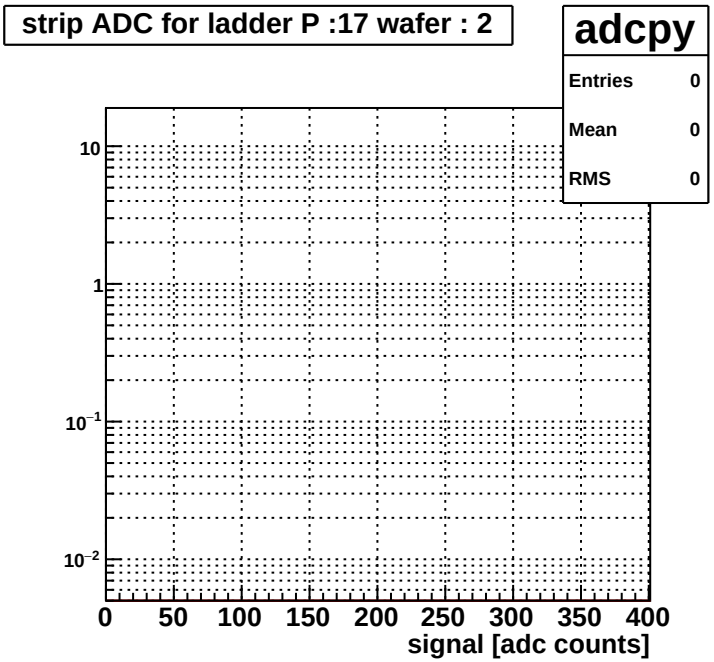

Strip adc vs. channels for ladder N : 17 wafer : 1
stripADCCHANsideN256
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

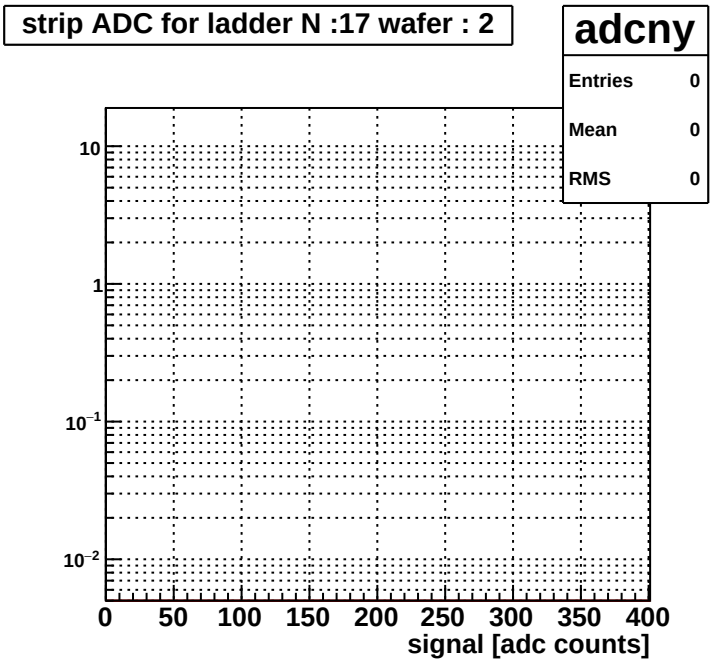

strip ADC for ladder P :17 wafer : 1
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

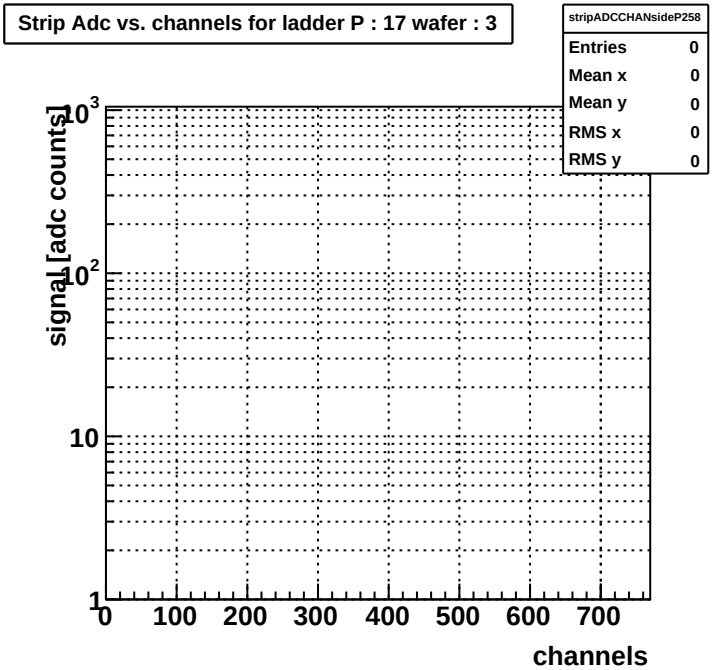

strip ADC for ladder N :17 wafer : 1
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

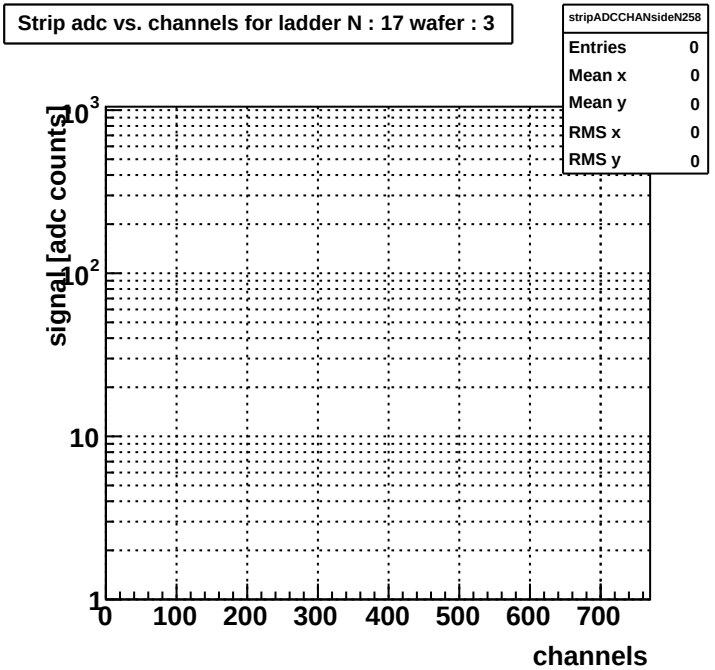

Strip Adc vs. channels for ladder P : 17 wafer : 2
stripADCCHANsideP257
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

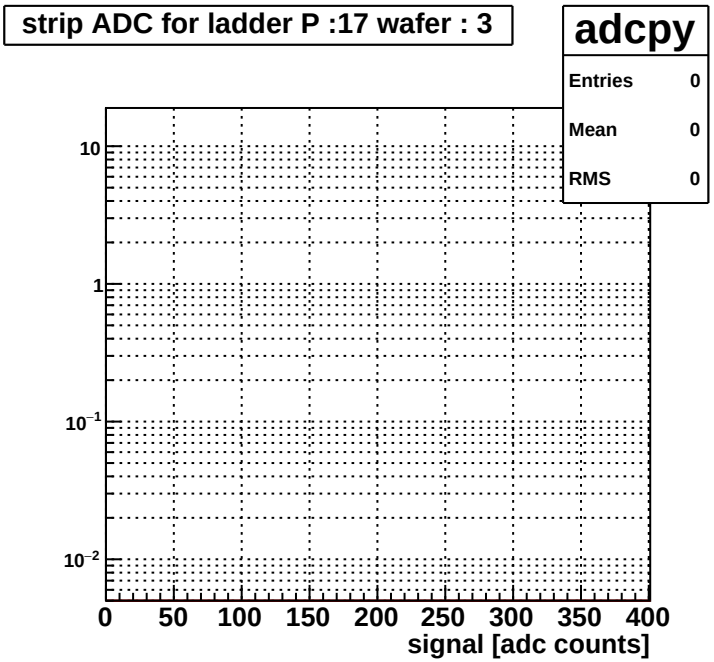

Strip adc vs. channels for ladder N : 17 wafer : 2
stripADCCHANsideN257
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

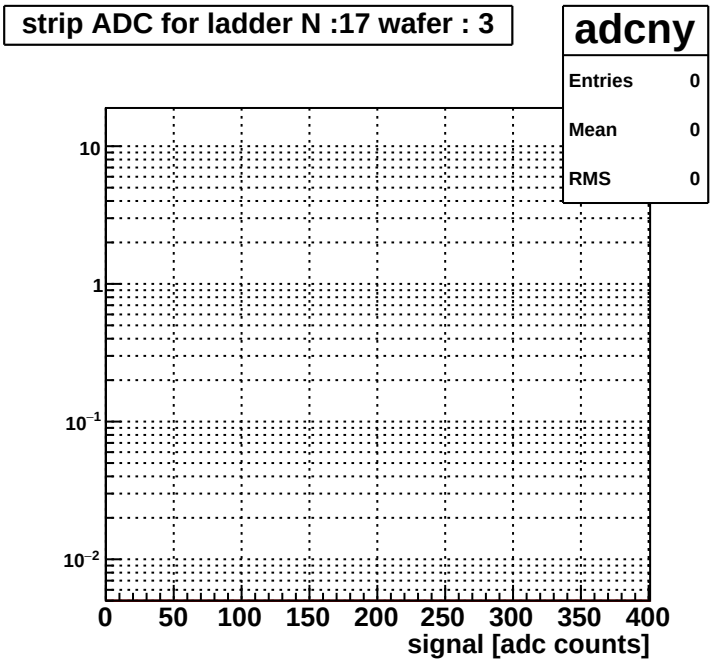

strip ADC for ladder P :17 wafer : 2
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

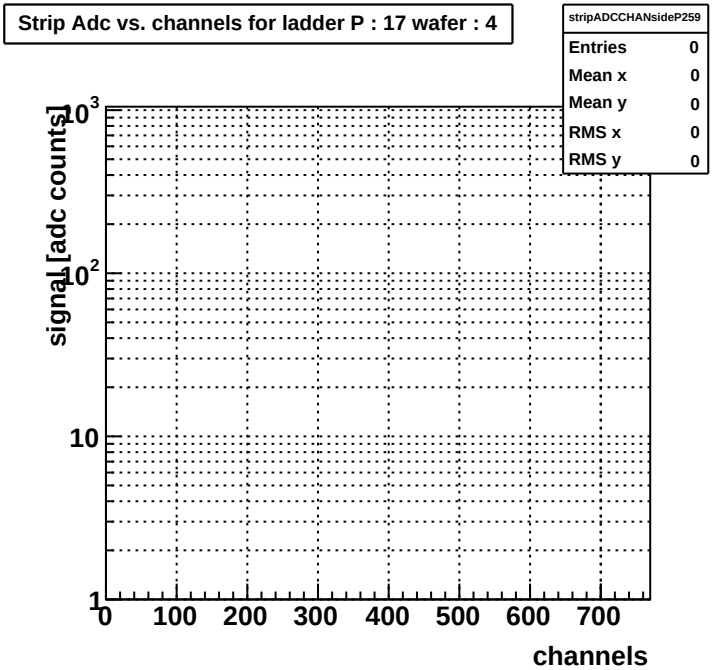

strip ADC for ladder N :17 wafer : 2
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

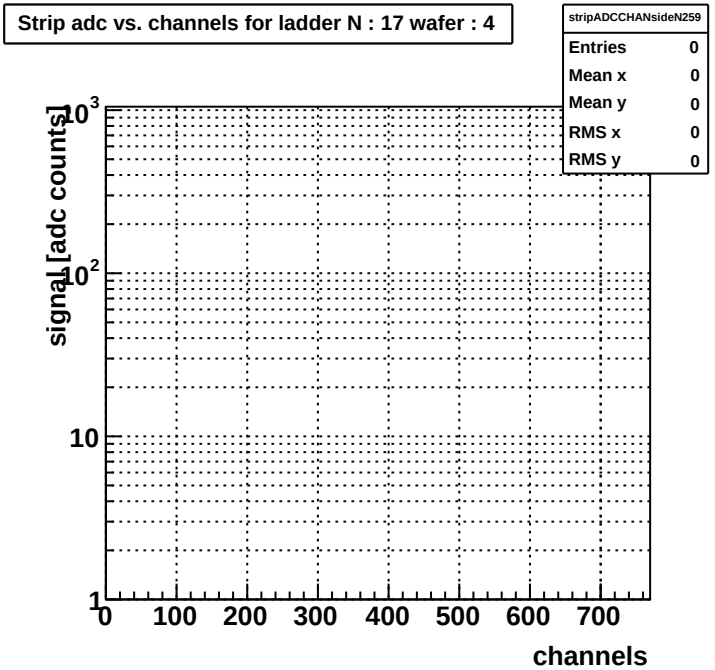

Strip Adc vs. channels for ladder P : 17 wafer : 3
stripADCCHANsideP258
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

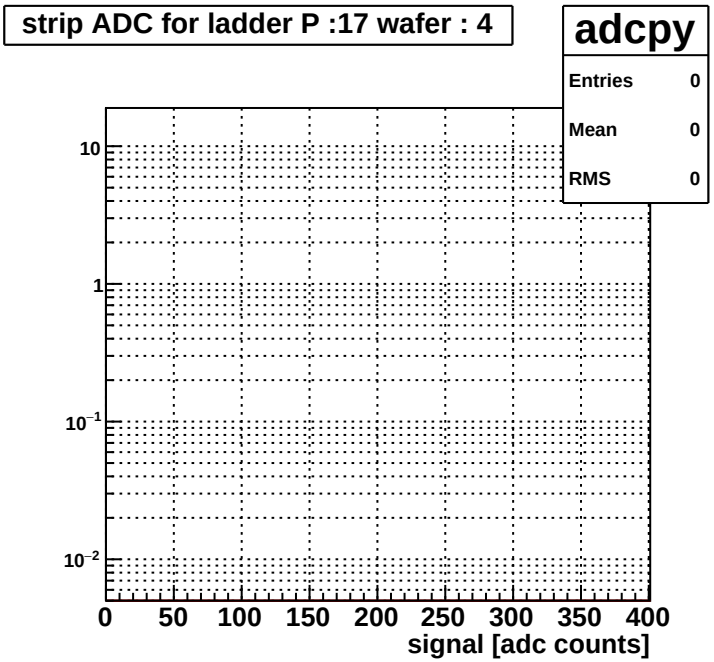

Strip adc vs. channels for ladder N : 17 wafer : 3
stripADCCHANsideN258
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

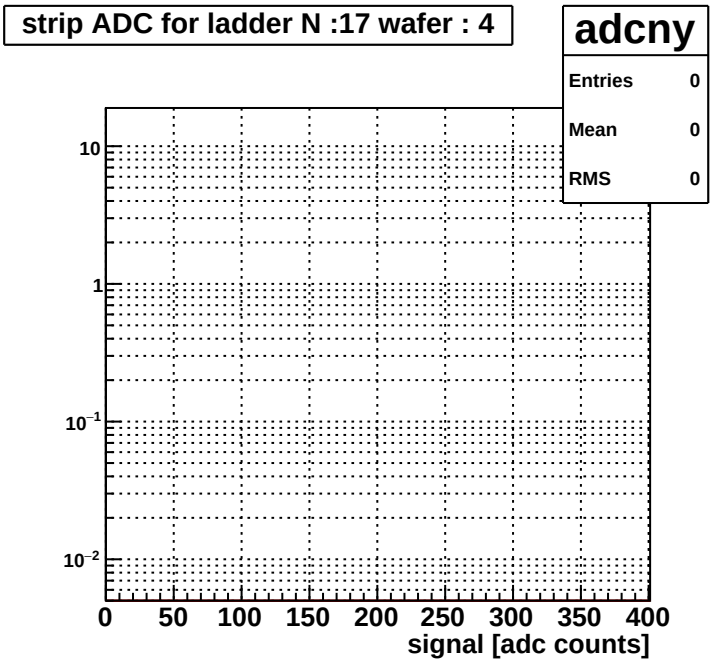

strip ADC for ladder P :17 wafer : 3
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

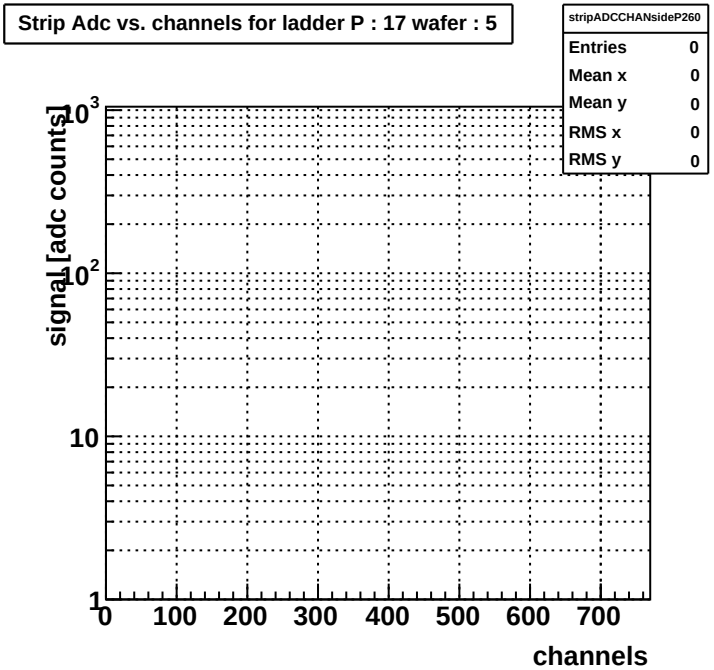

strip ADC for ladder N :17 wafer : 3
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

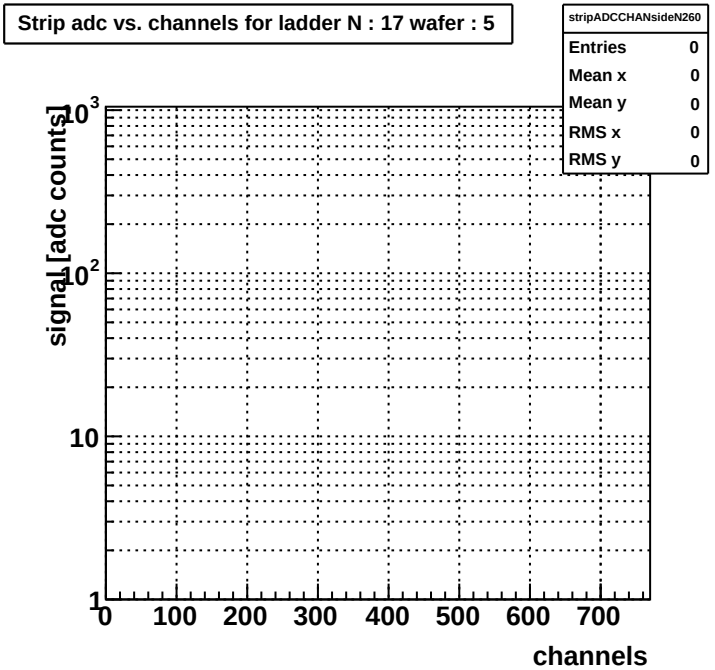

Strip Adc vs. channels for ladder P : 17 wafer : 4
stripADCCHANsideP259
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

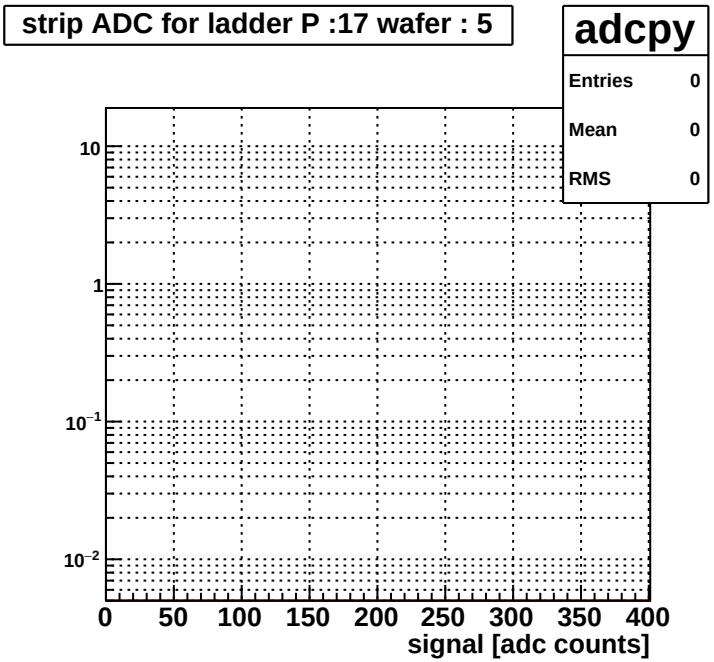

Strip adc vs. channels for ladder N : 17 wafer : 4
stripADCCHANsideN259
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

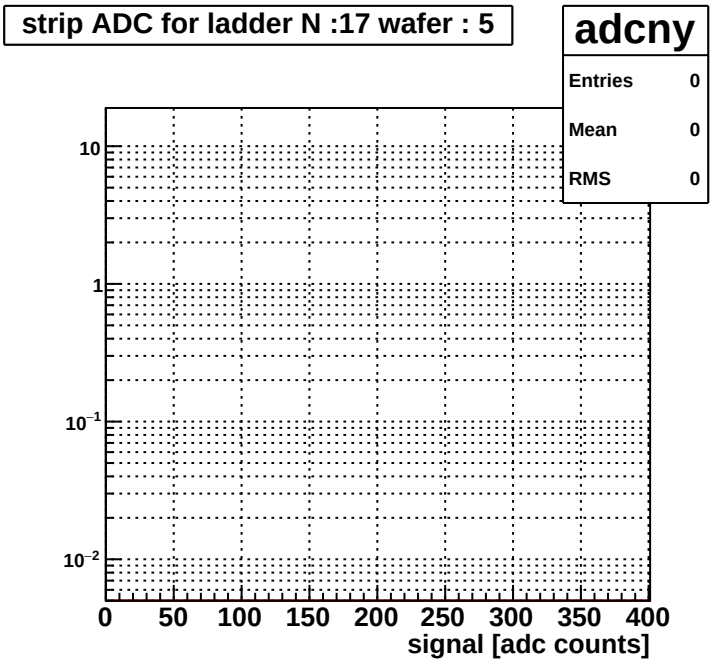

strip ADC for ladder P :17 wafer : 4
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

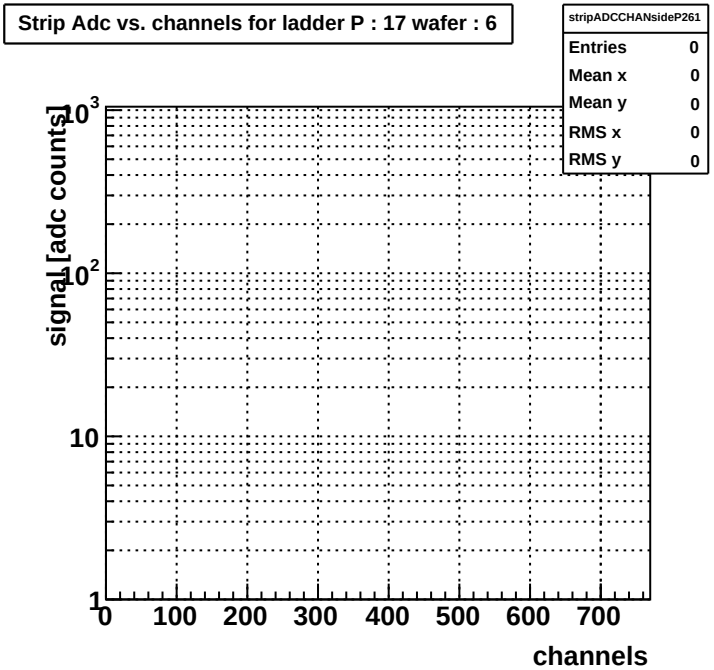

strip ADC for ladder N :17 wafer : 4
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

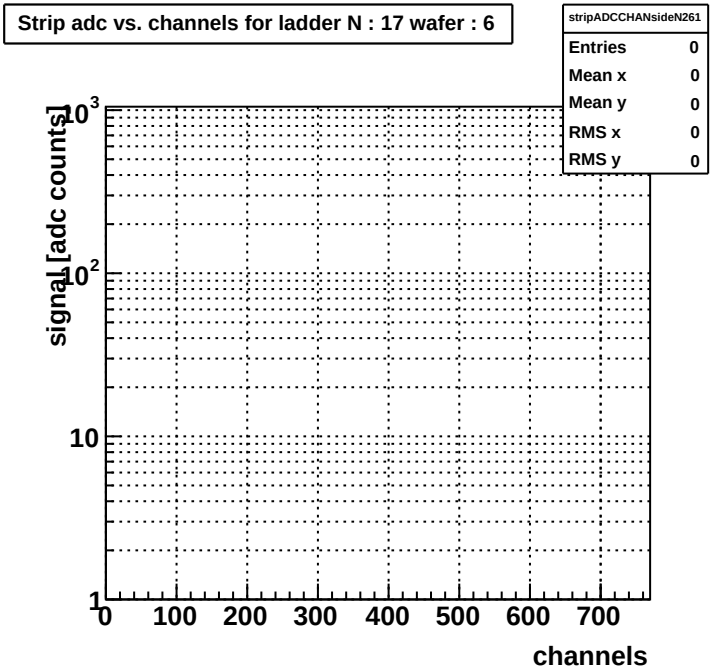

Strip Adc vs. channels for ladder P : 17 wafer : 5
stripADCCHANsideP260
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

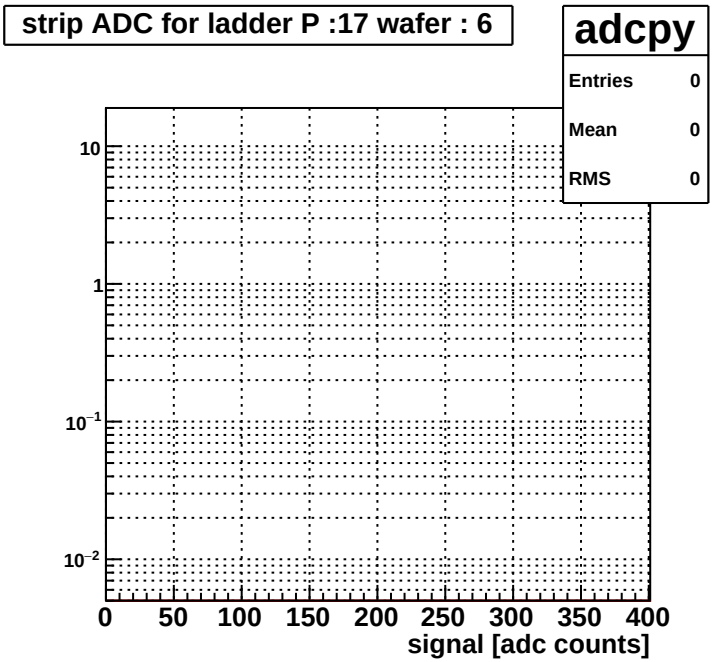

Strip adc vs. channels for ladder N : 17 wafer : 5
stripADCCHANsideN260
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

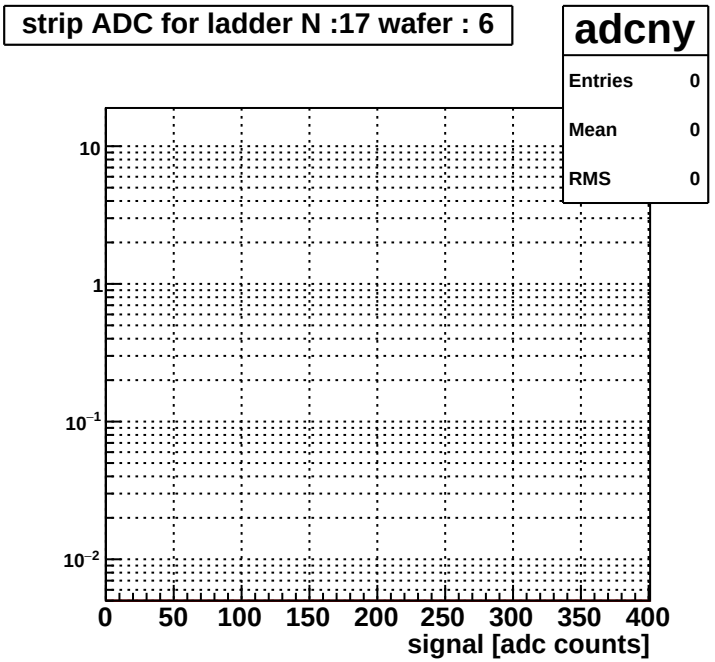

strip ADC for ladder P :17 wafer : 5
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

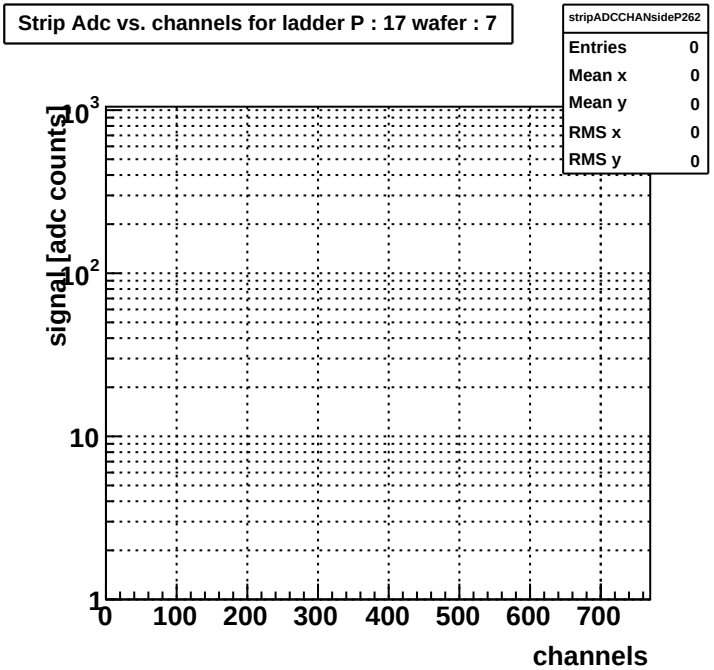

strip ADC for ladder N :17 wafer : 5
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

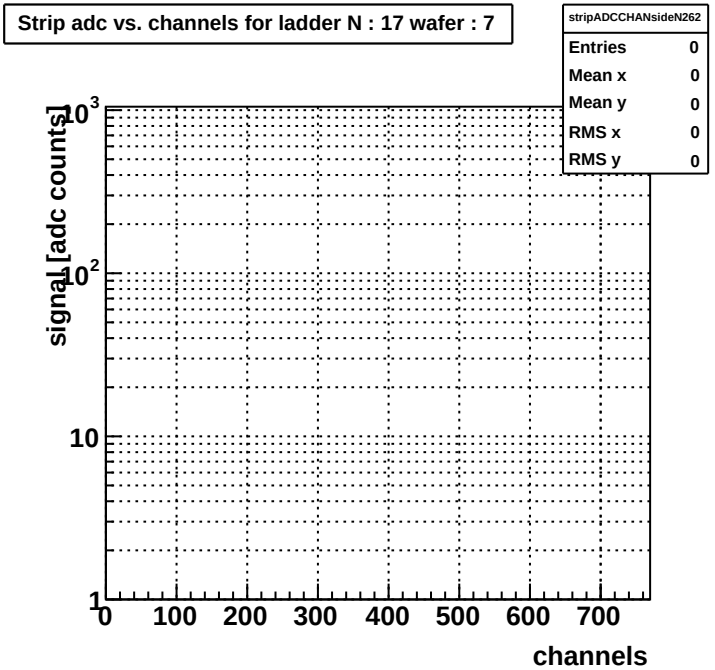

Strip Adc vs. channels for ladder P : 17 wafer : 6
stripADCCHANsideP261
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

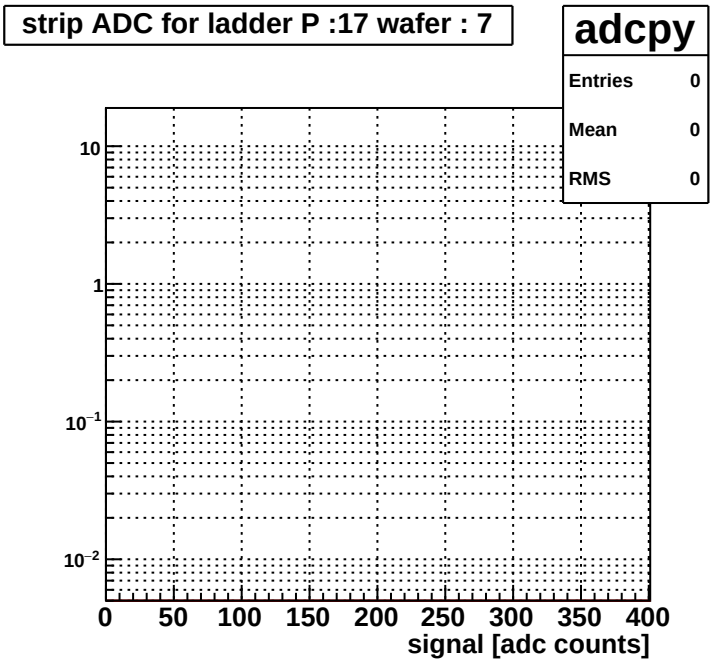

Strip adc vs. channels for ladder N : 17 wafer : 6
stripADCCHANsideN261
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

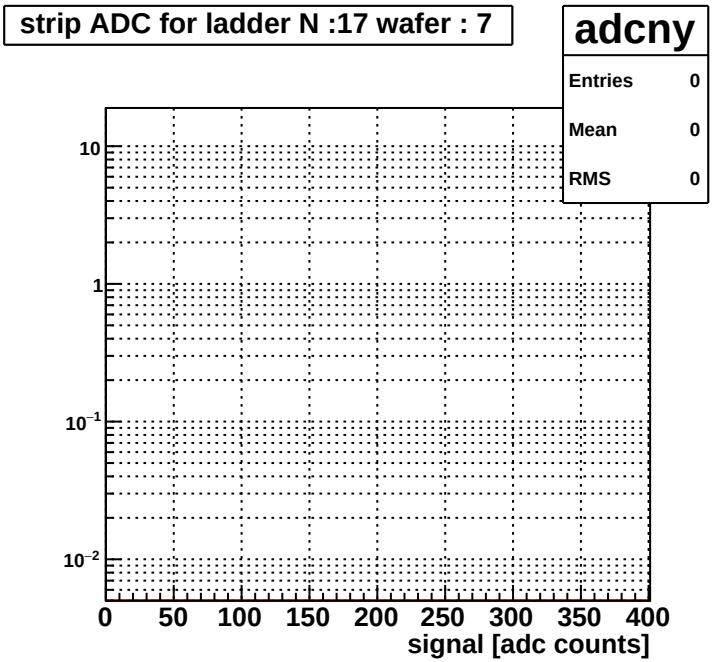

strip ADC for ladder P :17 wafer : 6
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

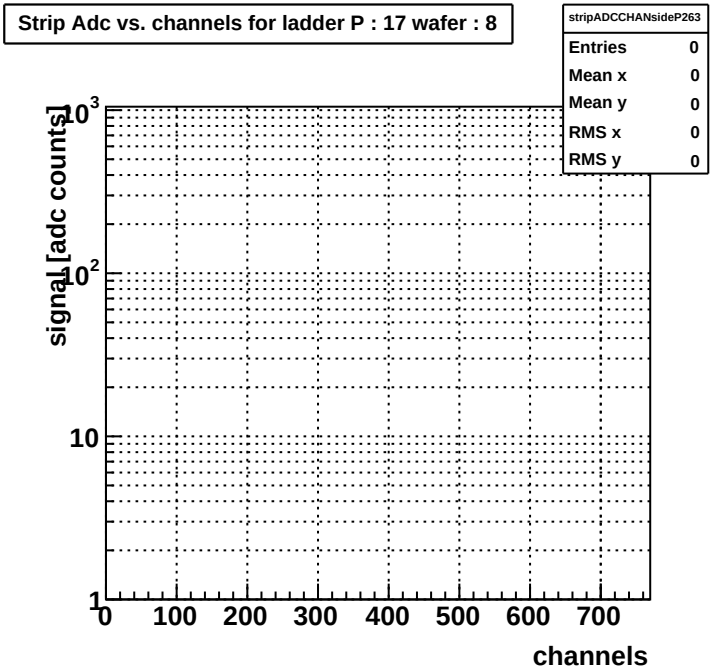

strip ADC for ladder N :17 wafer : 6
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

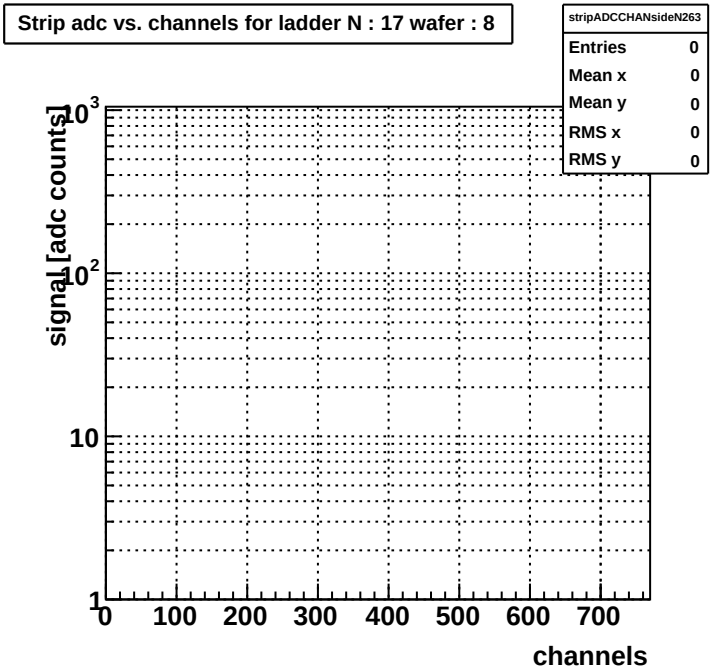

Strip Adc vs. channels for ladder P : 17 wafer : 7
stripADCCHANsideP262
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

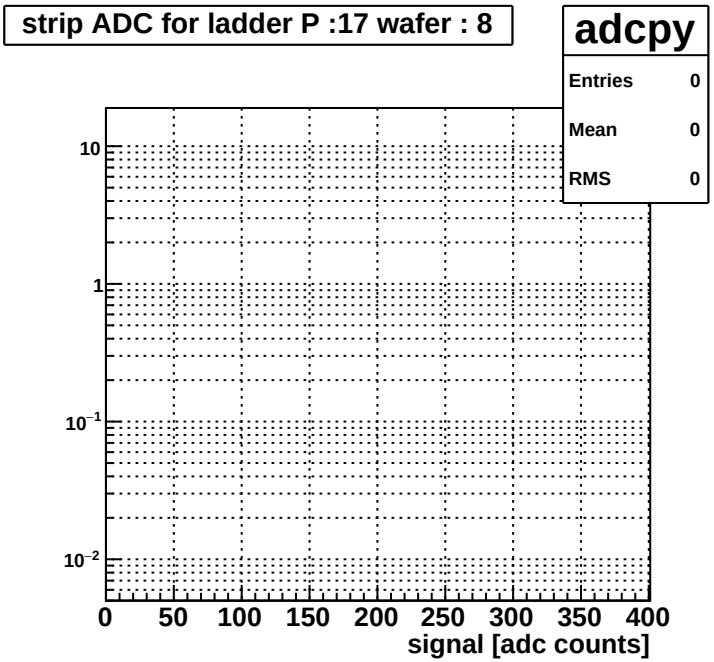

Strip adc vs. channels for ladder N : 17 wafer : 7
stripADCCHANsideN262
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

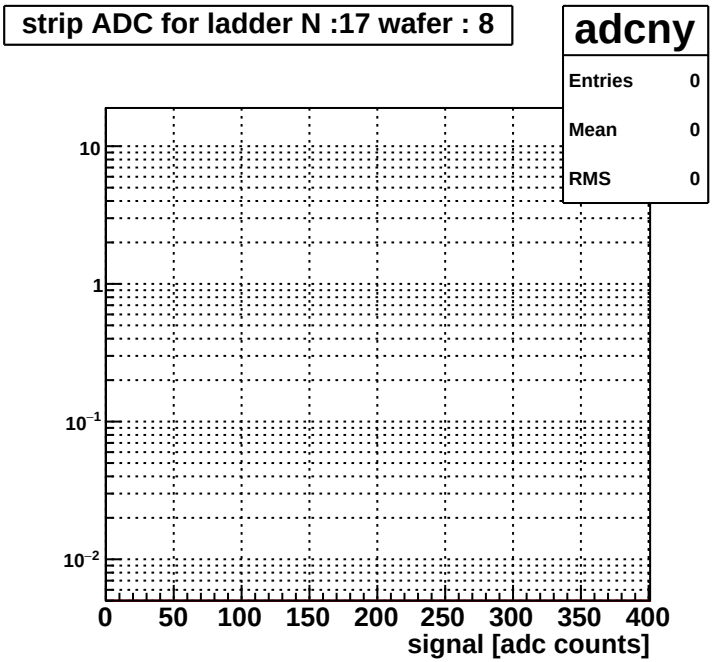

strip ADC for ladder P :17 wafer : 7
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

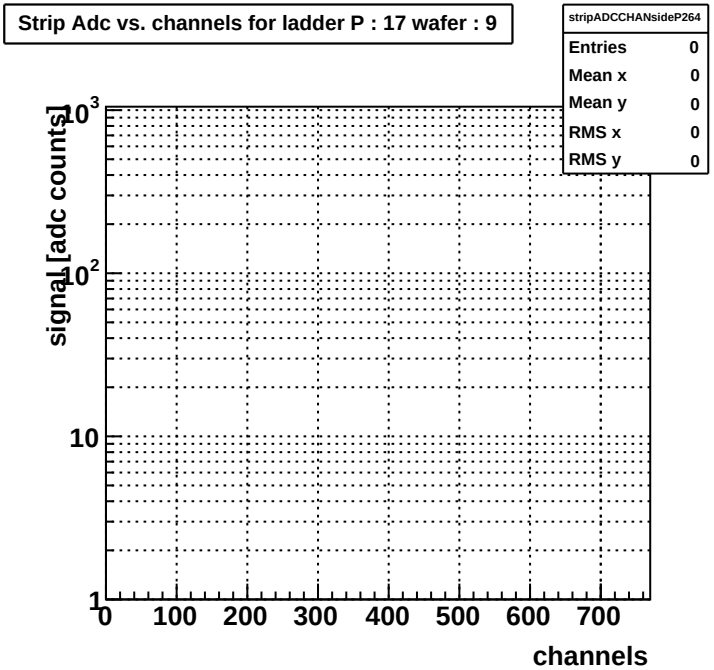

strip ADC for ladder N :17 wafer : 7
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

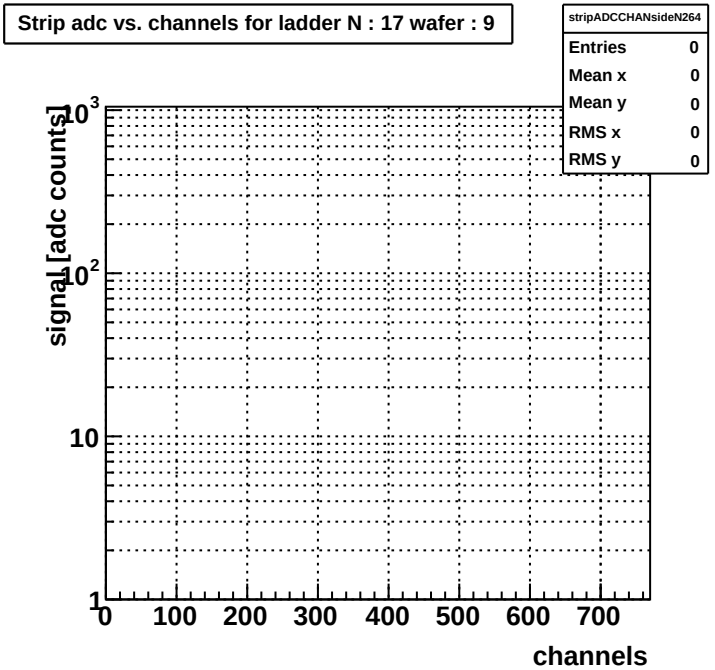

Strip Adc vs. channels for ladder P : 17 wafer : 8
stripADCCHANsideP263
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

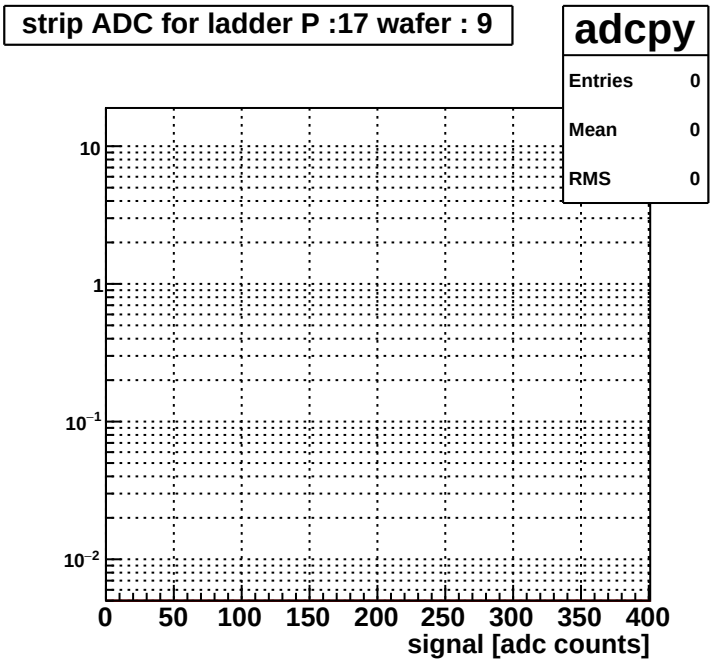

Strip adc vs. channels for ladder N : 17 wafer : 8
stripADCCHANsideN263
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

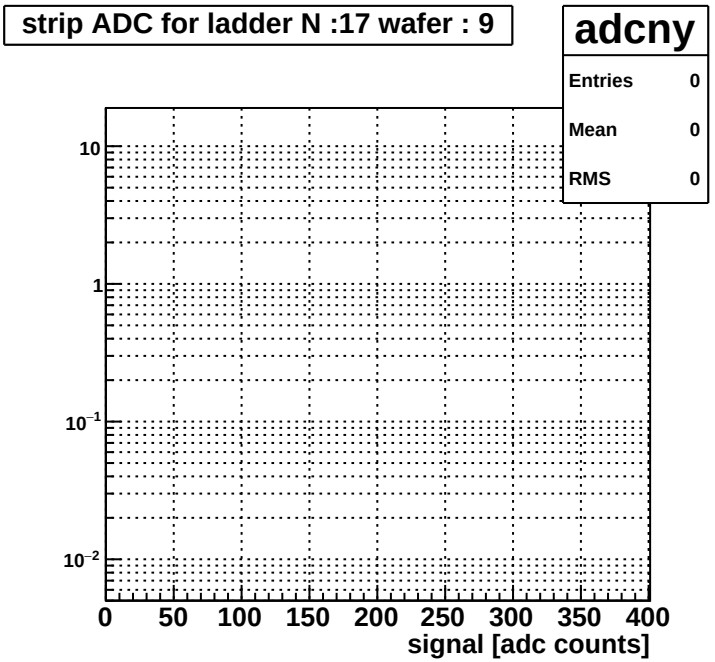

strip ADC for ladder P :17 wafer : 8
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

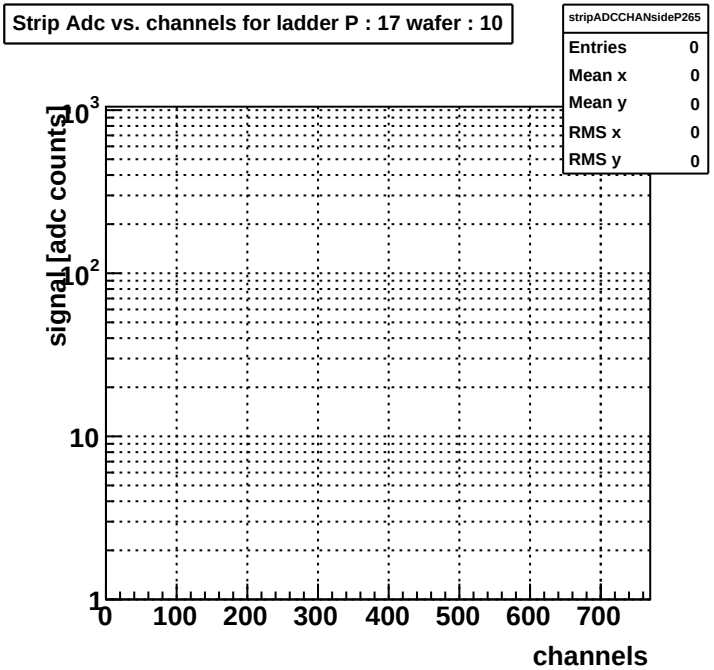

strip ADC for ladder N :17 wafer : 8
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

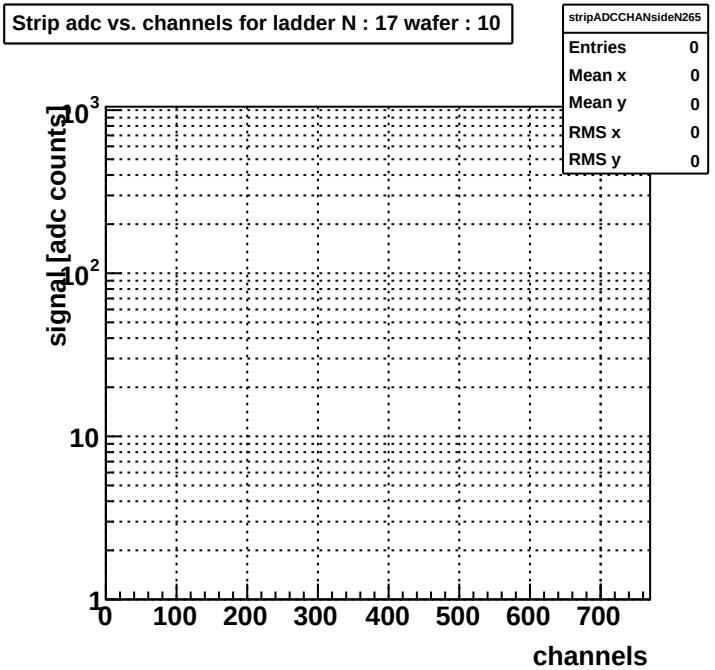

Strip Adc vs. channels for ladder P : 17 wafer : 9
stripADCCHANsideP264
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

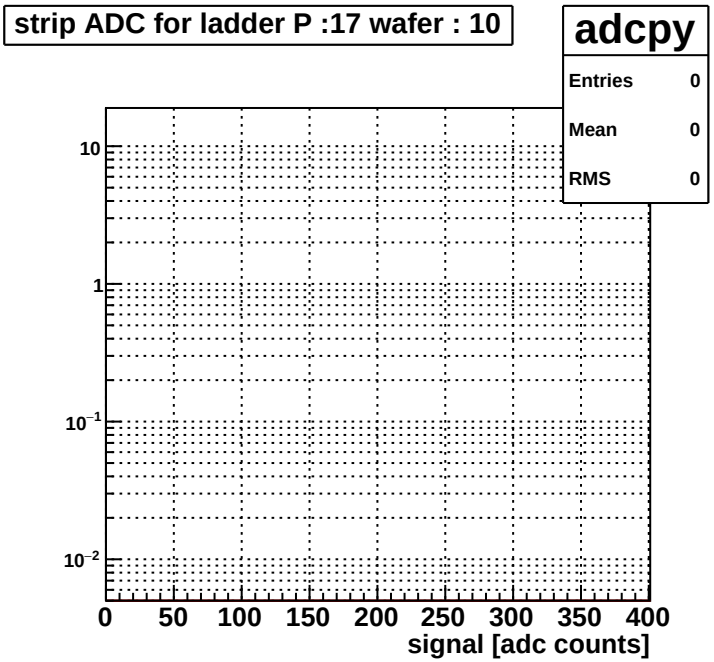

Strip adc vs. channels for ladder N : 17 wafer : 9
stripADCCHANsideN264
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

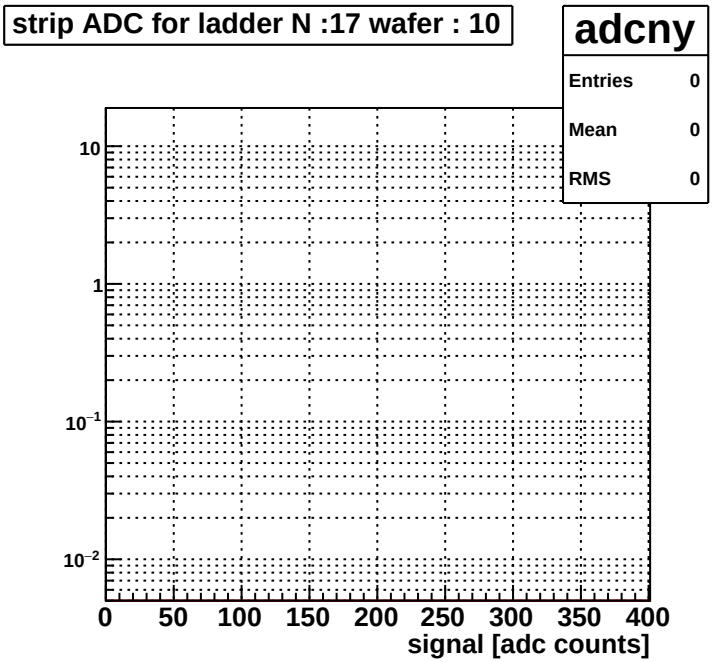

strip ADC for ladder P :17 wafer : 9
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

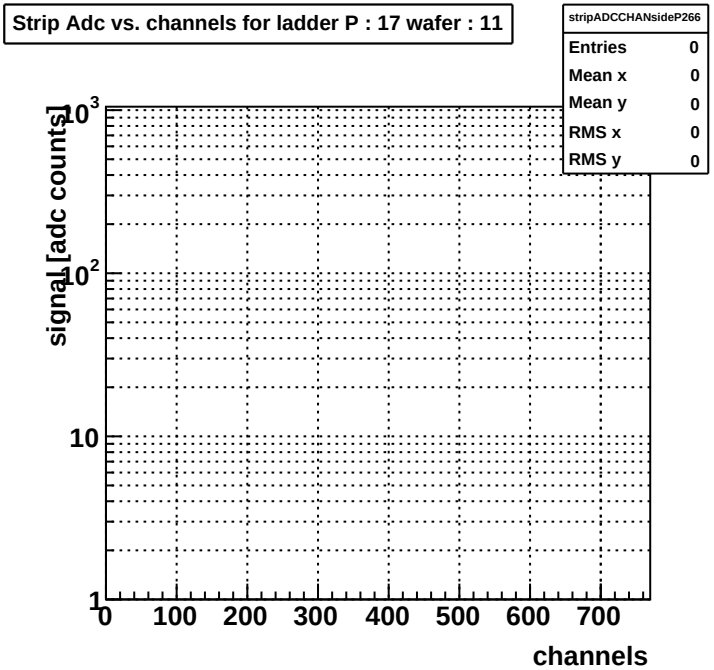

strip ADC for ladder N :17 wafer : 9
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

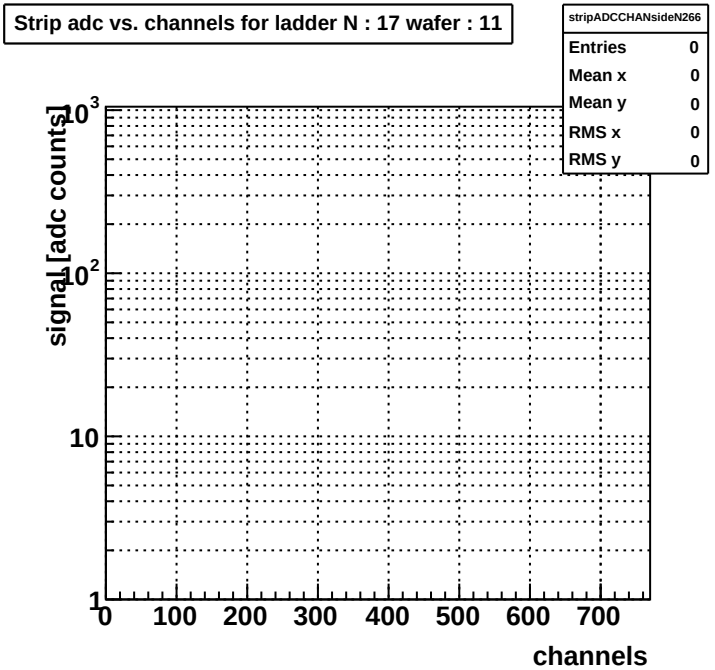

Strip Adc vs. channels for ladder P : 17 wafer : 10
stripADCCHANsideP265
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

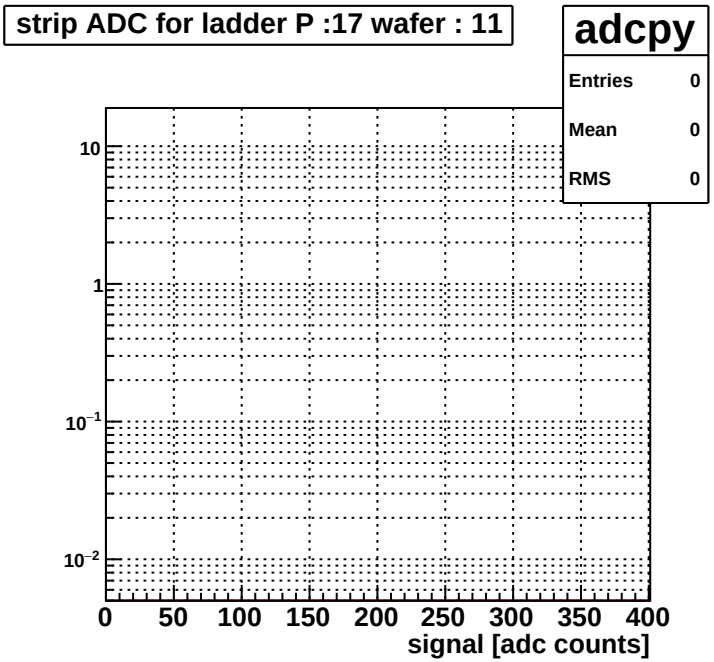

Strip adc vs. channels for ladder N : 17 wafer : 10
stripADCCHANsideN265
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

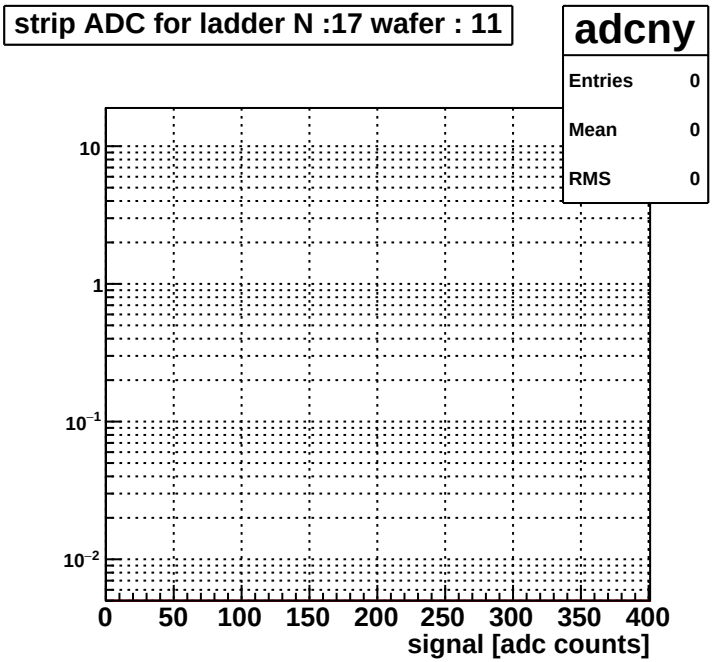

strip ADC for ladder P :17 wafer : 10
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

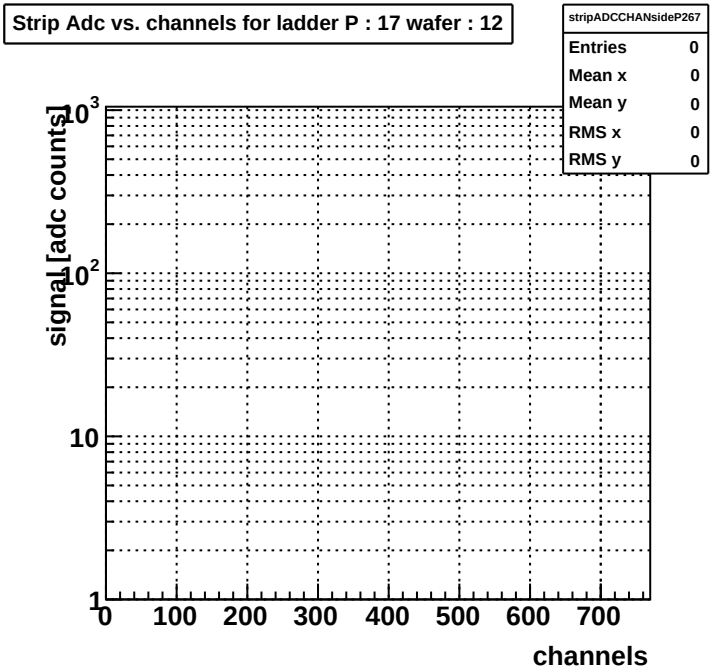

strip ADC for ladder N :17 wafer : 10
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

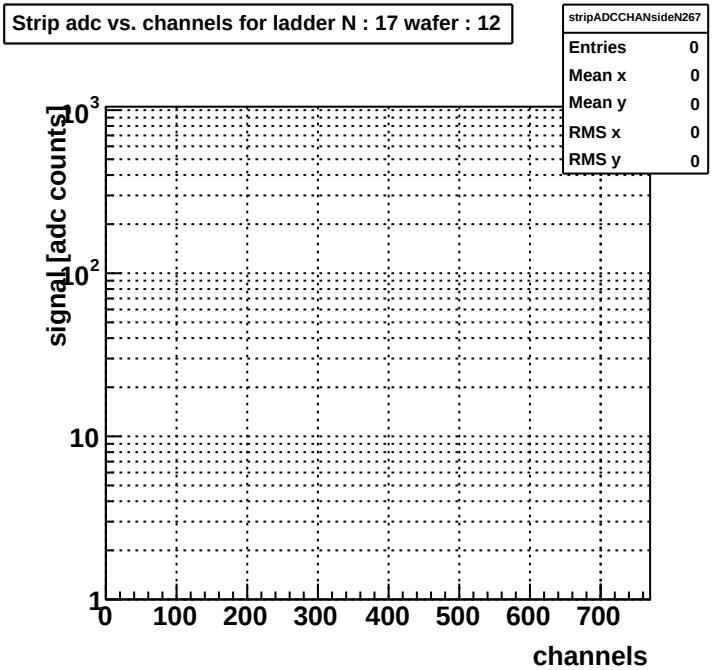

Strip Adc vs. channels for ladder P : 17 wafer : 11
stripADCCHANsideP266
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

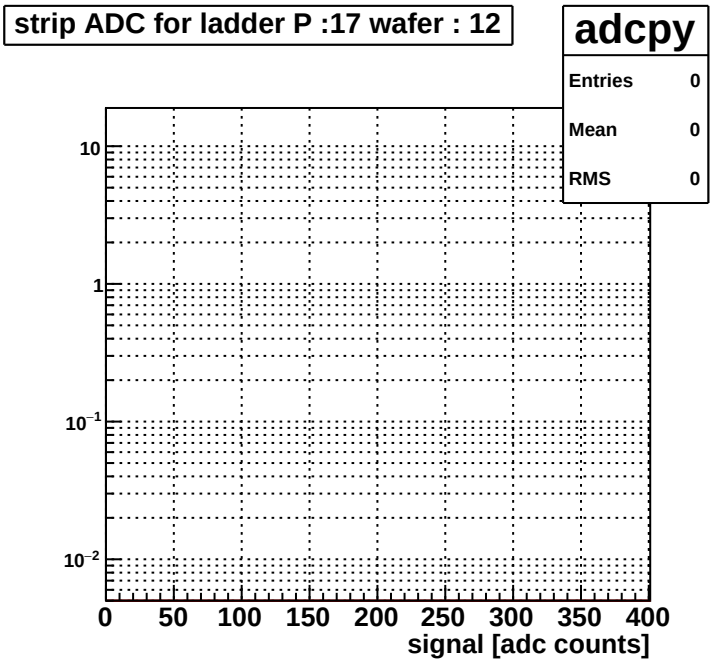

Strip adc vs. channels for ladder N : 17 wafer : 11
stripADCCHANsideN266
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

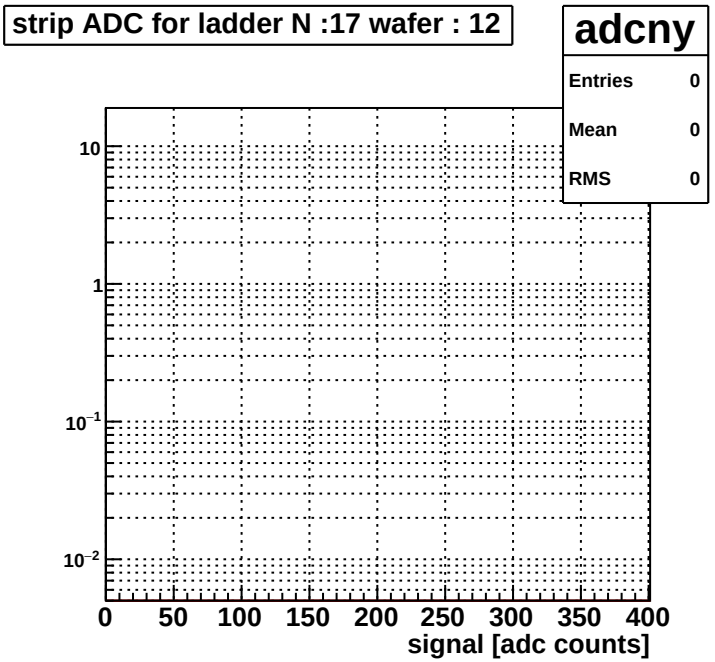

strip ADC for ladder P :17 wafer : 11
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

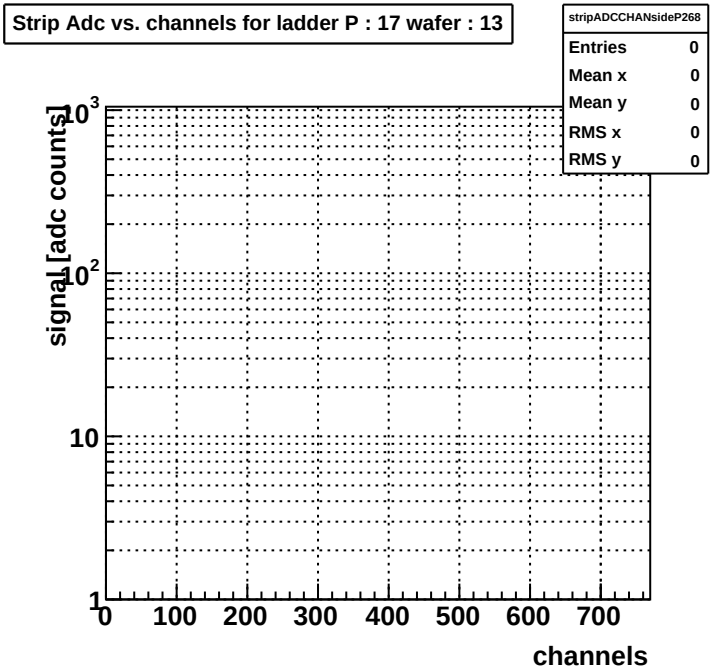

strip ADC for ladder N :17 wafer : 11
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

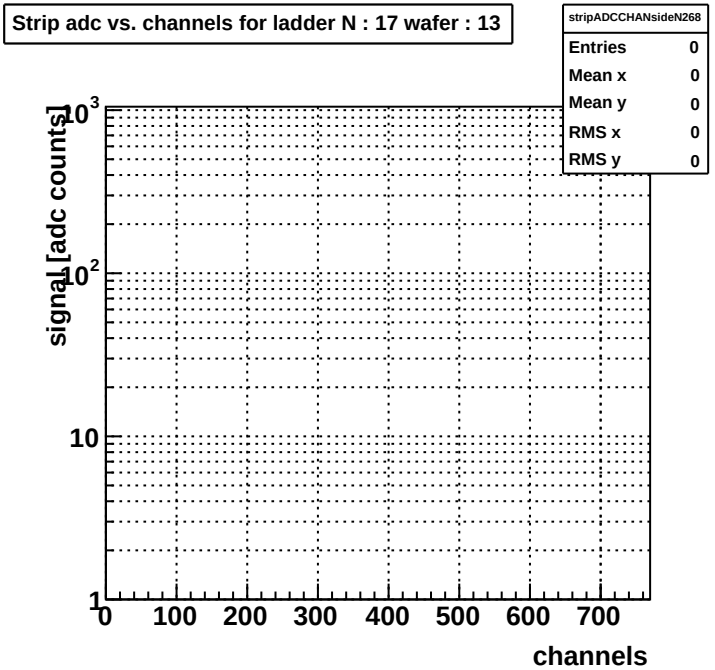

Strip Adc vs. channels for ladder P : 17 wafer : 12
stripADCCHANsideP267
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

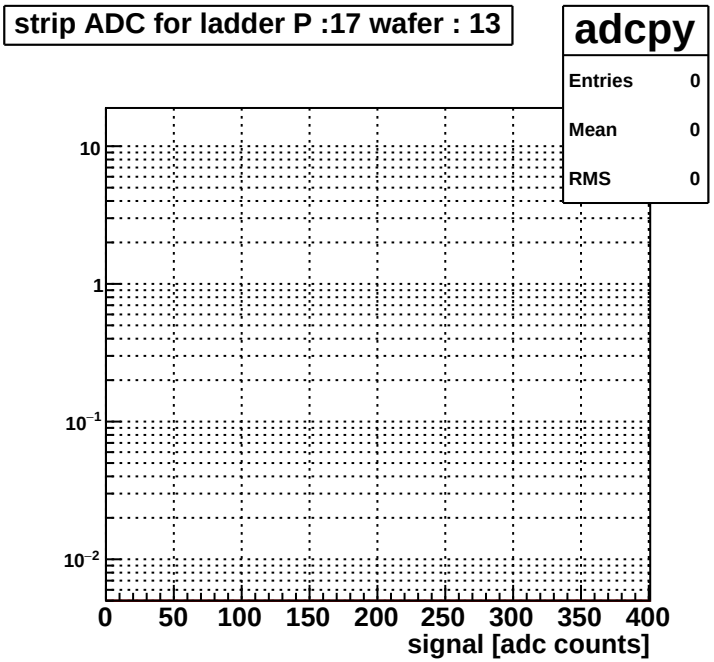

Strip adc vs. channels for ladder N : 17 wafer : 12
stripADCCHANsideN267
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

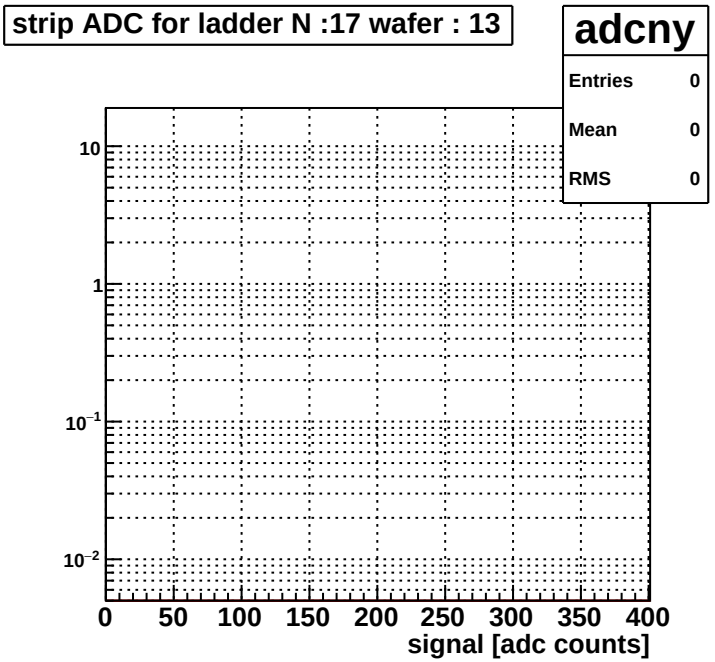

strip ADC for ladder P :17 wafer : 12
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

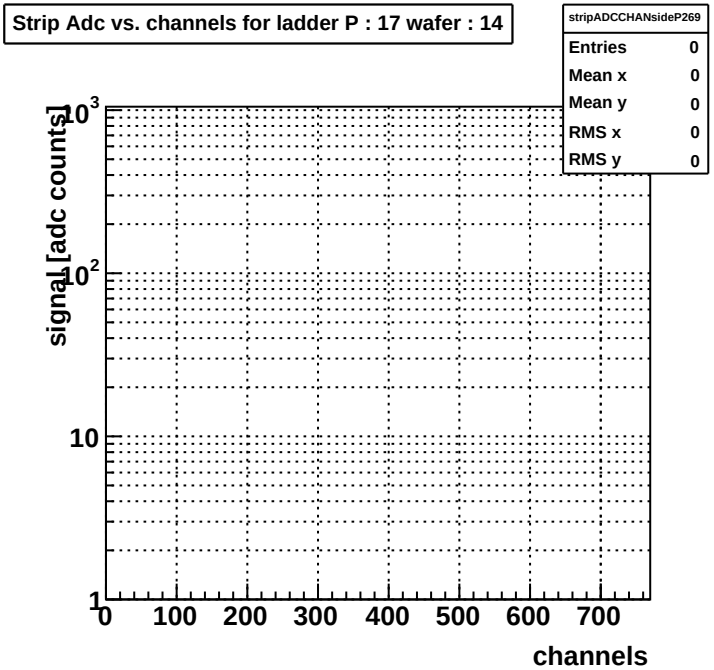

strip ADC for ladder N :17 wafer : 12
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

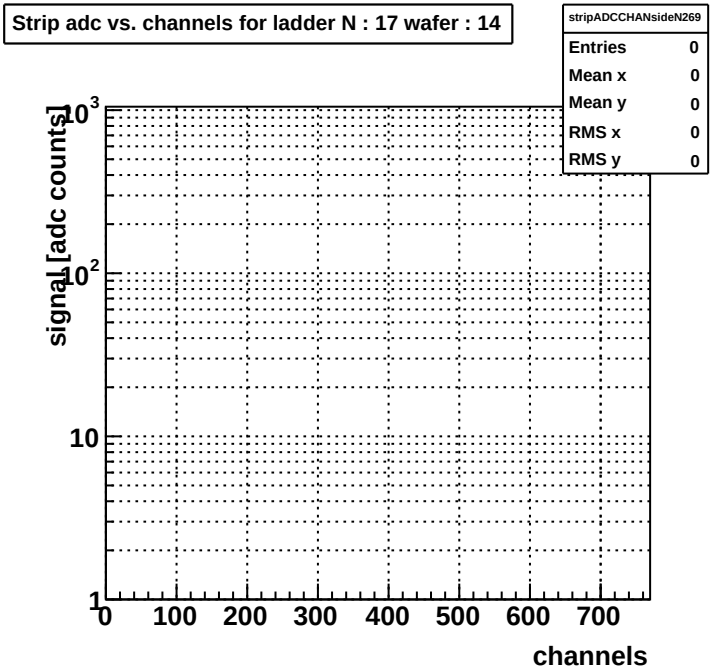

Strip Adc vs. channels for ladder P : 17 wafer : 13
stripADCCHANsideP268
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

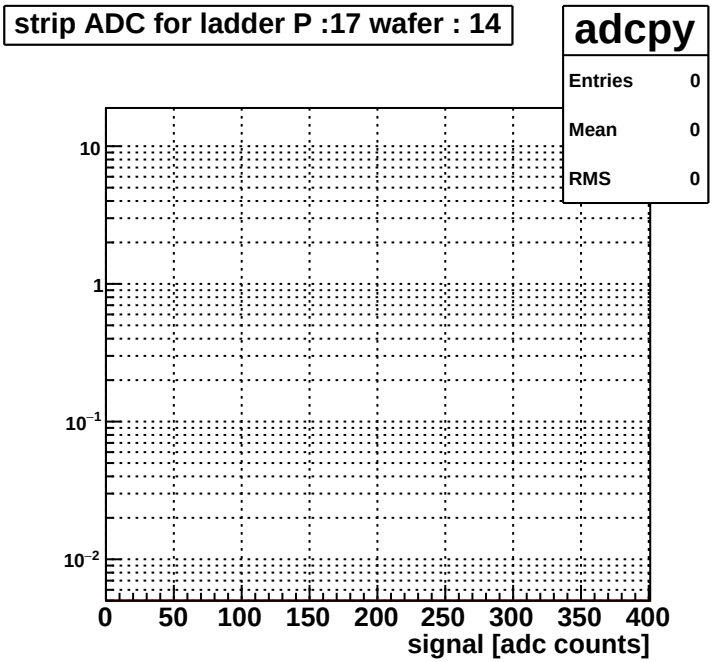

Strip adc vs. channels for ladder N : 17 wafer : 13
stripADCCHANsideN268
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

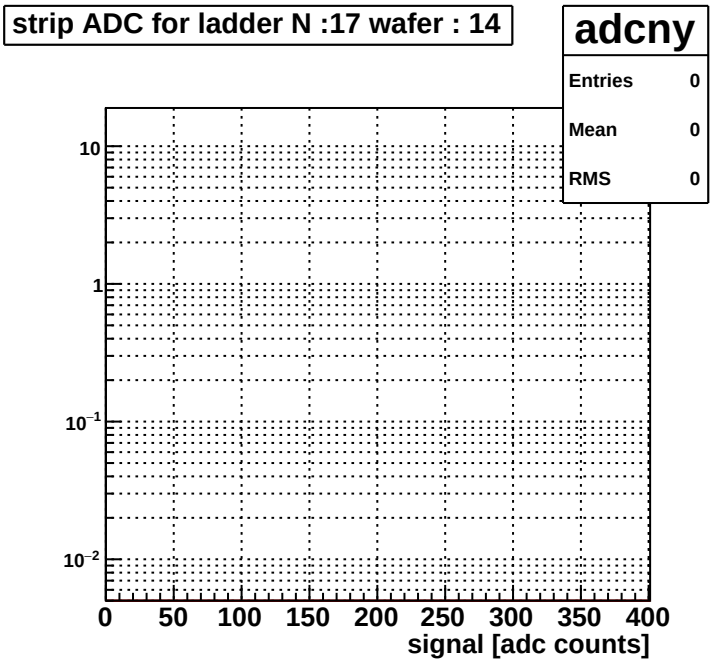

strip ADC for ladder P :17 wafer : 13
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

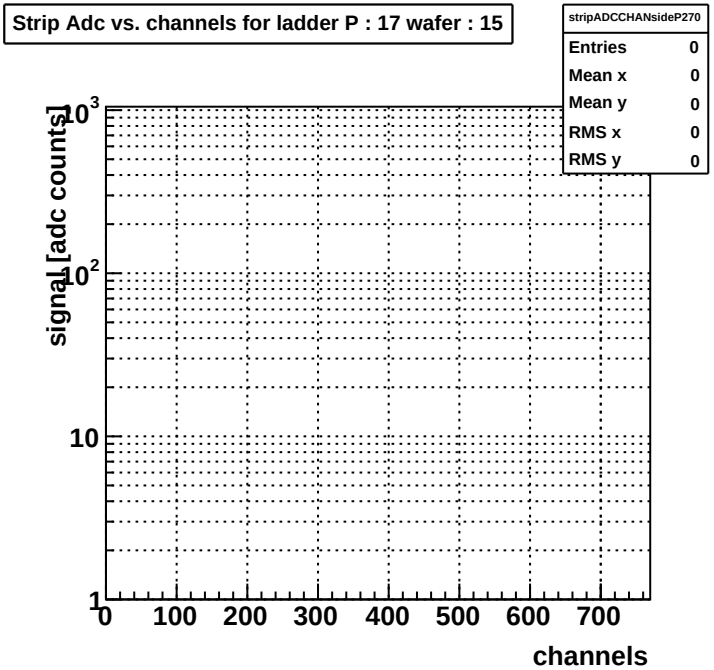

strip ADC for ladder N :17 wafer : 13
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

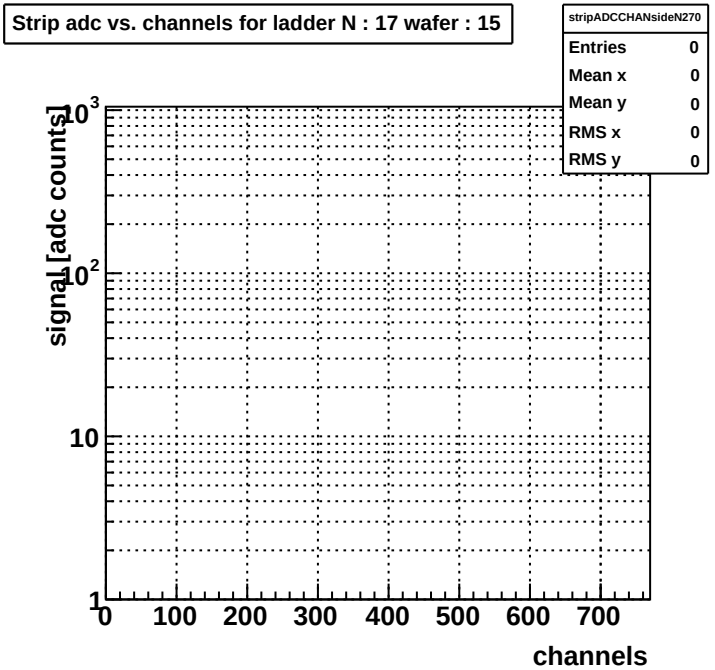

Strip Adc vs. channels for ladder P : 17 wafer : 14
stripADCCHANsideP269
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

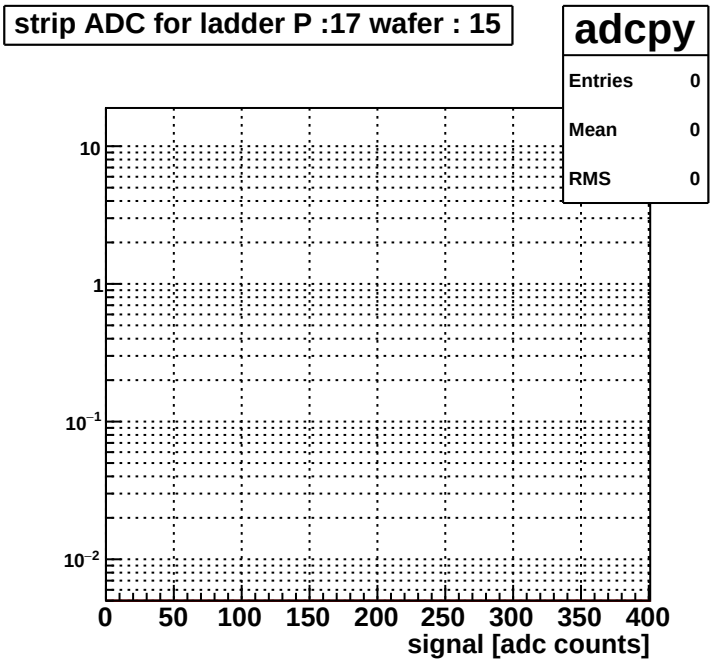

Strip adc vs. channels for ladder N : 17 wafer : 14
stripADCCHANsideN269
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

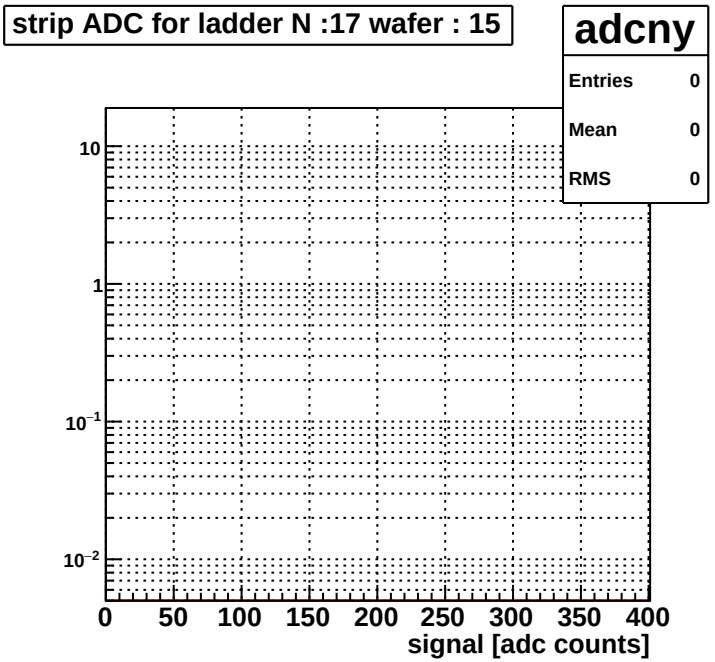

strip ADC for ladder P :17 wafer : 14
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

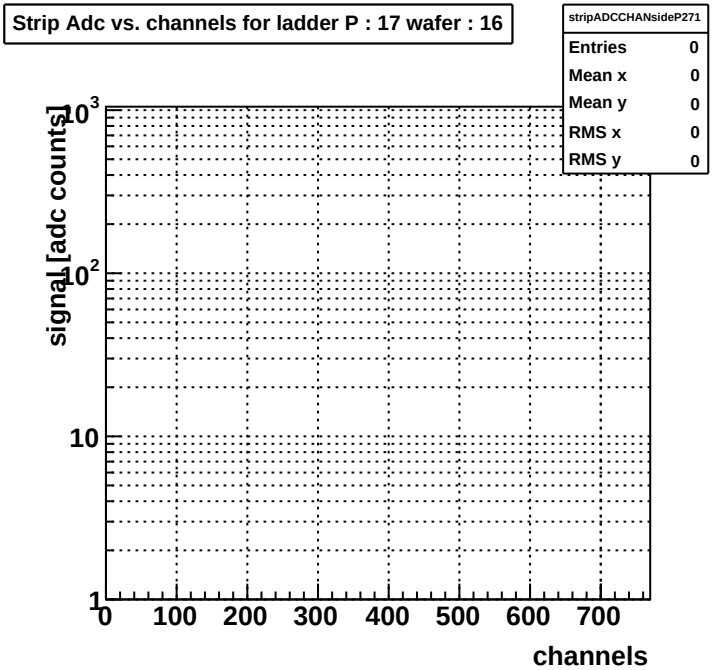

strip ADC for ladder N :17 wafer : 14
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

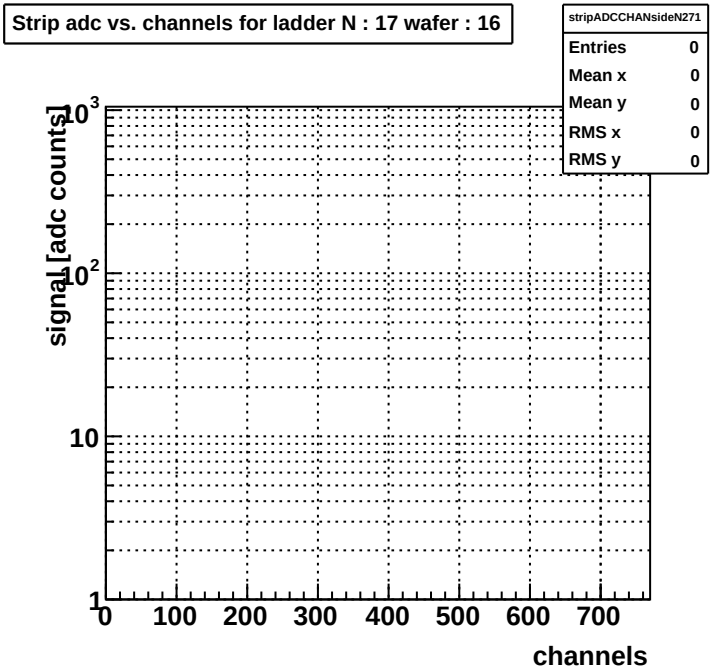

Strip Adc vs. channels for ladder P : 17 wafer : 15
stripADCCHANsideP270
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

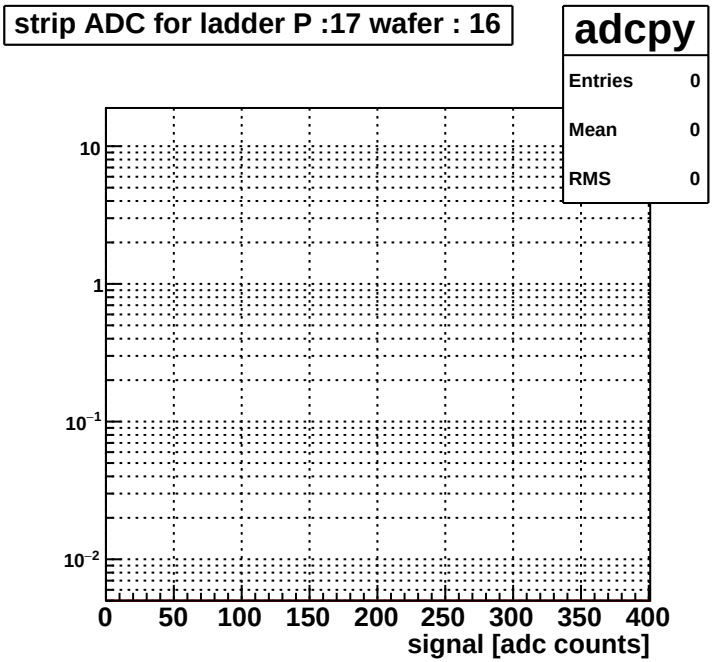

Strip adc vs. channels for ladder N : 17 wafer : 15
stripADCCHANsideN270
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

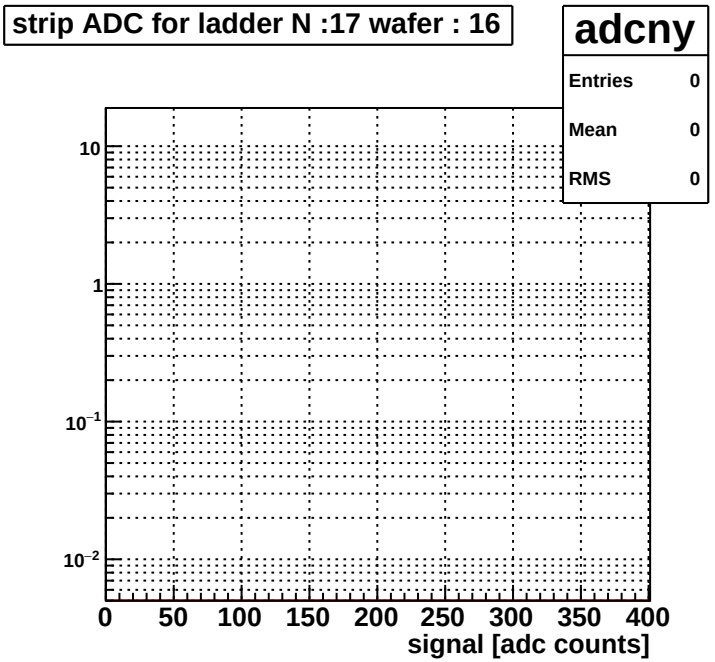

strip ADC for ladder P :17 wafer : 15
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

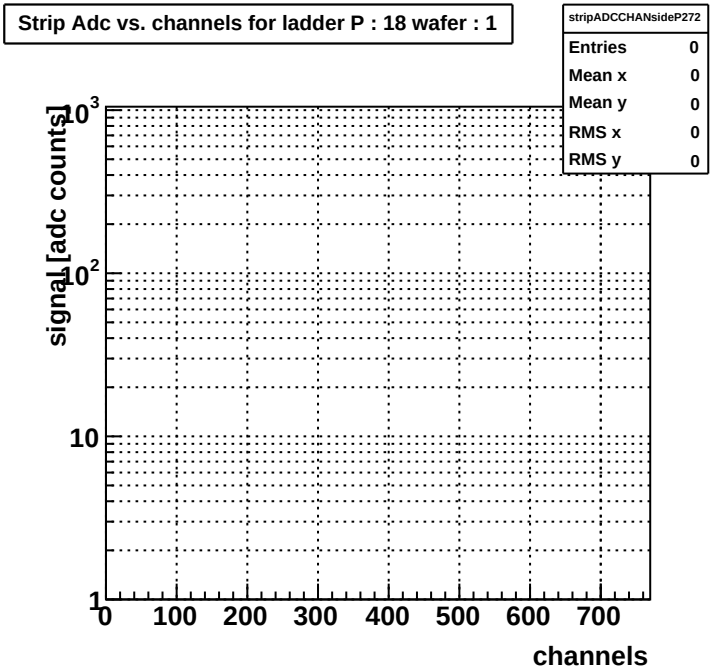

strip ADC for ladder N :17 wafer : 15
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

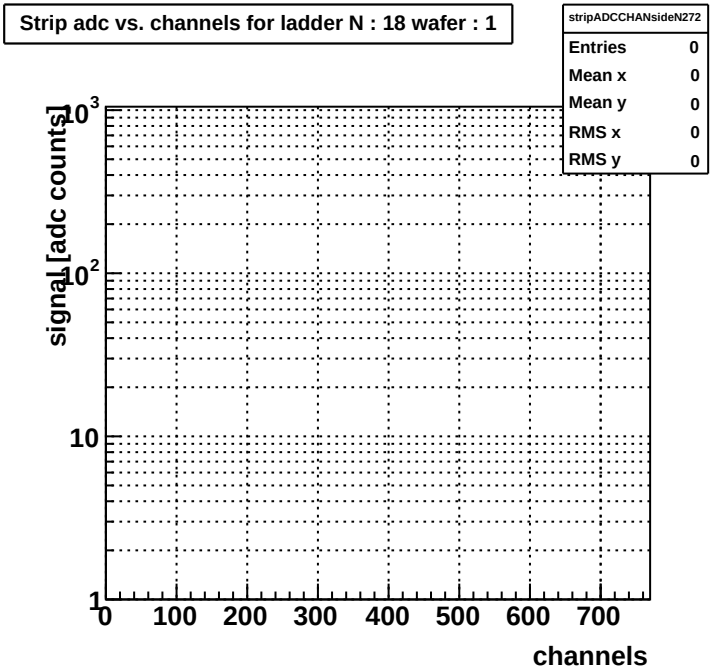

Strip Adc vs. channels for ladder P : 17 wafer : 16
stripADCCHANsideP271
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

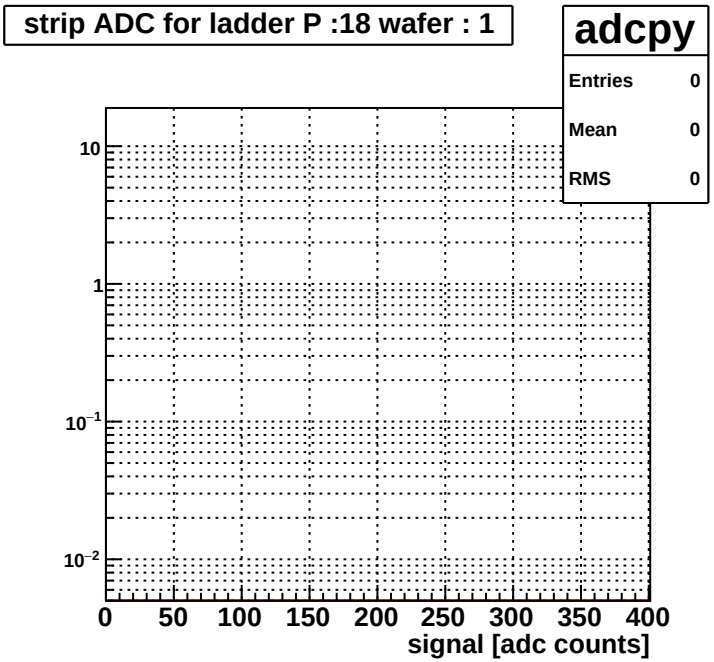

Strip adc vs. channels for ladder N : 17 wafer : 16
stripADCCHANsideN271
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

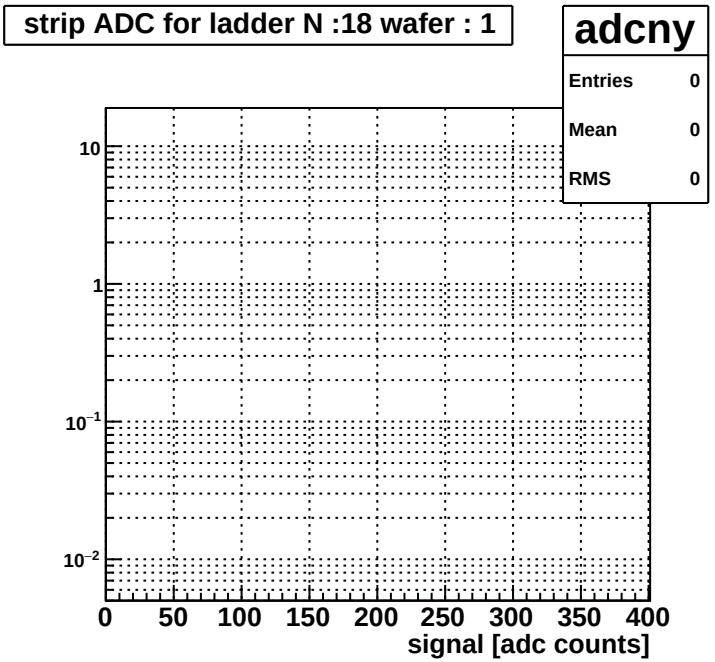

strip ADC for ladder P :17 wafer : 16
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

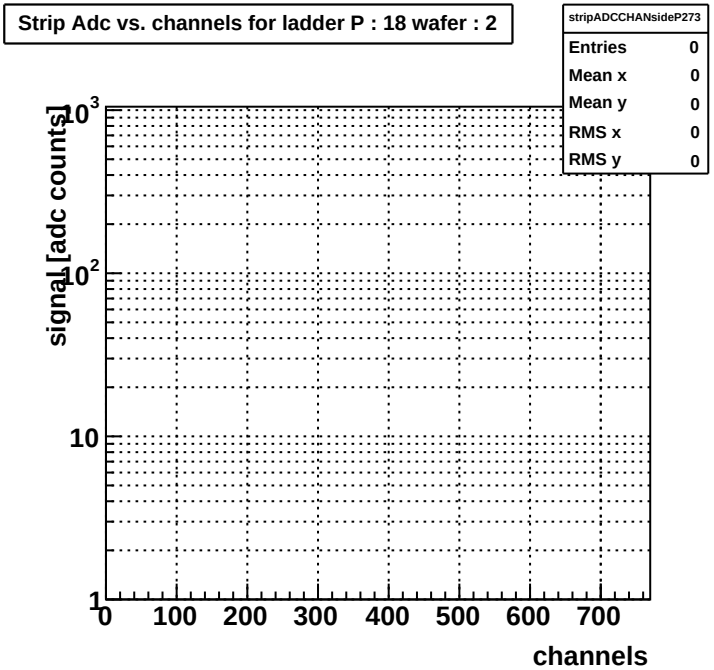

strip ADC for ladder N :17 wafer : 16
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

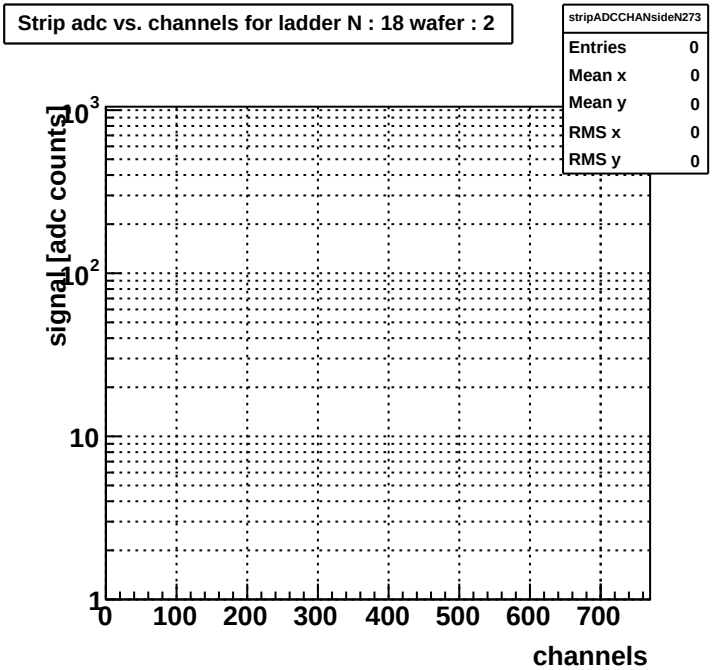

Strip Adc vs. channels for ladder P : 18 wafer : 1
stripADCCHANsideP272
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

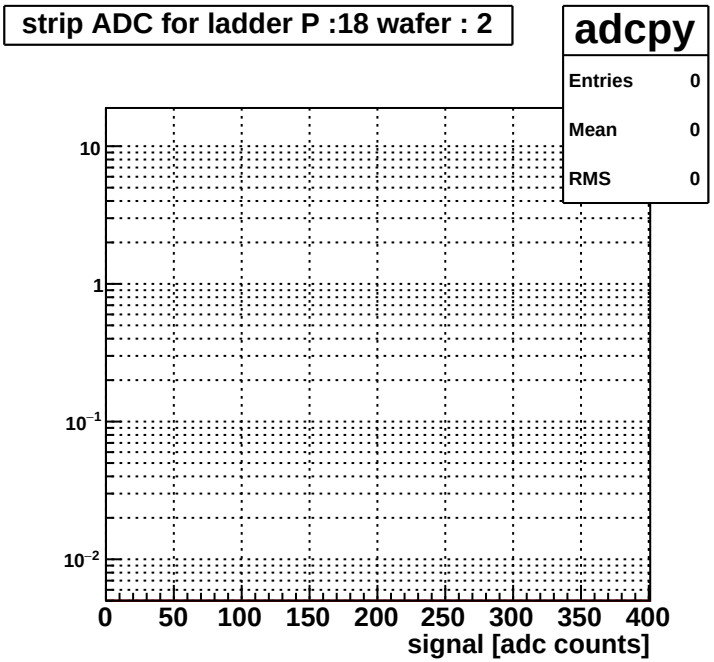

Strip adc vs. channels for ladder N : 18 wafer : 1
stripADCCHANsideN272
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

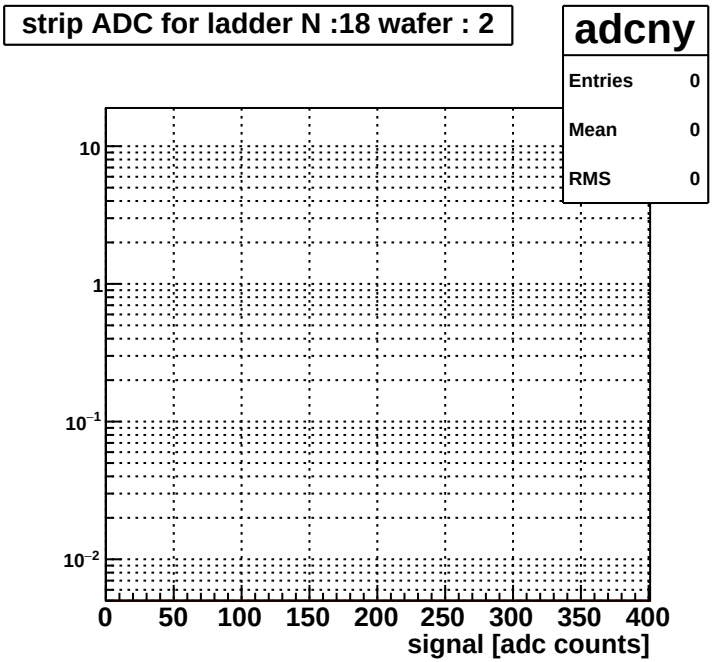

strip ADC for ladder P :18 wafer : 1
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

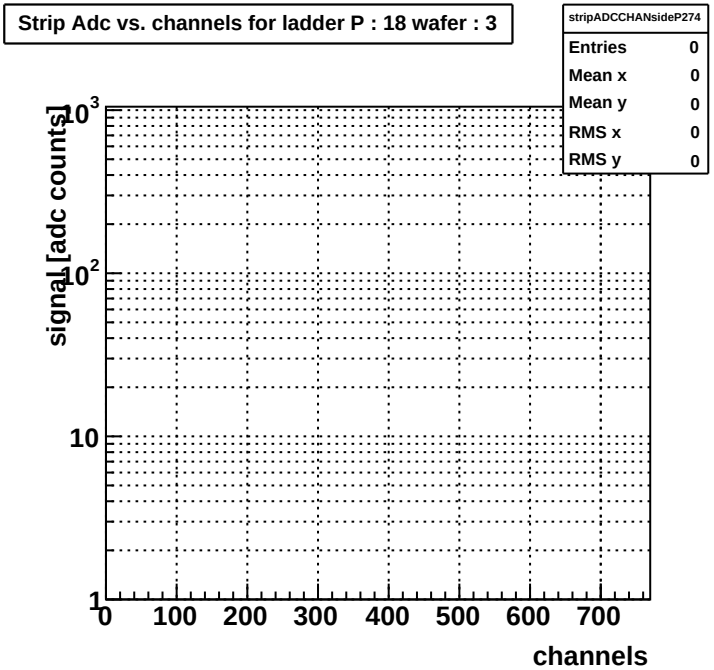

strip ADC for ladder N :18 wafer : 1
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

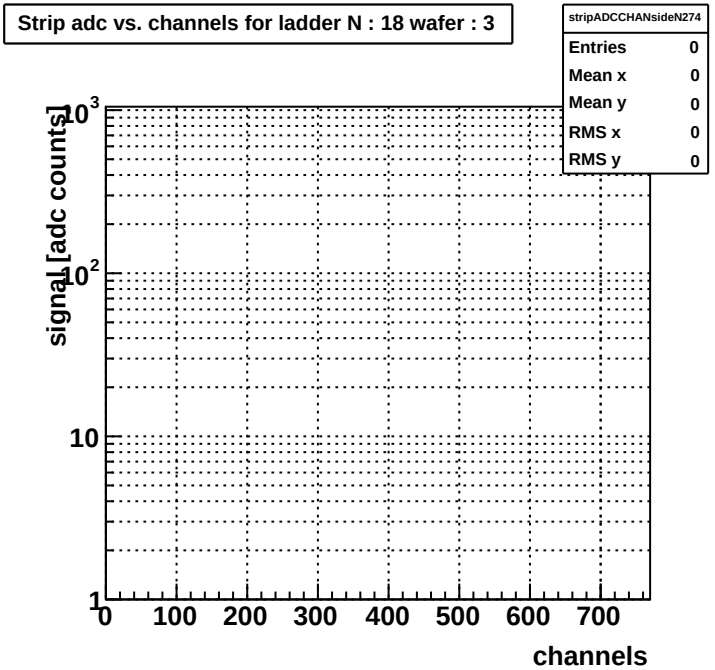

Strip Adc vs. channels for ladder P : 18 wafer : 2
stripADCCHANsideP273
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

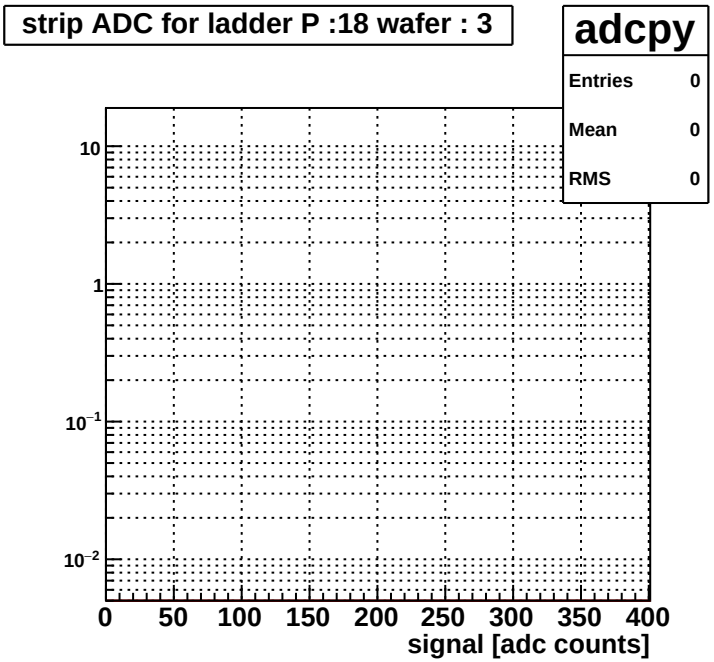

Strip adc vs. channels for ladder N : 18 wafer : 2
stripADCCHANsideN273
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

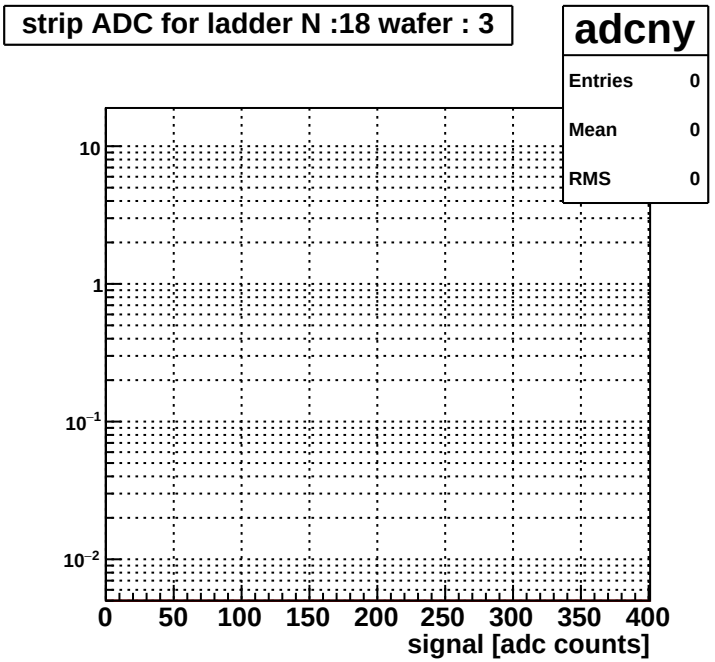

strip ADC for ladder P :18 wafer : 2
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

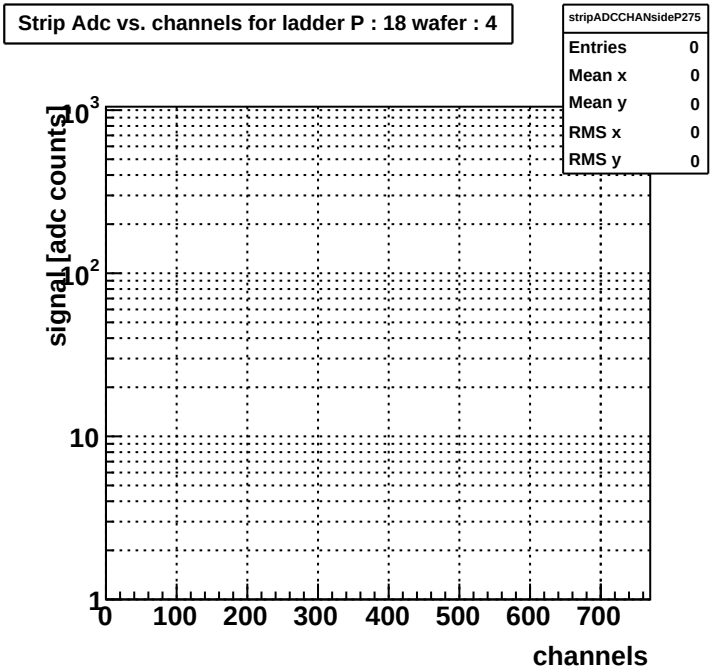

strip ADC for ladder N :18 wafer : 2
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

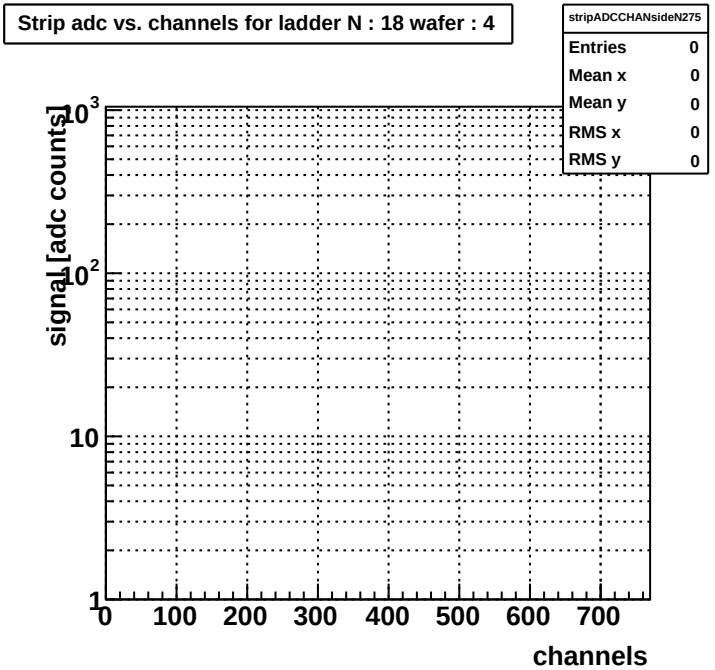

Strip Adc vs. channels for ladder P : 18 wafer : 3
stripADCCHANsideP274
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

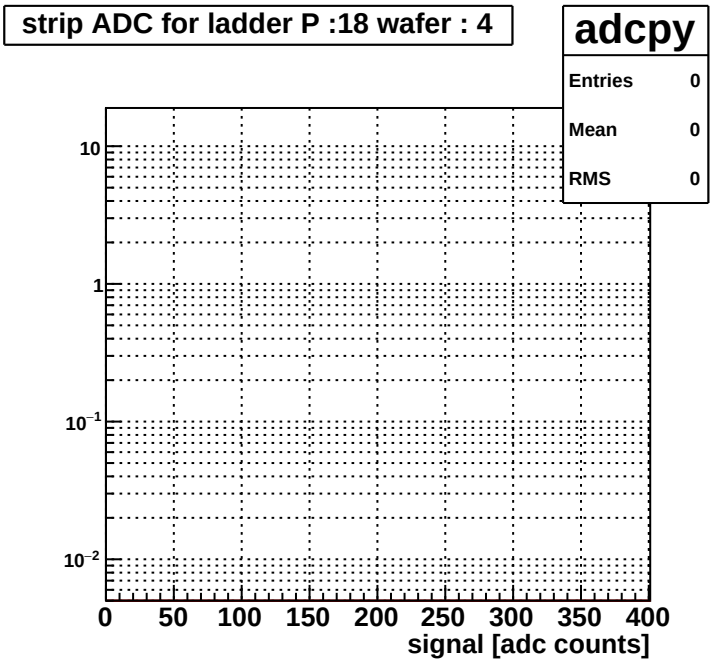

Strip adc vs. channels for ladder N : 18 wafer : 3
stripADCCHANsideN274
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

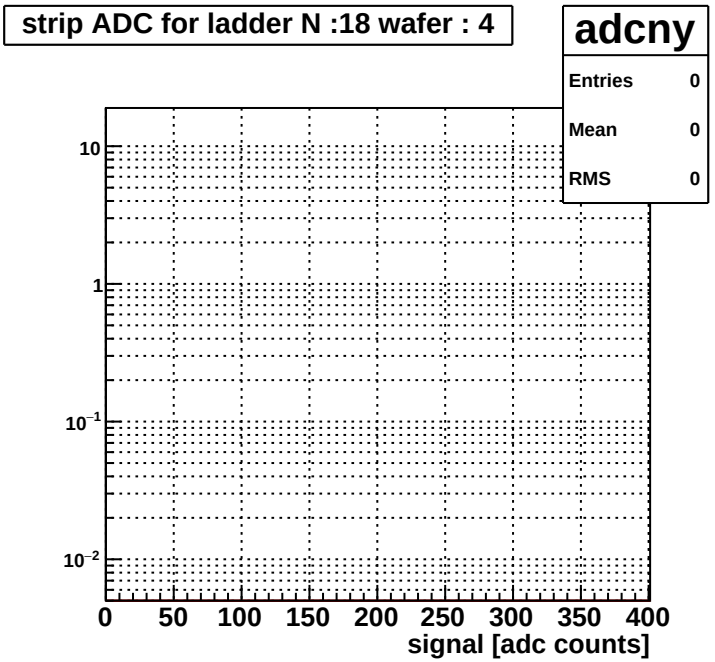

strip ADC for ladder P :18 wafer : 3
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

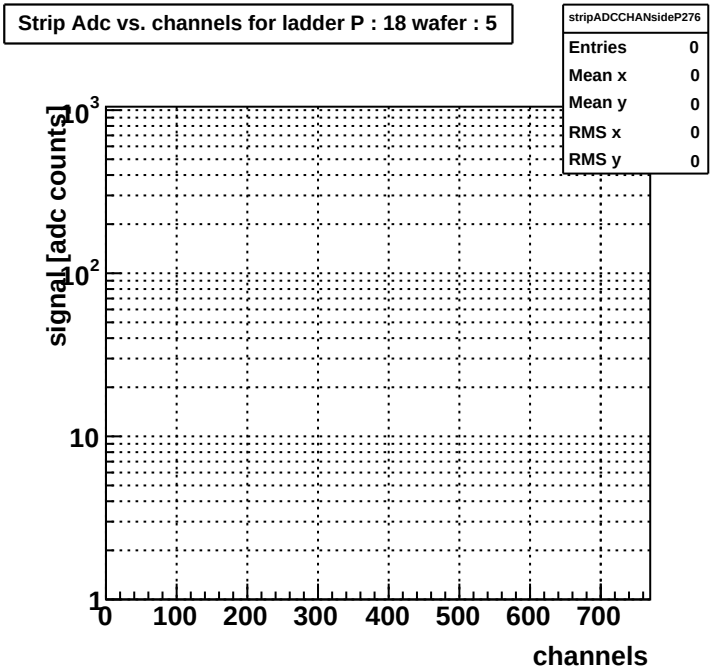

strip ADC for ladder N :18 wafer : 3
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

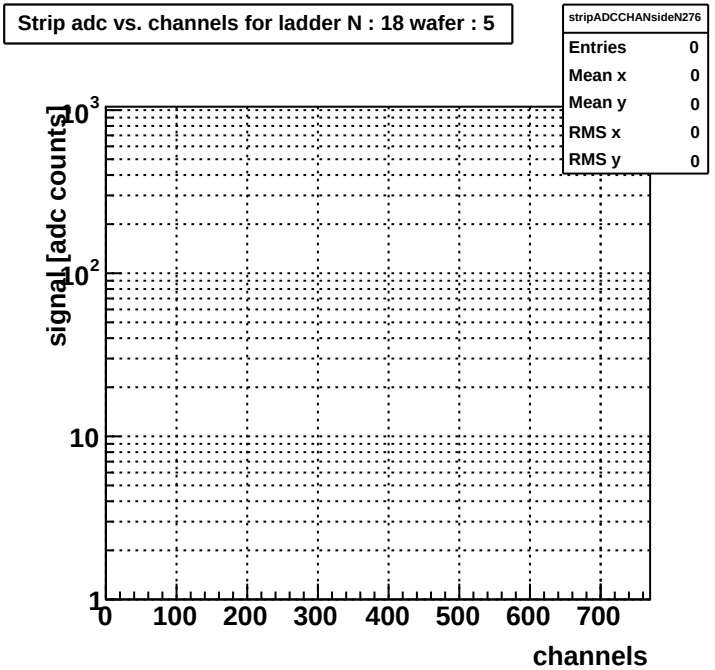

Strip Adc vs. channels for ladder P : 18 wafer : 4
stripADCCHANsideP275
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

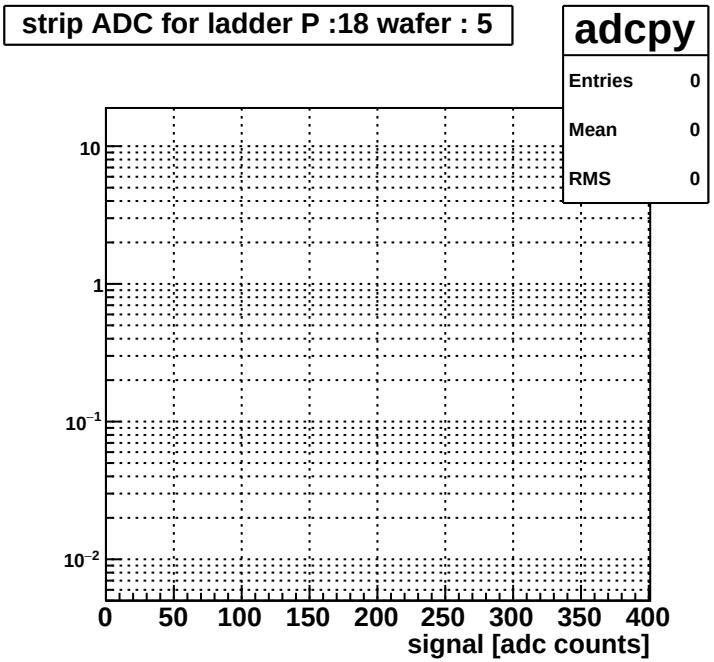

Strip adc vs. channels for ladder N : 18 wafer : 4
stripADCCHANsideN275
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

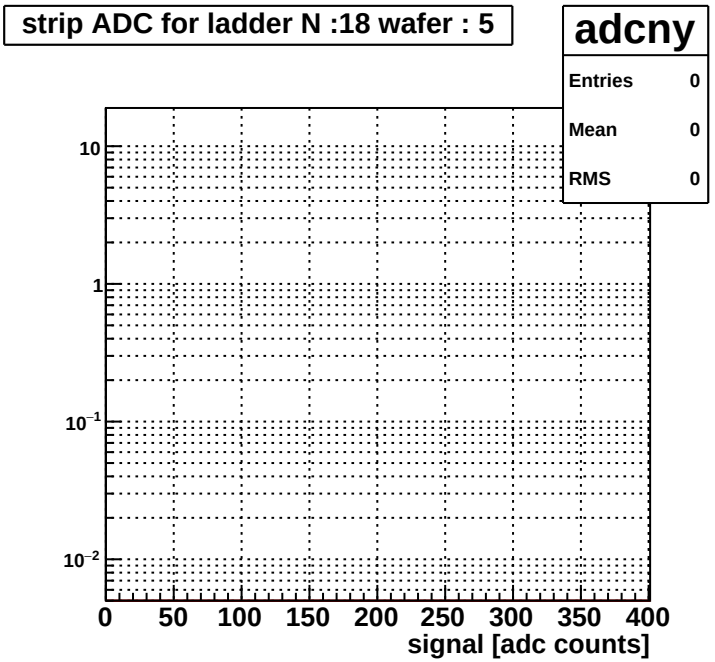

strip ADC for ladder P :18 wafer : 4
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

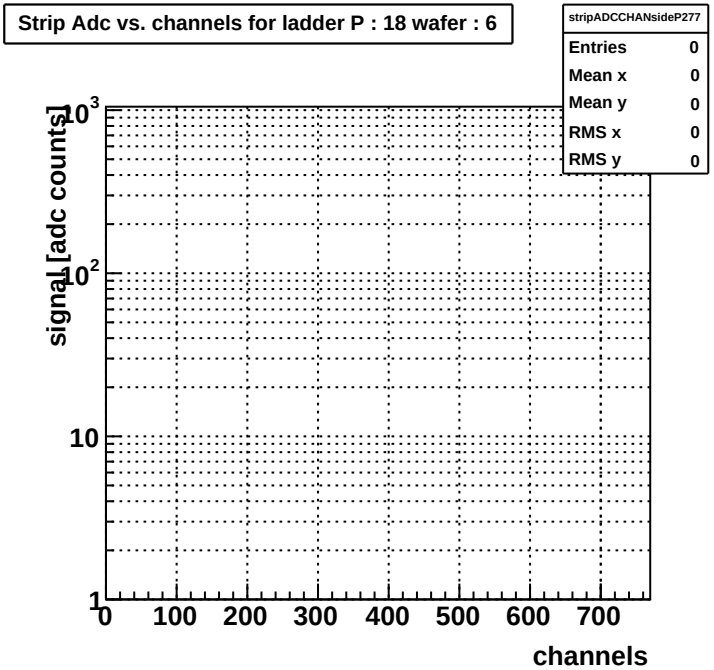

strip ADC for ladder N :18 wafer : 4
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

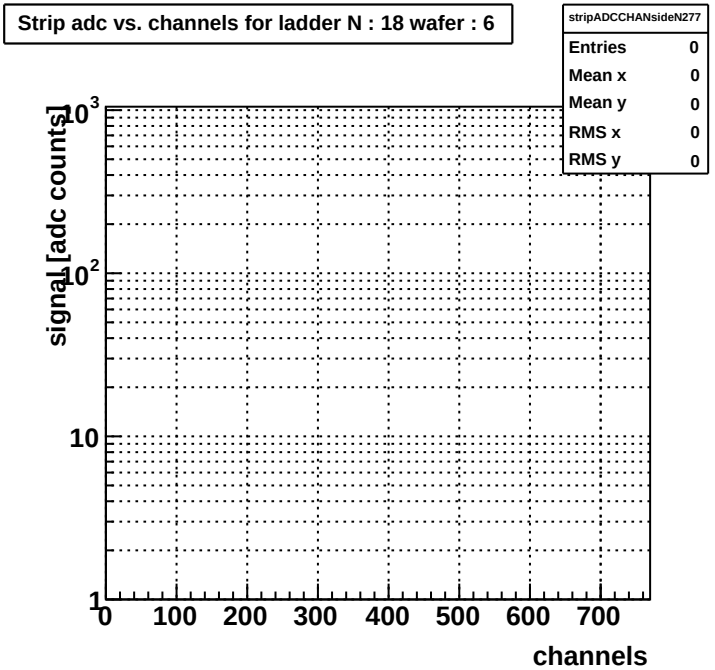

Strip Adc vs. channels for ladder P : 18 wafer : 5
stripADCCHANsideP276
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

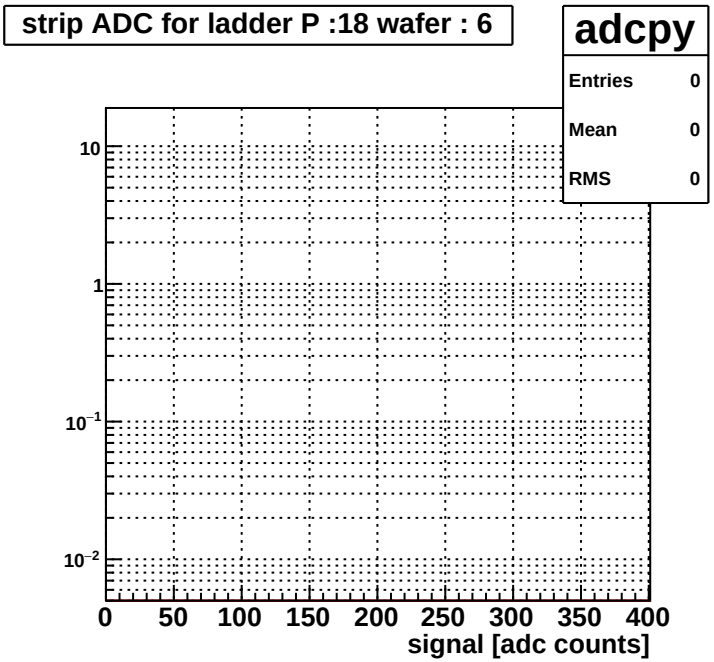

Strip adc vs. channels for ladder N : 18 wafer : 5
stripADCCHANsideN276
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

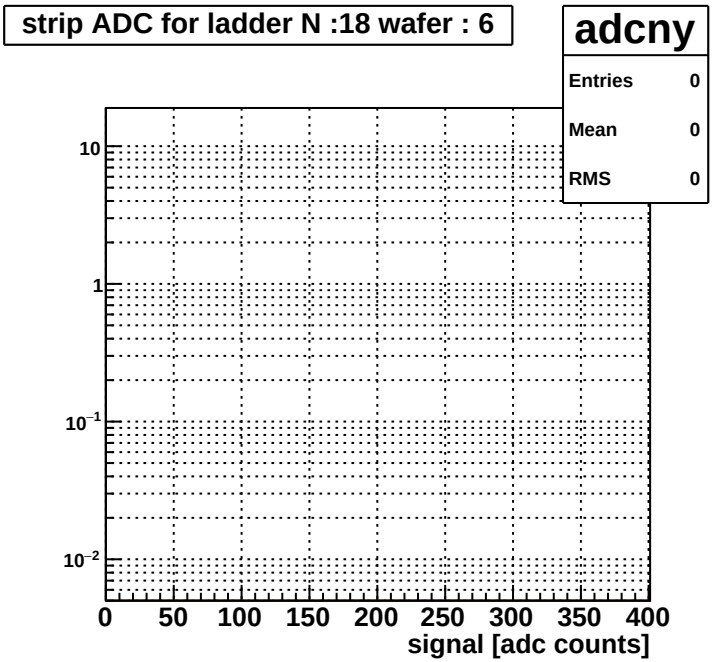

strip ADC for ladder P :18 wafer : 5
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

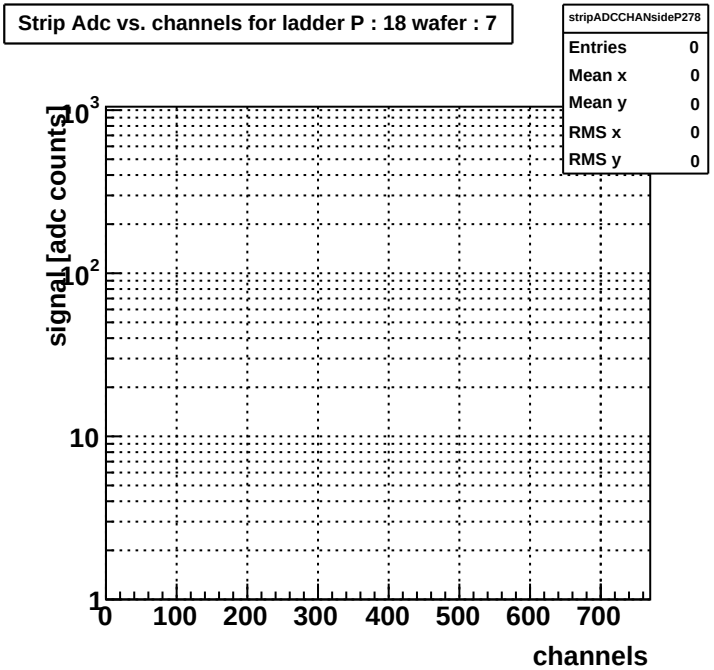

strip ADC for ladder N :18 wafer : 5
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

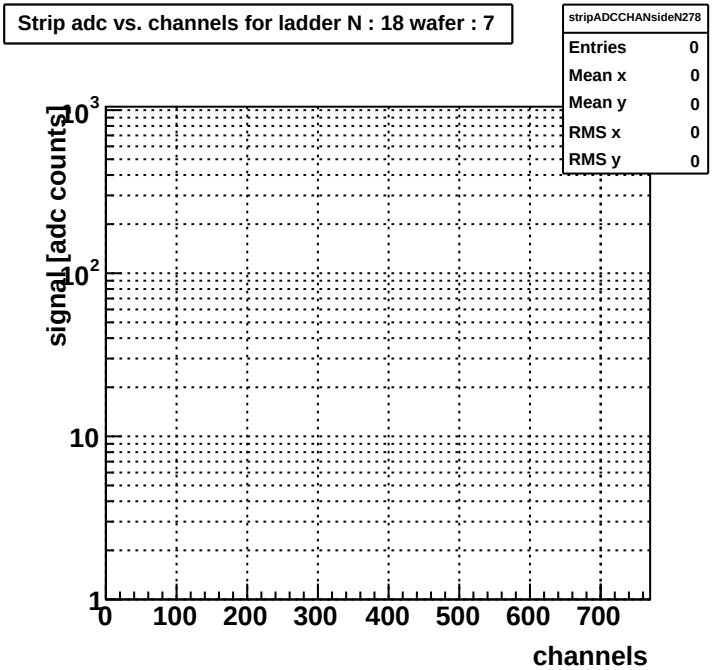

Strip Adc vs. channels for ladder P : 18 wafer : 6
stripADCCHANsideP277
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

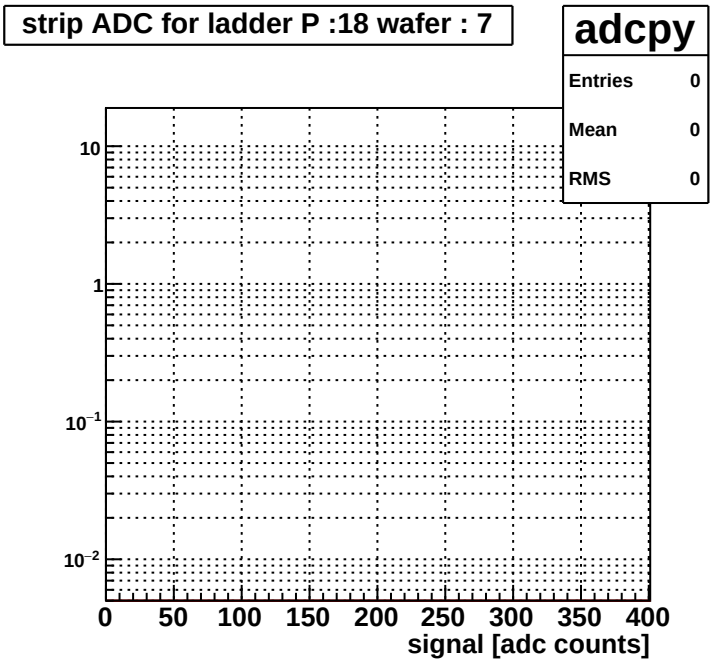

Strip adc vs. channels for ladder N : 18 wafer : 6
stripADCCHANsideN277
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

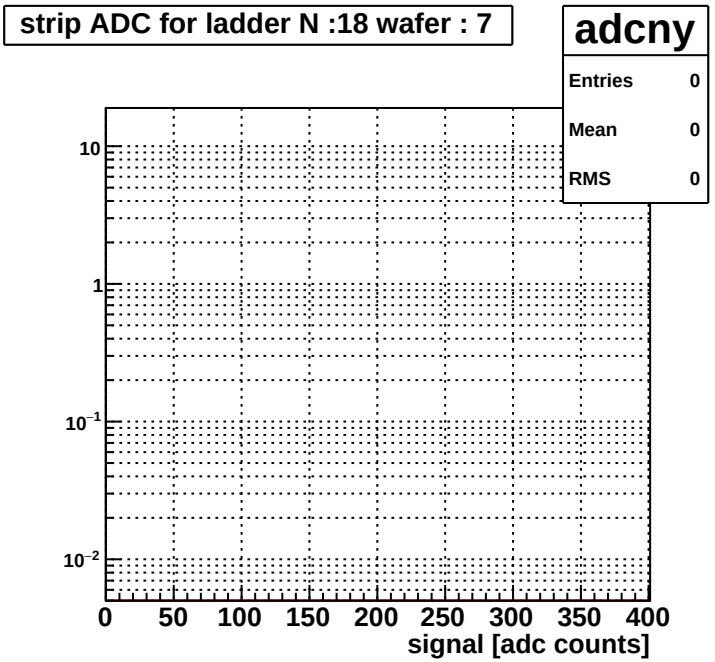

strip ADC for ladder P :18 wafer : 6
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

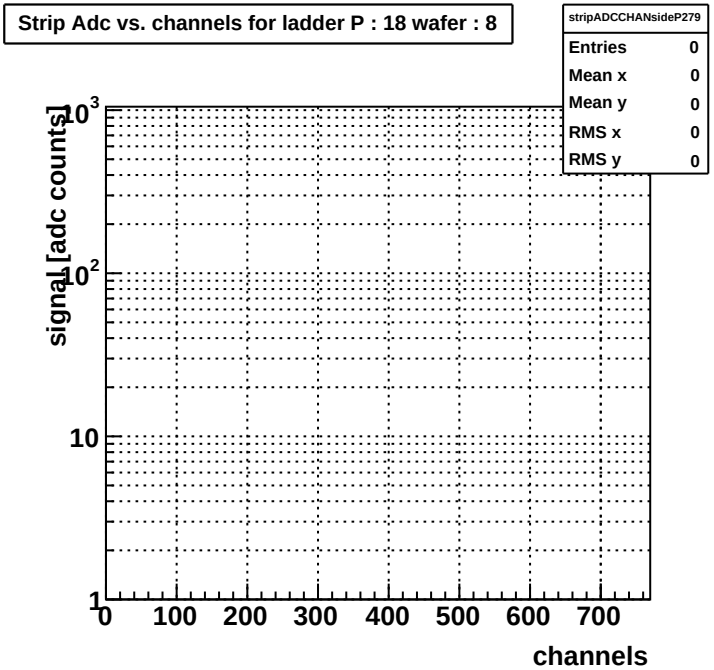

strip ADC for ladder N :18 wafer : 6
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

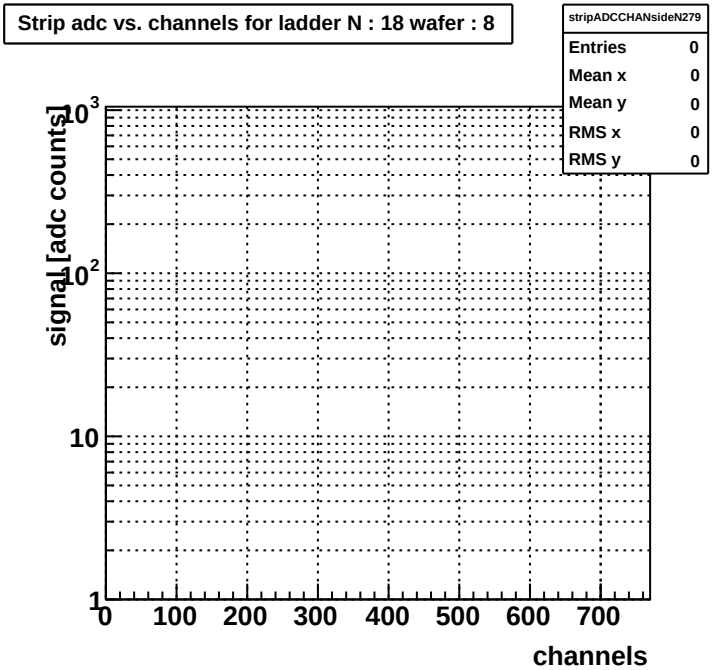

Strip Adc vs. channels for ladder P : 18 wafer : 7
stripADCCHANsideP278
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

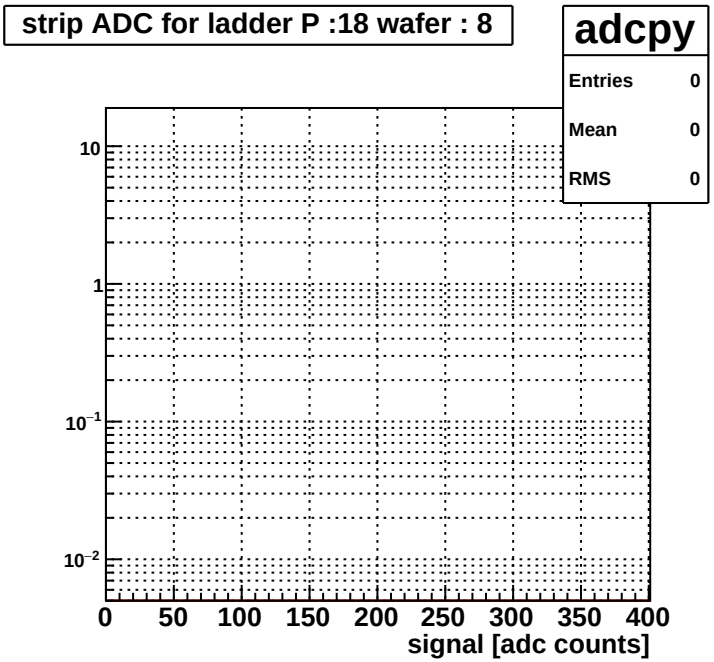

Strip adc vs. channels for ladder N : 18 wafer : 7
stripADCCHANsideN278
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

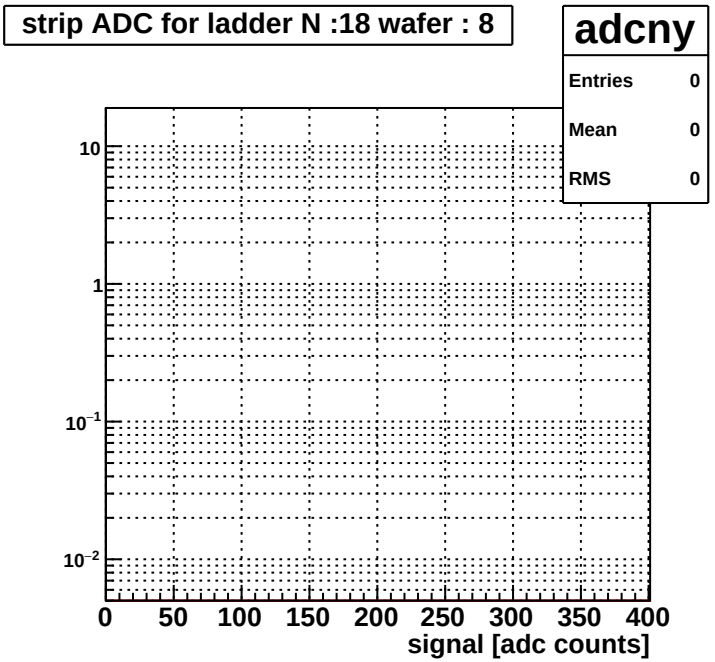

strip ADC for ladder P :18 wafer : 7
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

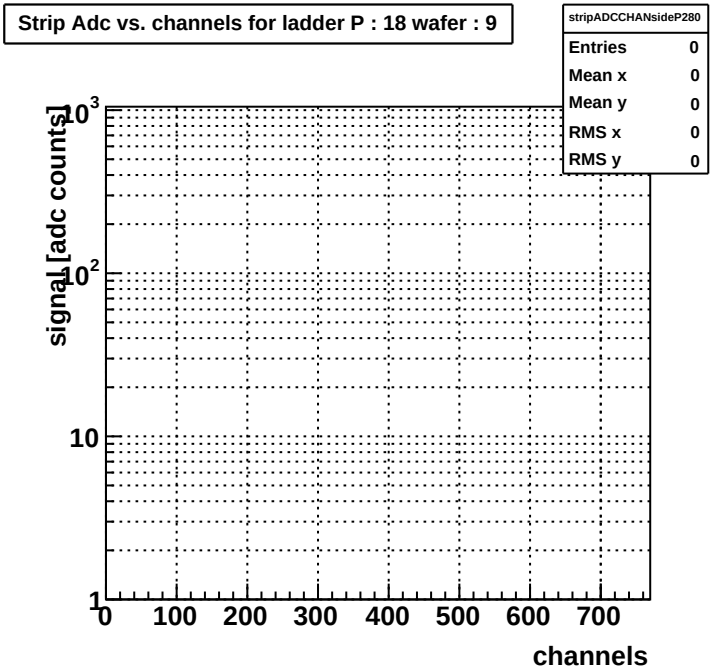

strip ADC for ladder N :18 wafer : 7
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

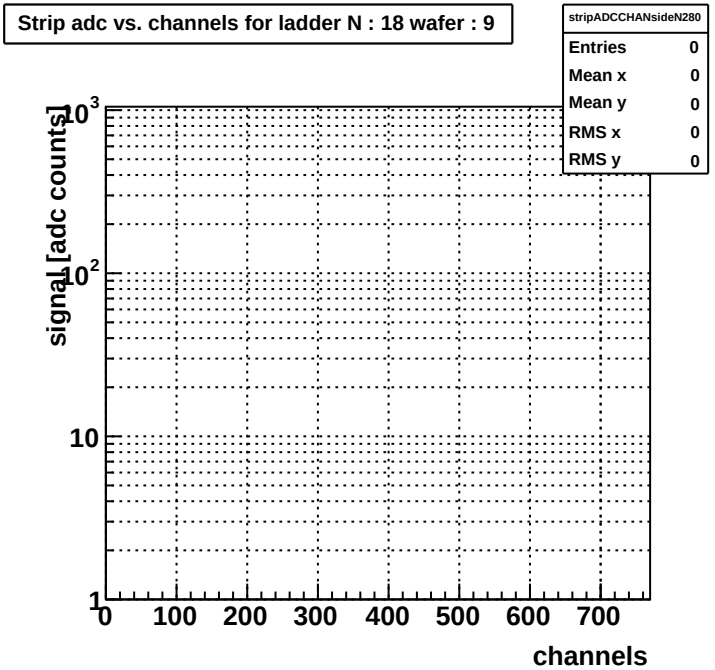

Strip Adc vs. channels for ladder P : 18 wafer : 8
stripADCCHANsideP279
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

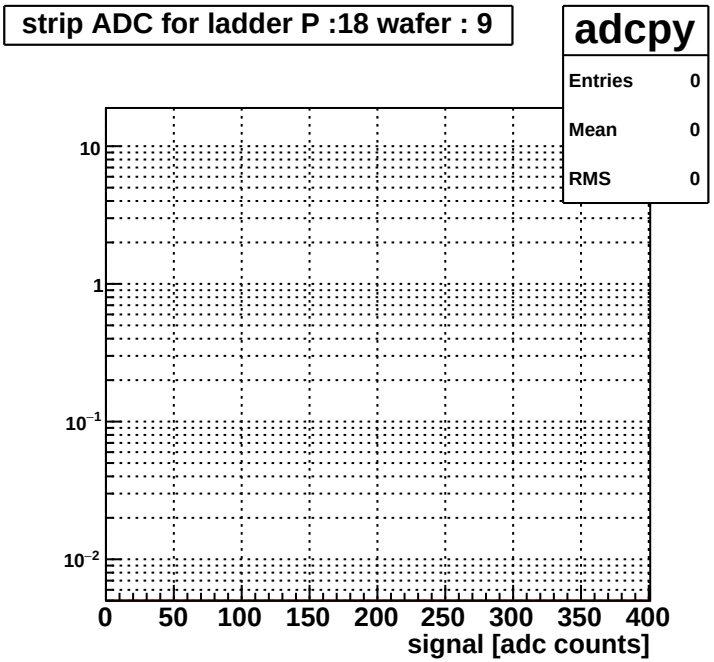

Strip adc vs. channels for ladder N : 18 wafer : 8
stripADCCHANsideN279
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

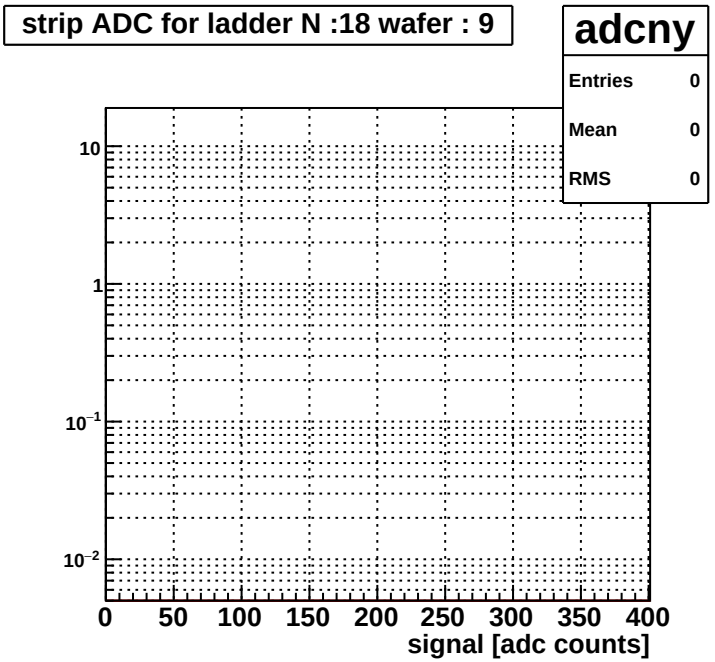

strip ADC for ladder P :18 wafer : 8
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

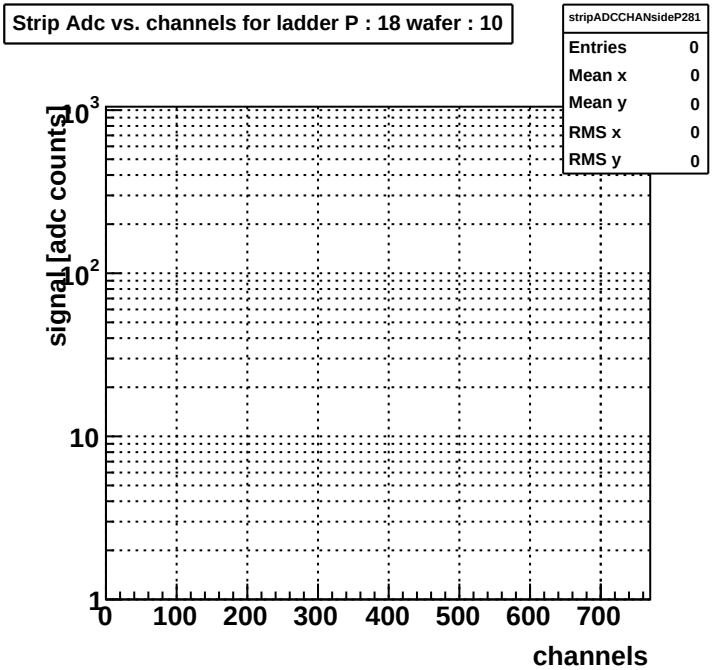

strip ADC for ladder N :18 wafer : 8
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

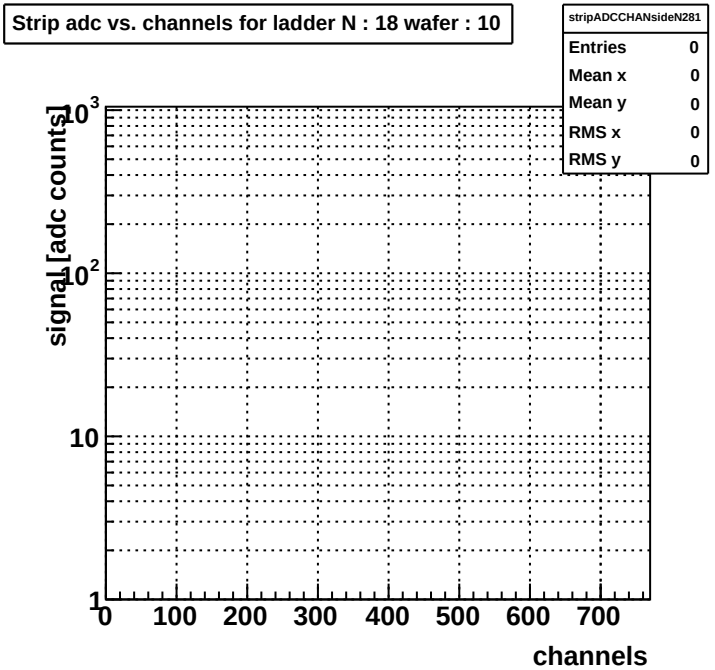

Strip Adc vs. channels for ladder P : 18 wafer : 9
stripADCCHANsideP280
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

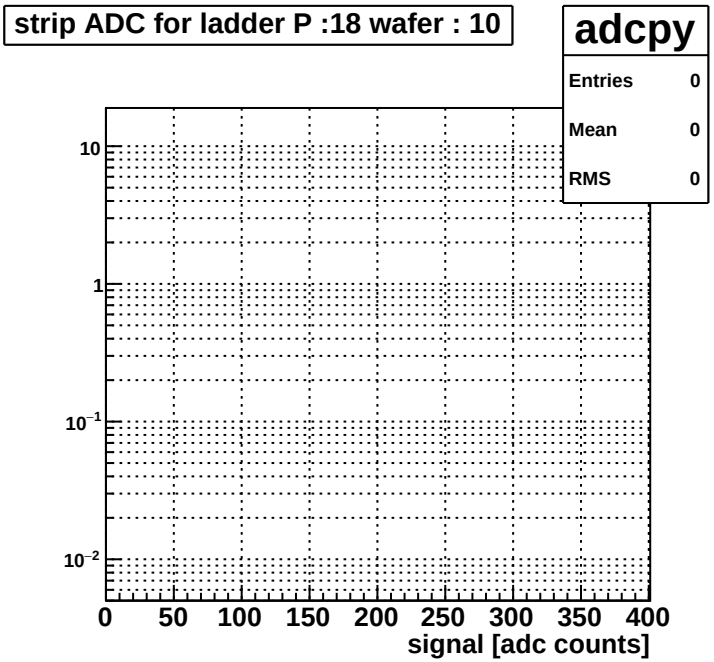

Strip adc vs. channels for ladder N : 18 wafer : 9
stripADCCHANsideN280
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

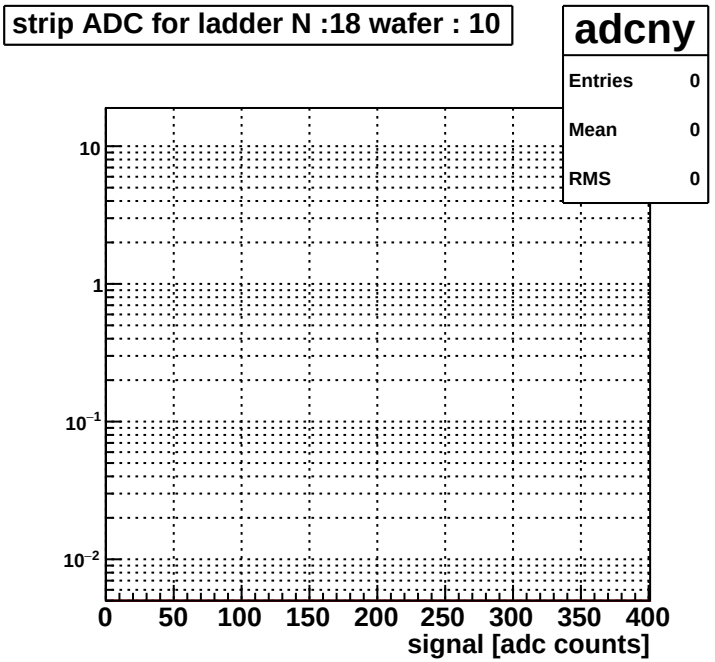

strip ADC for ladder P :18 wafer : 9
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

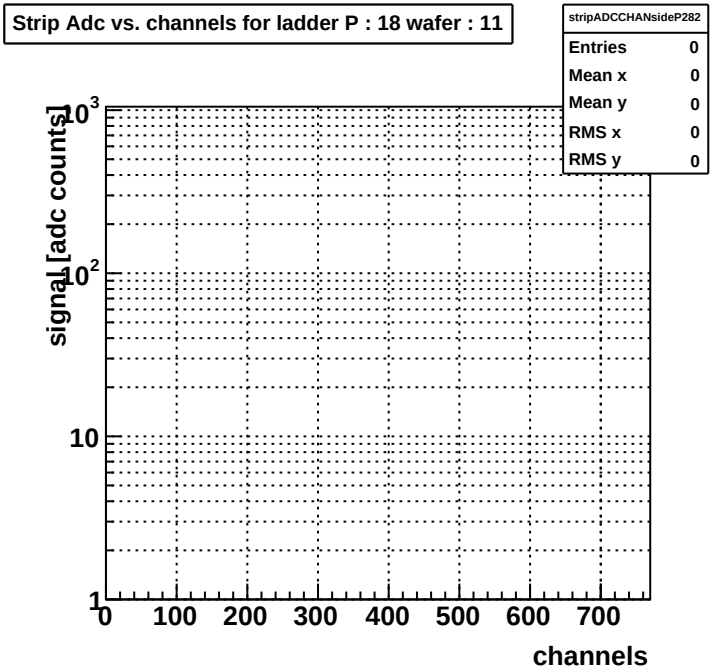

strip ADC for ladder N :18 wafer : 9
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

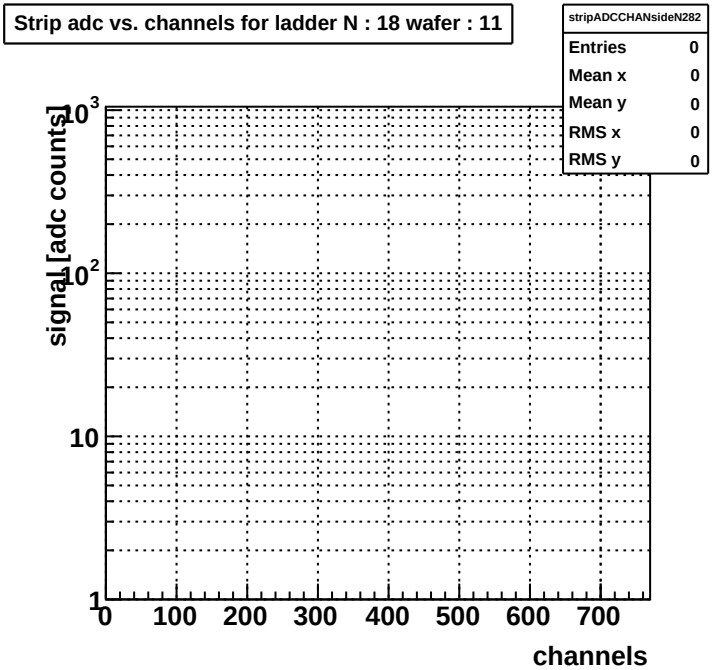

Strip Adc vs. channels for ladder P : 18 wafer : 10
stripADCCHANsideP281
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

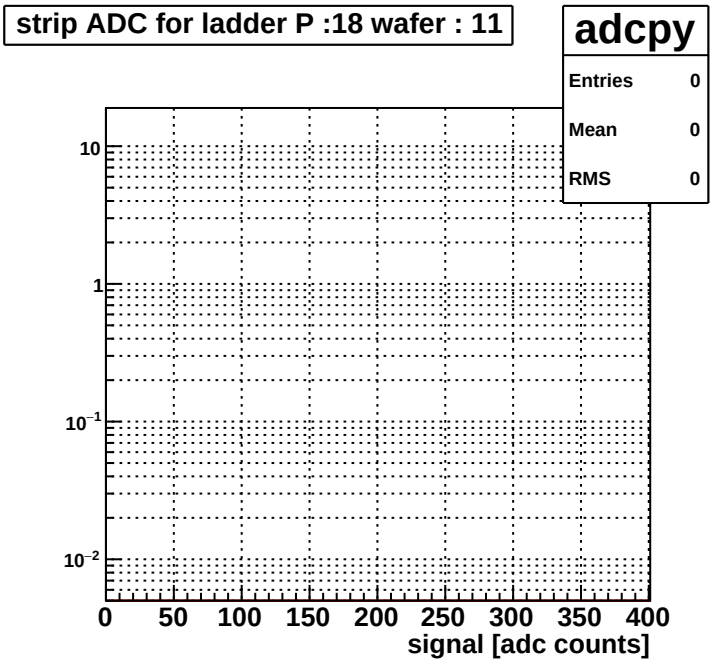

Strip adc vs. channels for ladder N : 18 wafer : 10
stripADCCHANsideN281
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

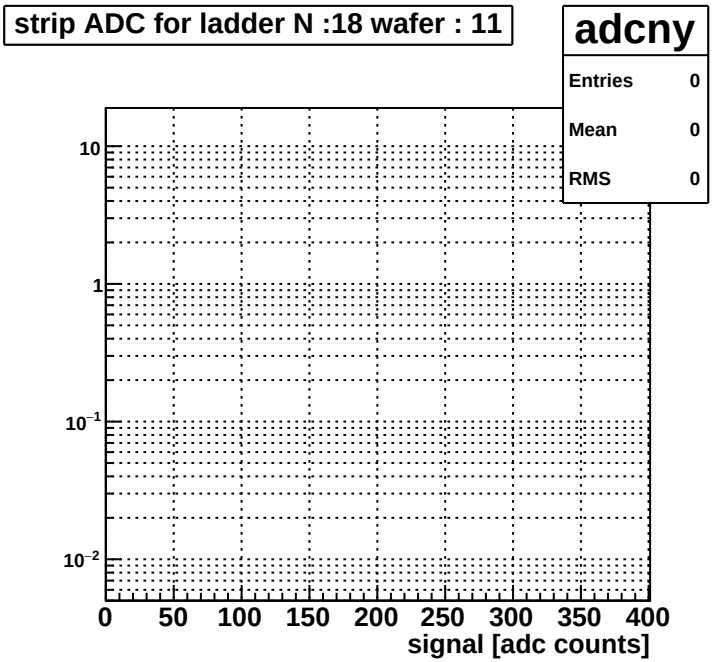

strip ADC for ladder P :18 wafer : 10
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

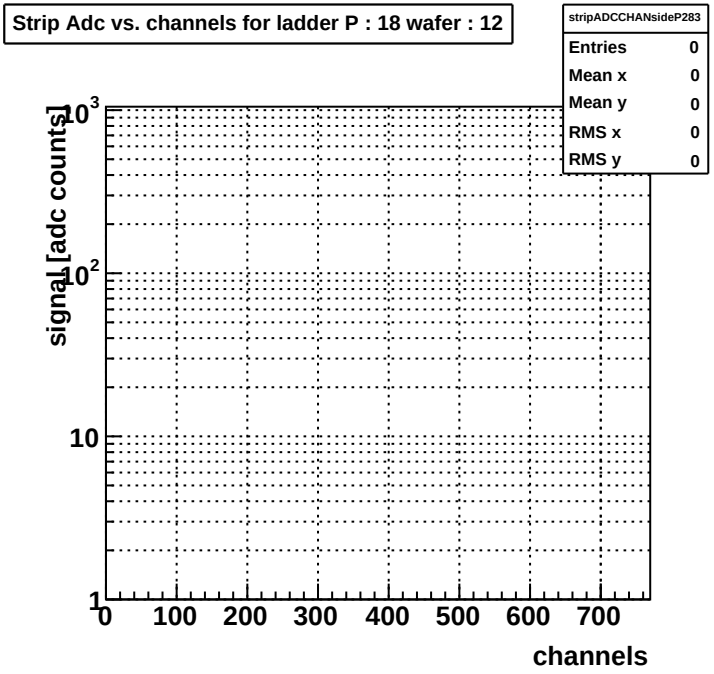

strip ADC for ladder N :18 wafer : 10
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

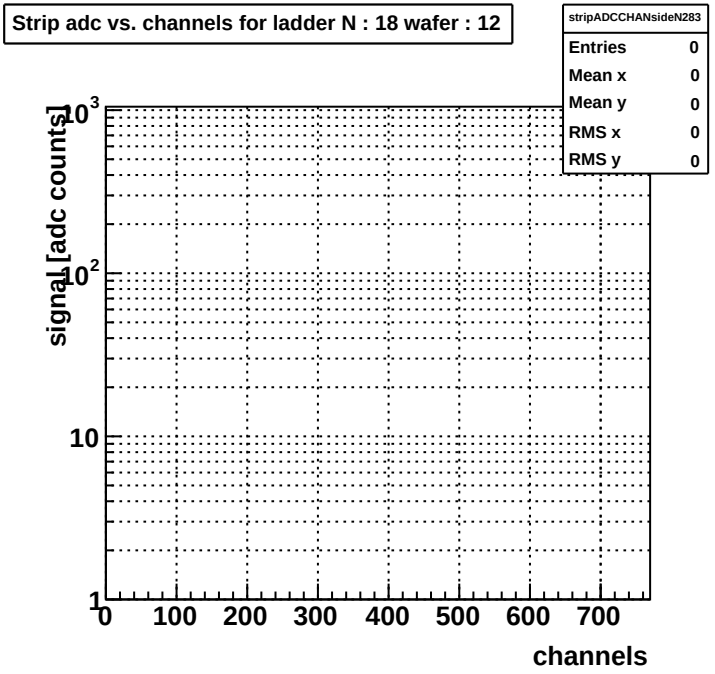

Strip Adc vs. channels for ladder P : 18 wafer : 11
stripADCCHANsideP282
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

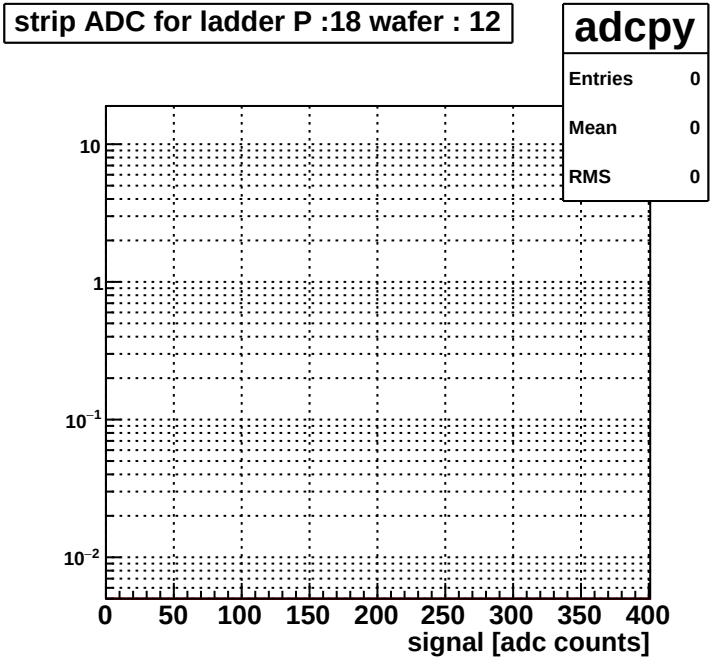

Strip adc vs. channels for ladder N : 18 wafer : 11
stripADCCHANsideN282
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

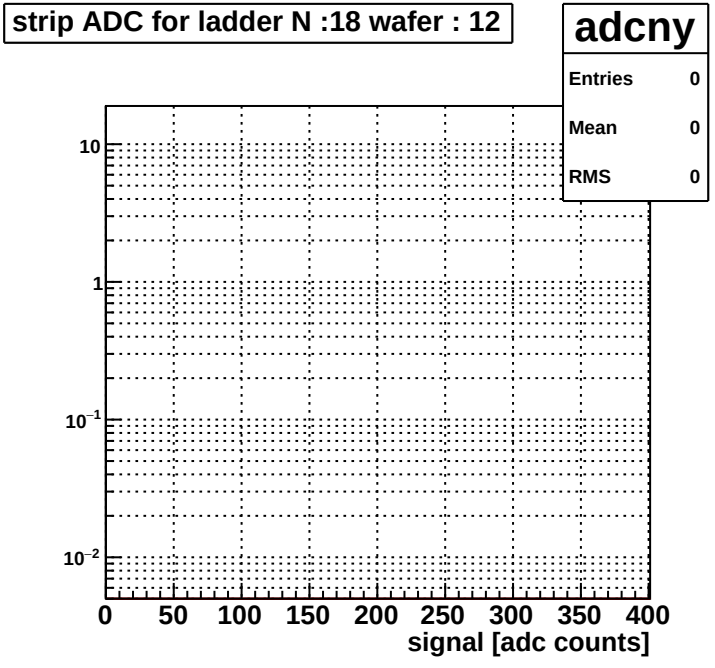

strip ADC for ladder P :18 wafer : 11
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

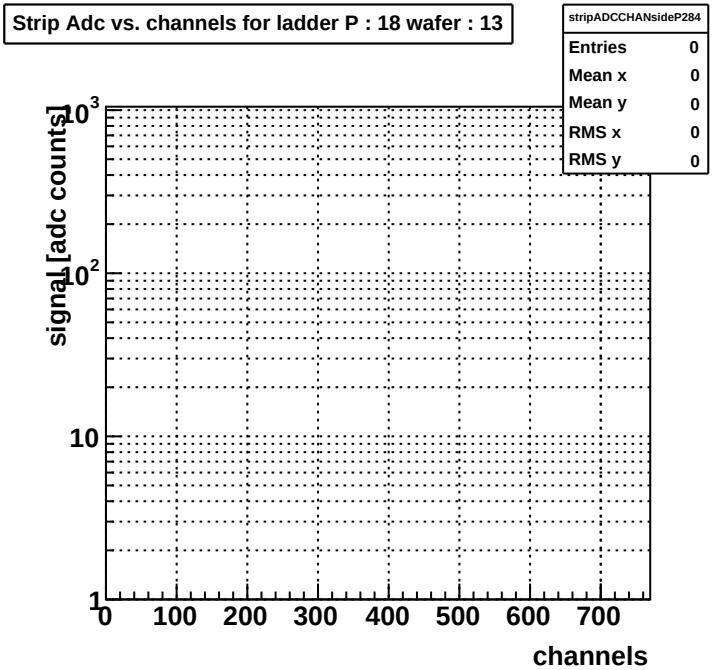

strip ADC for ladder N :18 wafer : 11
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

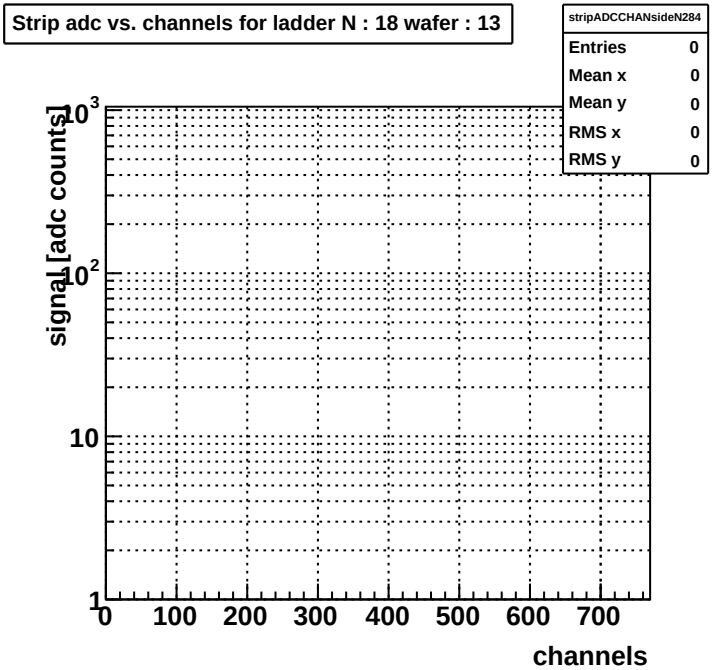

Strip Adc vs. channels for ladder P : 18 wafer : 12
stripADCCHANsideP283
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

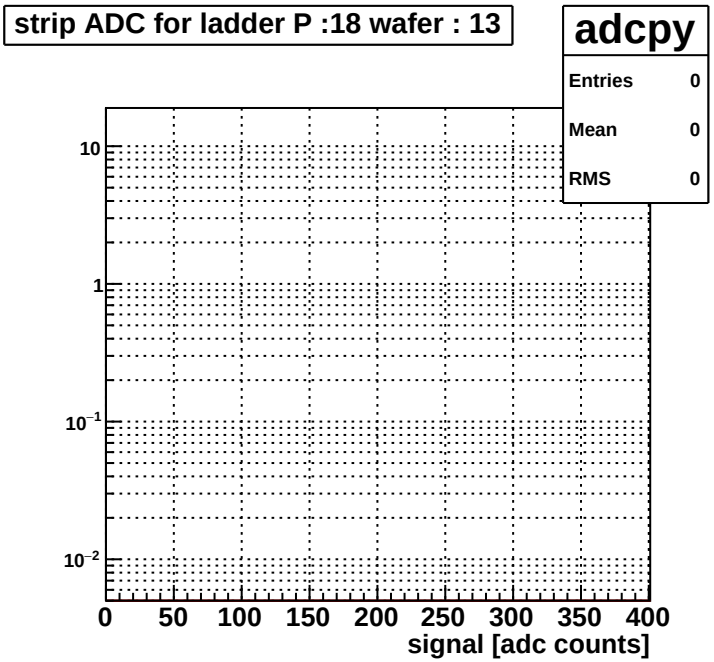

Strip adc vs. channels for ladder N : 18 wafer : 12
stripADCCHANsideN283
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

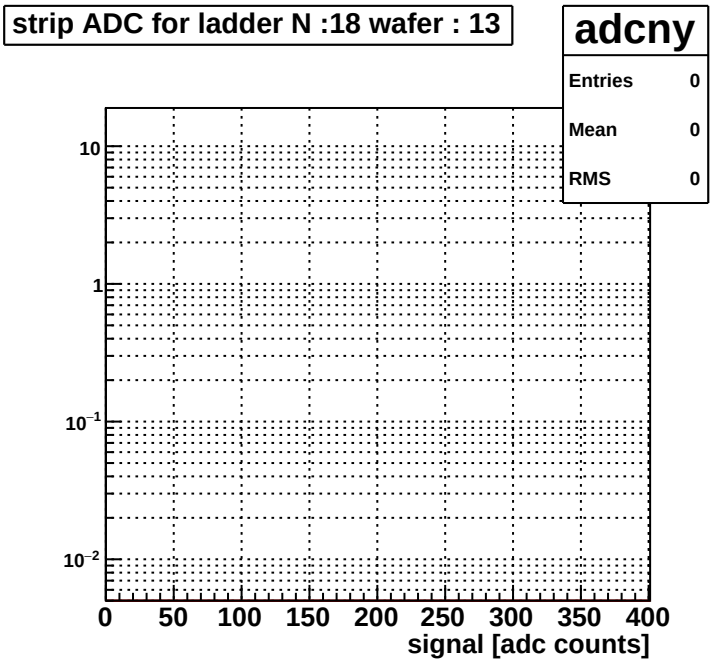

strip ADC for ladder P :18 wafer : 12
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

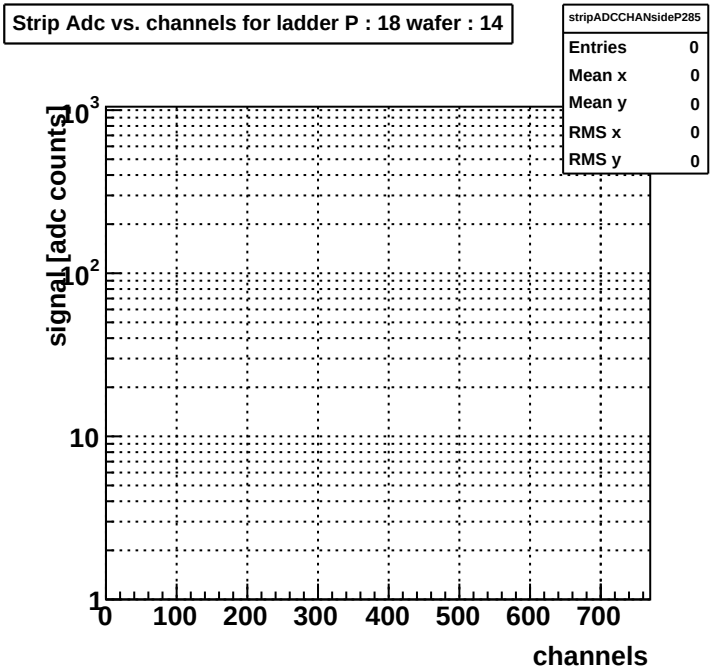

strip ADC for ladder N :18 wafer : 12
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

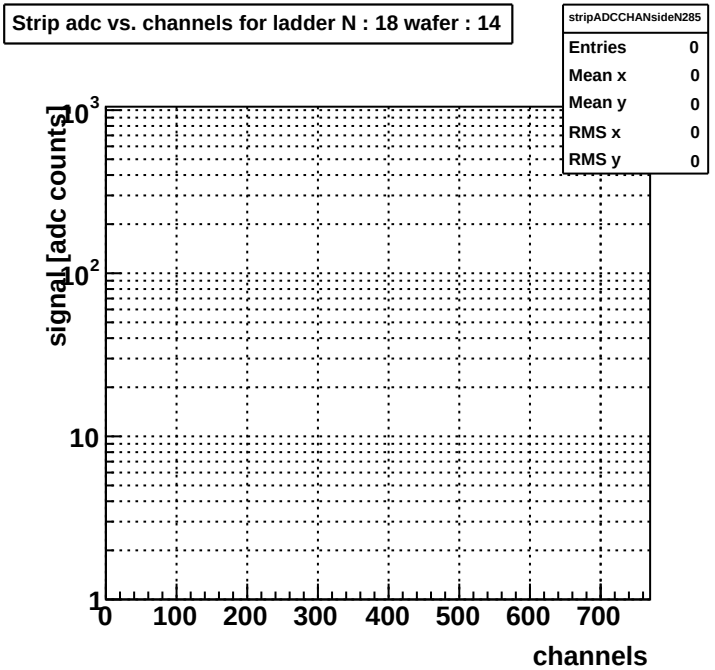

Strip Adc vs. channels for ladder P : 18 wafer : 13
stripADCCHANsideP284
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

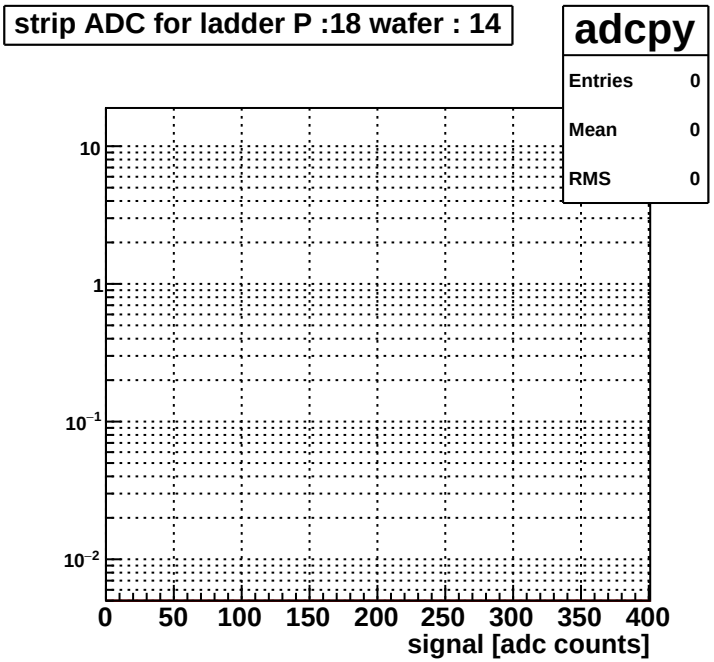

Strip adc vs. channels for ladder N : 18 wafer : 13
stripADCCHANsideN284
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

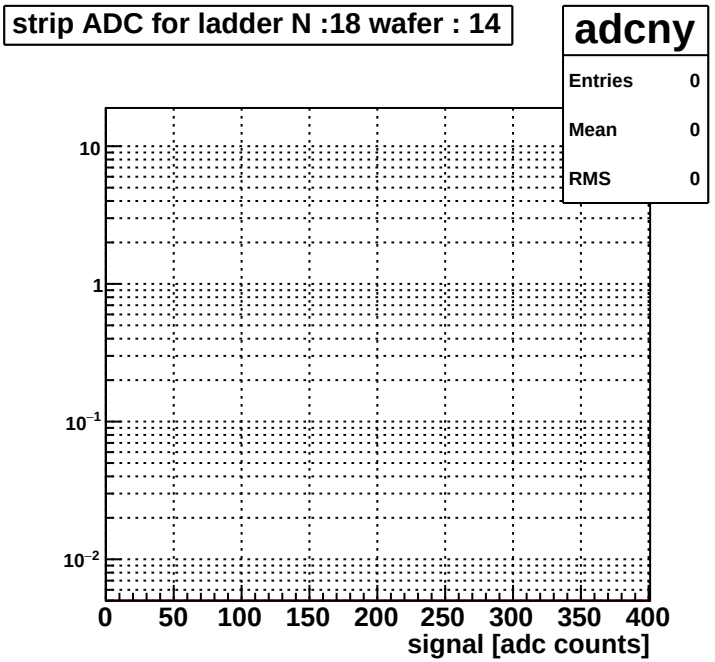

strip ADC for ladder P :18 wafer : 13
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

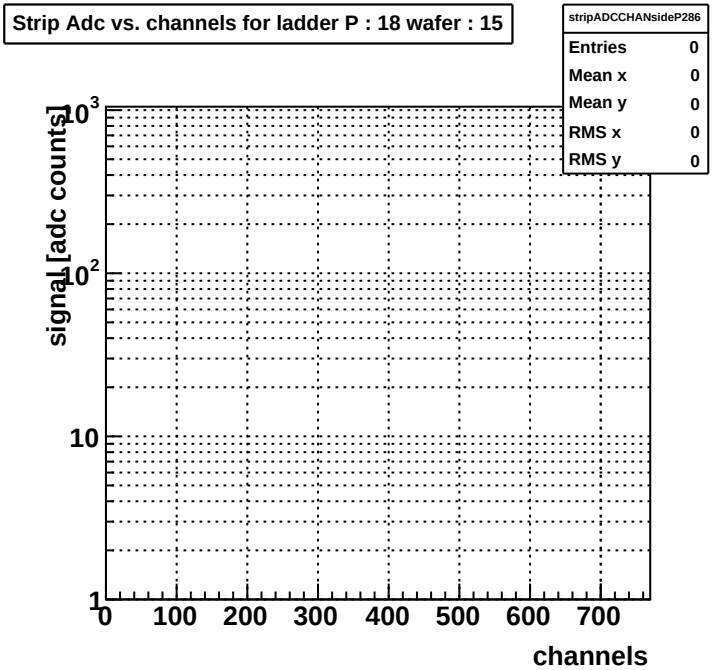

strip ADC for ladder N :18 wafer : 13
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

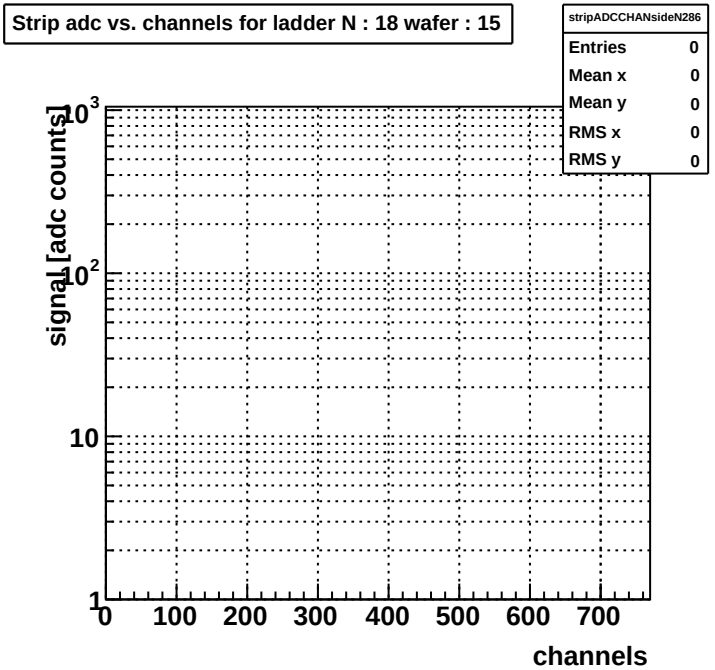

Strip Adc vs. channels for ladder P : 18 wafer : 14
stripADCCHANsideP285
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

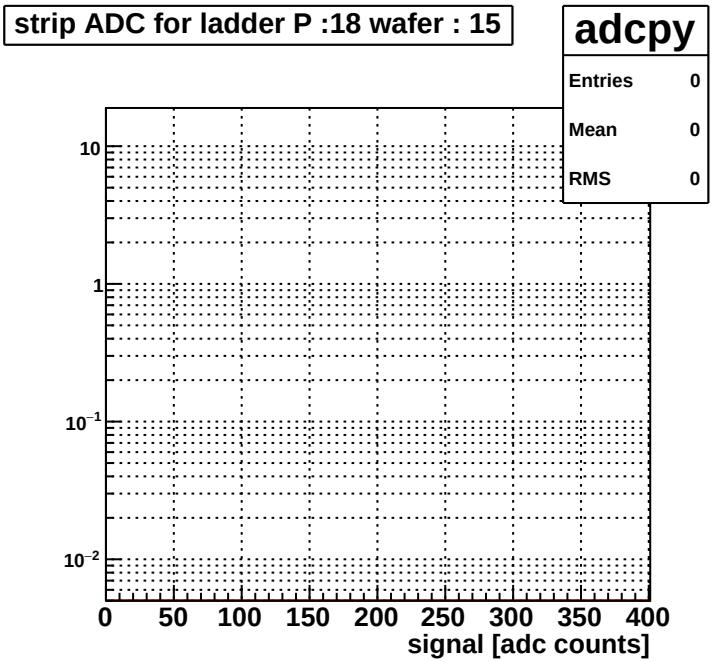

Strip adc vs. channels for ladder N : 18 wafer : 14
stripADCCHANsideN285
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

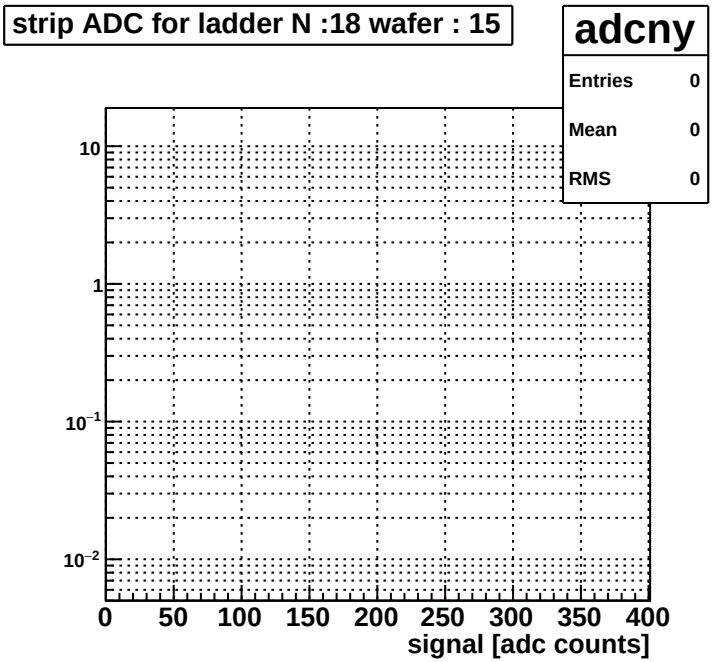

strip ADC for ladder P :18 wafer : 14
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

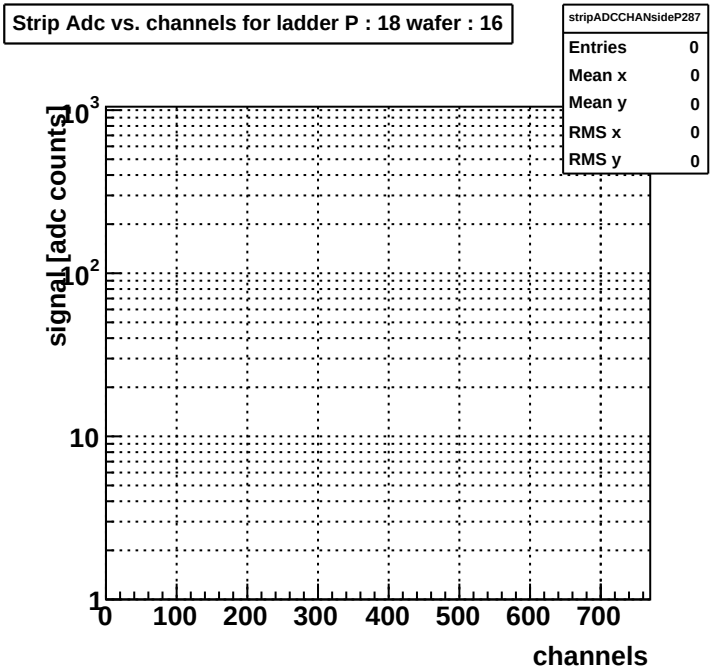

strip ADC for ladder N :18 wafer : 14
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

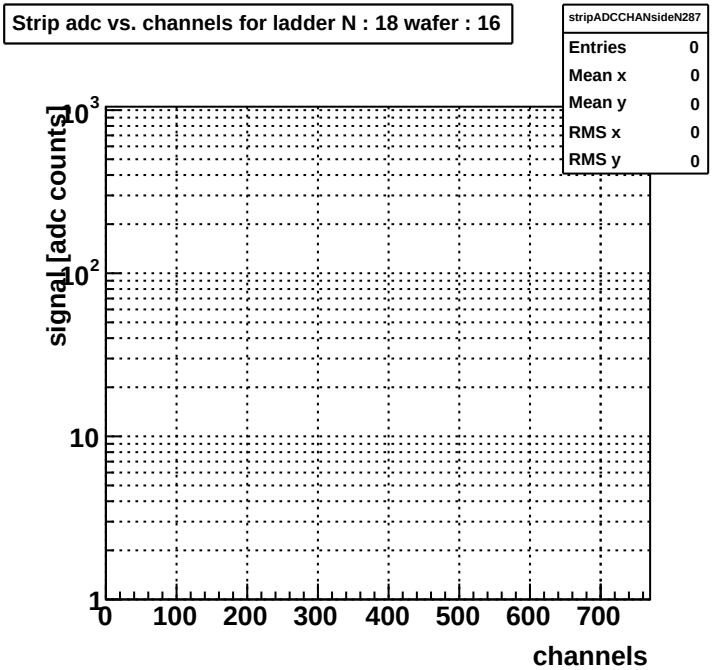

Strip Adc vs. channels for ladder P : 18 wafer : 15
stripADCCHANsideP286
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

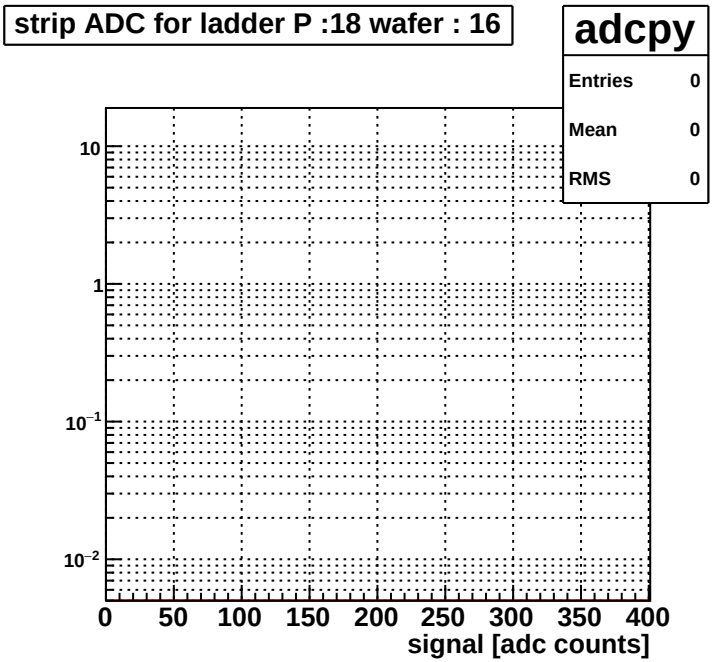

Strip adc vs. channels for ladder N : 18 wafer : 15
stripADCCHANsideN286
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

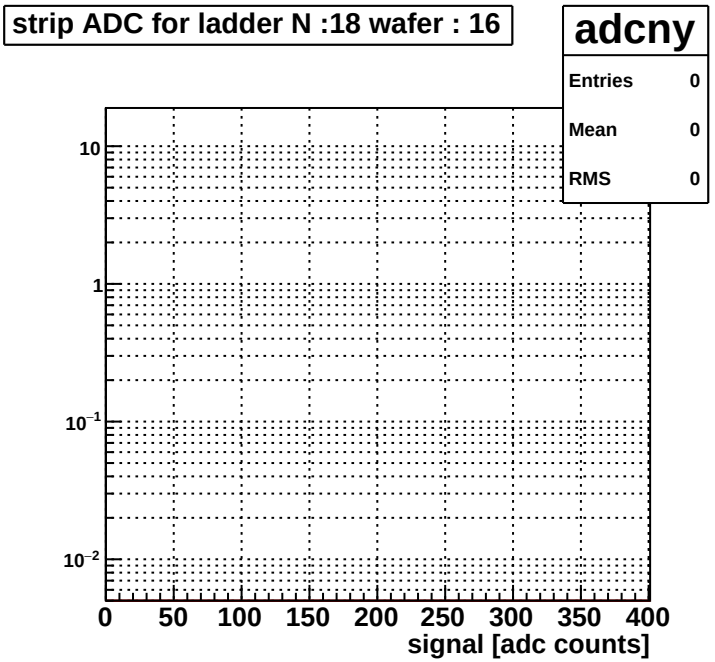

strip ADC for ladder P :18 wafer : 15
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

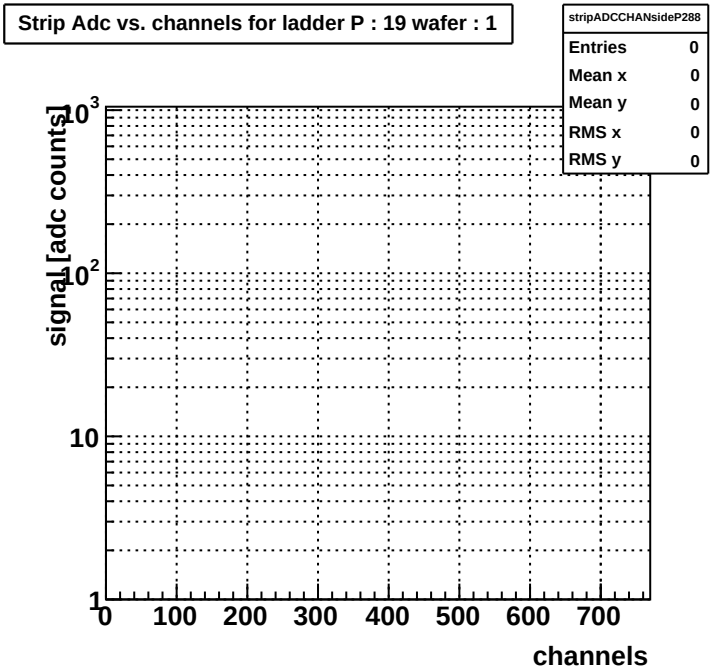

strip ADC for ladder N :18 wafer : 15
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

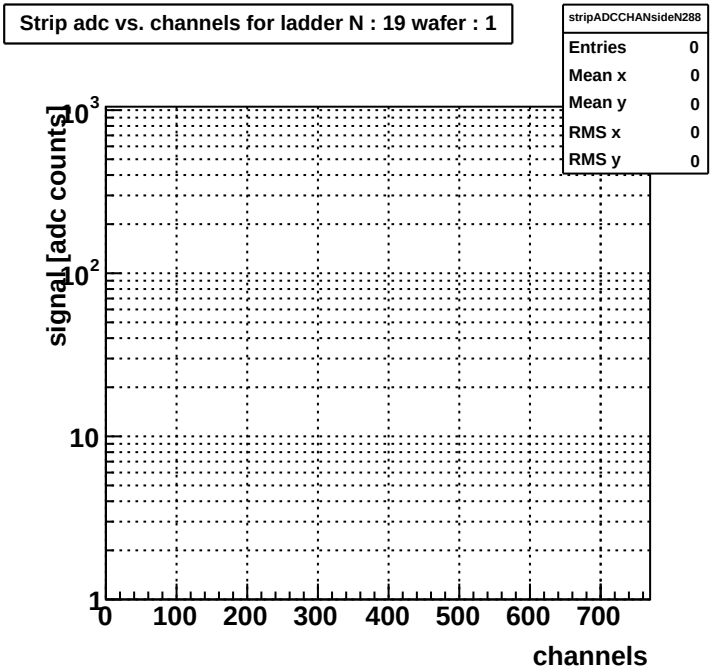

Strip Adc vs. channels for ladder P : 18 wafer : 16
stripADCCHANsideP287
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

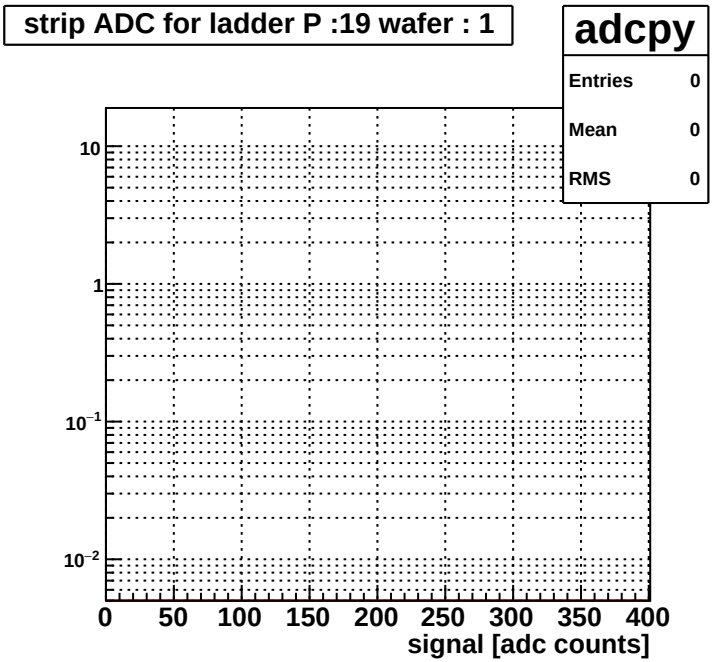

Strip adc vs. channels for ladder N : 18 wafer : 16
stripADCCHANsideN287
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

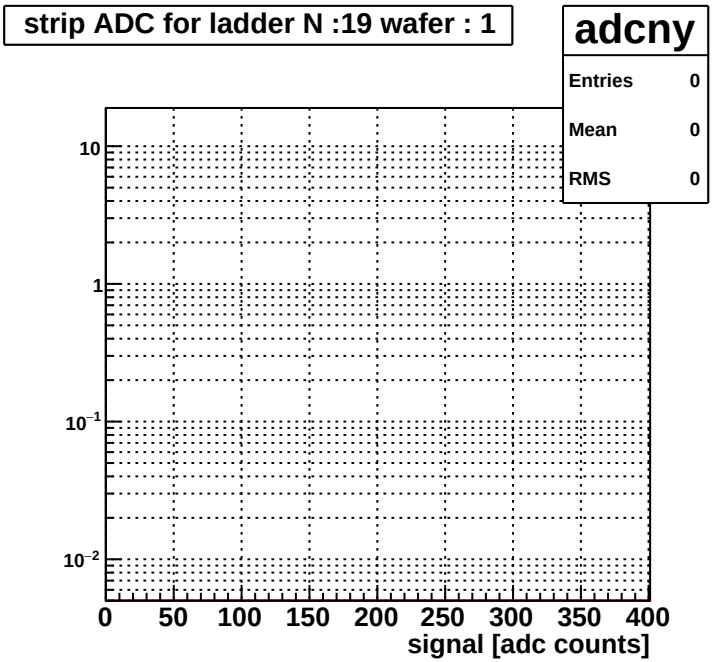

strip ADC for ladder P :18 wafer : 16
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

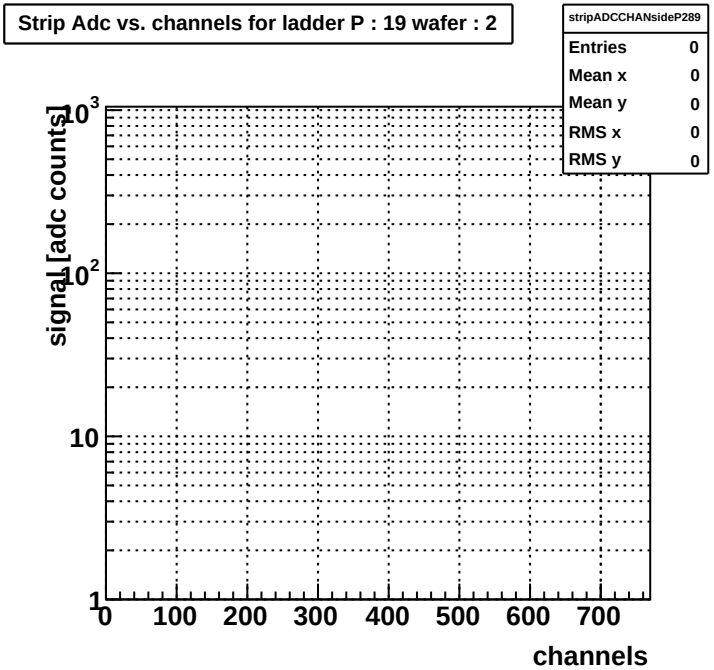

strip ADC for ladder N :18 wafer : 16
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

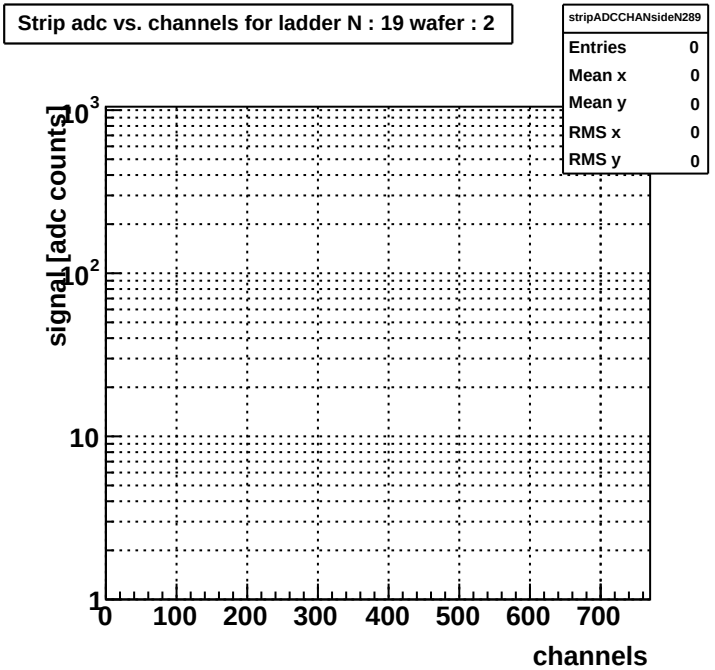

Strip Adc vs. channels for ladder P : 19 wafer : 1
stripADCCHANsideP288
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

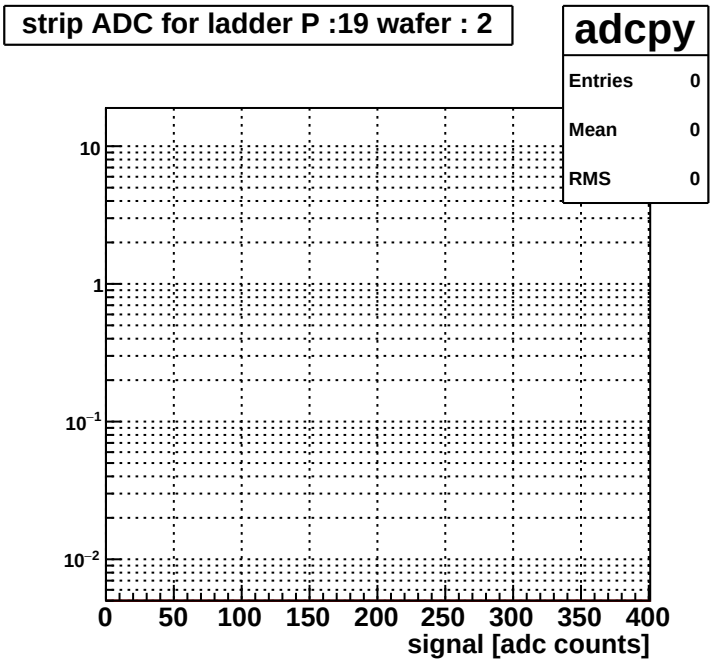

Strip adc vs. channels for ladder N : 19 wafer : 1
stripADCCHANsideN288
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

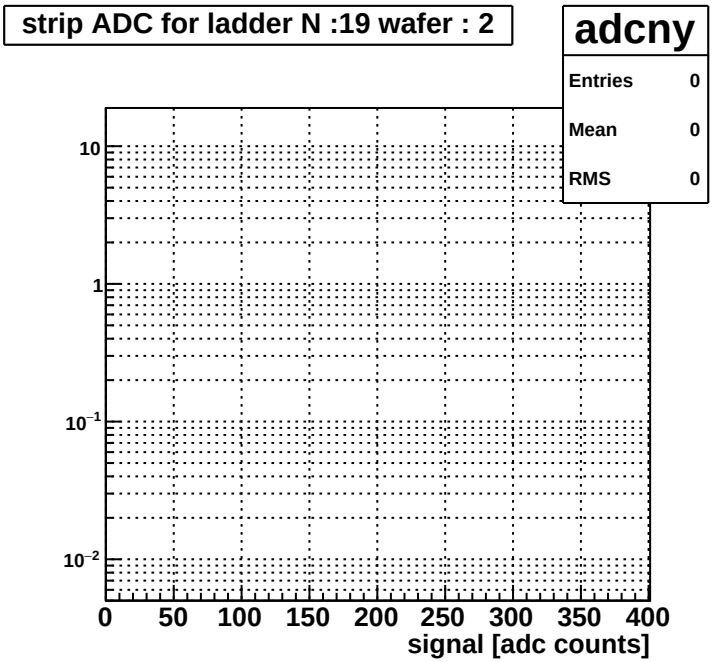

strip ADC for ladder P :19 wafer : 1
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

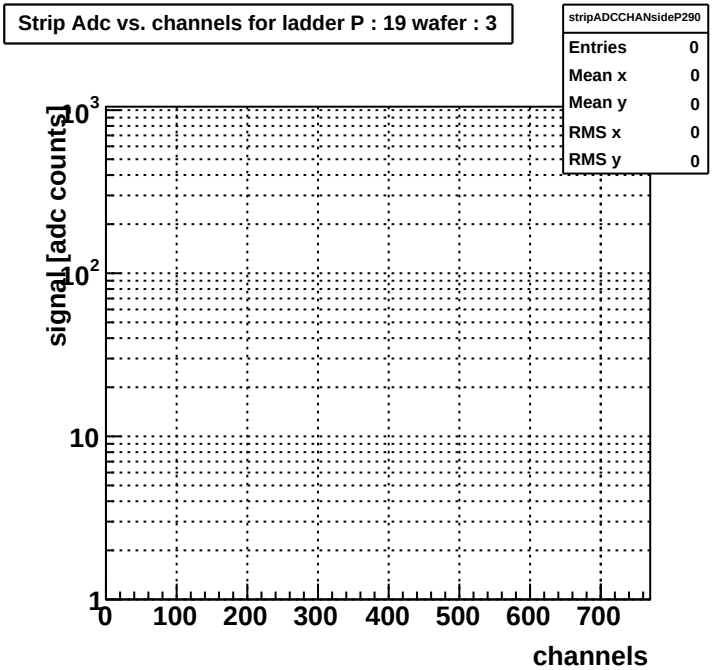

strip ADC for ladder N :19 wafer : 1
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

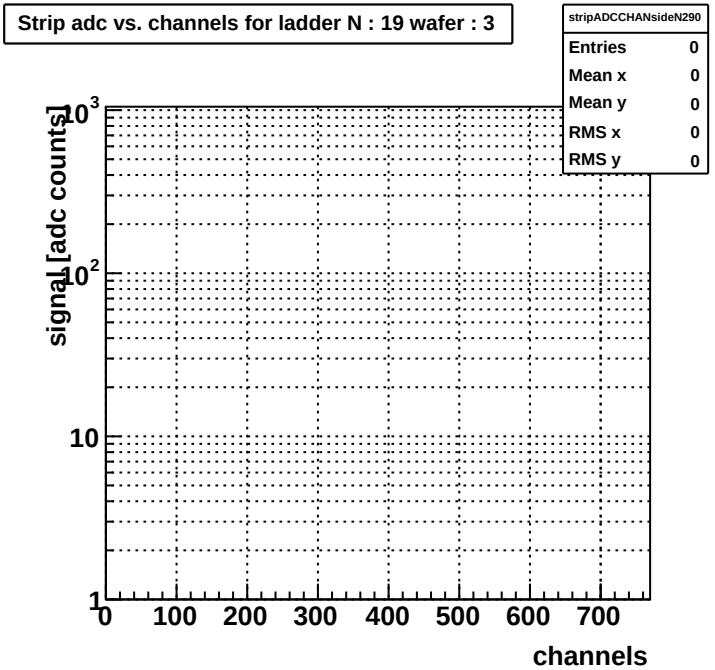

Strip Adc vs. channels for ladder P : 19 wafer : 2
stripADCCHANsideP289
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

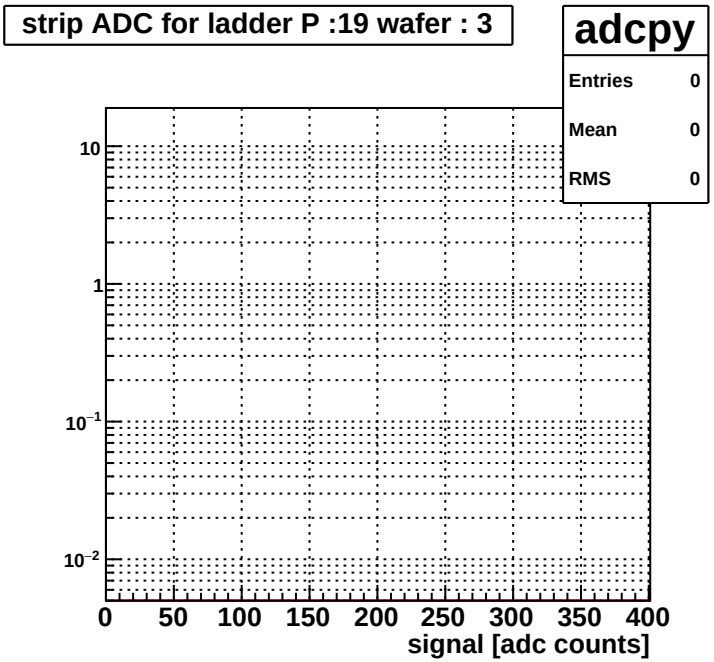

Strip adc vs. channels for ladder N : 19 wafer : 2
stripADCCHANsideN289
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

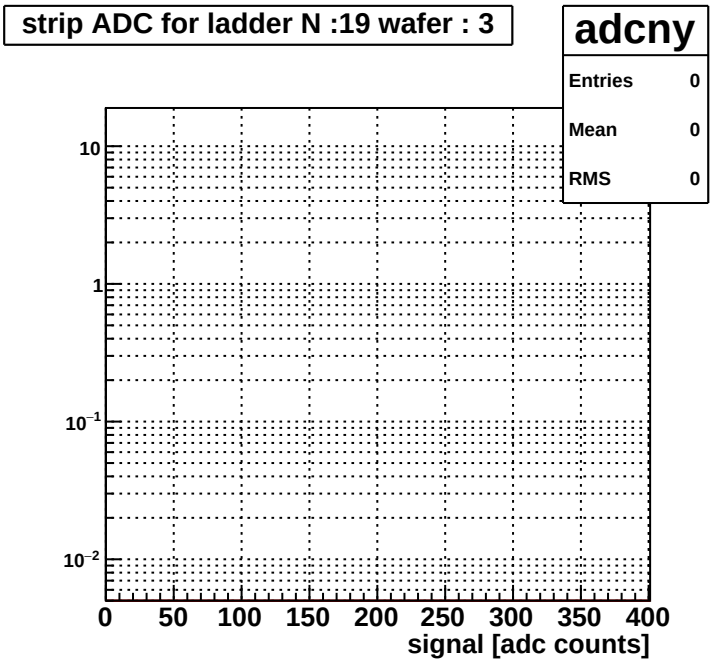

strip ADC for ladder P :19 wafer : 2
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

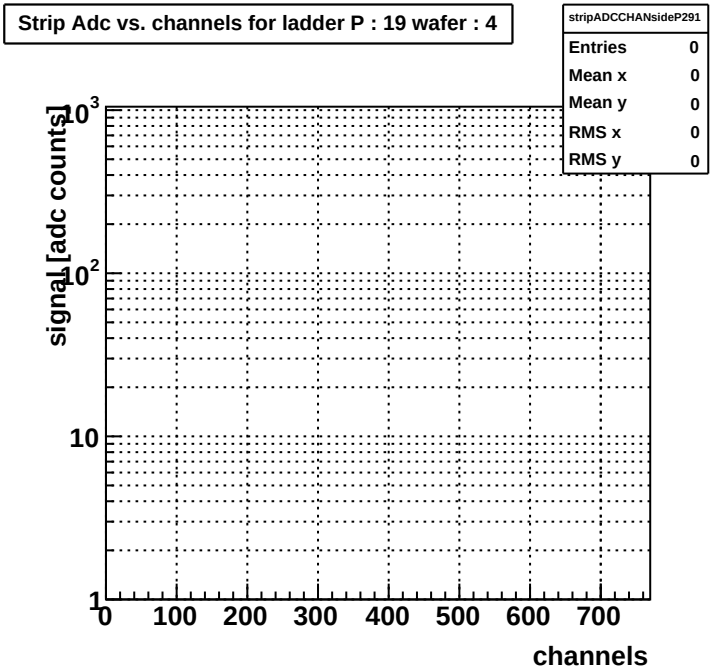

strip ADC for ladder N :19 wafer : 2
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

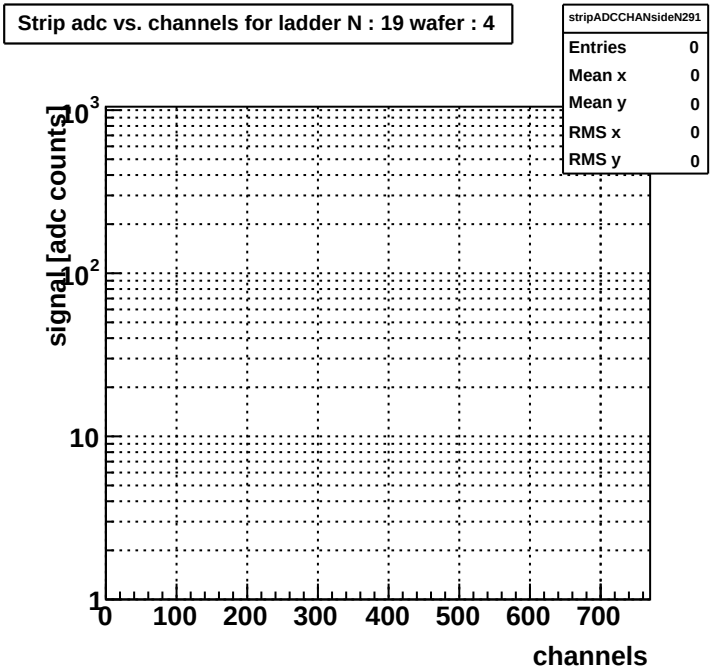

Strip Adc vs. channels for ladder P : 19 wafer : 3
stripADCCHANsideP290
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

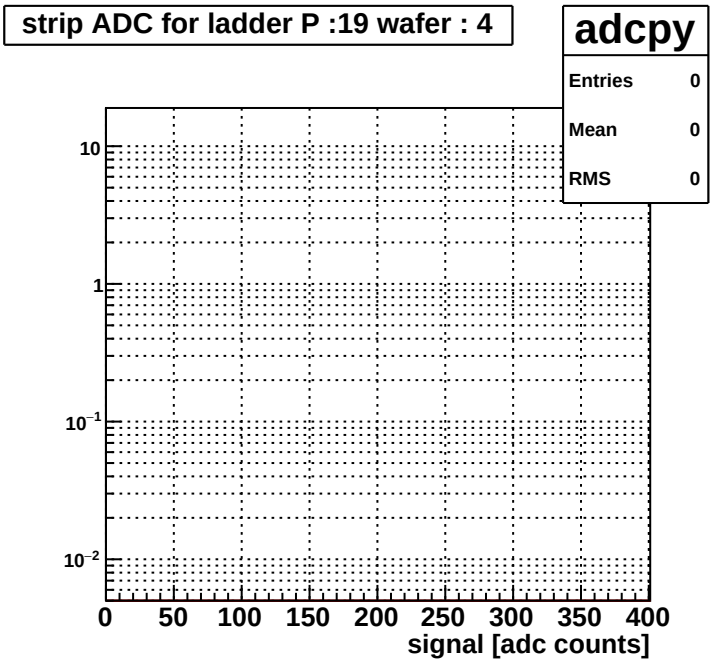

Strip adc vs. channels for ladder N : 19 wafer : 3
stripADCCHANsideN290
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

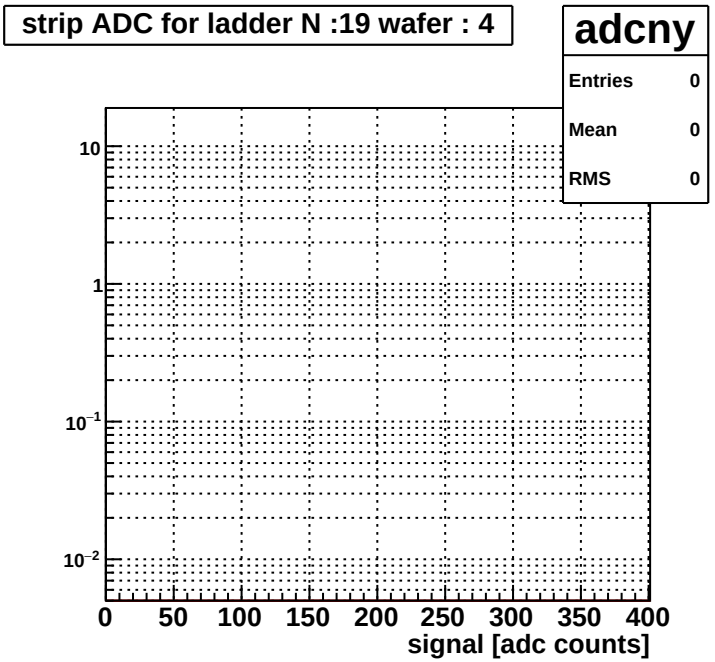

strip ADC for ladder P :19 wafer : 3
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

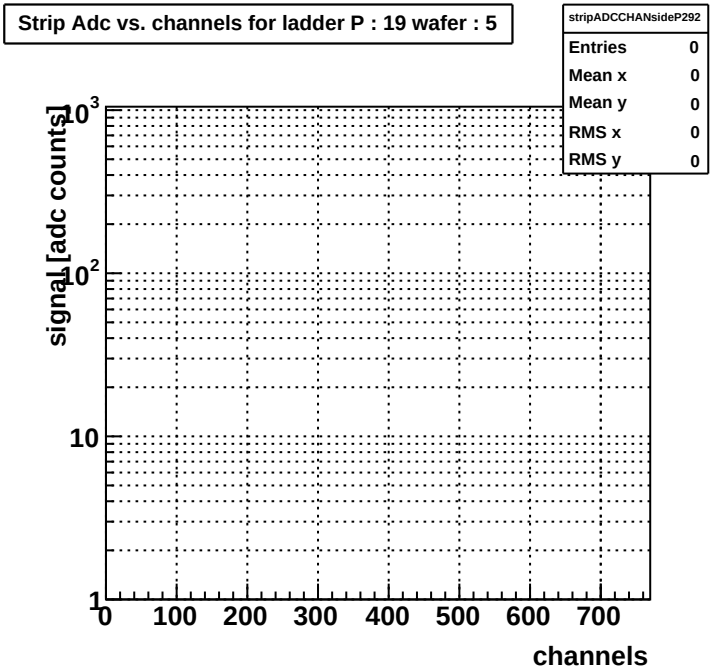

strip ADC for ladder N :19 wafer : 3
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

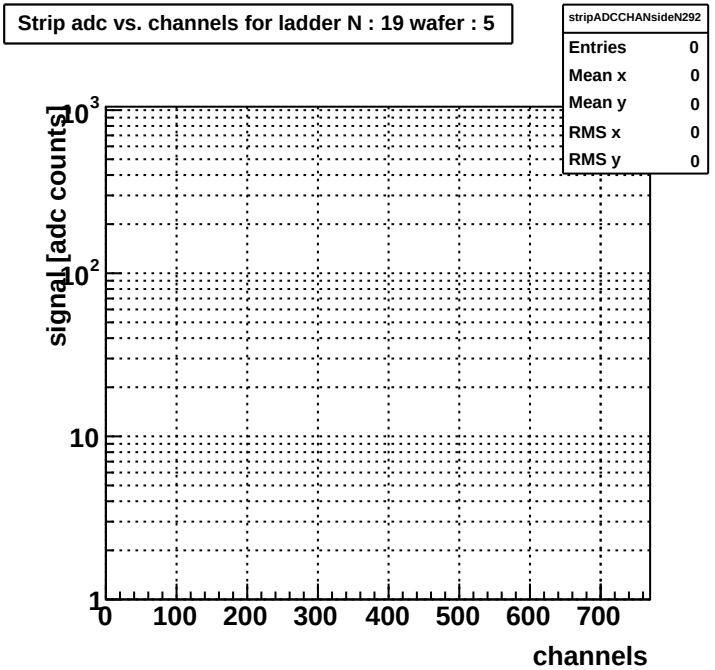

Strip Adc vs. channels for ladder P : 19 wafer : 4
stripADCCHANsideP291
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

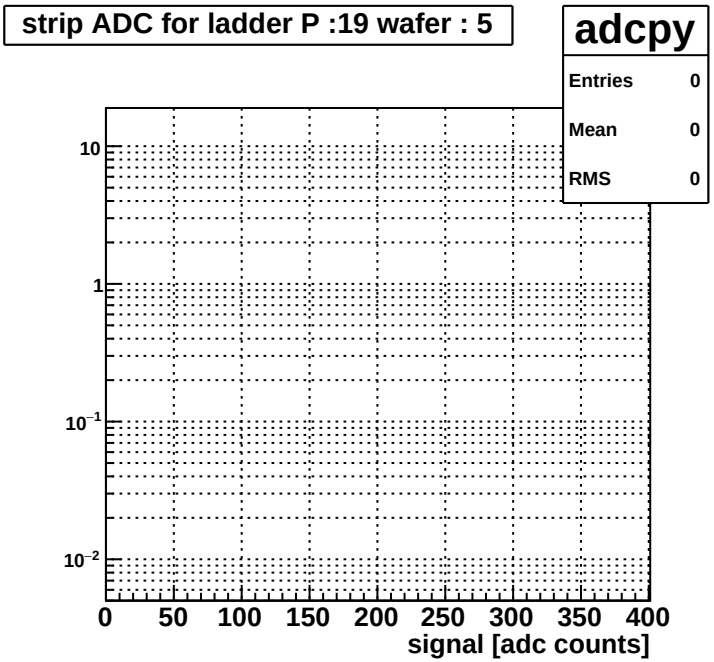

Strip adc vs. channels for ladder N : 19 wafer : 4
stripADCCHANsideN291
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

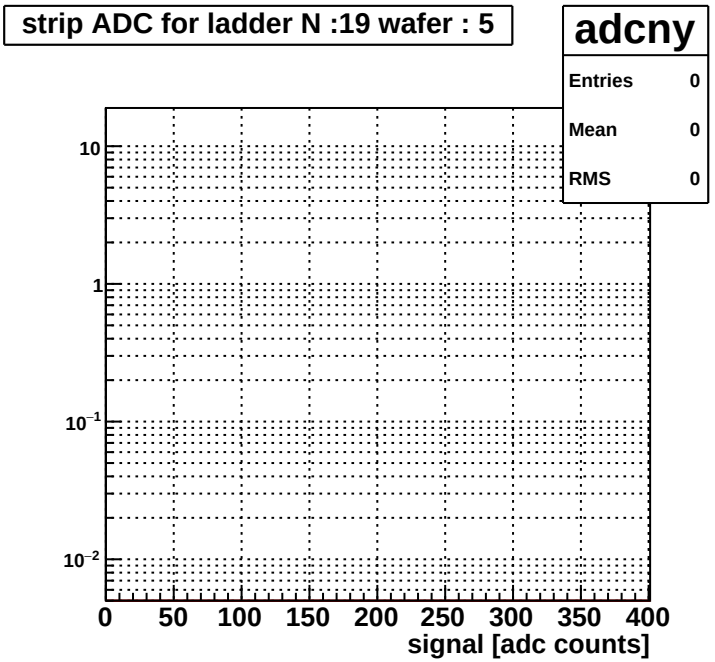

strip ADC for ladder P :19 wafer : 4
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

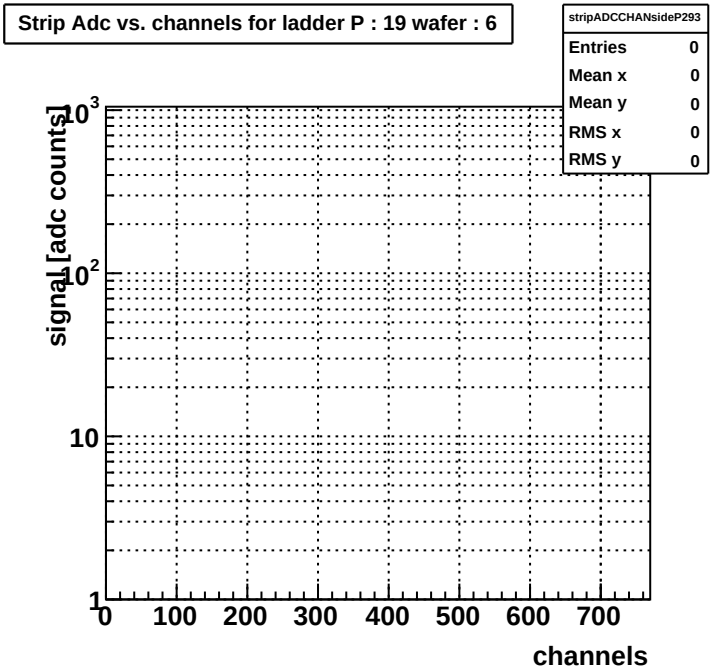

strip ADC for ladder N :19 wafer : 4
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

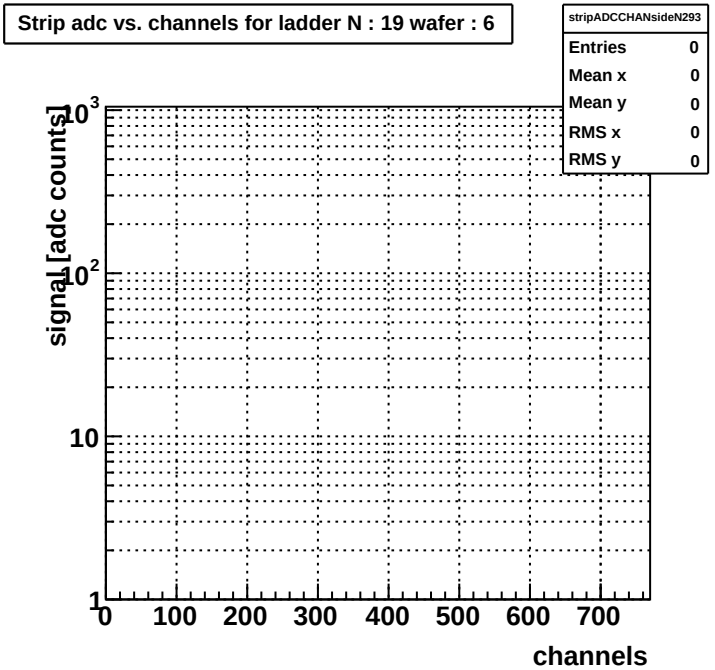

Strip Adc vs. channels for ladder P : 19 wafer : 5
stripADCCHANsideP292
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

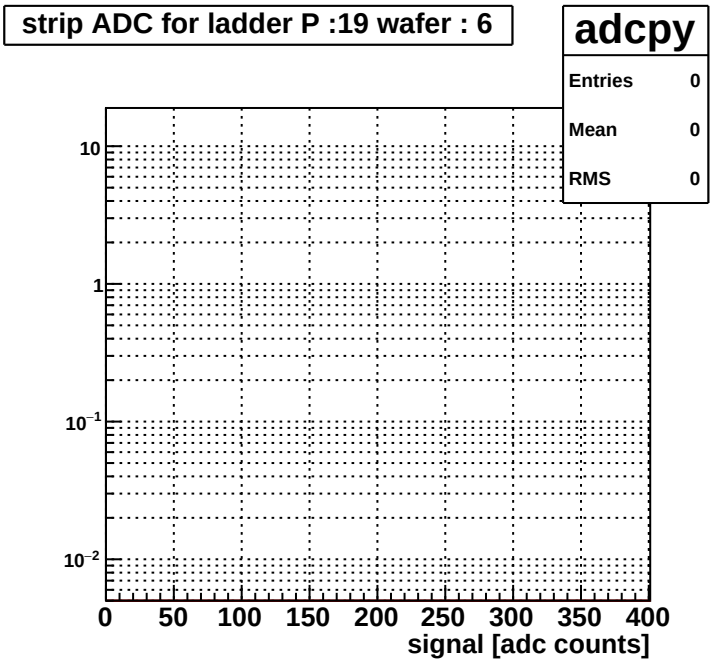

Strip adc vs. channels for ladder N : 19 wafer : 5
stripADCCHANsideN292
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

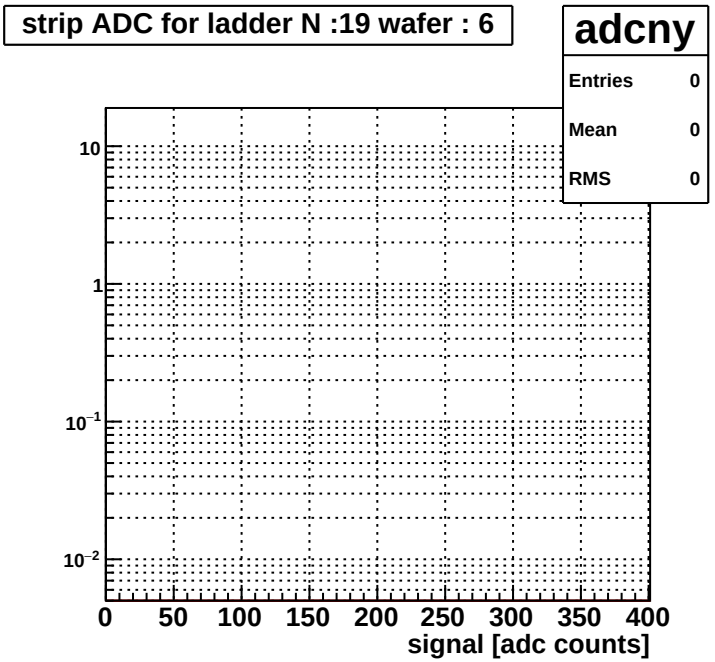

strip ADC for ladder P :19 wafer : 5
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

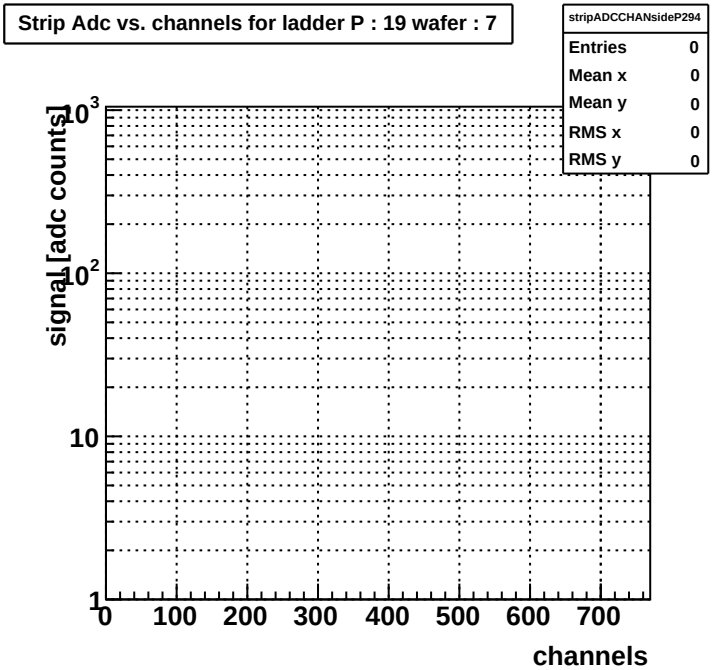

strip ADC for ladder N :19 wafer : 5
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

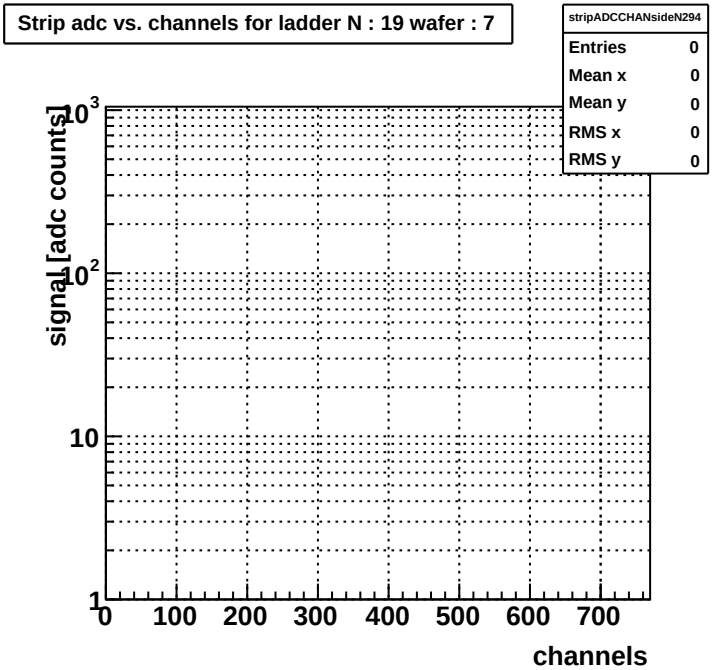

Strip Adc vs. channels for ladder P : 19 wafer : 6
stripADCCHANsideP293
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

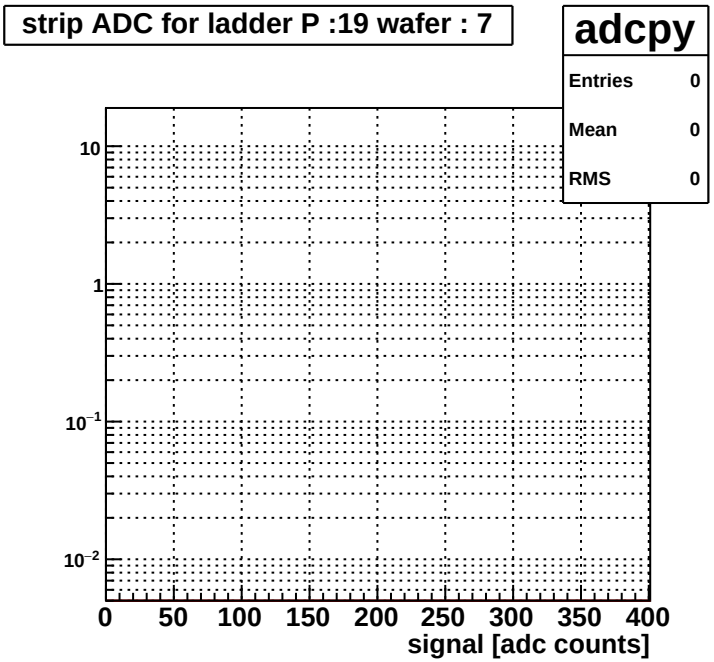

Strip adc vs. channels for ladder N : 19 wafer : 6
stripADCCHANsideN293
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

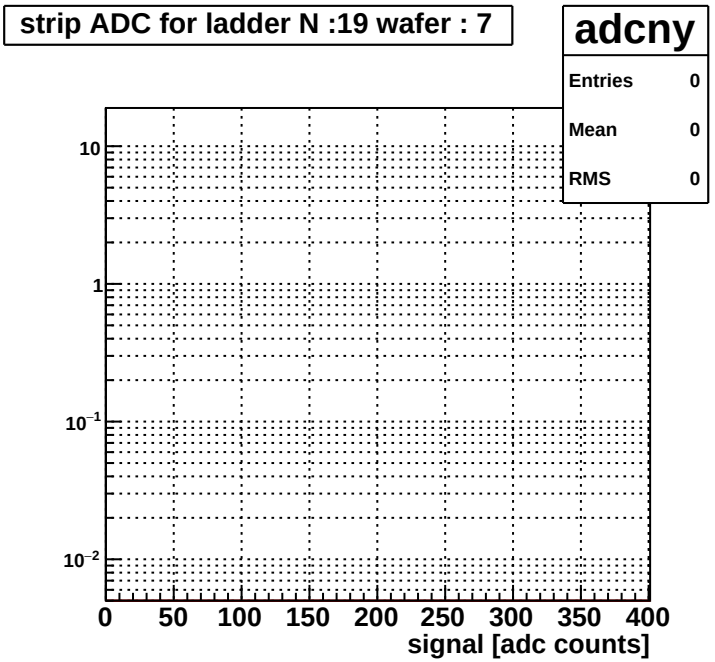

strip ADC for ladder P :19 wafer : 6
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

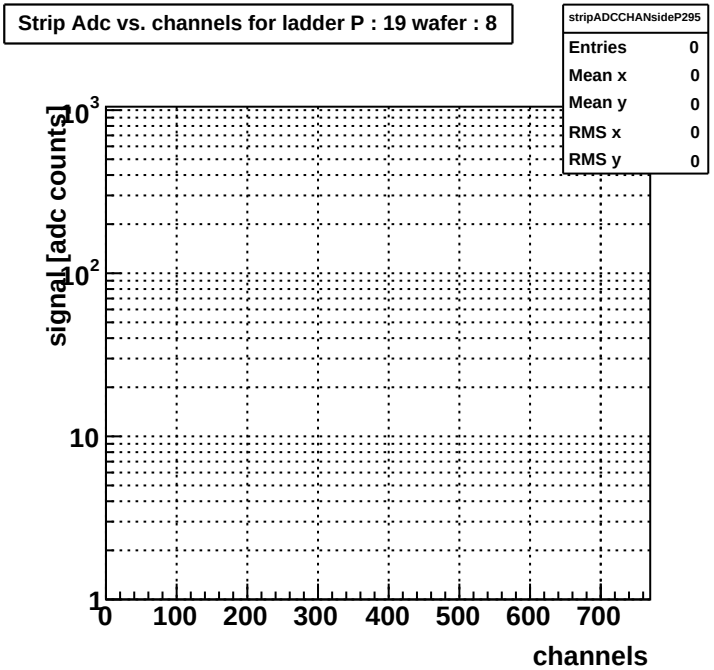

strip ADC for ladder N :19 wafer : 6
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

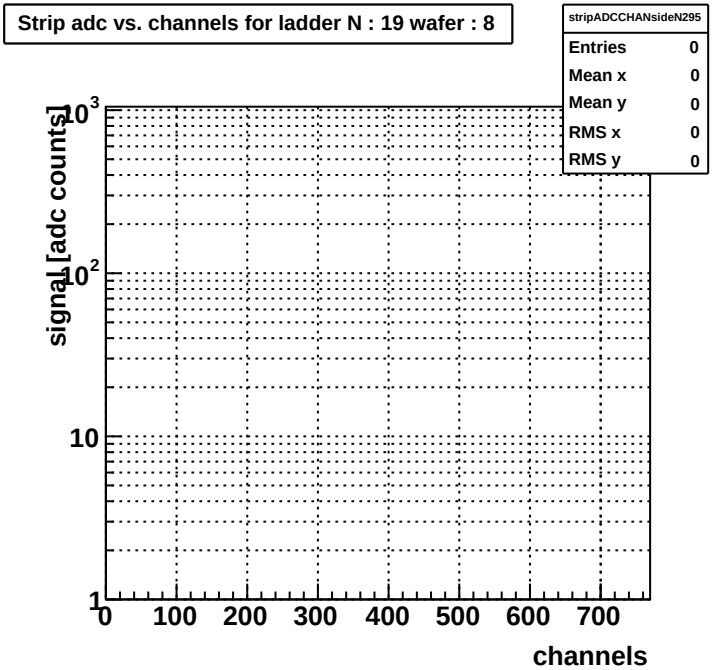

Strip Adc vs. channels for ladder P : 19 wafer : 7
stripADCCHANsideP294
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

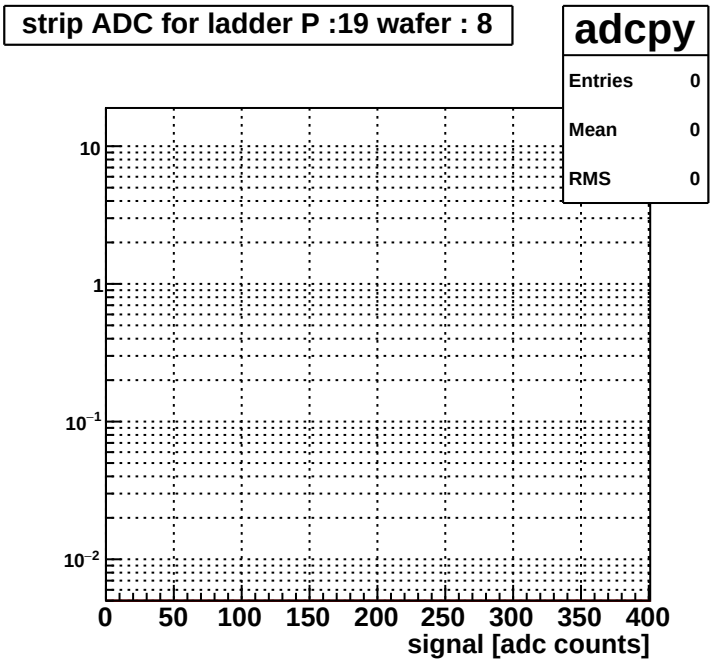

Strip adc vs. channels for ladder N : 19 wafer : 7
stripADCCHANsideN294
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

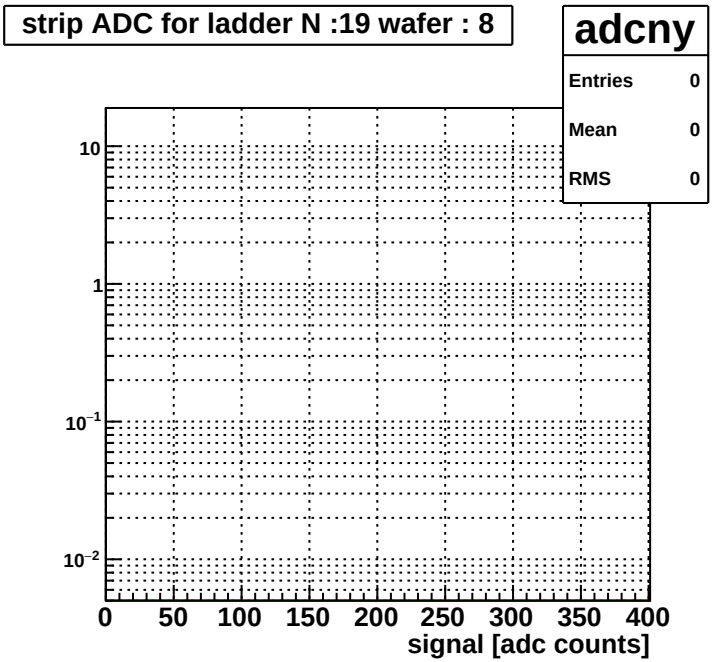

strip ADC for ladder P :19 wafer : 7
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

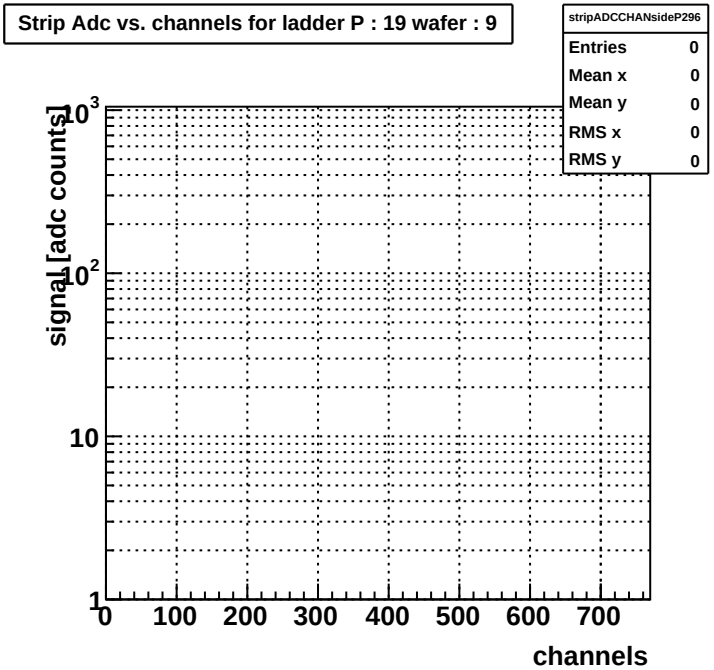

strip ADC for ladder N :19 wafer : 7
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

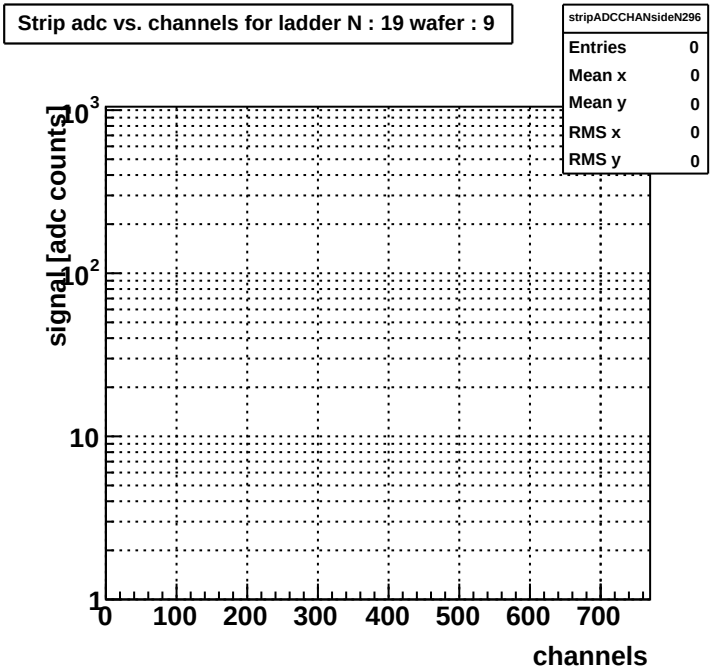

Strip Adc vs. channels for ladder P : 19 wafer : 8
stripADCCHANsideP295
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

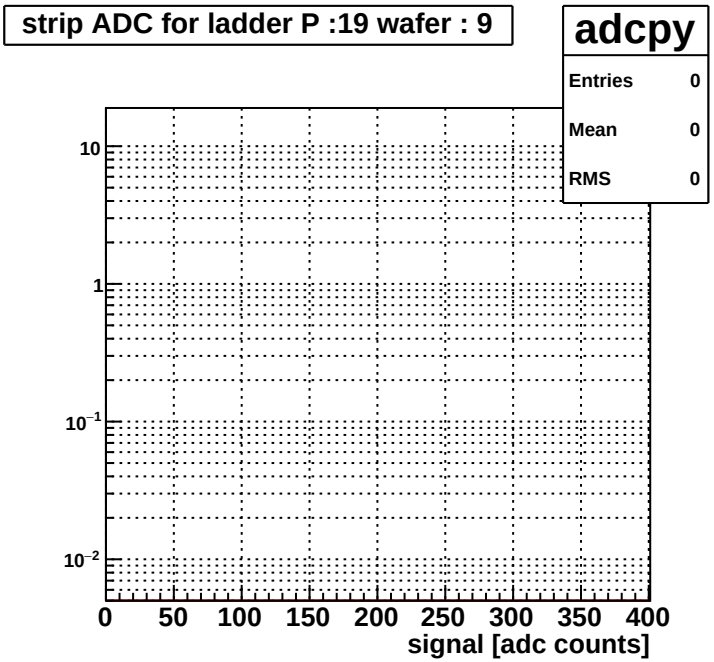

Strip adc vs. channels for ladder N : 19 wafer : 8
stripADCCHANsideN295
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

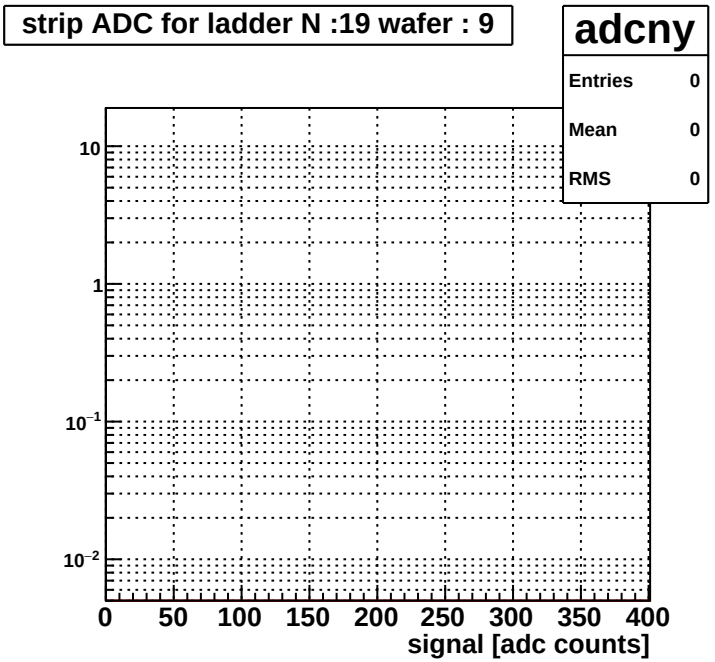

strip ADC for ladder P :19 wafer : 8
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

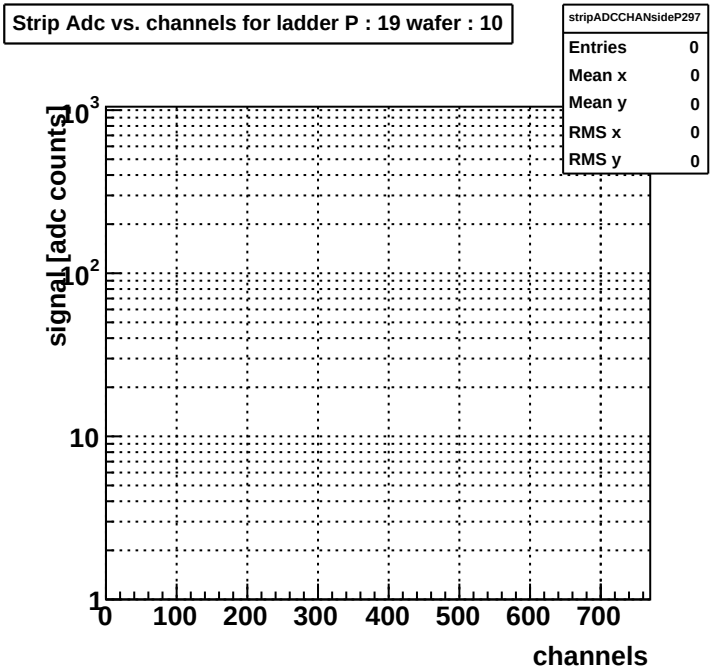

strip ADC for ladder N :19 wafer : 8
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

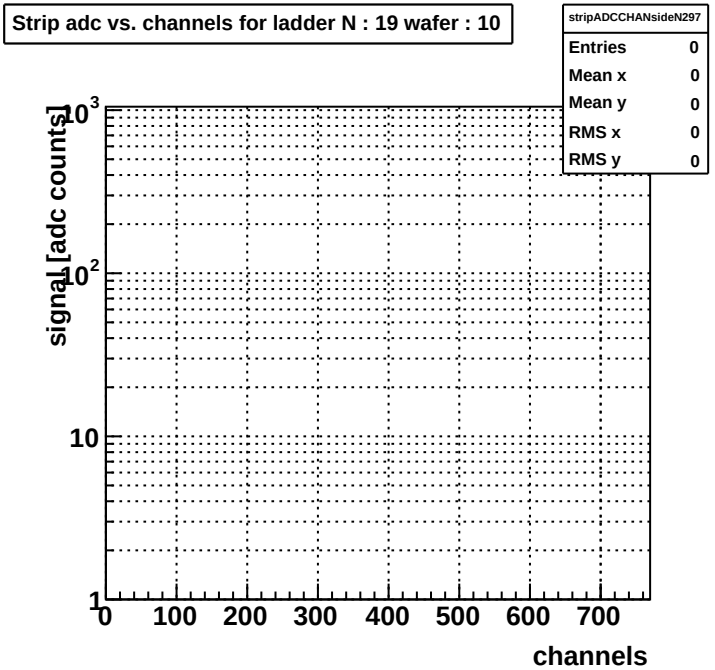

Strip Adc vs. channels for ladder P : 19 wafer : 9
stripADCCHANsideP296
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

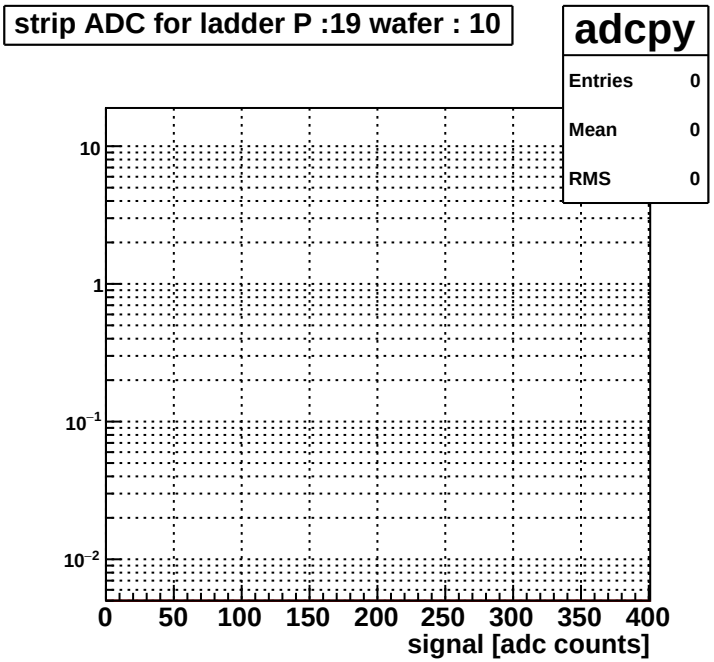

Strip adc vs. channels for ladder N : 19 wafer : 9
stripADCCHANsideN296
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

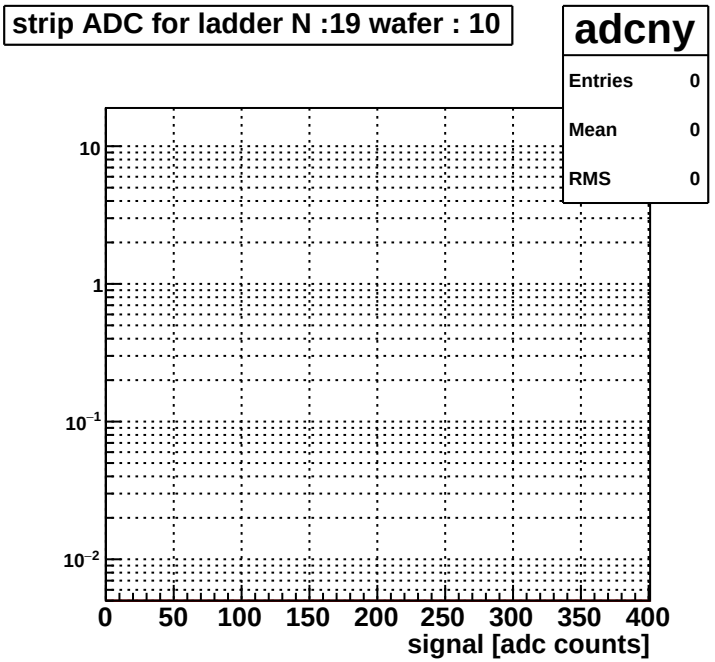

strip ADC for ladder P :19 wafer : 9
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

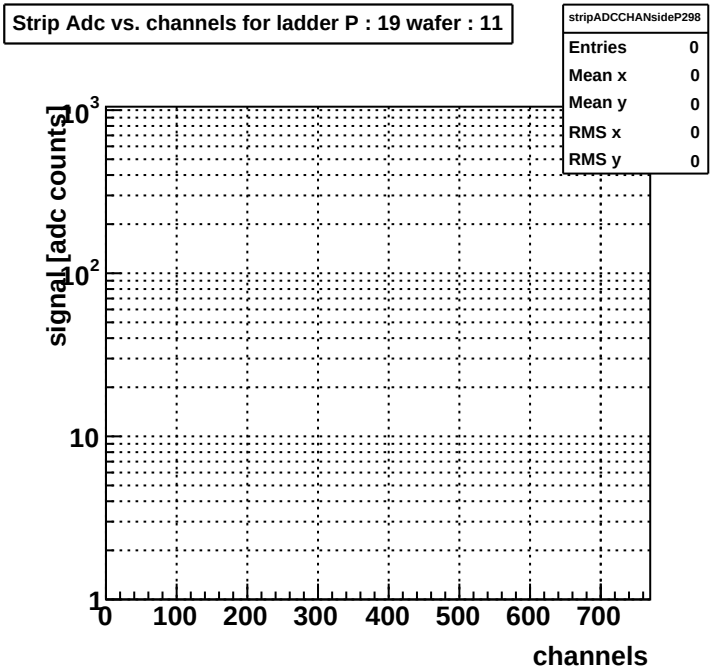

strip ADC for ladder N :19 wafer : 9
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

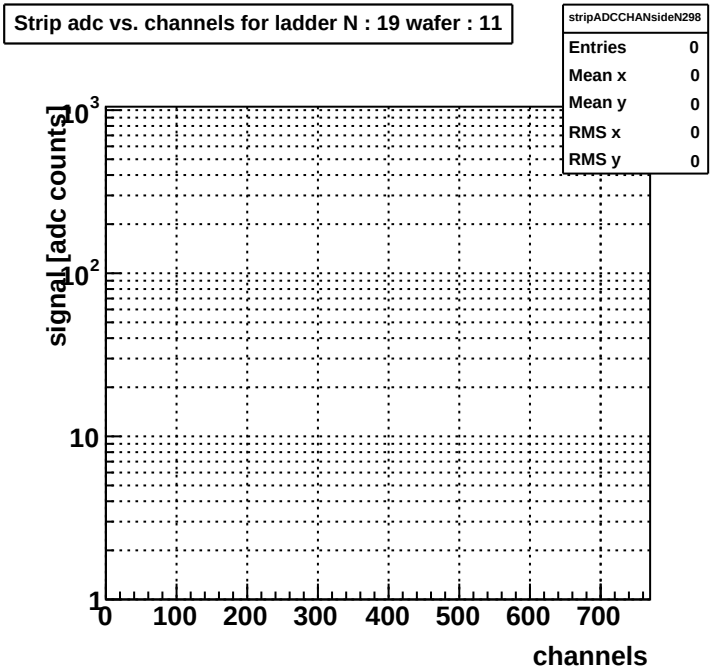

Strip Adc vs. channels for ladder P : 19 wafer : 10
stripADCCHANsideP297
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

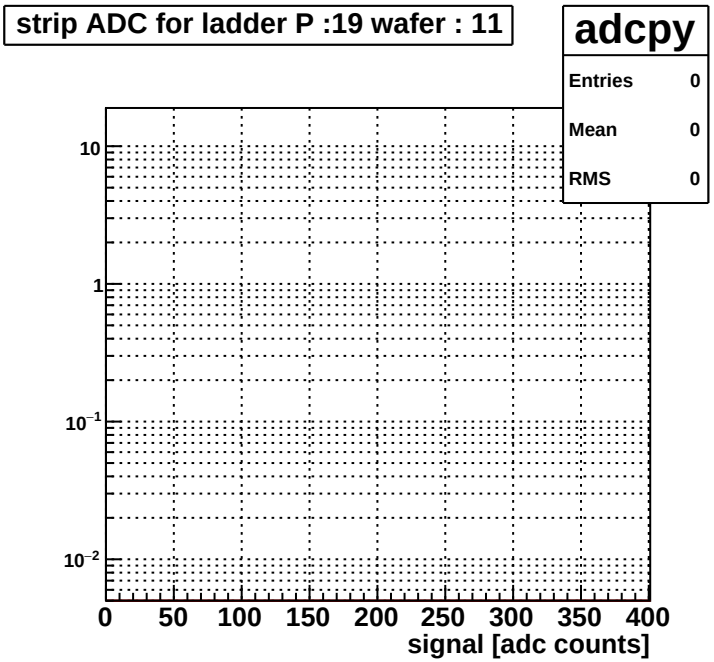

Strip adc vs. channels for ladder N : 19 wafer : 10
stripADCCHANsideN297
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

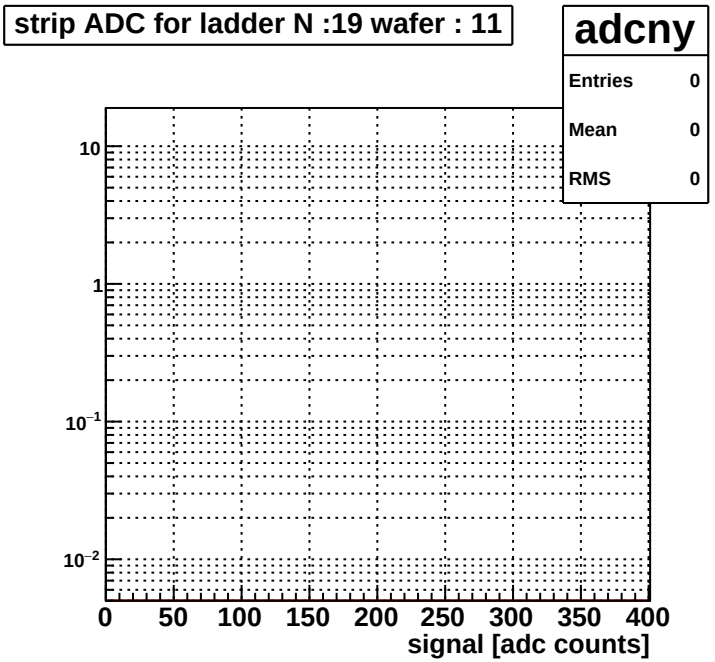

strip ADC for ladder P :19 wafer : 10
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

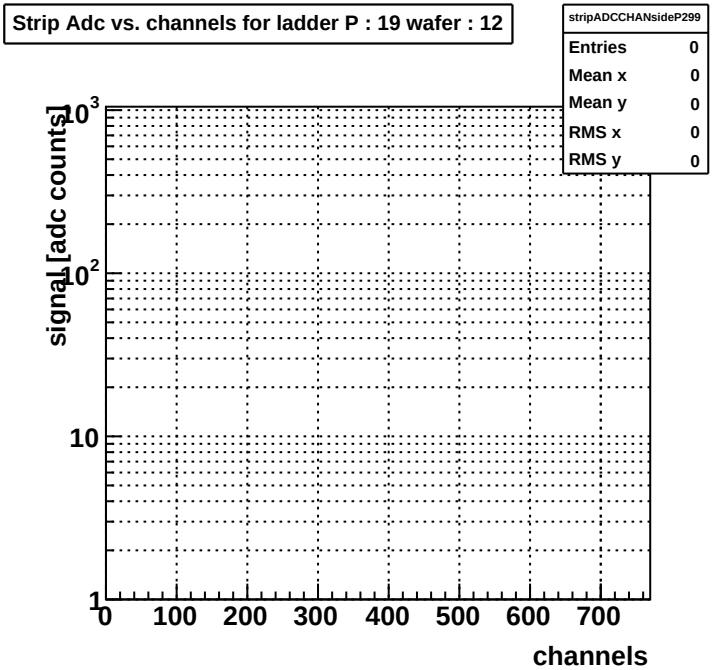

strip ADC for ladder N :19 wafer : 10
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

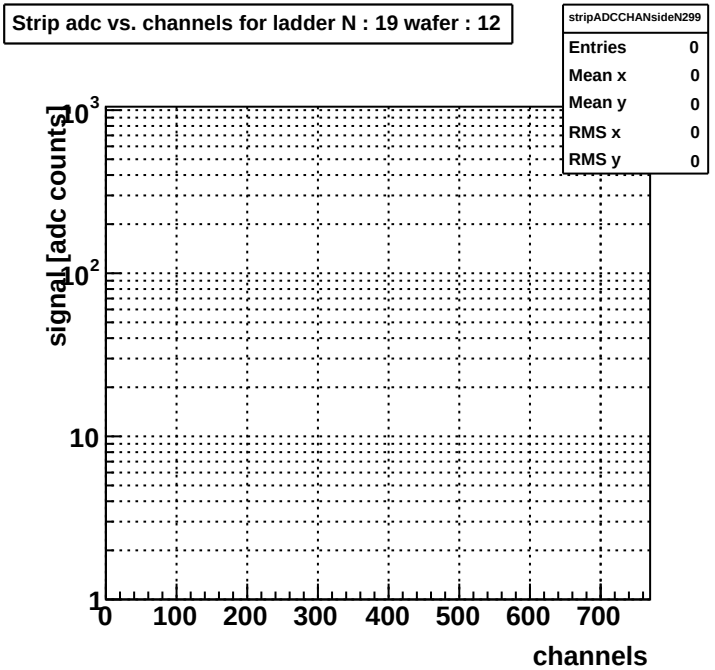

Strip Adc vs. channels for ladder P : 19 wafer : 11
stripADCCHANsideP298
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

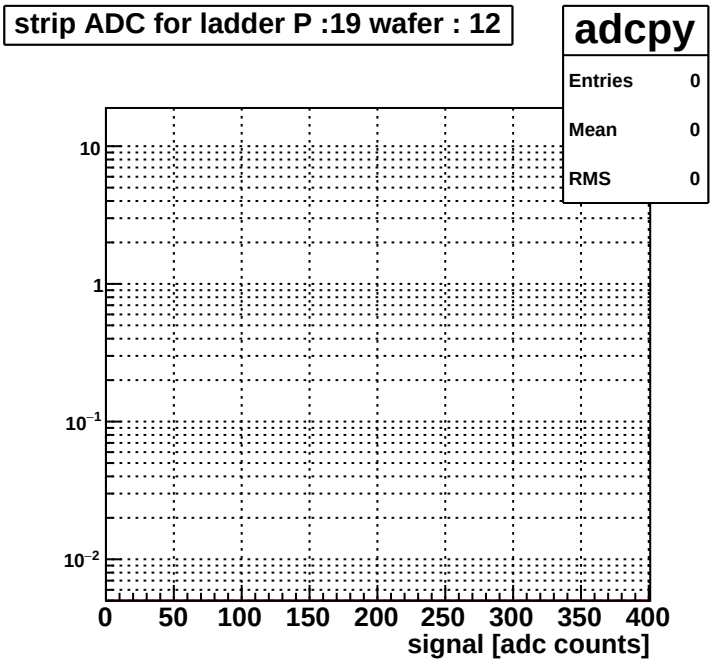

Strip adc vs. channels for ladder N : 19 wafer : 11
stripADCCHANsideN298
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

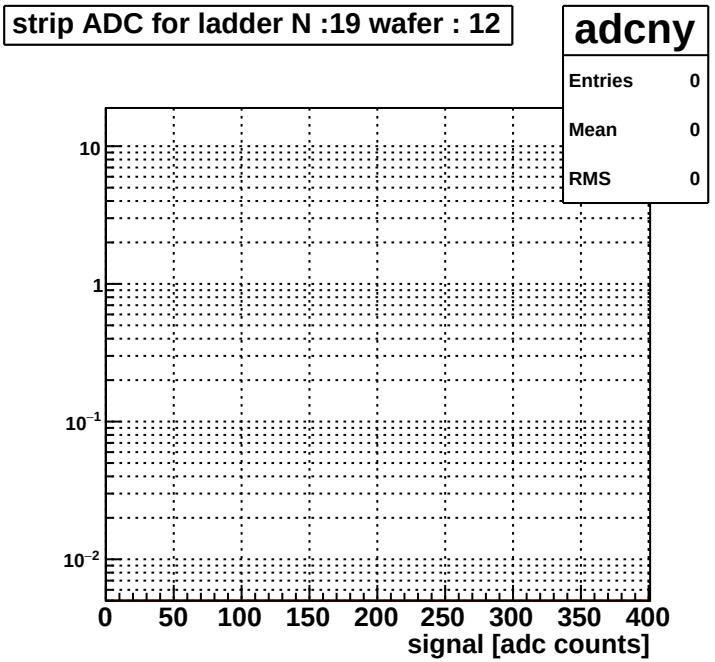

strip ADC for ladder P :19 wafer : 11
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

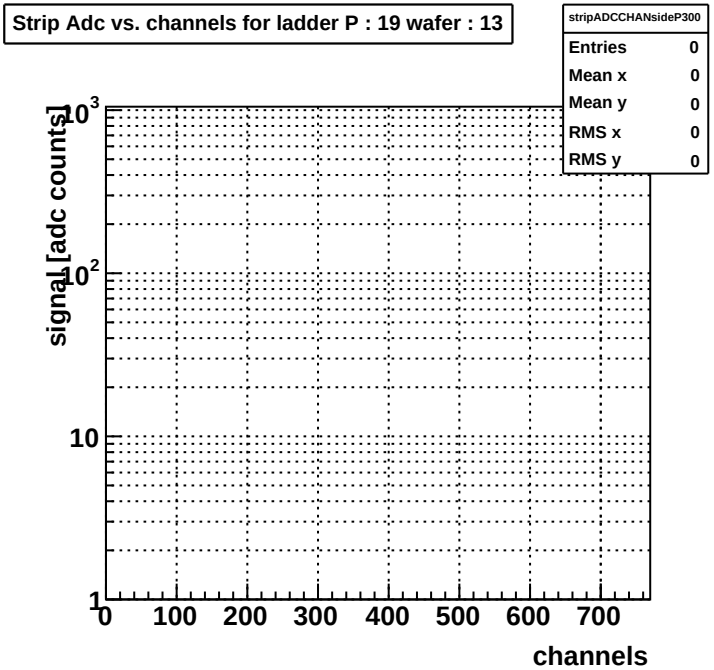

strip ADC for ladder N :19 wafer : 11
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

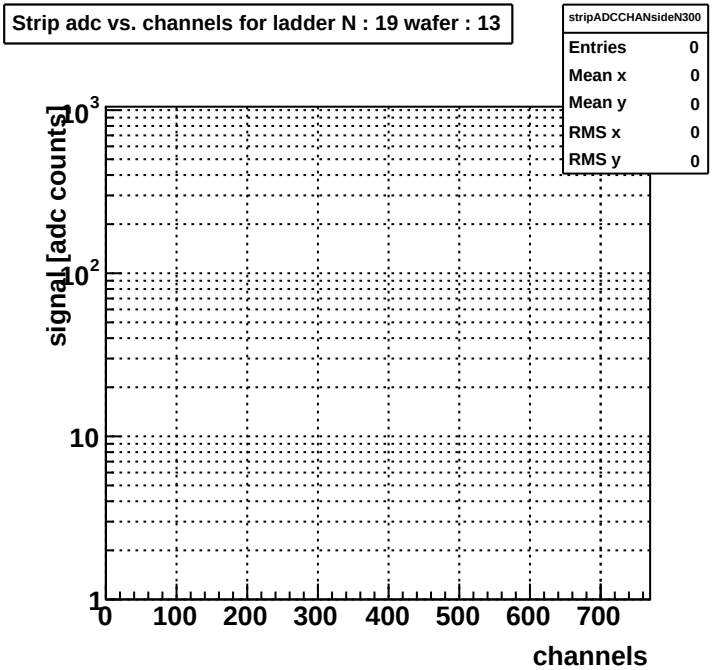

Strip Adc vs. channels for ladder P : 19 wafer : 12
stripADCCHANsideP299
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

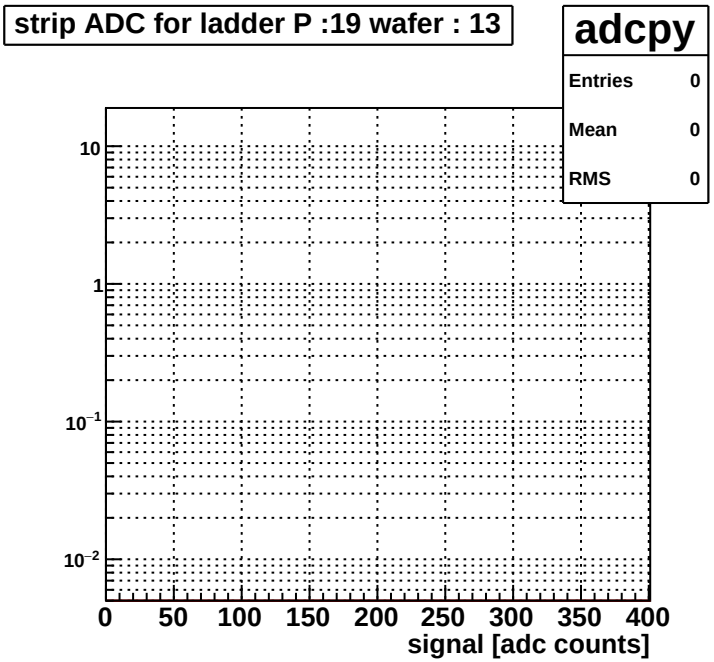

Strip adc vs. channels for ladder N : 19 wafer : 12
stripADCCHANsideN299
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

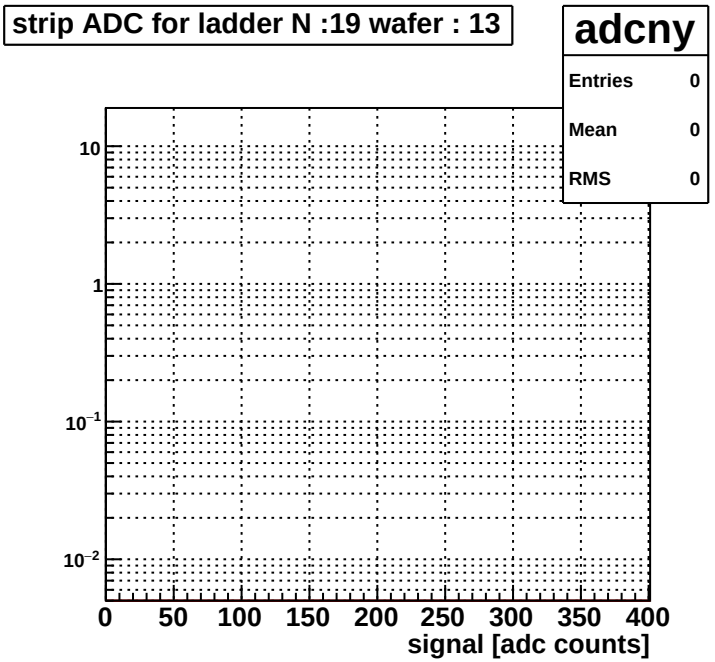

strip ADC for ladder P :19 wafer : 12
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

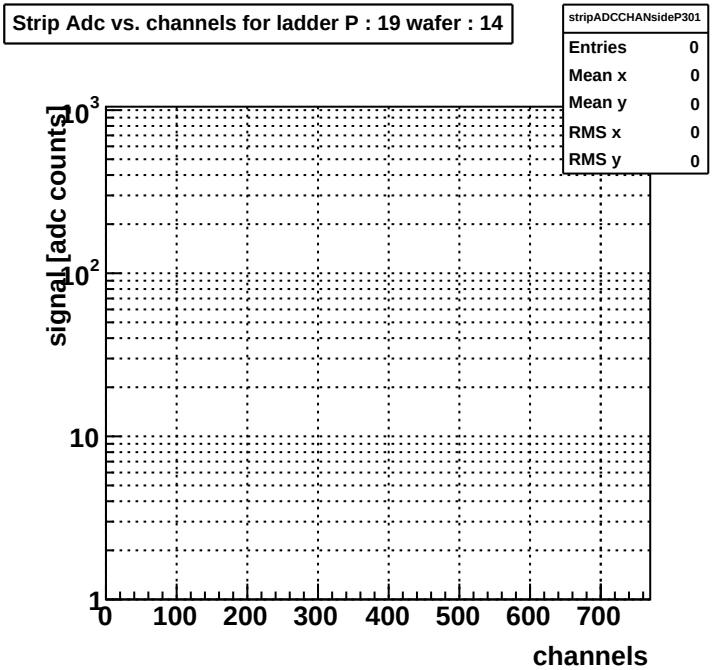

strip ADC for ladder N :19 wafer : 12
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

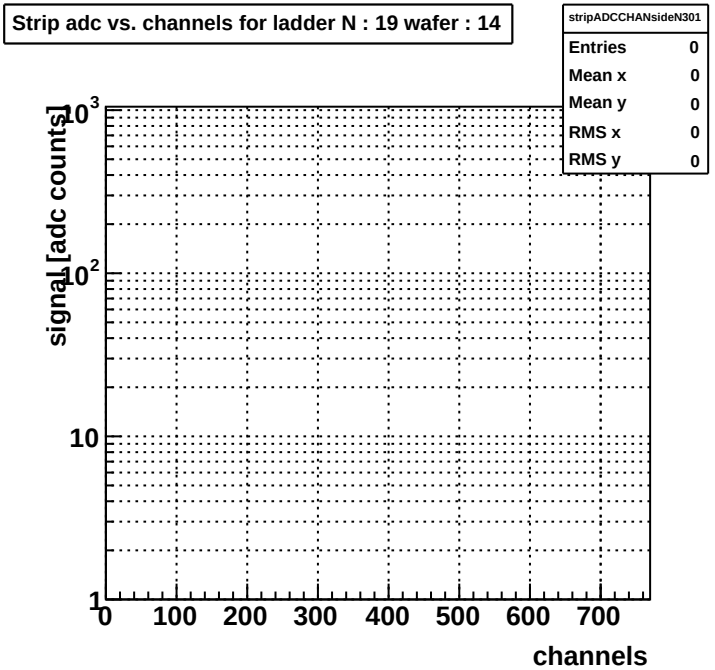

Strip Adc vs. channels for ladder P : 19 wafer : 13
stripADCCHANsideP300
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

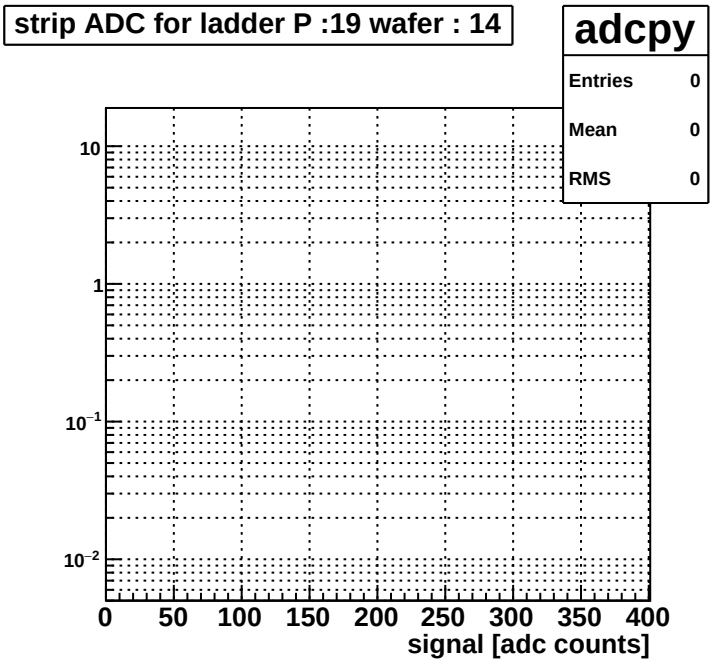

Strip adc vs. channels for ladder N : 19 wafer : 13
stripADCCHANsideN300
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

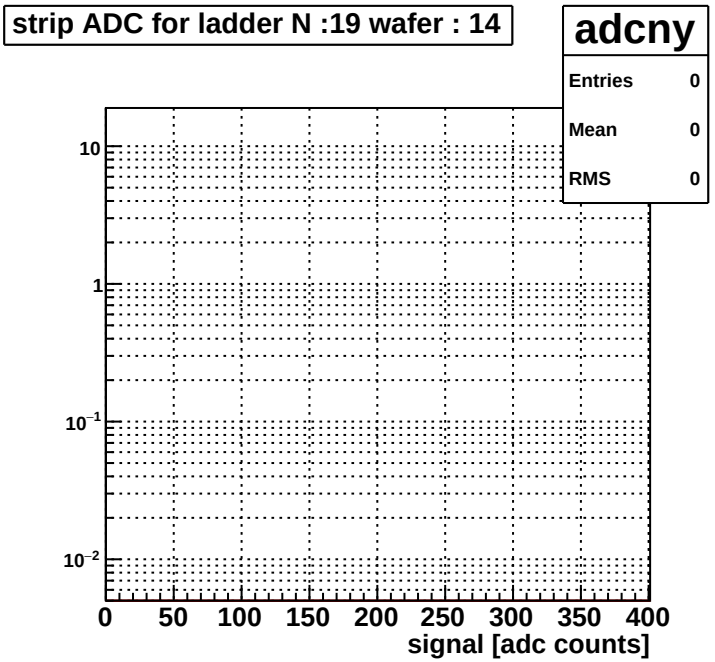

strip ADC for ladder P :19 wafer : 13
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

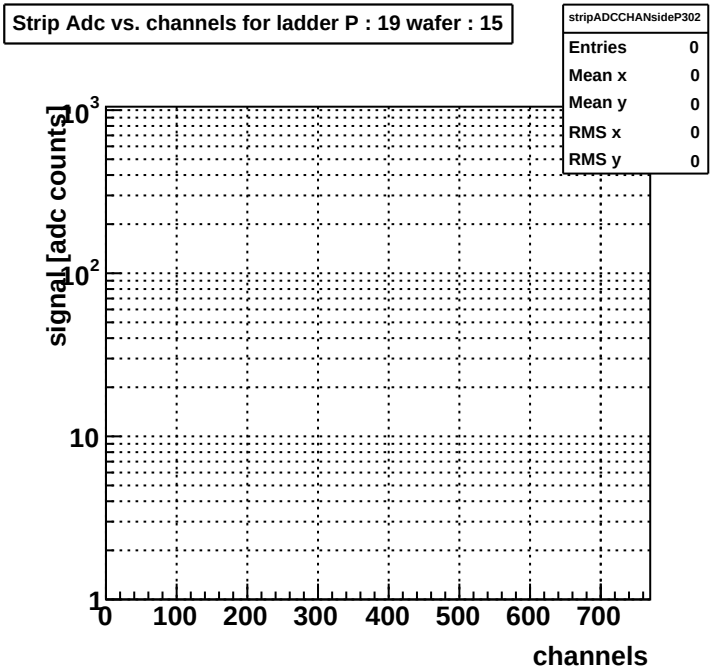

strip ADC for ladder N :19 wafer : 13
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

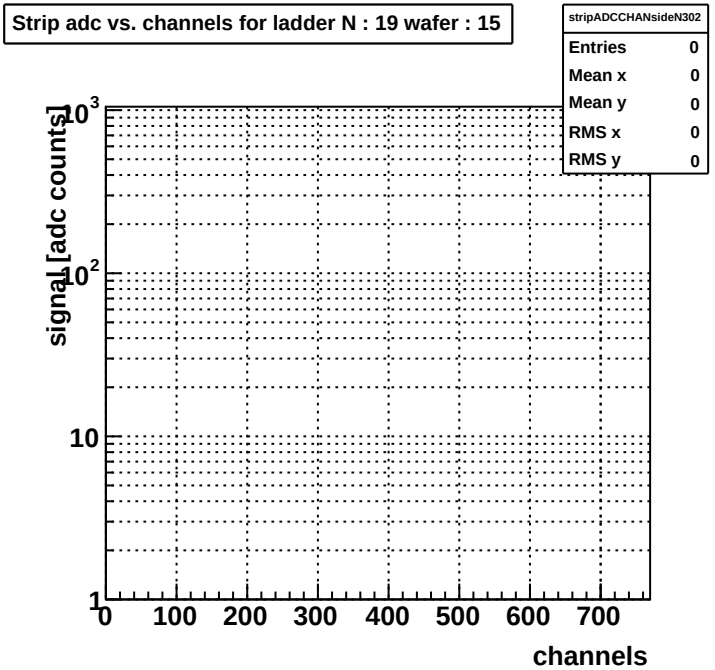

Strip Adc vs. channels for ladder P : 19 wafer : 14
stripADCCHANsideP301
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

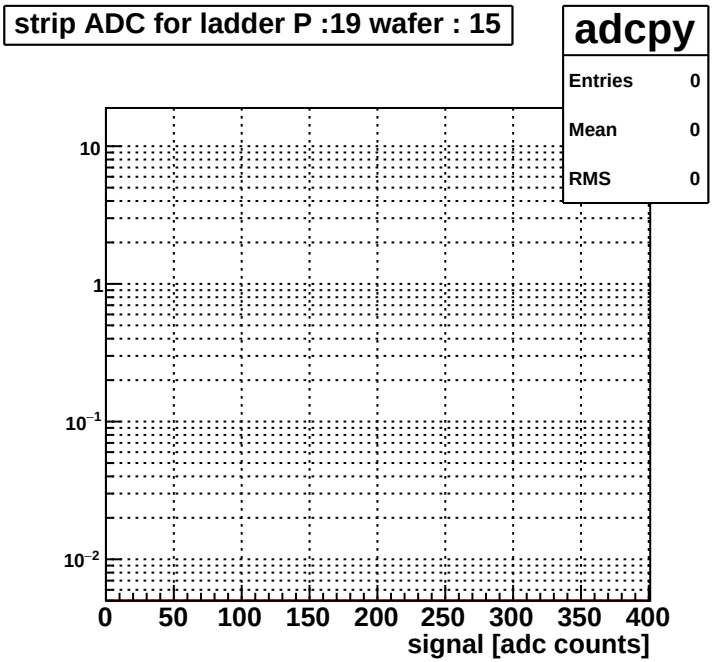

Strip adc vs. channels for ladder N : 19 wafer : 14
stripADCCHANsideN301
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

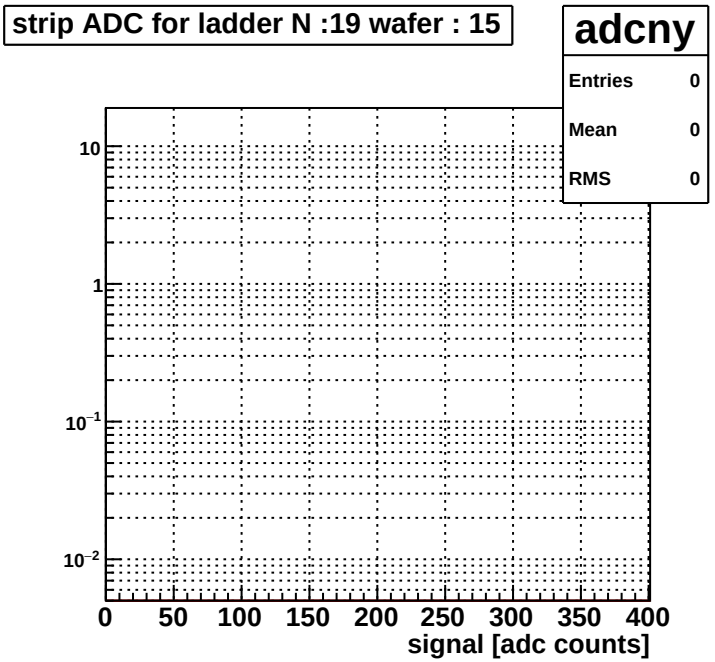

strip ADC for ladder P :19 wafer : 14
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

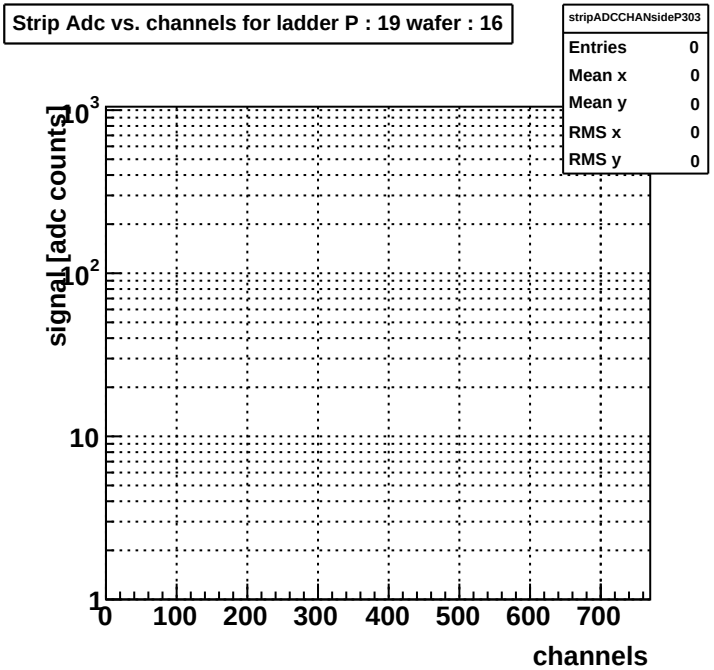

strip ADC for ladder N :19 wafer : 14
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

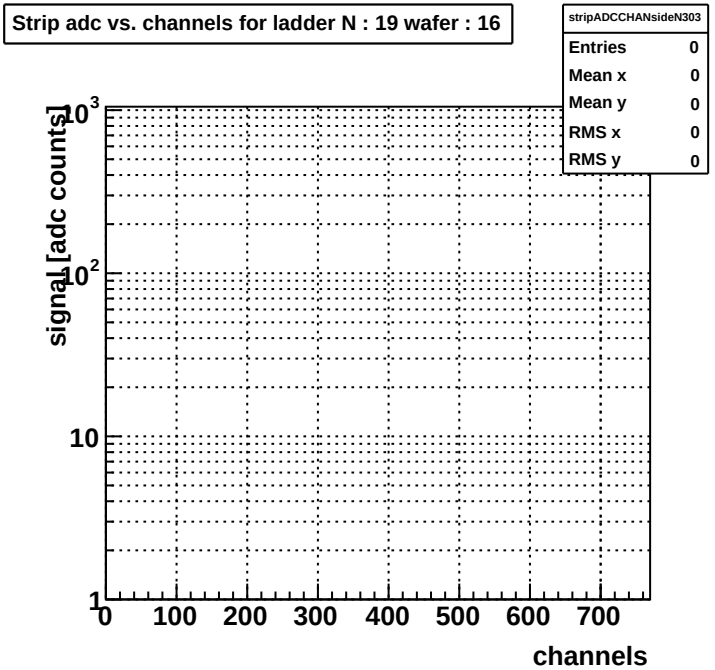

Strip Adc vs. channels for ladder P : 19 wafer : 15
stripADCCHANsideP302
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

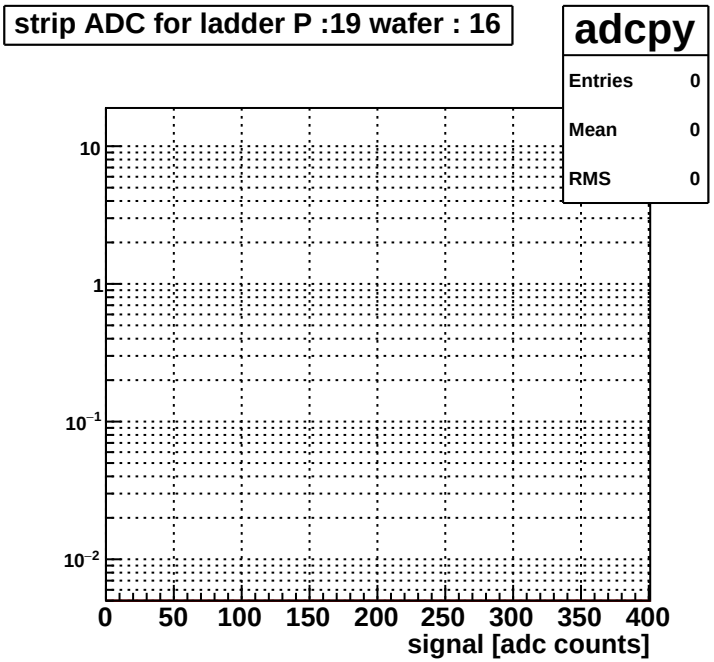

Strip adc vs. channels for ladder N : 19 wafer : 15
stripADCCHANsideN302
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

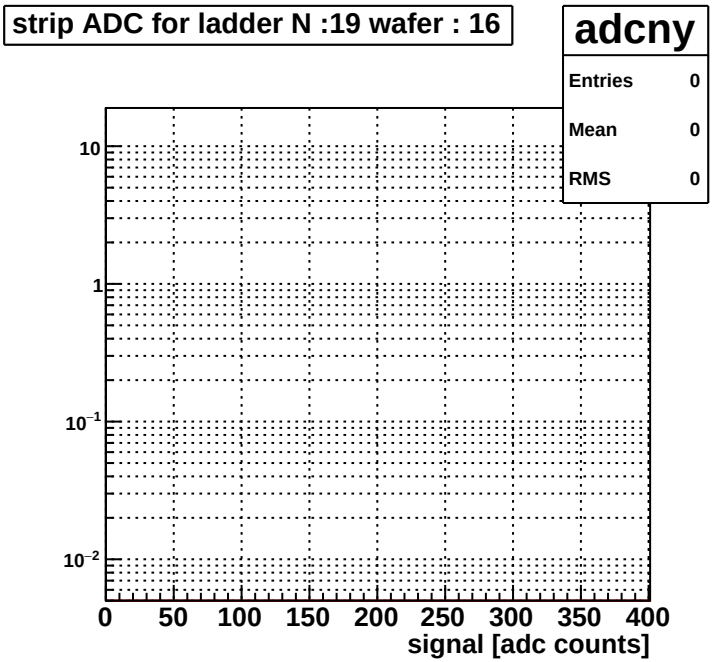

strip ADC for ladder P :19 wafer : 15
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

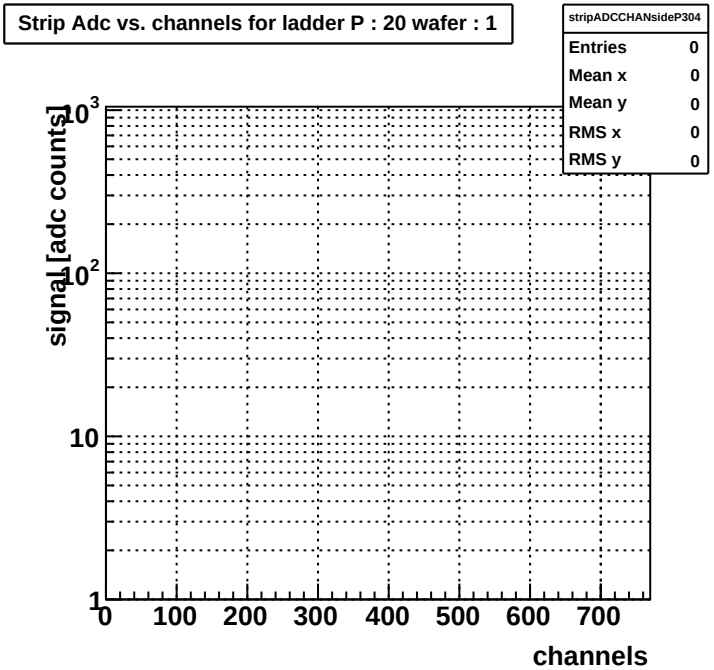

strip ADC for ladder N :19 wafer : 15
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

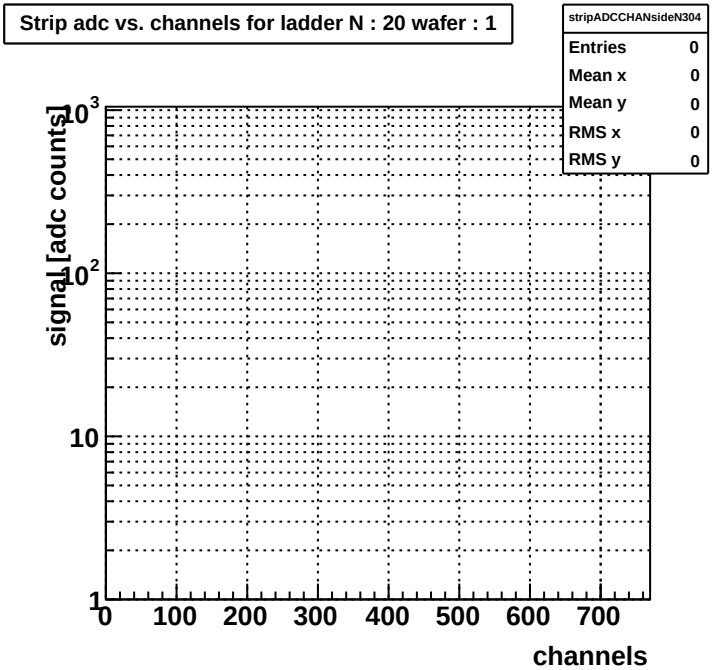

Strip Adc vs. channels for ladder P : 19 wafer : 16
stripADCCHANsideP303
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

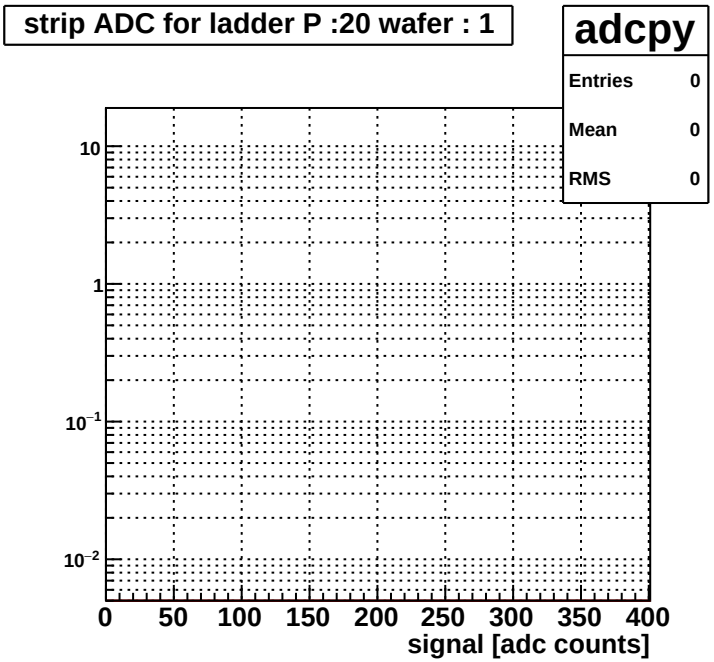

Strip adc vs. channels for ladder N : 19 wafer : 16
stripADCCHANsideN303
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

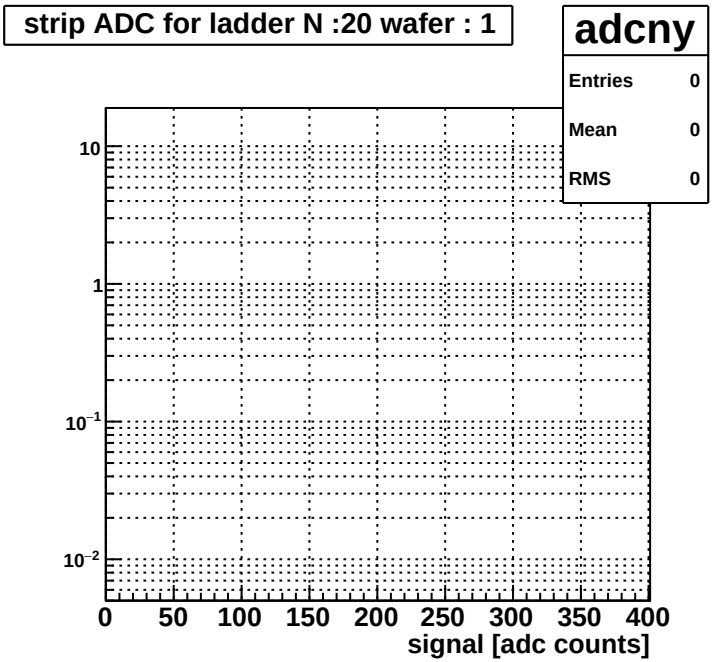

strip ADC for ladder P :19 wafer : 16
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

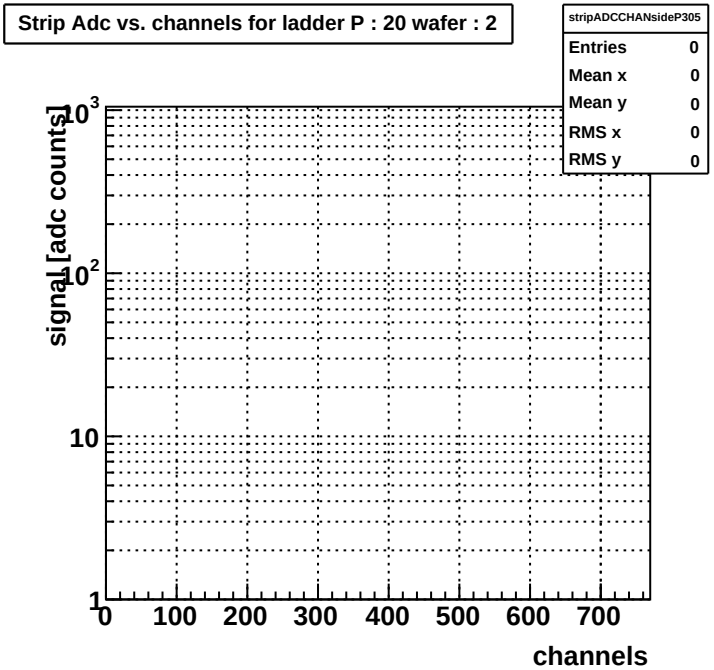

strip ADC for ladder N :19 wafer : 16
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

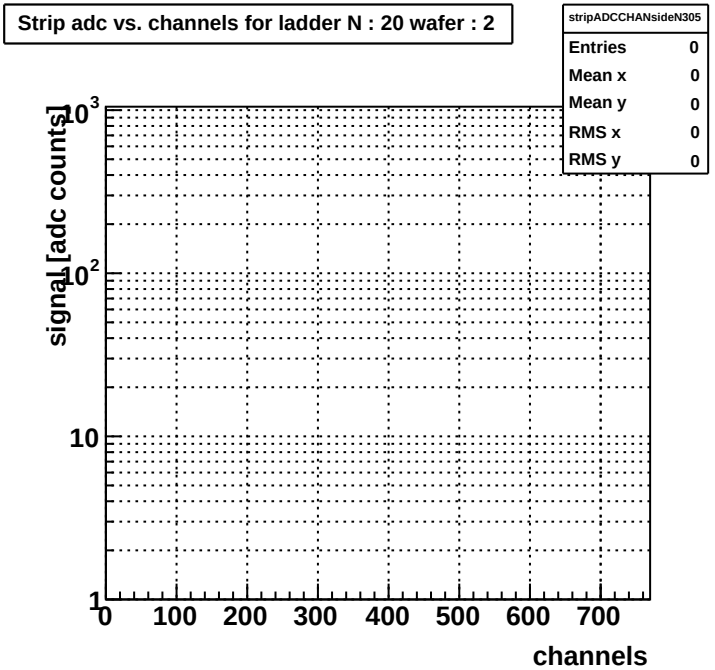

Strip Adc vs. channels for ladder P : 20 wafer : 1
stripADCCHANsideP304
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

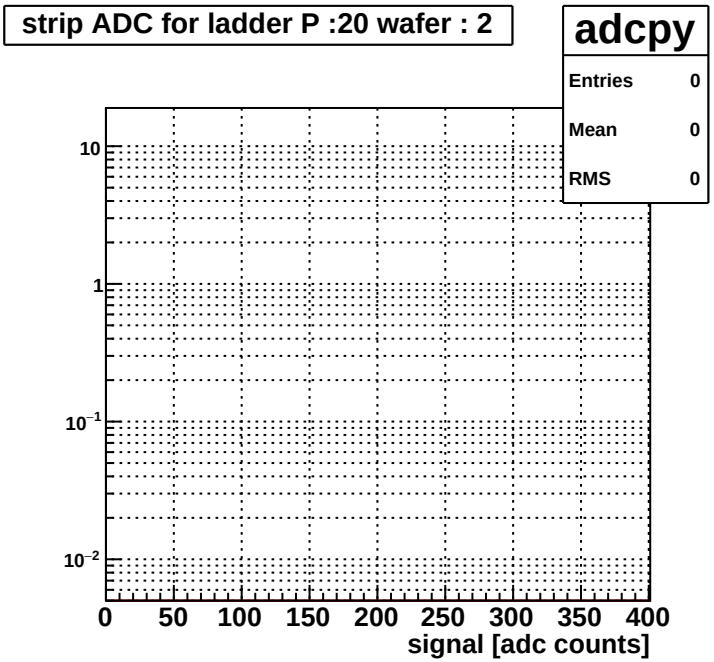

Strip adc vs. channels for ladder N : 20 wafer : 1
stripADCCHANsideN304
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

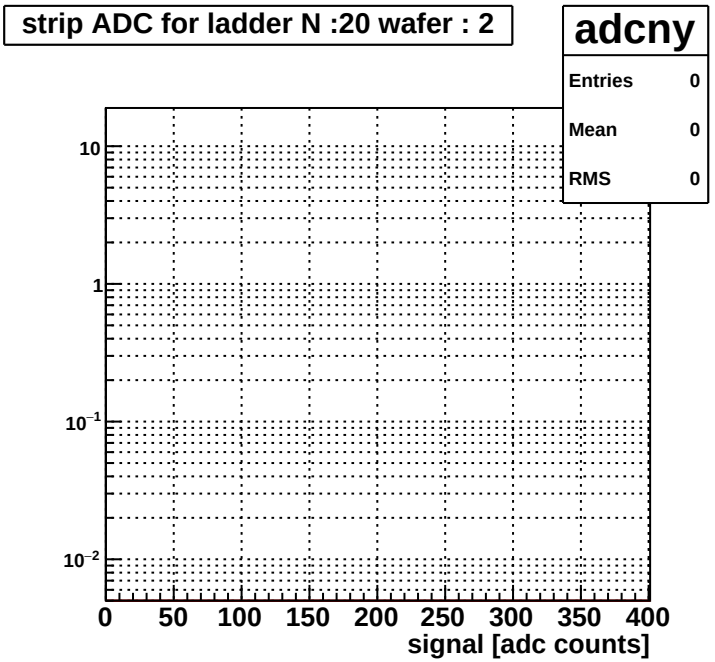

strip ADC for ladder P :20 wafer : 1
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

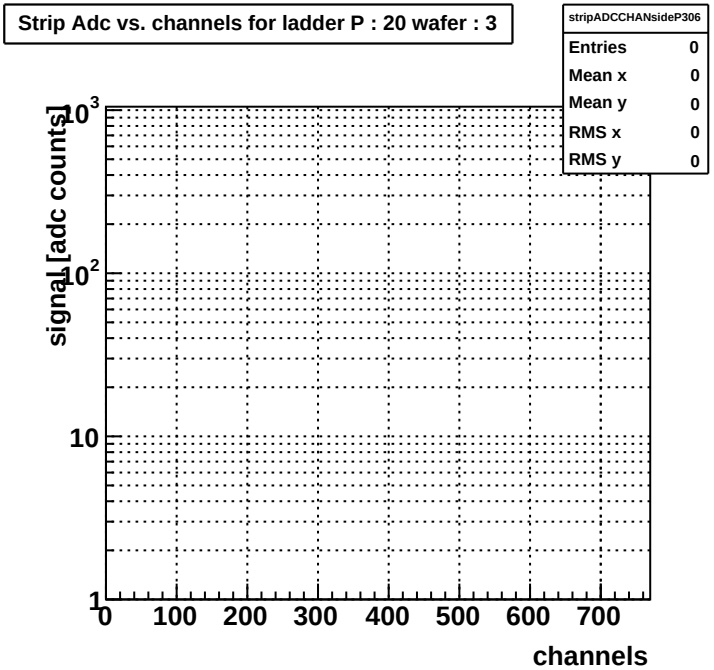

strip ADC for ladder N :20 wafer : 1
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

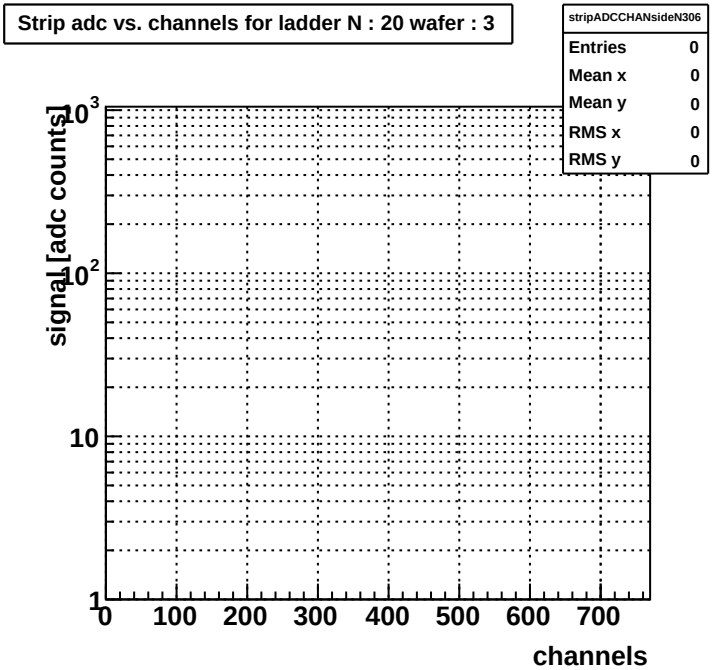

Strip Adc vs. channels for ladder P : 20 wafer : 2
stripADCCHANsideP305
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

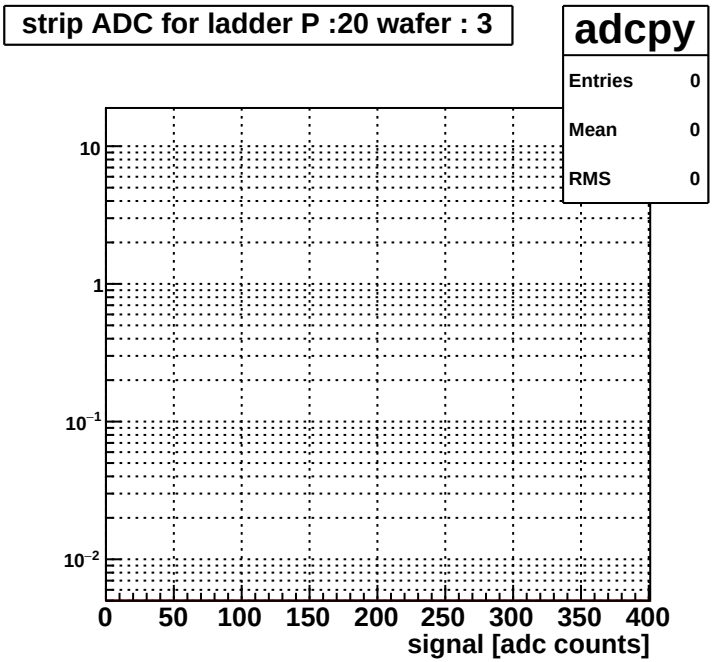

Strip adc vs. channels for ladder N : 20 wafer : 2
stripADCCHANsideN305
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

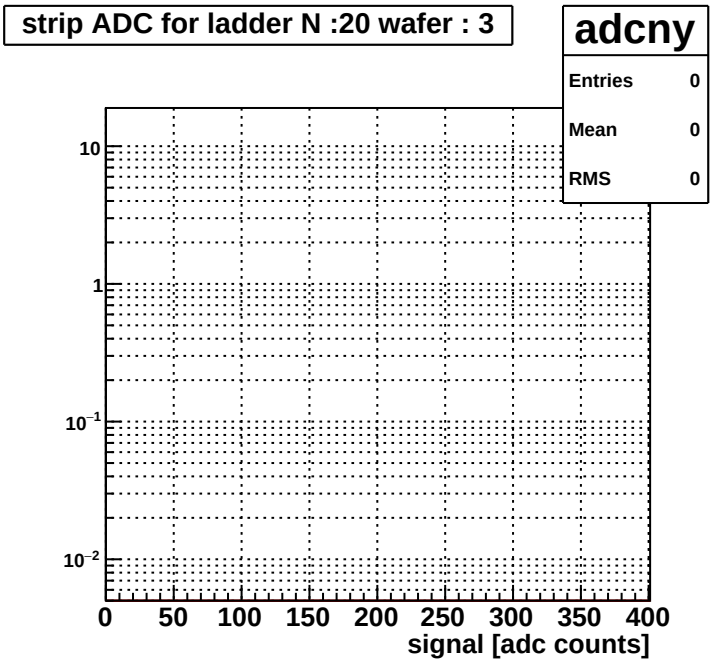

strip ADC for ladder P :20 wafer : 2
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

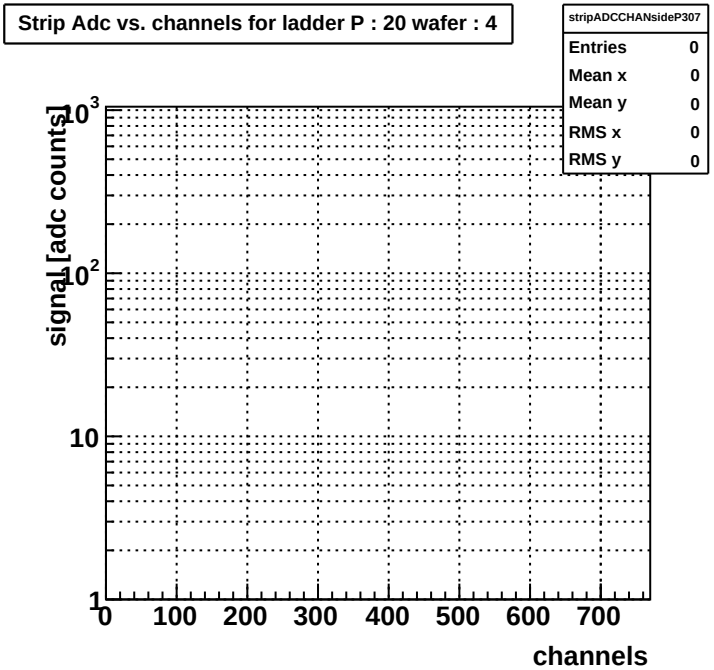

strip ADC for ladder N :20 wafer : 2
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

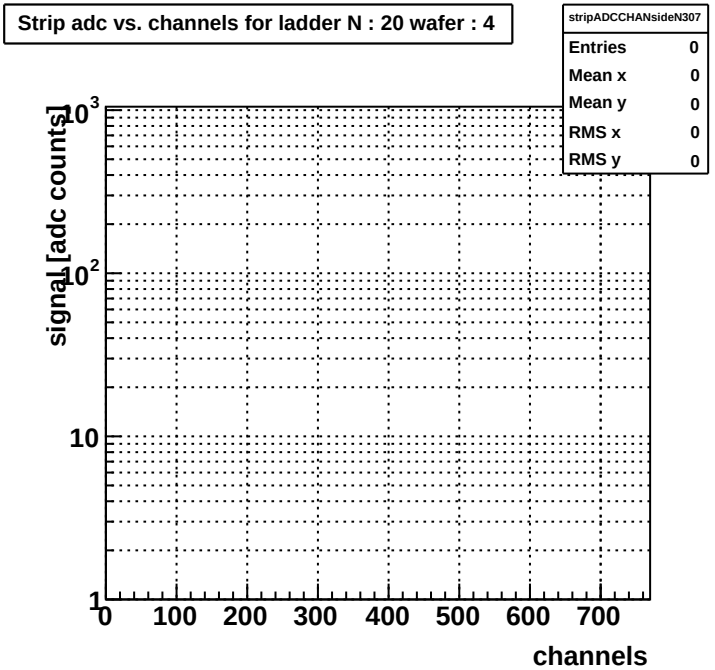

Strip Adc vs. channels for ladder P : 20 wafer : 3
stripADCCHANsideP306
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

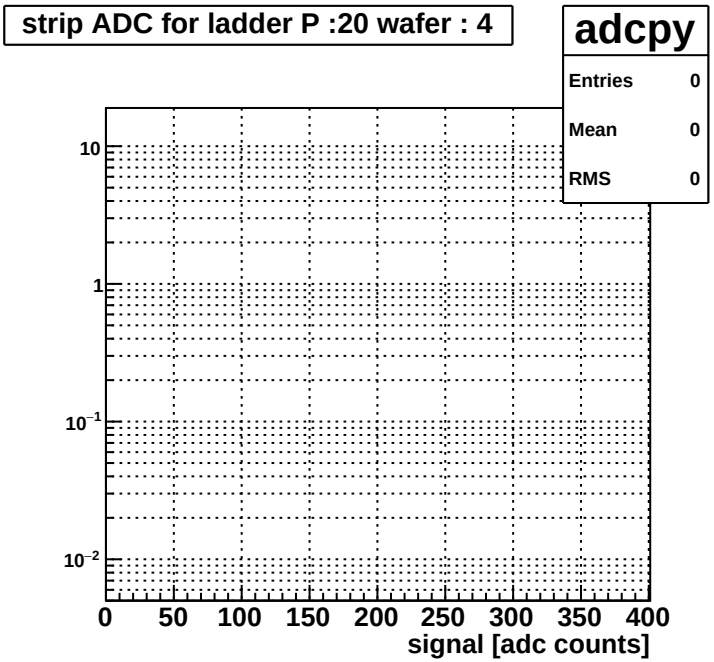

Strip adc vs. channels for ladder N : 20 wafer : 3
stripADCCHANsideN306
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

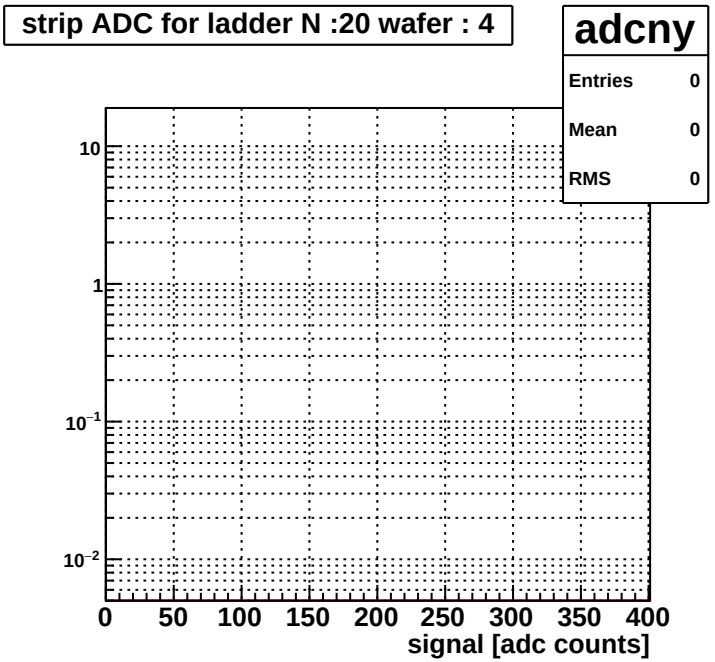

strip ADC for ladder P :20 wafer : 3
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

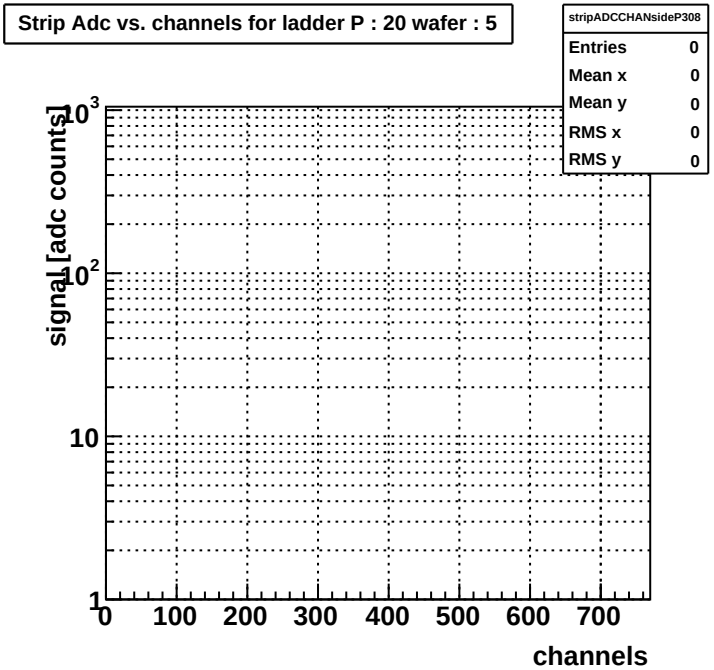

strip ADC for ladder N :20 wafer : 3
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

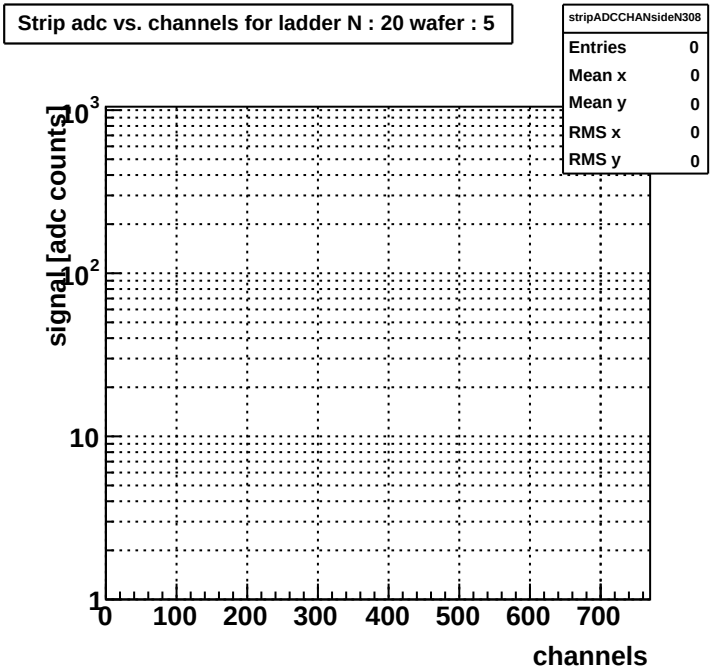

Strip Adc vs. channels for ladder P : 20 wafer : 4
stripADCCHANsideP307
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

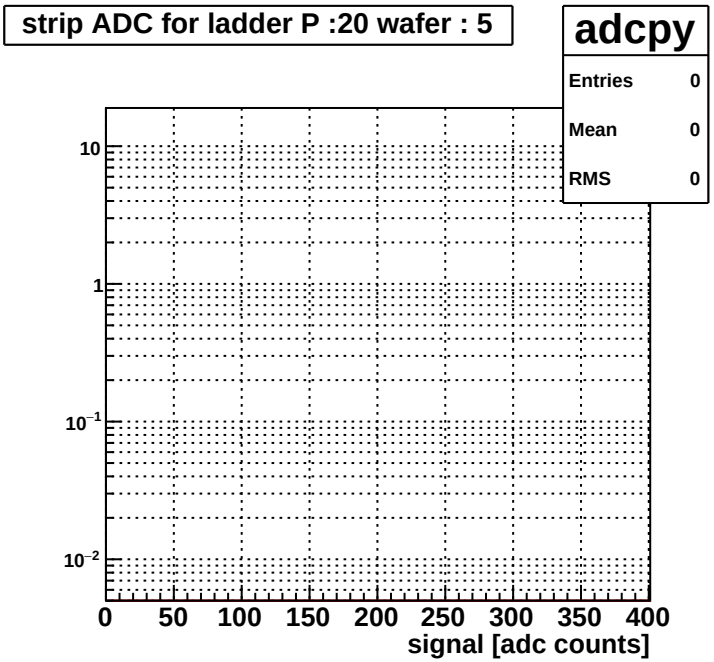

Strip adc vs. channels for ladder N : 20 wafer : 4
stripADCCHANsideN307
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

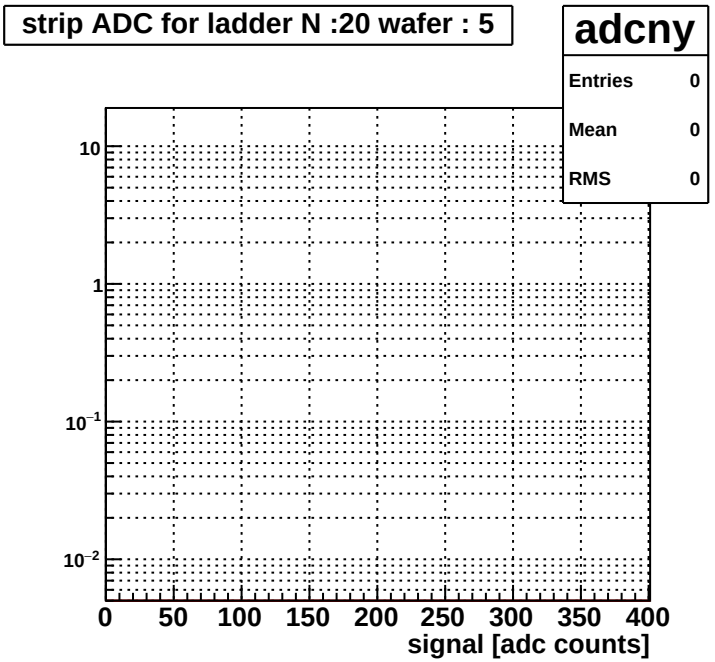

strip ADC for ladder P :20 wafer : 4
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

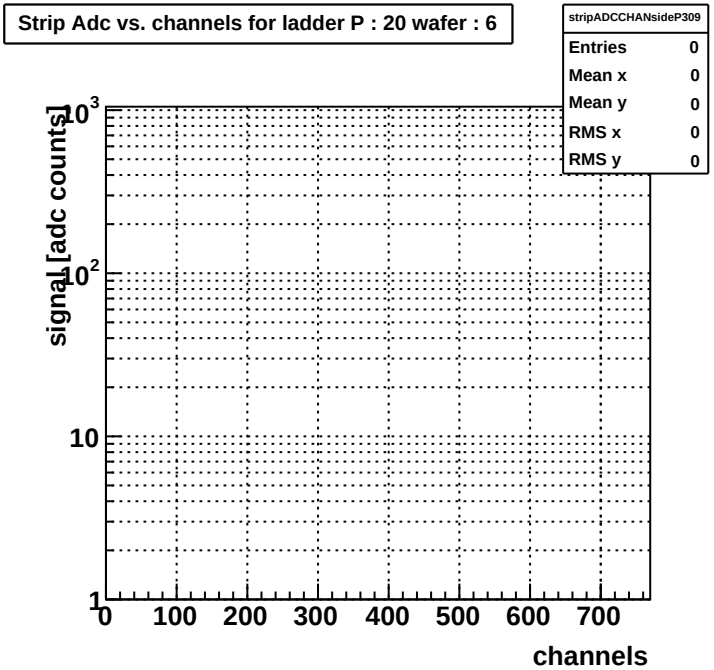

strip ADC for ladder N :20 wafer : 4
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

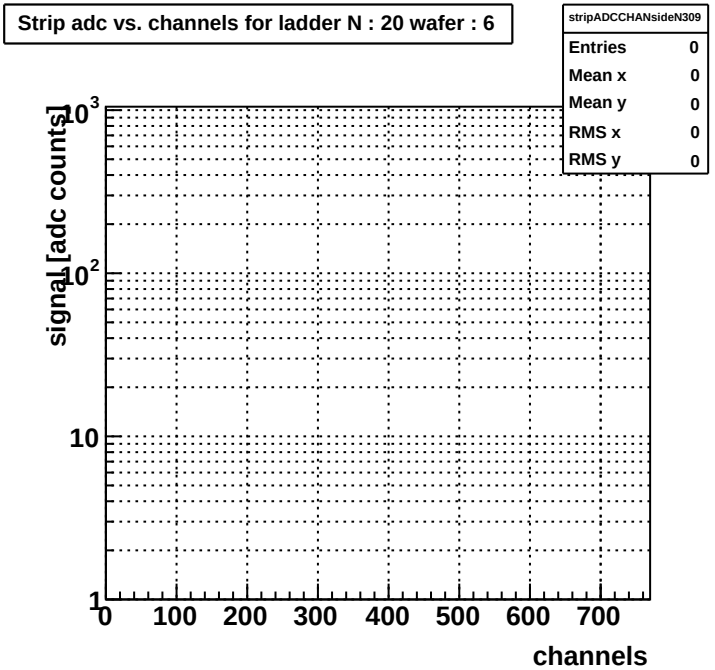

Strip Adc vs. channels for ladder P : 20 wafer : 5
stripADCCHANsideP308
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

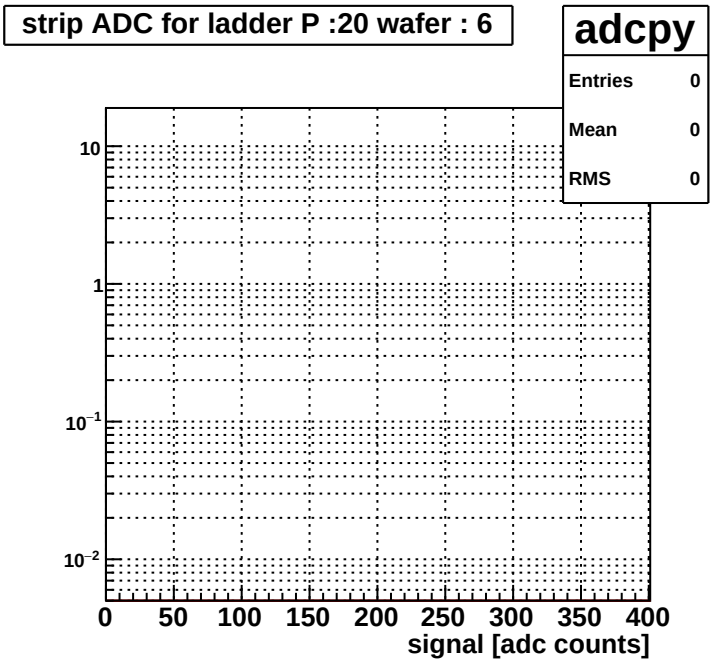

Strip adc vs. channels for ladder N : 20 wafer : 5
stripADCCHANsideN308
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

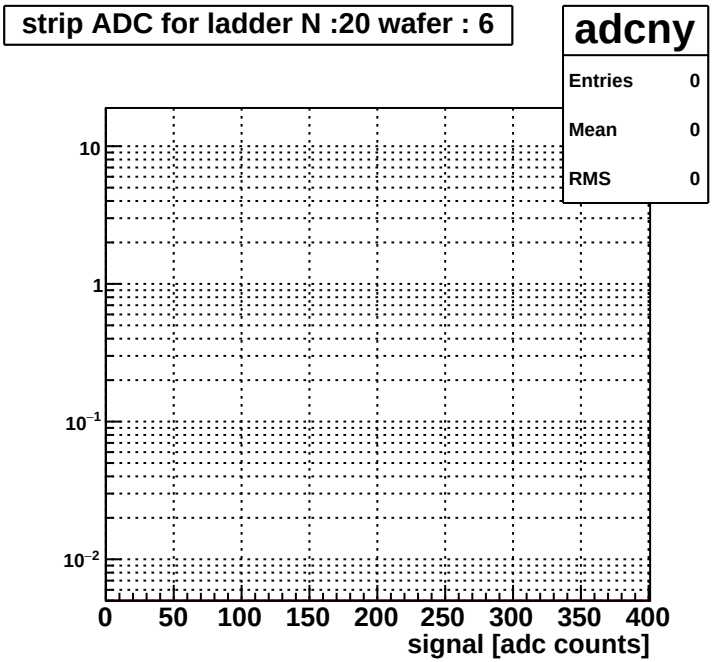

strip ADC for ladder P :20 wafer : 5
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

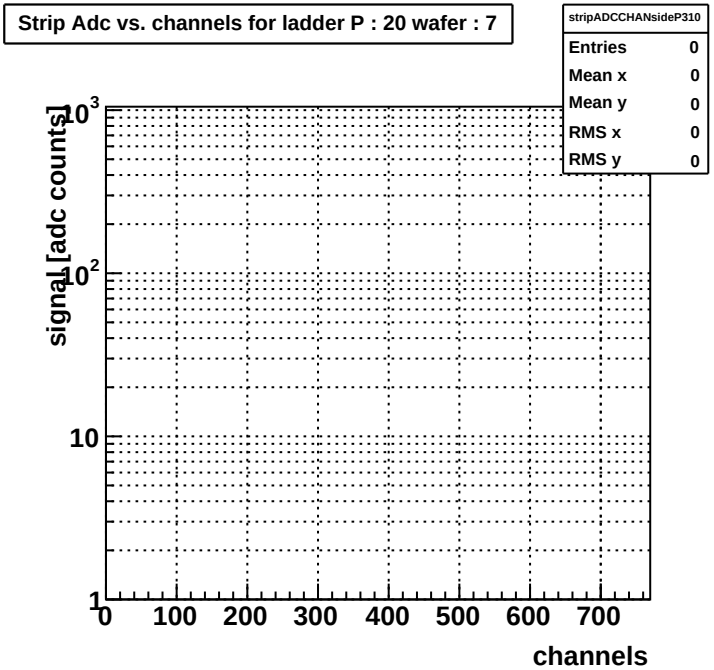

strip ADC for ladder N :20 wafer : 5
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

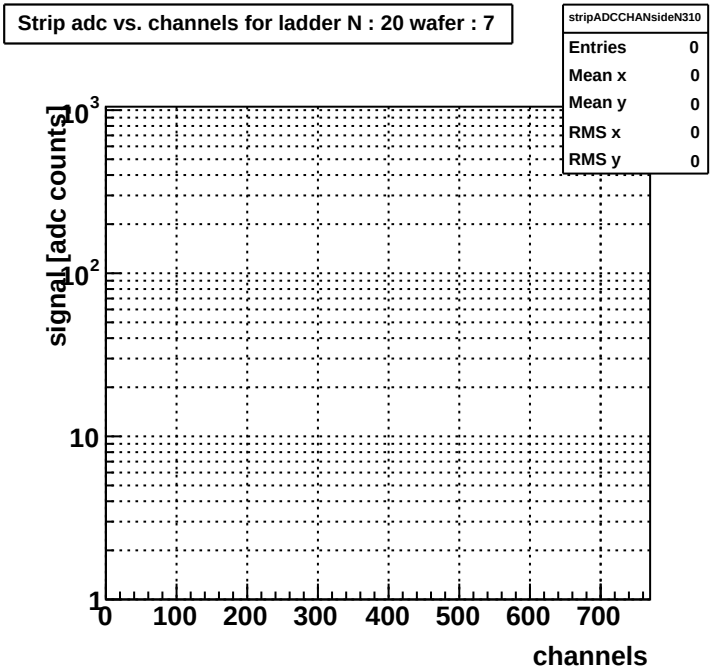

Strip Adc vs. channels for ladder P : 20 wafer : 6
stripADCCHANsideP309
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

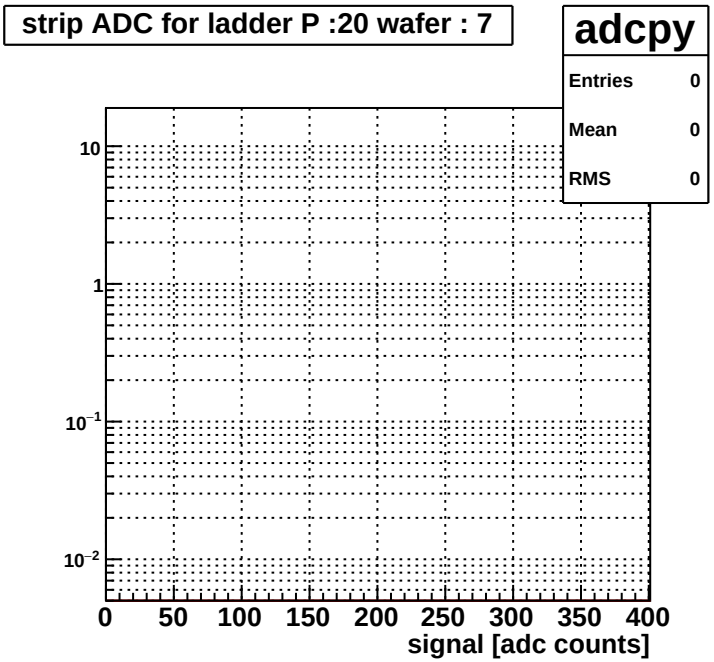

Strip adc vs. channels for ladder N : 20 wafer : 6
stripADCCHANsideN309
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

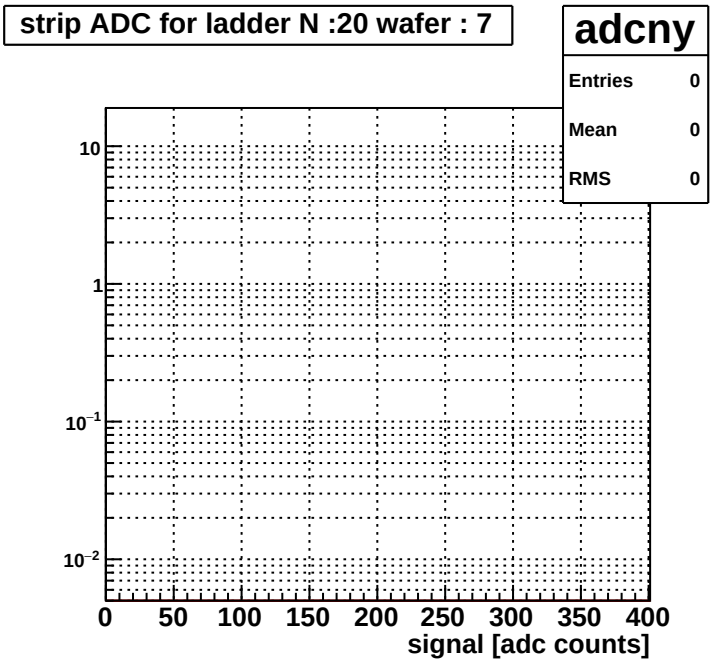

strip ADC for ladder P :20 wafer : 6
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

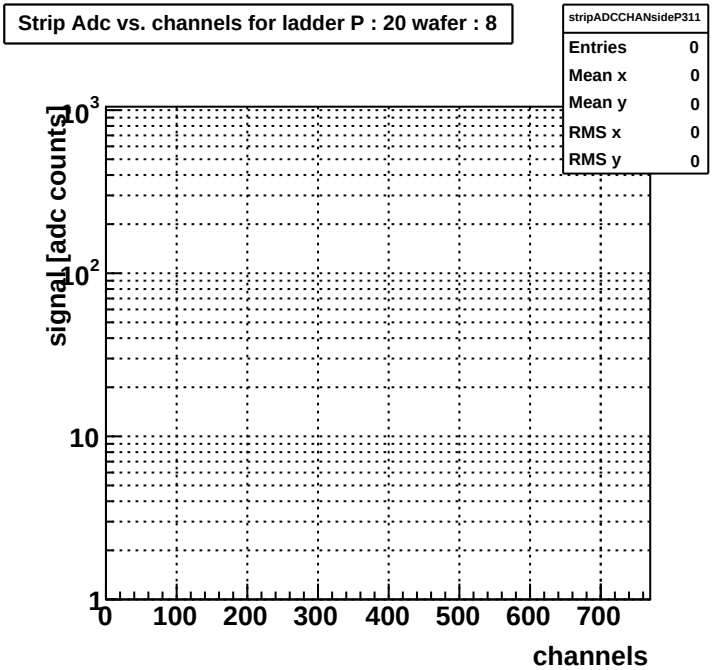

strip ADC for ladder N :20 wafer : 6
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

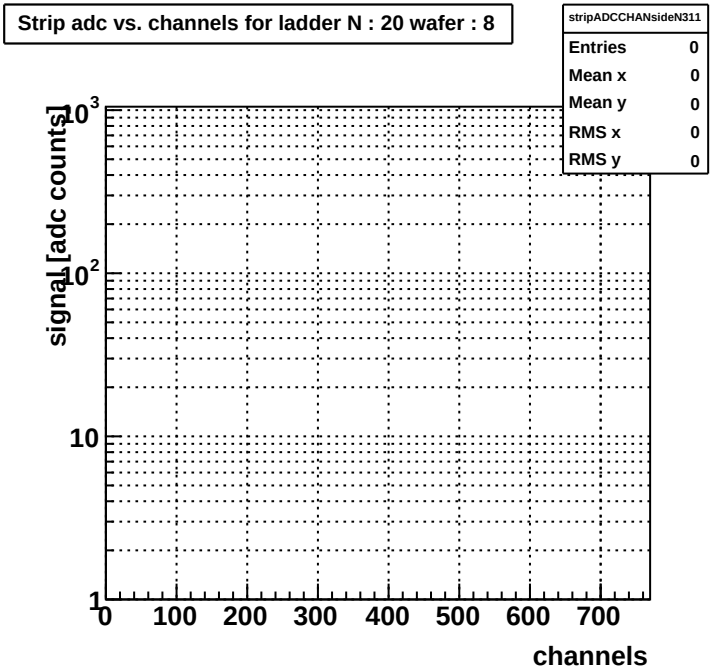

Strip Adc vs. channels for ladder P : 20 wafer : 7
stripADCCHANsideP310
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

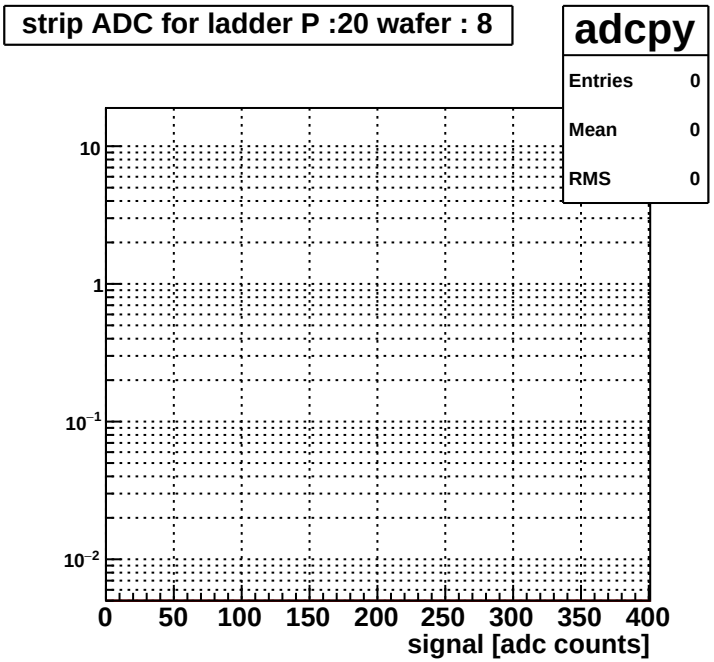

Strip adc vs. channels for ladder N : 20 wafer : 7
stripADCCHANsideN310
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

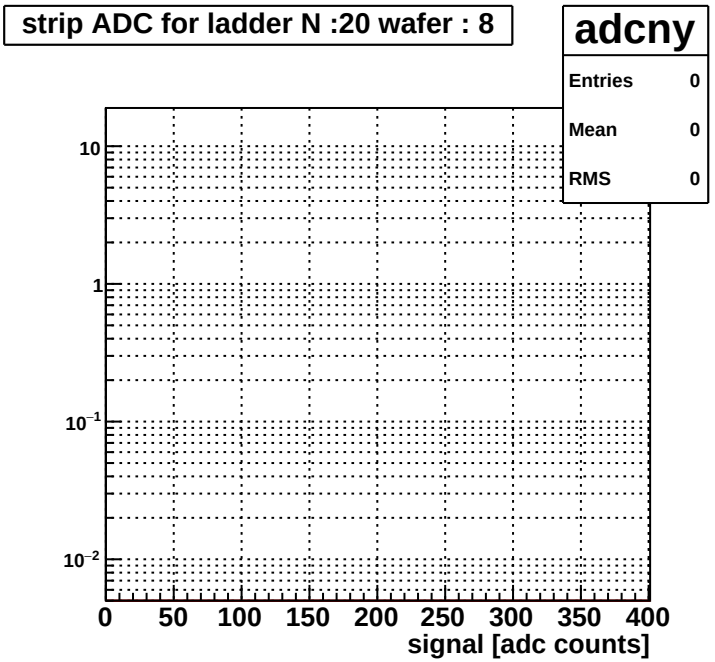

strip ADC for ladder P :20 wafer : 7
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

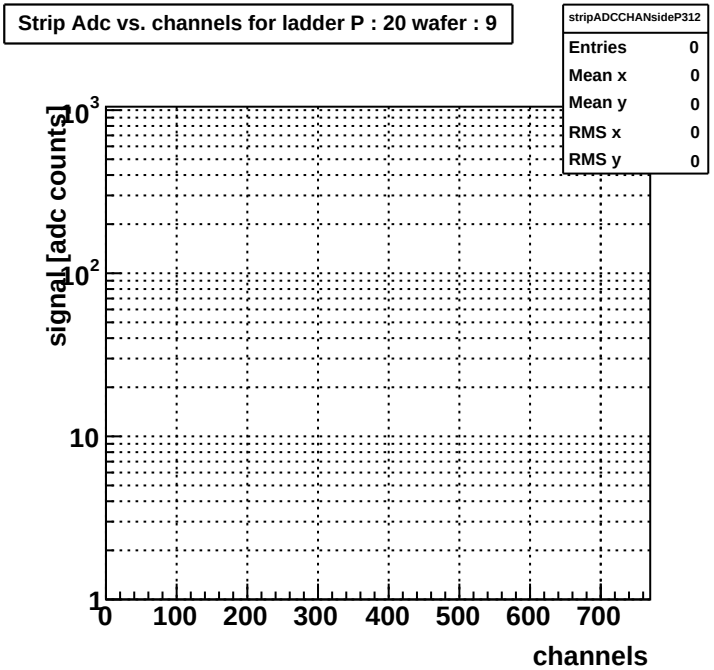

strip ADC for ladder N :20 wafer : 7
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

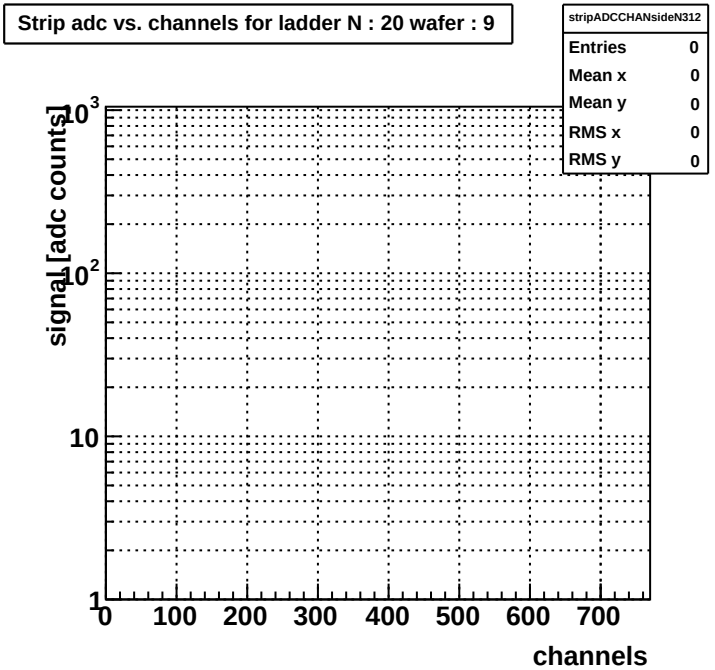

Strip Adc vs. channels for ladder P : 20 wafer : 8
stripADCCHANsideP311
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

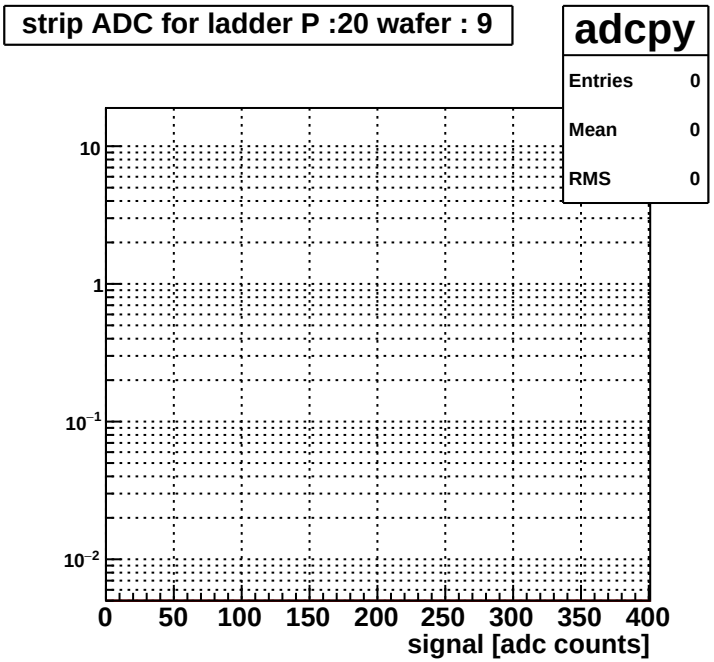

Strip adc vs. channels for ladder N : 20 wafer : 8
stripADCCHANsideN311
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

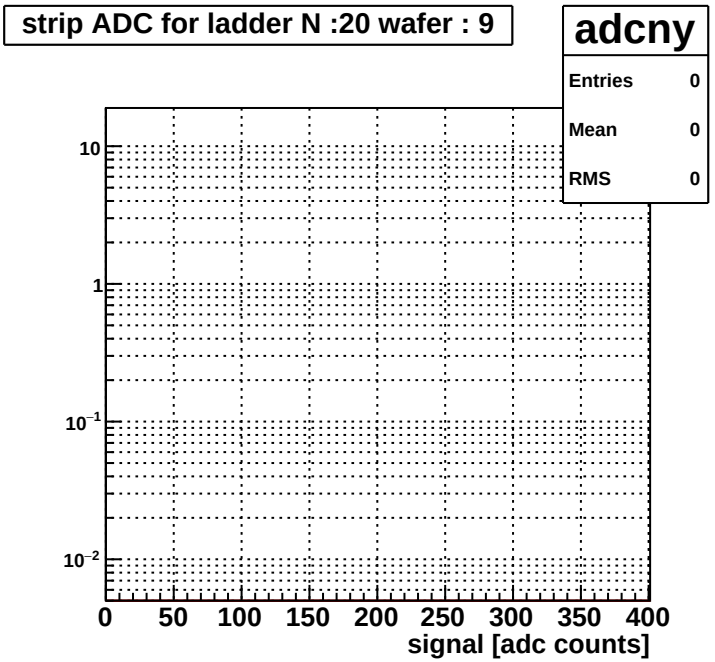

strip ADC for ladder P :20 wafer : 8
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

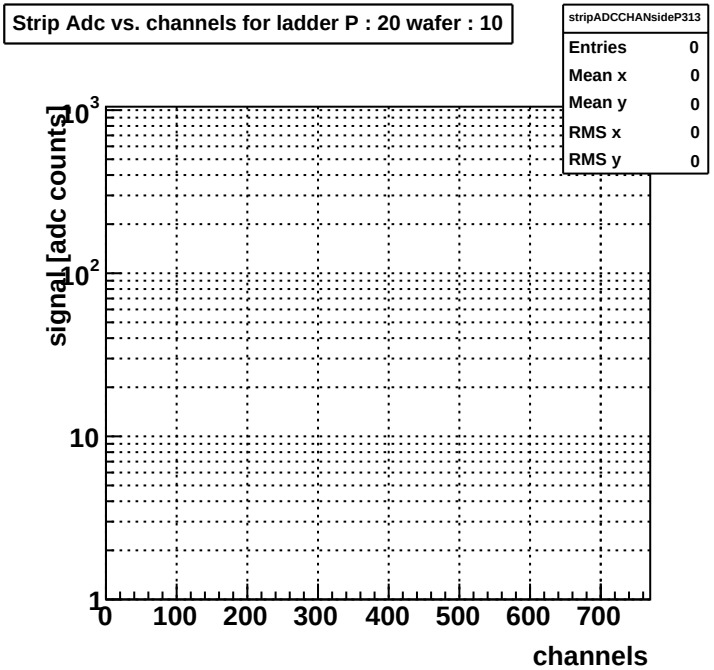

strip ADC for ladder N :20 wafer : 8
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

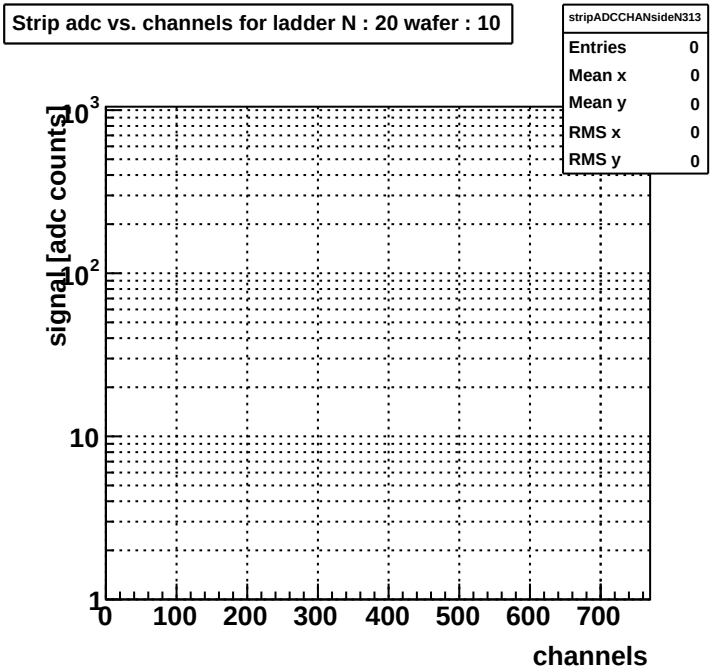

Strip Adc vs. channels for ladder P : 20 wafer : 9
stripADCCHANsideP312
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

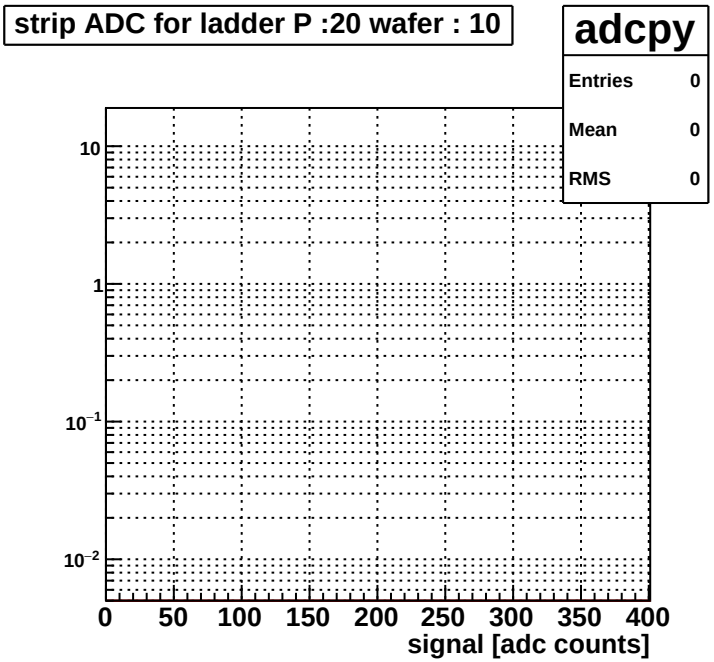

Strip adc vs. channels for ladder N : 20 wafer : 9
stripADCCHANsideN312
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

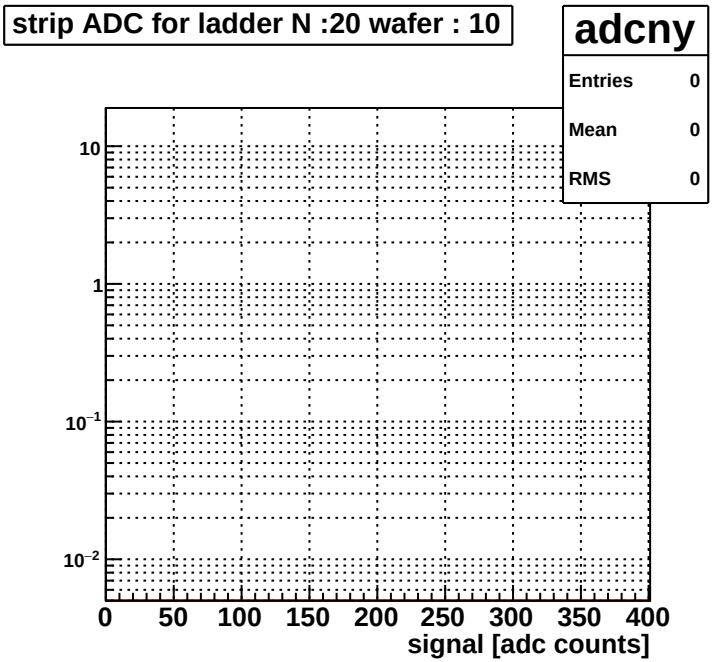

strip ADC for ladder P :20 wafer : 9
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

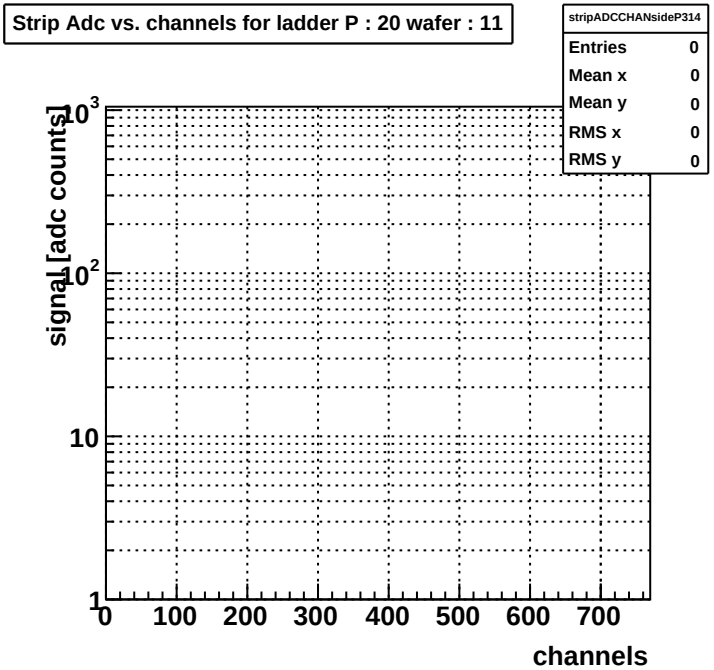

strip ADC for ladder N :20 wafer : 9
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

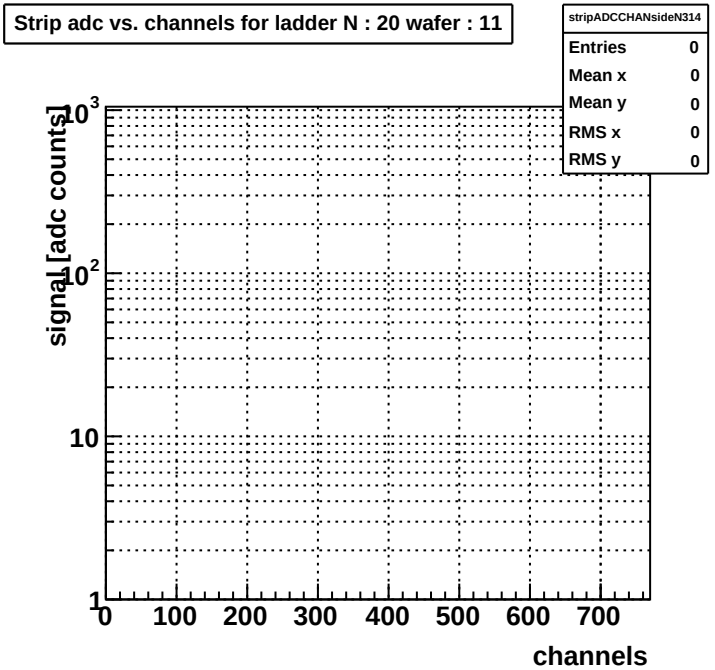

Strip Adc vs. channels for ladder P : 20 wafer : 10
stripADCCHANsideP313
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

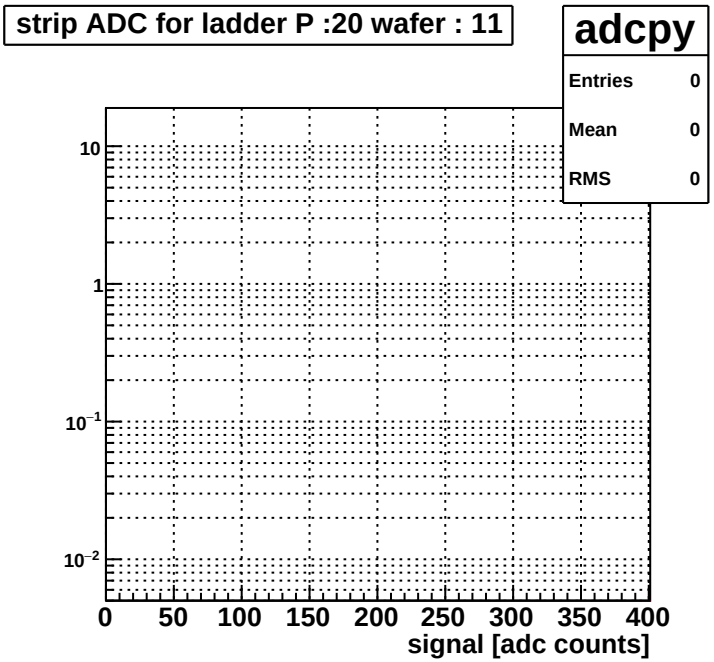

Strip adc vs. channels for ladder N : 20 wafer : 10
stripADCCHANsideN313
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

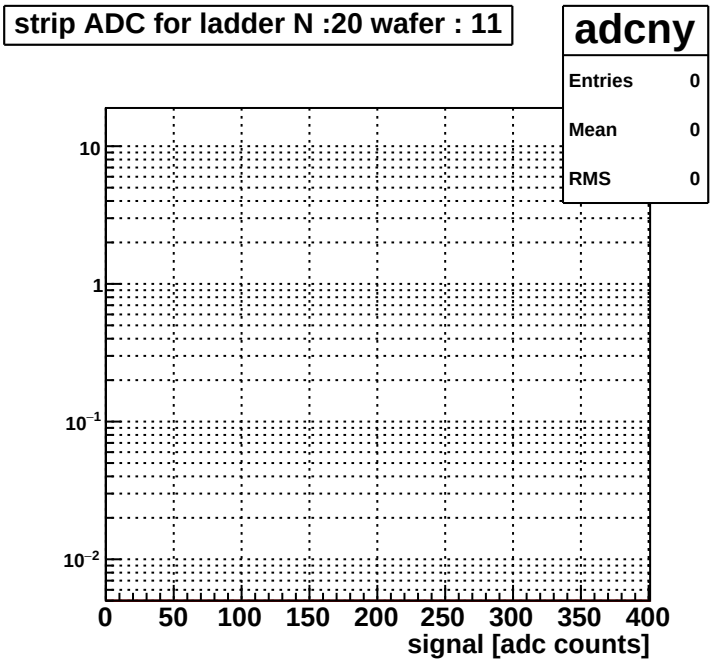

strip ADC for ladder P :20 wafer : 10
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

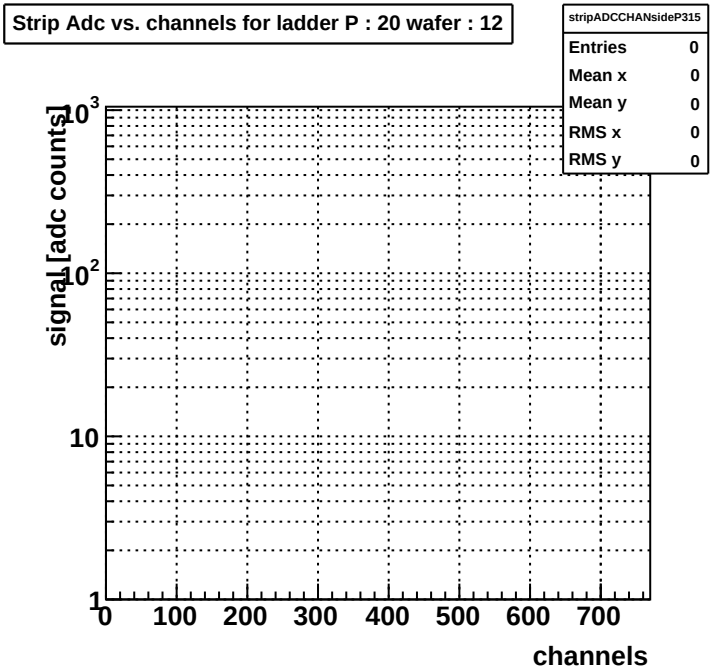

strip ADC for ladder N :20 wafer : 10
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

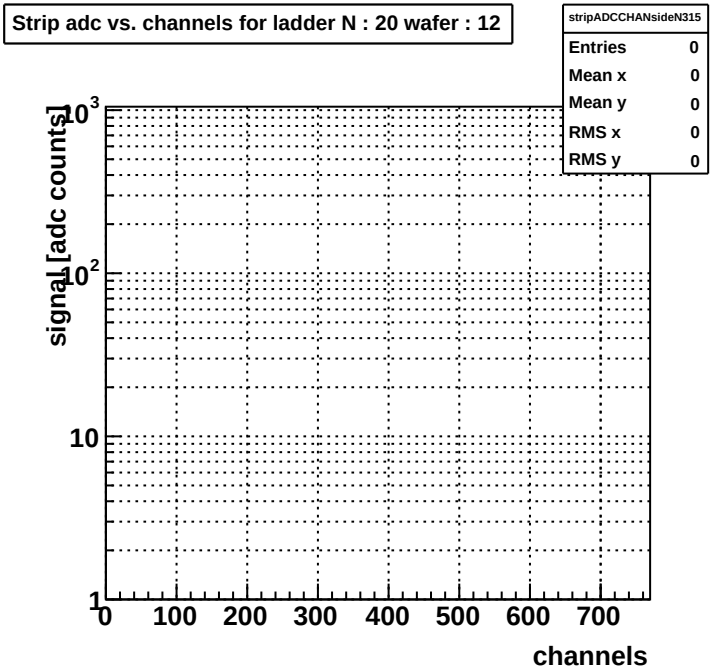

Strip Adc vs. channels for ladder P : 20 wafer : 11
stripADCCHANsideP314
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

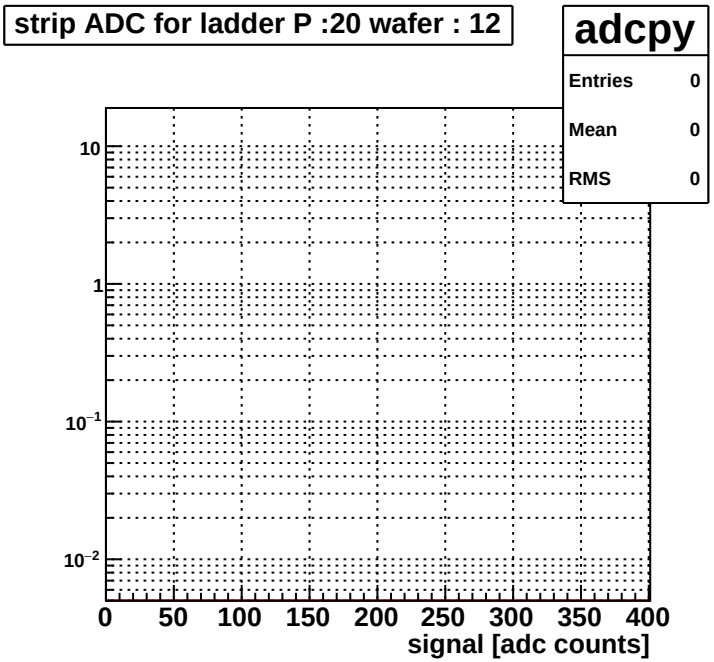

Strip adc vs. channels for ladder N : 20 wafer : 11
stripADCCHANsideN314
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

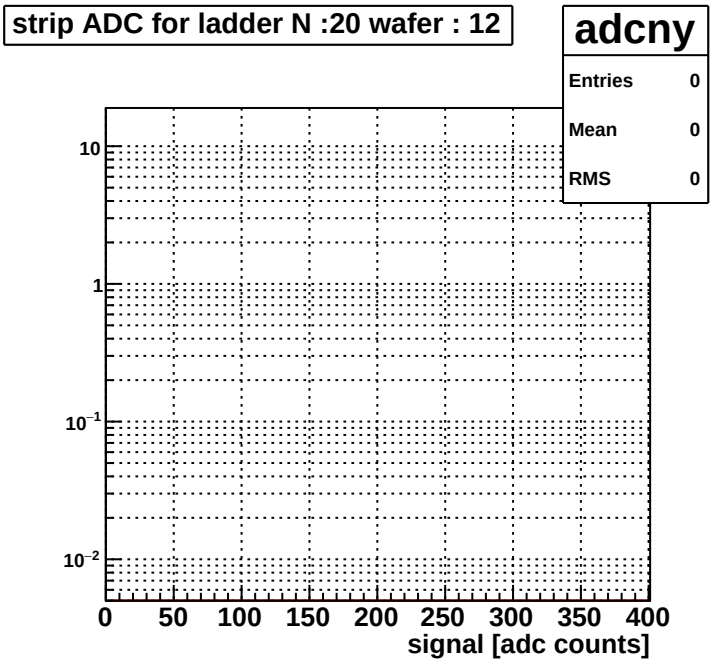

strip ADC for ladder P :20 wafer : 11
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

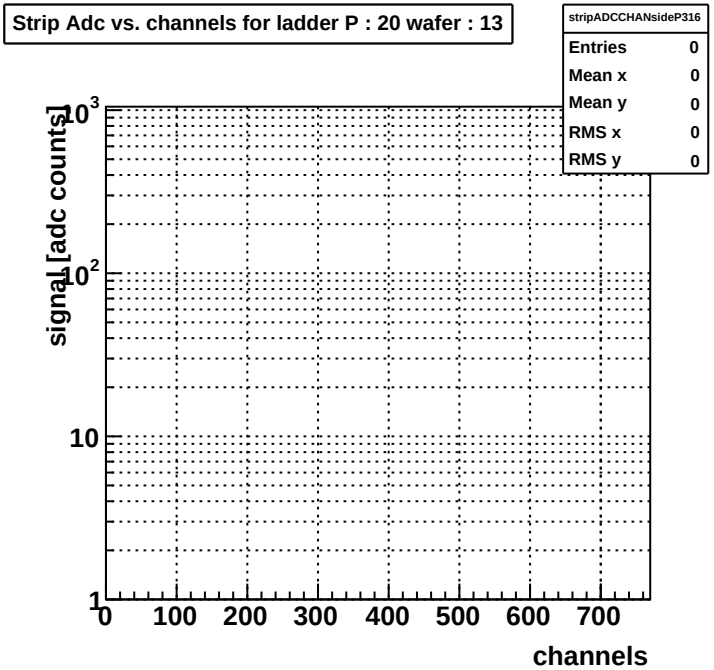

strip ADC for ladder N :20 wafer : 11
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

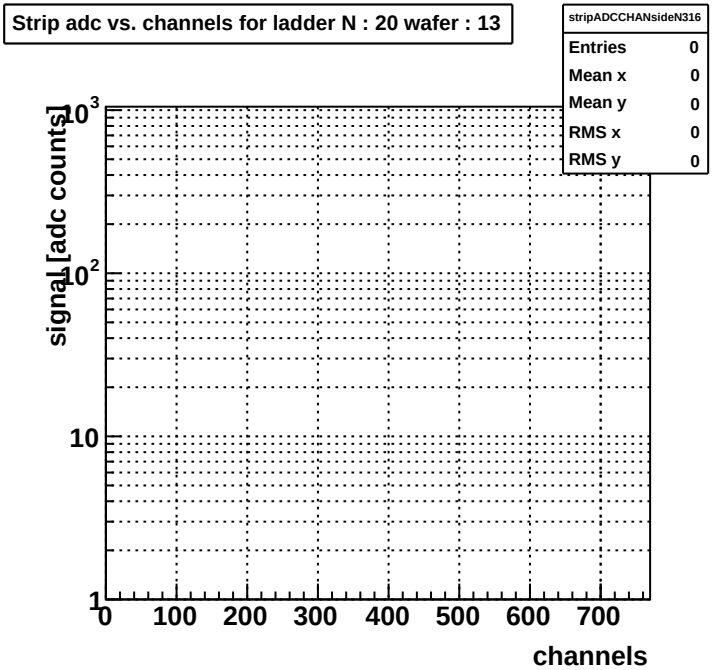

Strip Adc vs. channels for ladder P : 20 wafer : 12
stripADCCHANsideP315
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

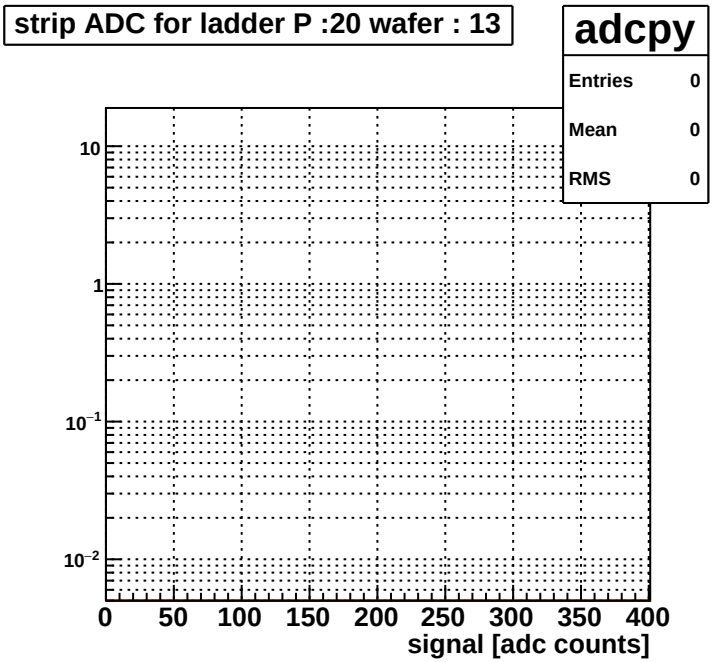

Strip adc vs. channels for ladder N : 20 wafer : 12
stripADCCHANsideN315
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

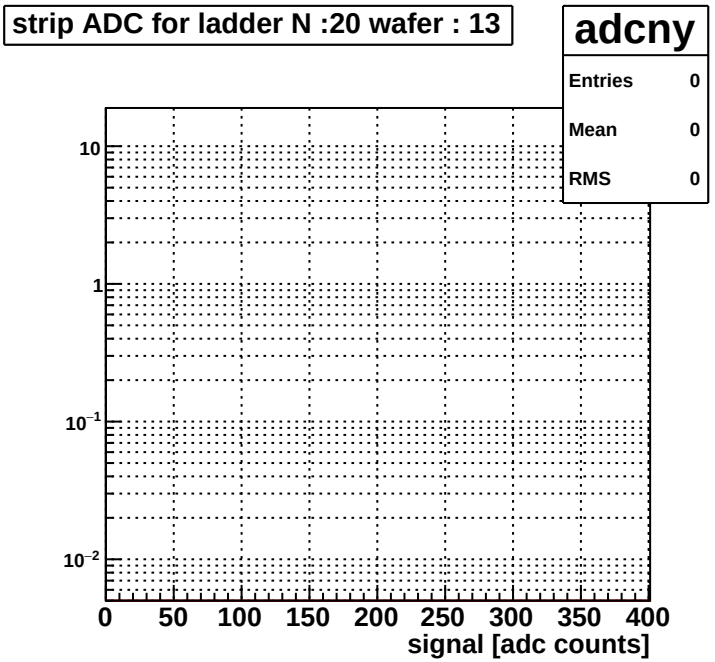

strip ADC for ladder P :20 wafer : 12
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

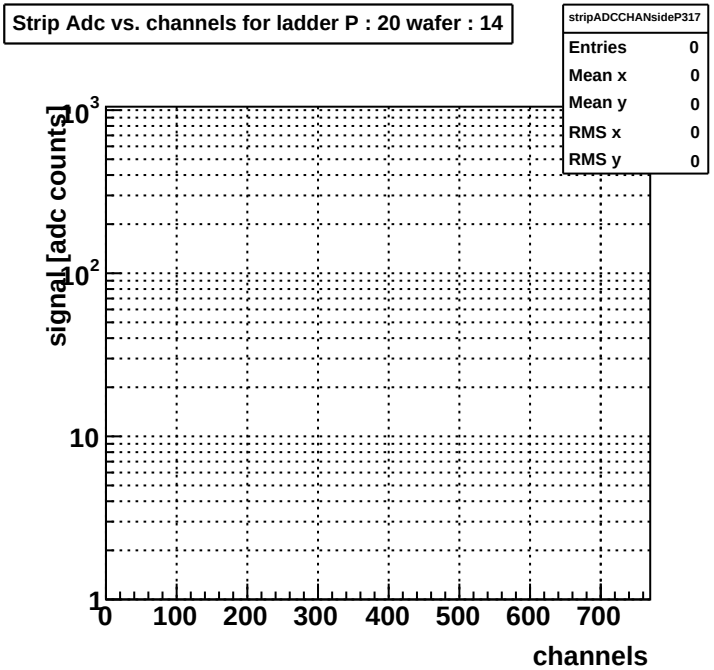

strip ADC for ladder N :20 wafer : 12
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

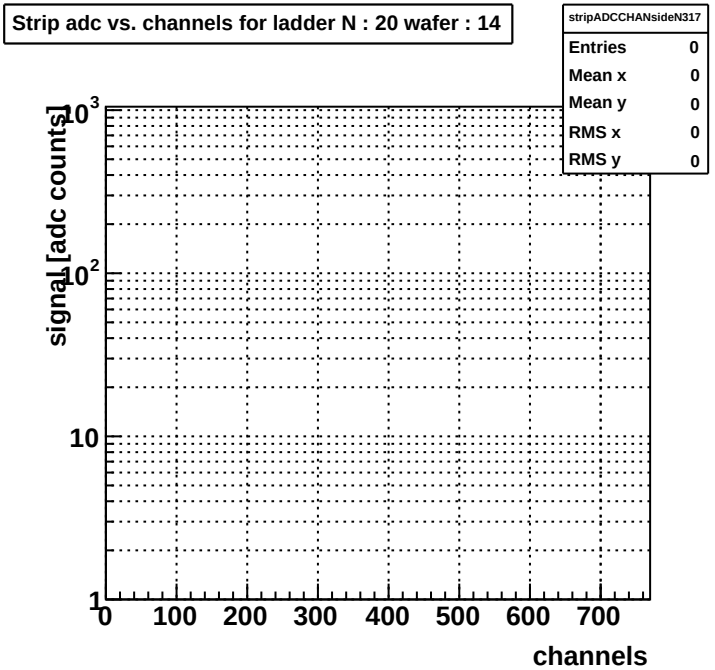

Strip Adc vs. channels for ladder P : 20 wafer : 13
stripADCCHANsideP316
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

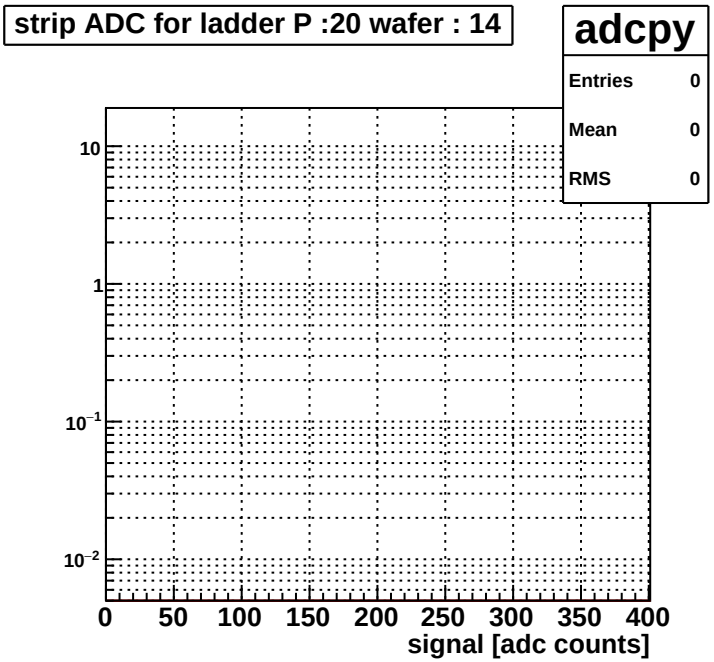

Strip adc vs. channels for ladder N : 20 wafer : 13
stripADCCHANsideN316
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

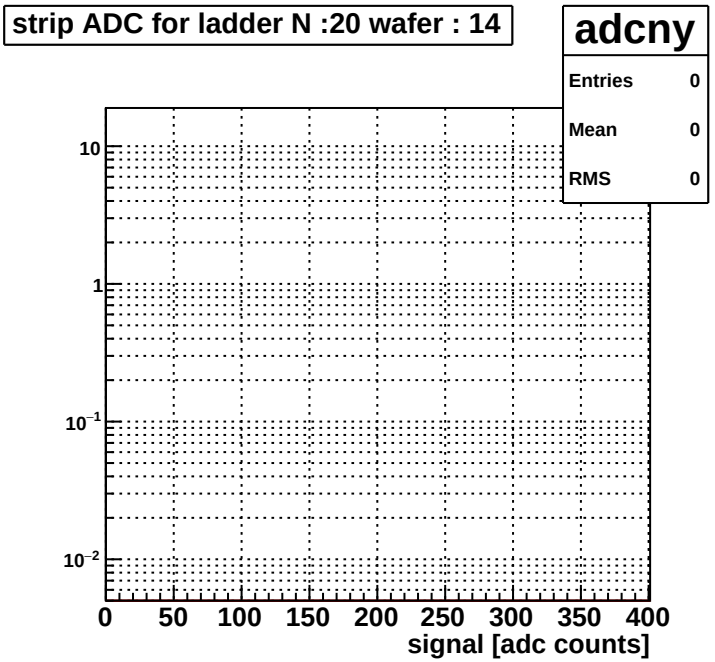

strip ADC for ladder P :20 wafer : 13
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

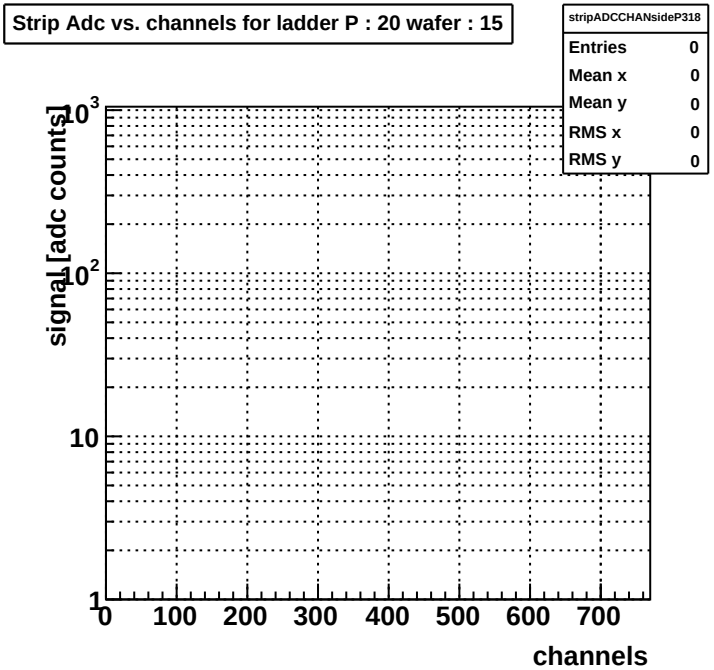

strip ADC for ladder N :20 wafer : 13
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

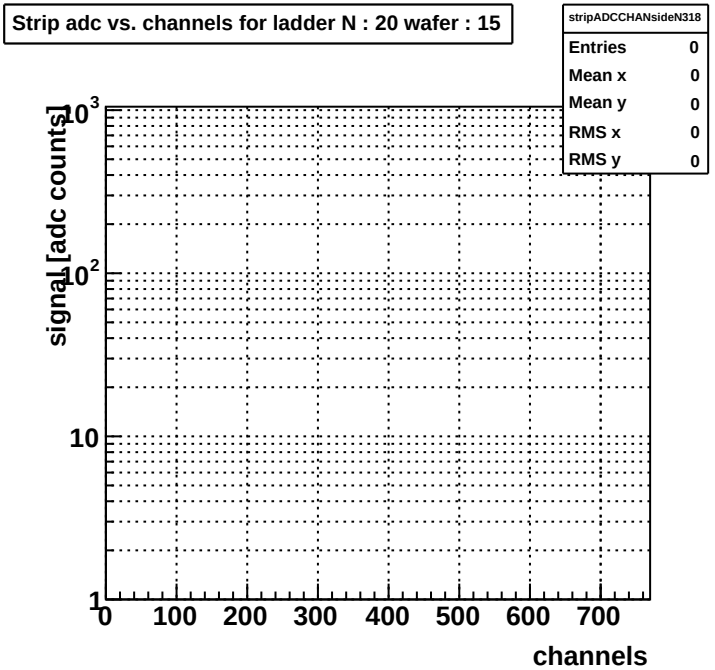

Strip Adc vs. channels for ladder P : 20 wafer : 14
stripADCCHANsideP317
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

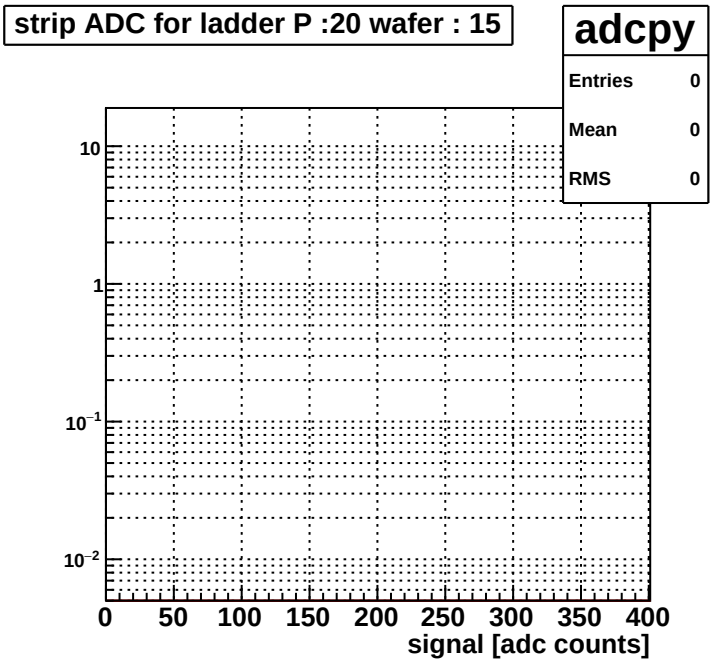

Strip adc vs. channels for ladder N : 20 wafer : 14
stripADCCHANsideN317
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

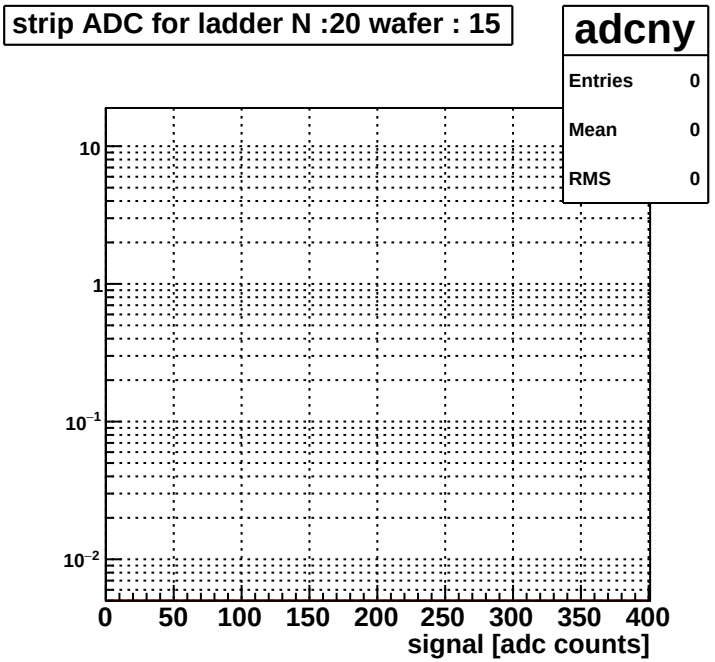

strip ADC for ladder P :20 wafer : 14
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]

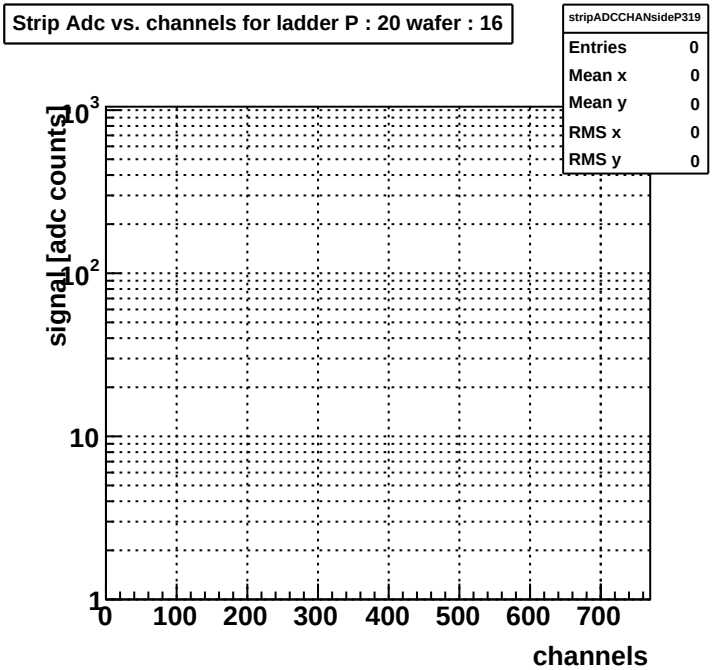

strip ADC for ladder N :20 wafer : 14
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

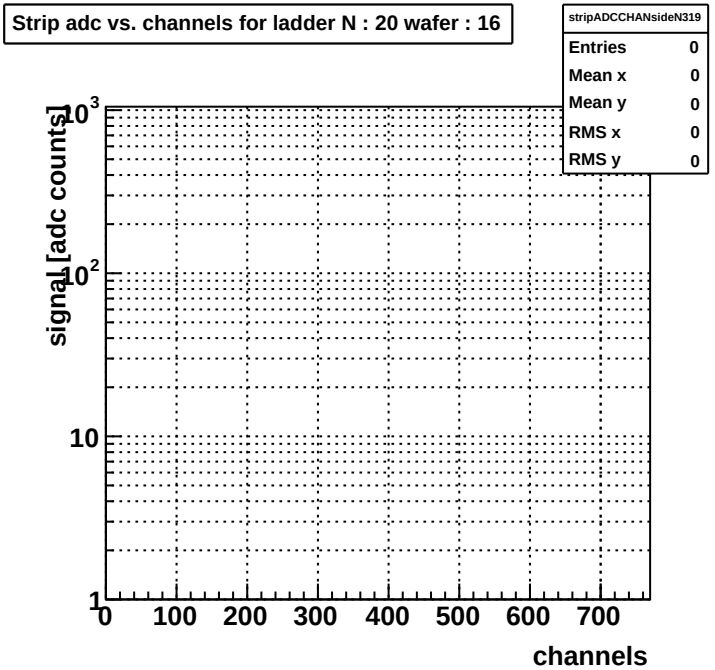

Strip Adc vs. channels for ladder P : 20 wafer : 15
stripADCCHANsideP318
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

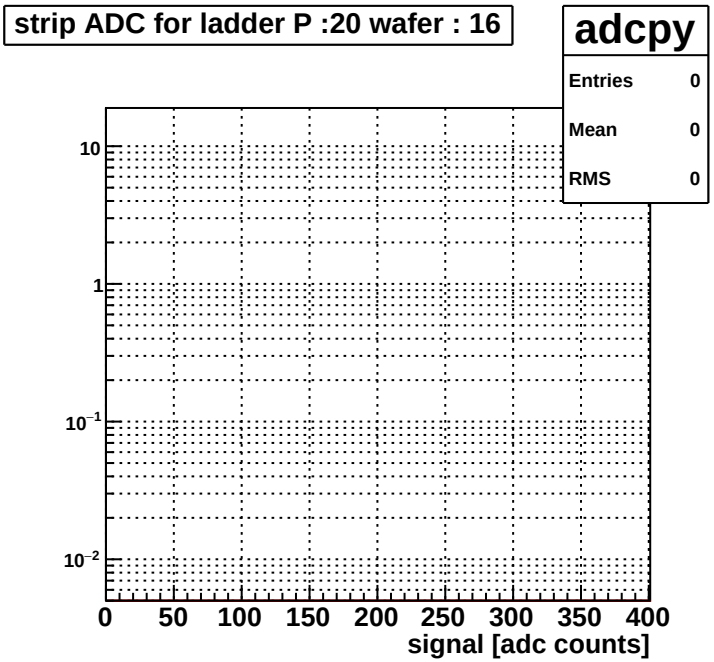

Strip adc vs. channels for ladder N : 20 wafer : 15
stripADCCHANsideN318
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels

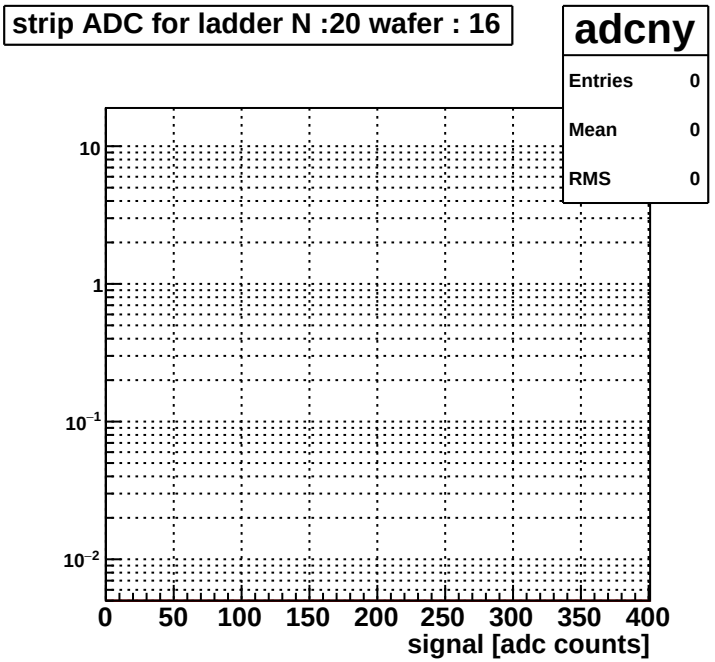

strip ADC for ladder P :20 wafer : 15
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]


strip ADC for ladder N :20 wafer : 15
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

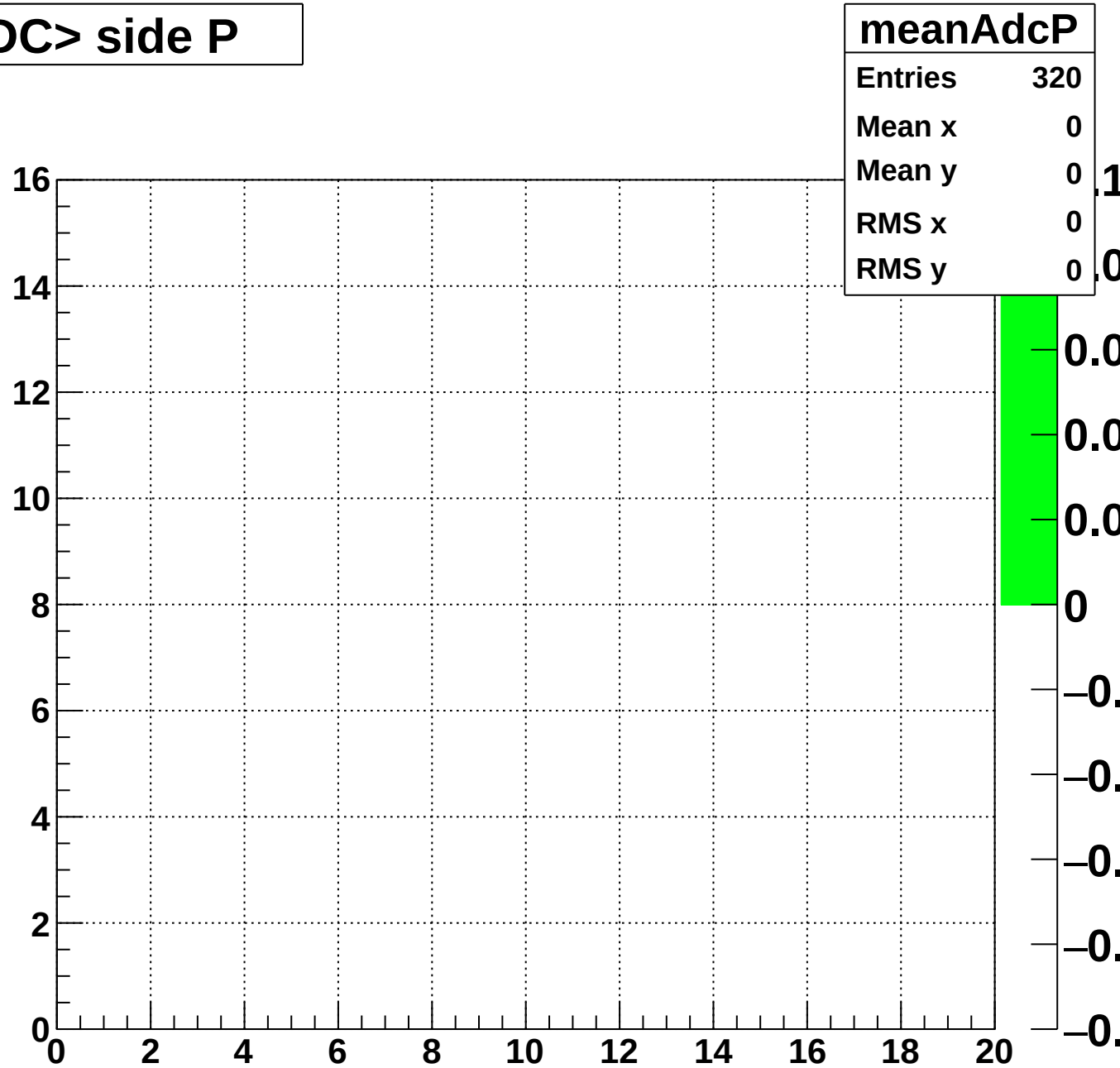

Strip Adc vs. channels for ladder P : 20 wafer : 16
stripADCCHANsideP319
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels


Strip adc vs. channels for ladder N : 20 wafer : 16
stripADCCHANsideN319
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
signal [adc counts]
channels


strip ADC for ladder P :20 wafer : 16
adcpy
Entries 0
Mean 0
RMS 0
signal [adc counts]


strip ADC for ladder N :20 wafer : 16
adcny
Entries 0
Mean 0
RMS 0
signal [adc counts]

<ADC> side P



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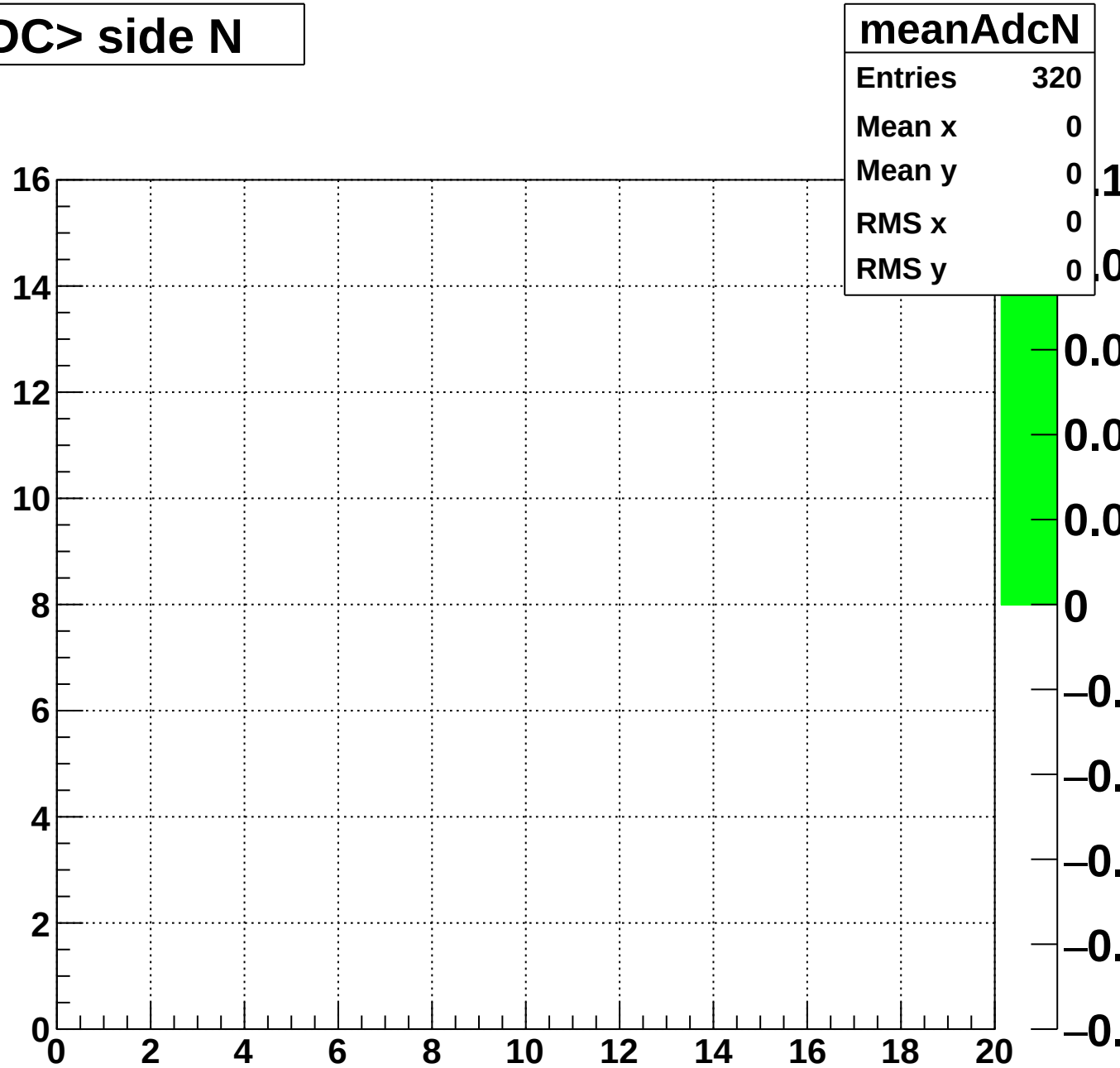
<ADC> side N



2016-05-25 04:38:48