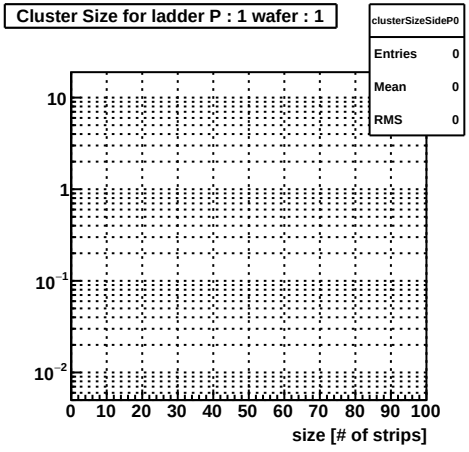
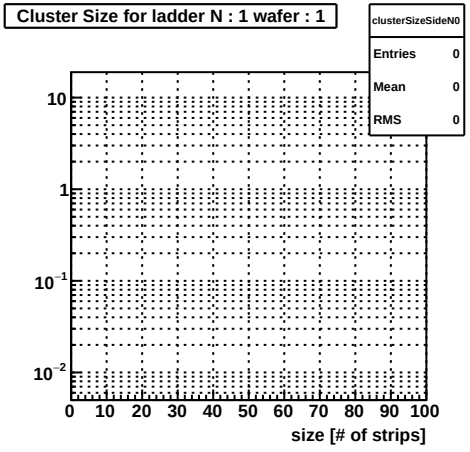
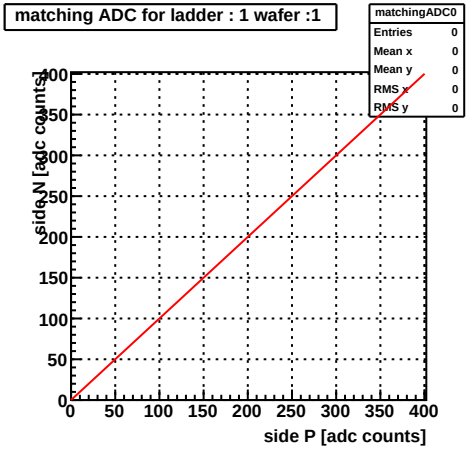
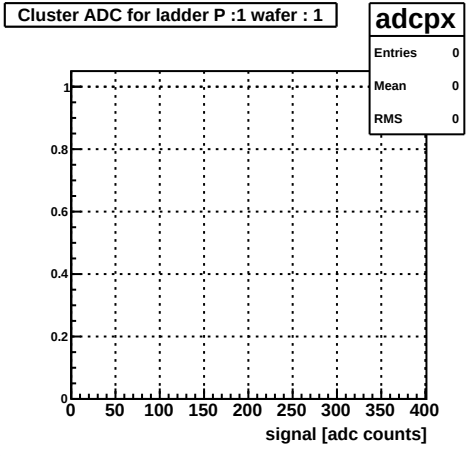
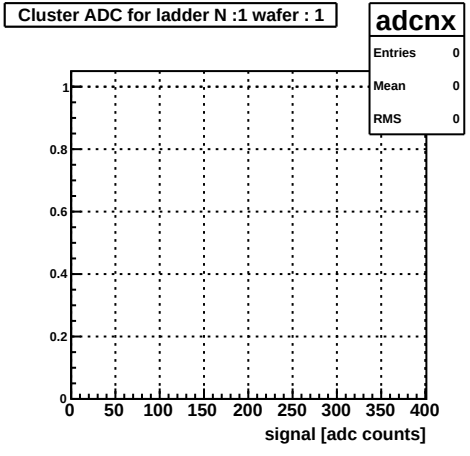
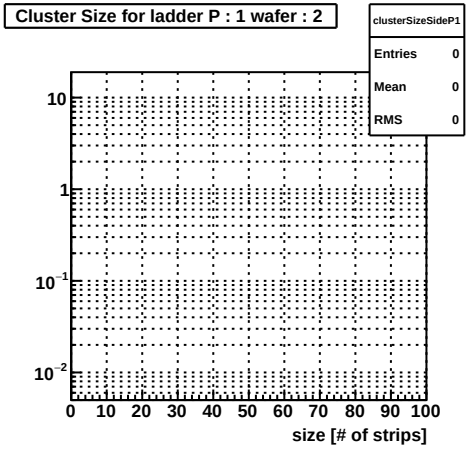

Cluster Size for ladder P : 1 wafer : 1
clusterSizeSideP0
Entries 0
Mean 0
RMS 0
size [# of strips]

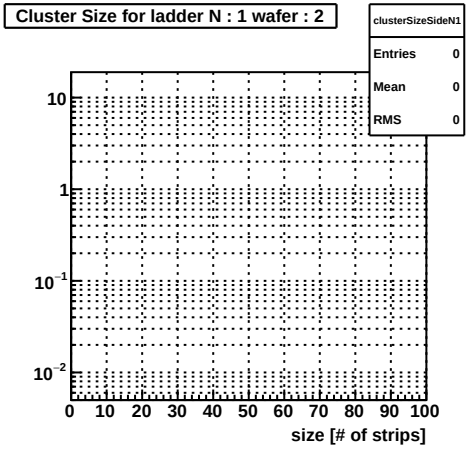

Cluster Size for ladder N : 1 wafer : 1
clusterSizeSideN0
Entries 0
Mean 0
RMS 0
size [# of strips]

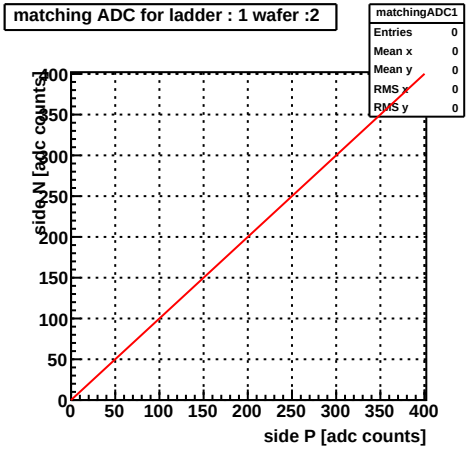

matching ADC for ladder : 1 wafer :1
matchingADC0
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

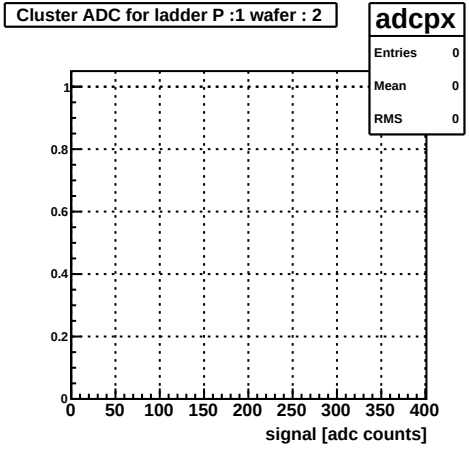

Cluster ADC for ladder P :1 wafer : 1
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

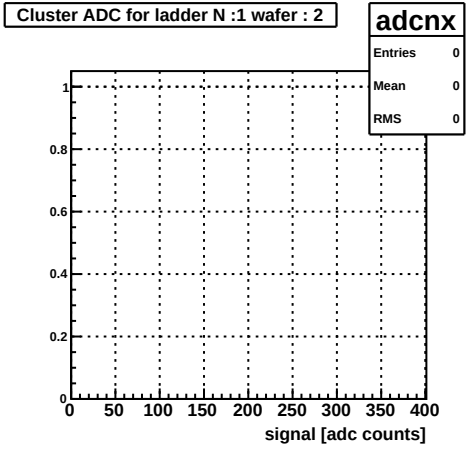

Cluster ADC for ladder N :1 wafer : 1
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

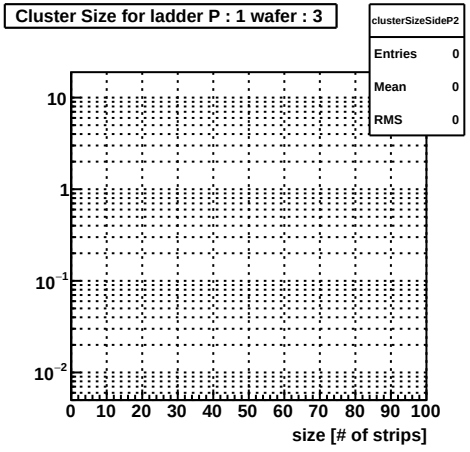

Cluster Size for ladder P : 1 wafer : 2
clusterSizeSideP1
Entries 0
Mean 0
RMS 0
size [# of strips]

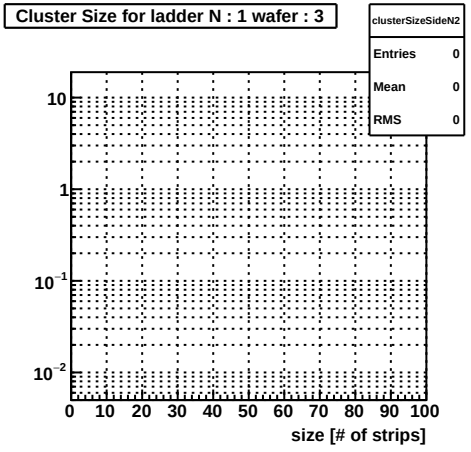

Cluster Size for ladder N : 1 wafer : 2
clusterSizeSideN1
Entries 0
Mean 0
RMS 0
size [# of strips]

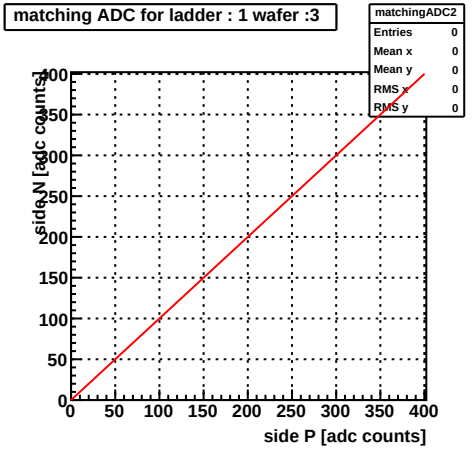

matching ADC for ladder : 1 wafer :2
matchingADC1
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

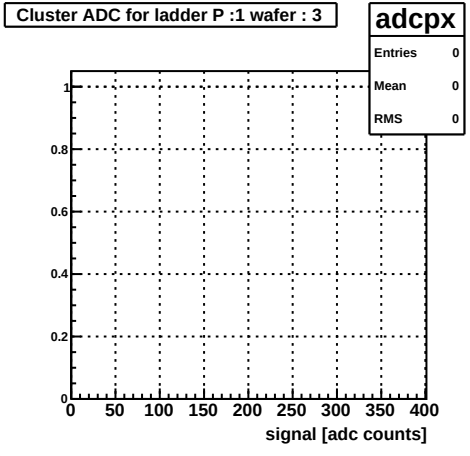

Cluster ADC for ladder P :1 wafer : 2
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

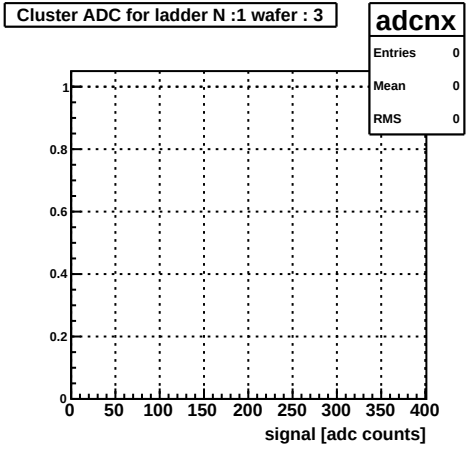

Cluster ADC for ladder N :1 wafer : 2
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

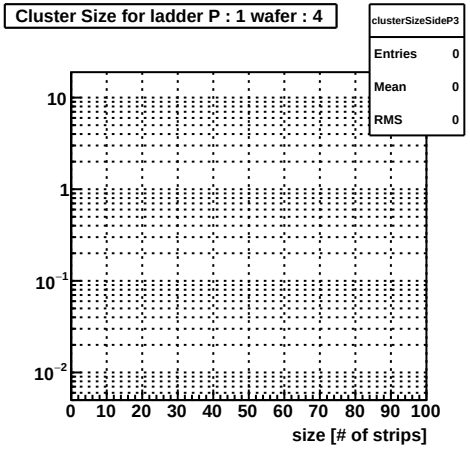

Cluster Size for ladder P : 1 wafer : 3
clusterSizeSideP2
Entries 0
Mean 0
RMS 0
size [# of strips]

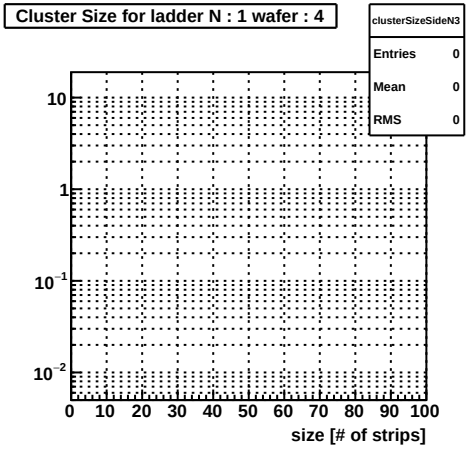

Cluster Size for ladder N : 1 wafer : 3
clusterSizeSideN2
Entries 0
Mean 0
RMS 0
size [# of strips]

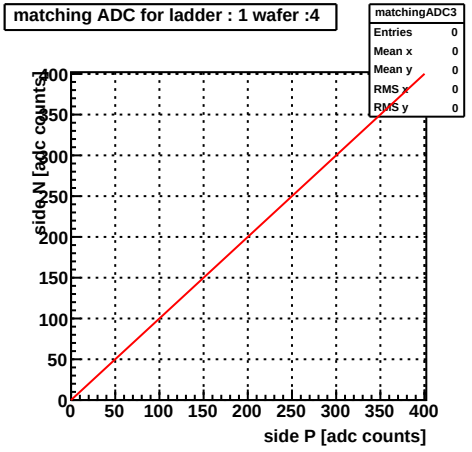

matching ADC for ladder : 1 wafer :3
matchingADC2
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

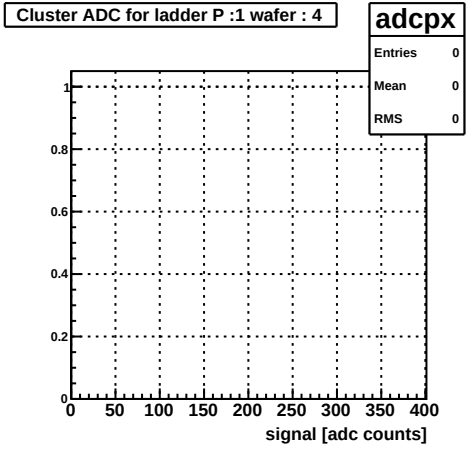

Cluster ADC for ladder P :1 wafer : 3
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

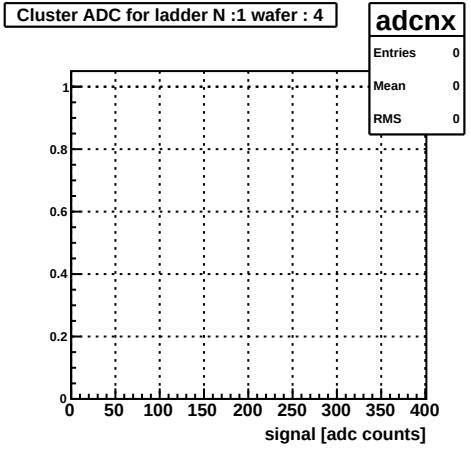

Cluster ADC for ladder N :1 wafer : 3
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

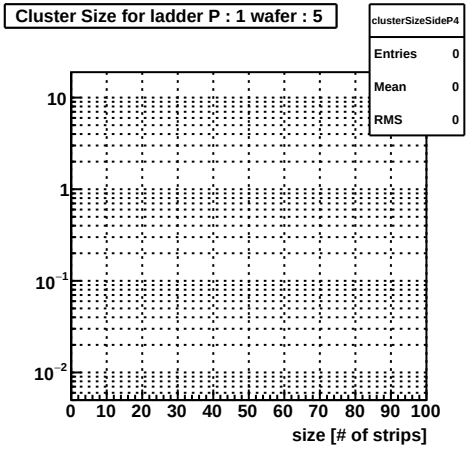

Cluster Size for ladder P : 1 wafer : 4
clusterSizeSideP3
Entries 0
Mean 0
RMS 0
size [# of strips]

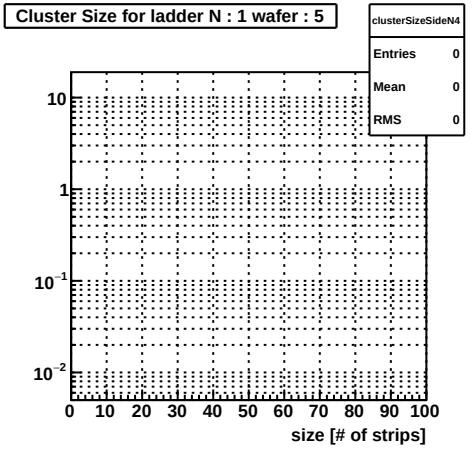

Cluster Size for ladder N : 1 wafer : 4
clusterSizeSideN3
Entries 0
Mean 0
RMS 0
size [# of strips]

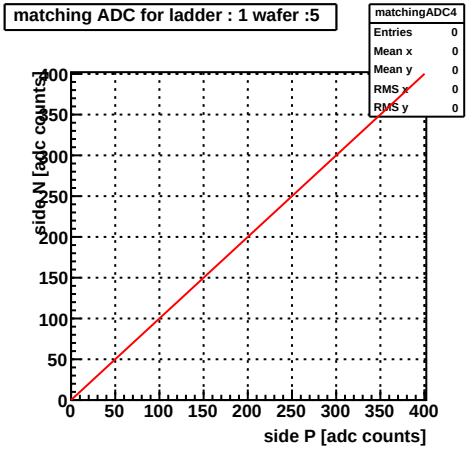

matching ADC for ladder : 1 wafer :4
matchingADC3
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

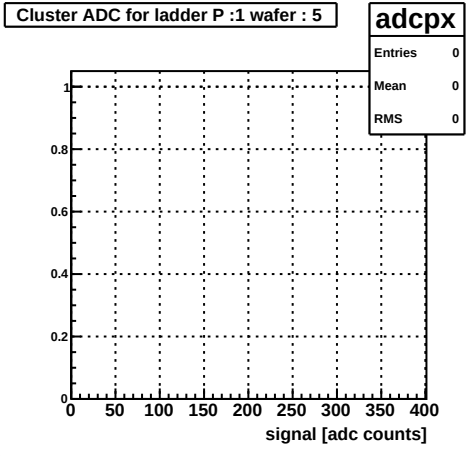

Cluster ADC for ladder P :1 wafer : 4
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

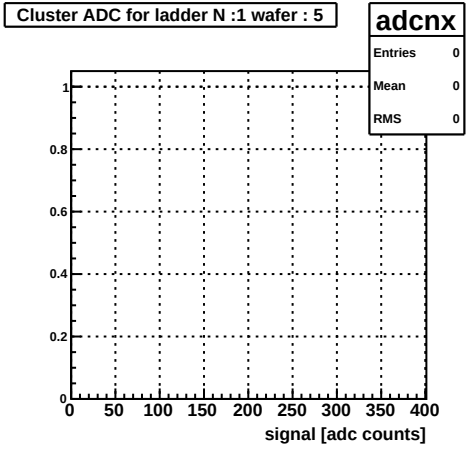

Cluster ADC for ladder N :1 wafer : 4
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

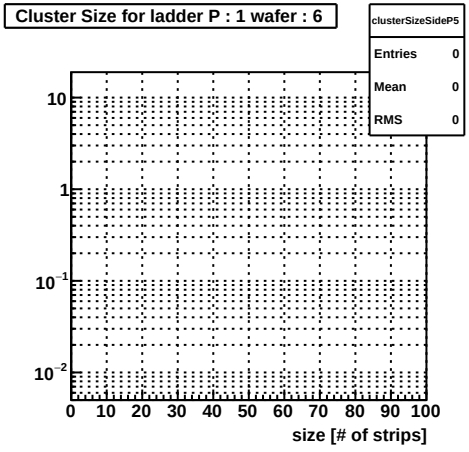

Cluster Size for ladder P : 1 wafer : 5
clusterSizeSideP4
Entries 0
Mean 0
RMS 0
size [# of strips]

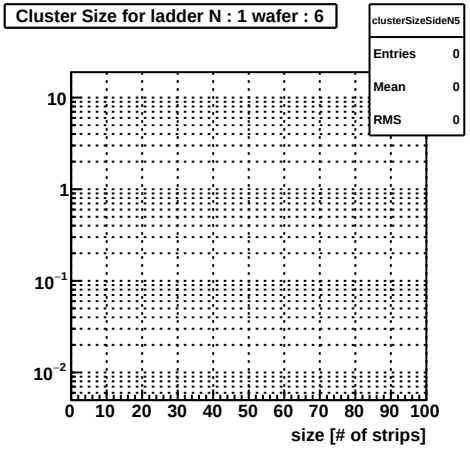

Cluster Size for ladder N : 1 wafer : 5
clusterSizeSideN4
Entries 0
Mean 0
RMS 0
size [# of strips]

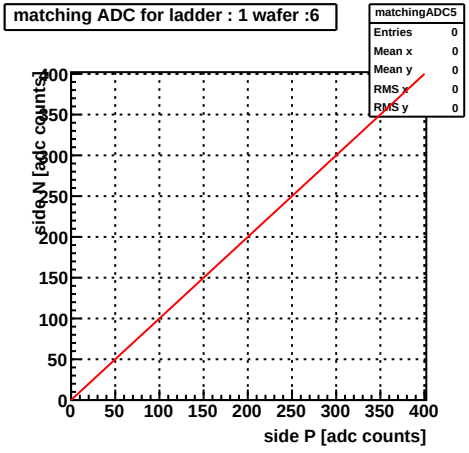

matching ADC for ladder : 1 wafer :5
matchingADC4
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

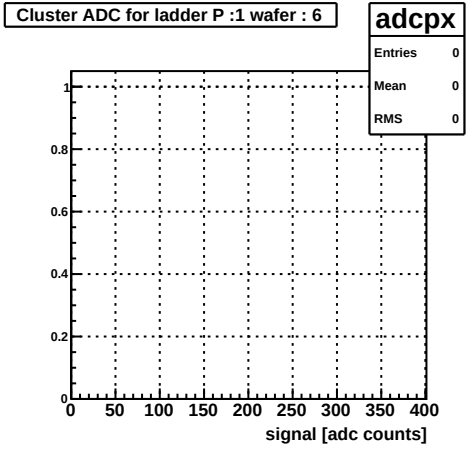

Cluster ADC for ladder P :1 wafer : 5
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

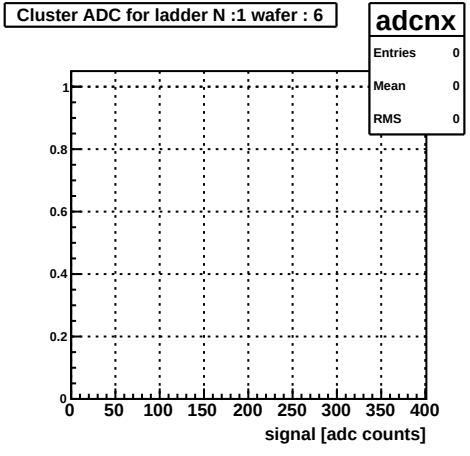

Cluster ADC for ladder N :1 wafer : 5
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

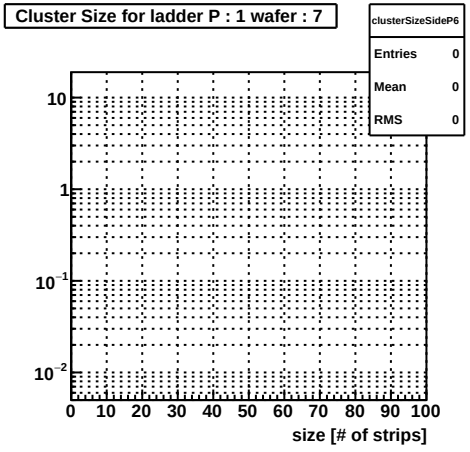

Cluster Size for ladder P : 1 wafer : 6
clusterSizeSideP5
Entries 0
Mean 0
RMS 0
size [# of strips]

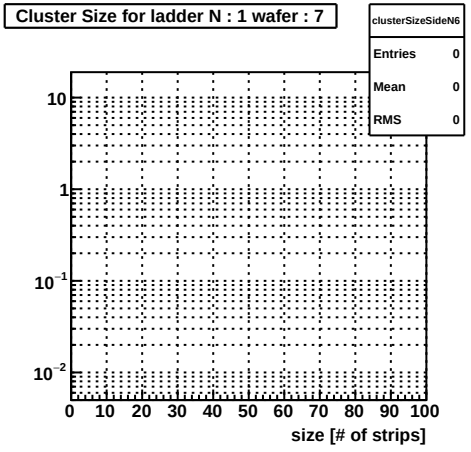

Cluster Size for ladder N : 1 wafer : 6
clusterSizeSideN5
Entries 0
Mean 0
RMS 0
size [# of strips]

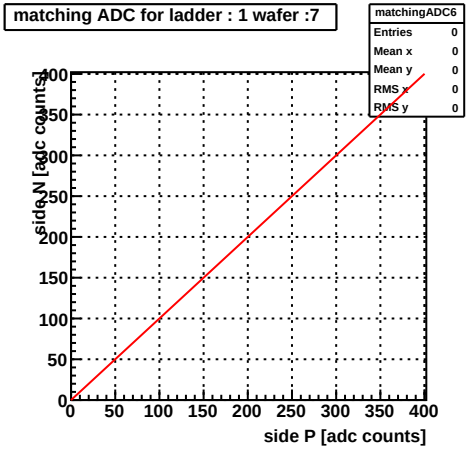

matching ADC for ladder : 1 wafer :6
matchingADC5
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

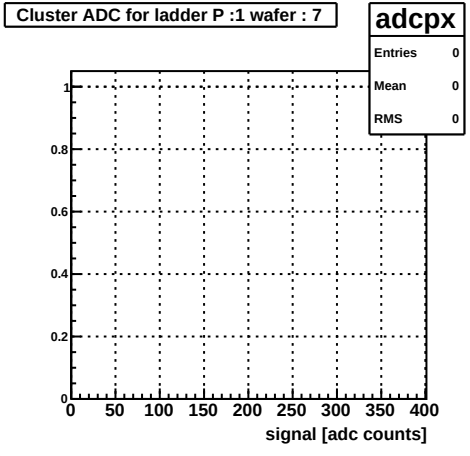

Cluster ADC for ladder P :1 wafer : 6
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

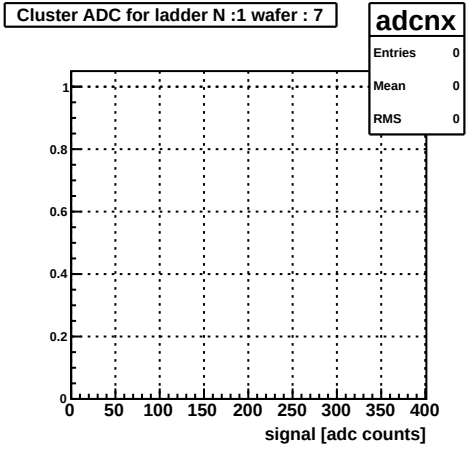

Cluster ADC for ladder N :1 wafer : 6
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

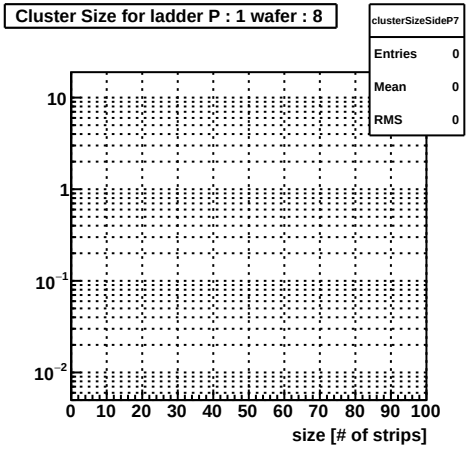

Cluster Size for ladder P : 1 wafer : 7
clusterSizeSideP6
Entries 0
Mean 0
RMS 0
size [# of strips]

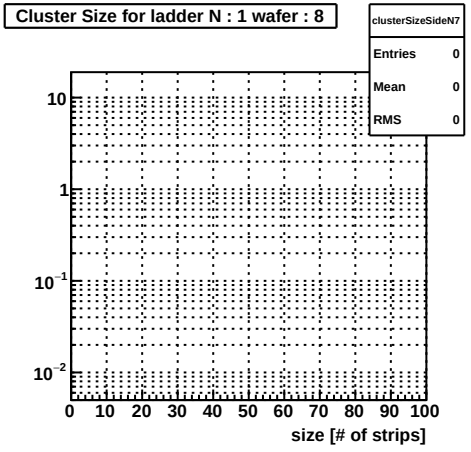

Cluster Size for ladder N : 1 wafer : 7
clusterSizeSideN6
Entries 0
Mean 0
RMS 0
size [# of strips]

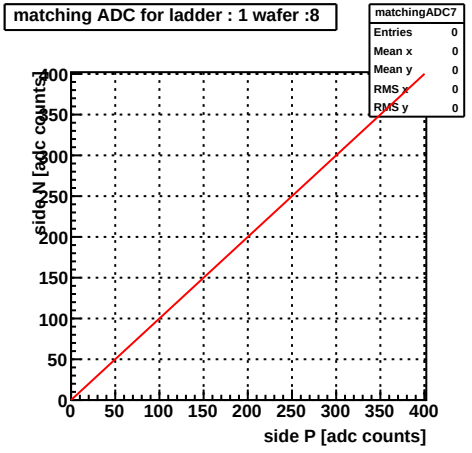

matching ADC for ladder : 1 wafer :7
matchingADC6
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

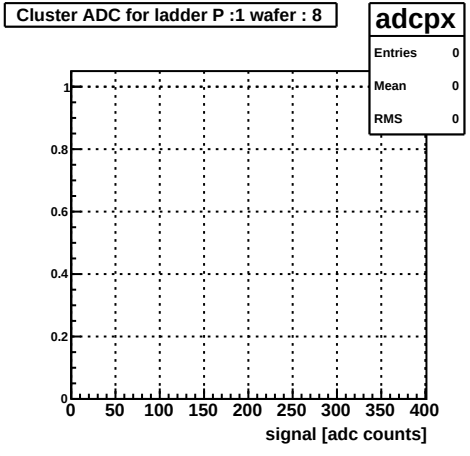

Cluster ADC for ladder P :1 wafer : 7
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

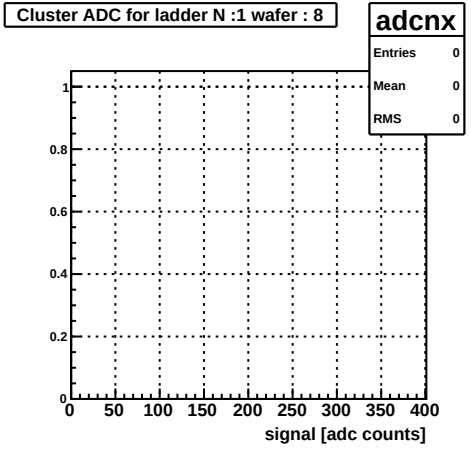

Cluster ADC for ladder N :1 wafer : 7
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

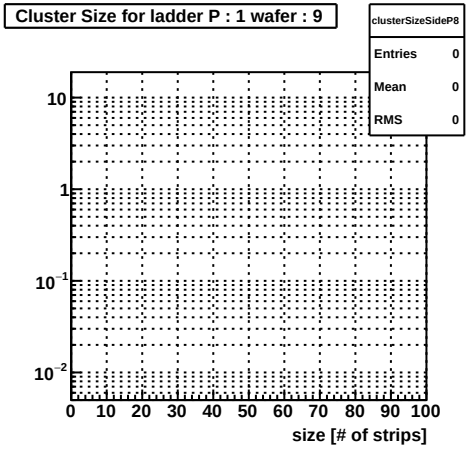

Cluster Size for ladder P : 1 wafer : 8
clusterSizeSideP7
Entries 0
Mean 0
RMS 0
size [# of strips]

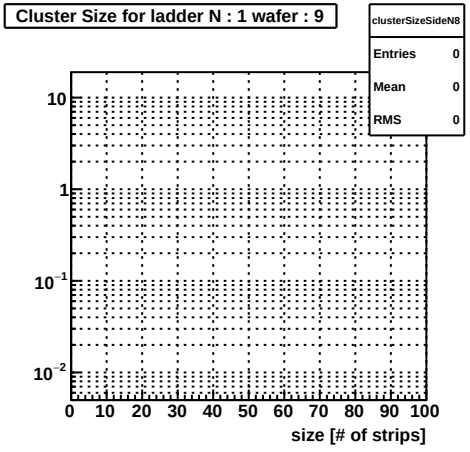

Cluster Size for ladder N : 1 wafer : 8
clusterSizeSideN7
Entries 0
Mean 0
RMS 0
size [# of strips]

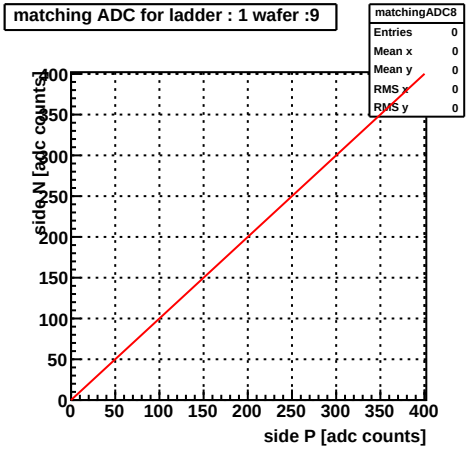

matching ADC for ladder : 1 wafer :8
matchingADC7
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

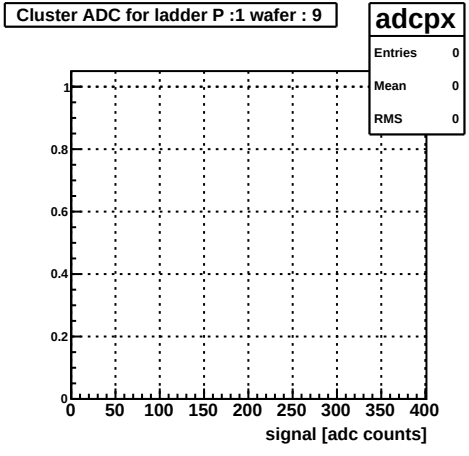

Cluster ADC for ladder P :1 wafer : 8
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

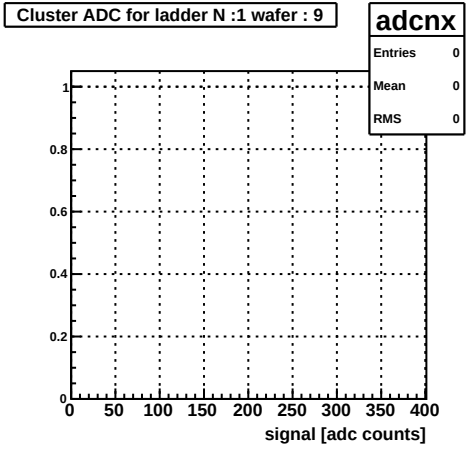

Cluster ADC for ladder N :1 wafer : 8
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

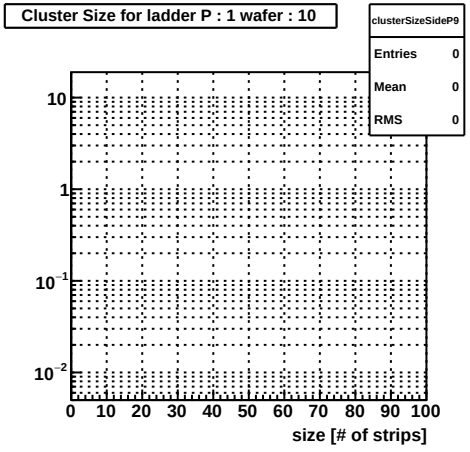

Cluster Size for ladder P : 1 wafer : 9
clusterSizeSideP8
Entries 0
Mean 0
RMS 0
size [# of strips]

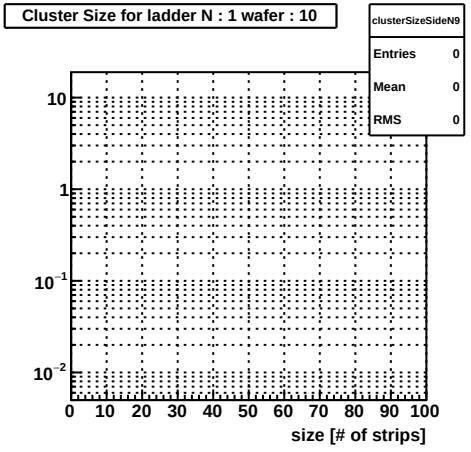

Cluster Size for ladder N : 1 wafer : 9
clusterSizeSideN8
Entries 0
Mean 0
RMS 0
size [# of strips]

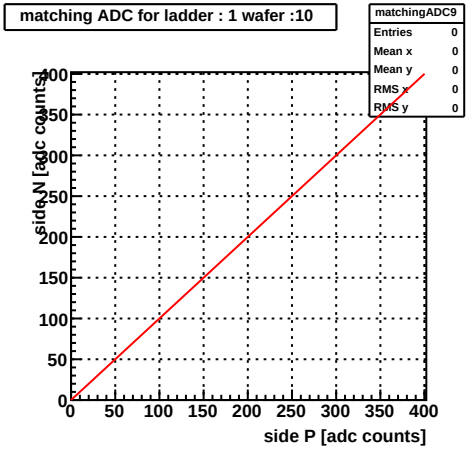

matching ADC for ladder : 1 wafer :9
matchingADC8
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

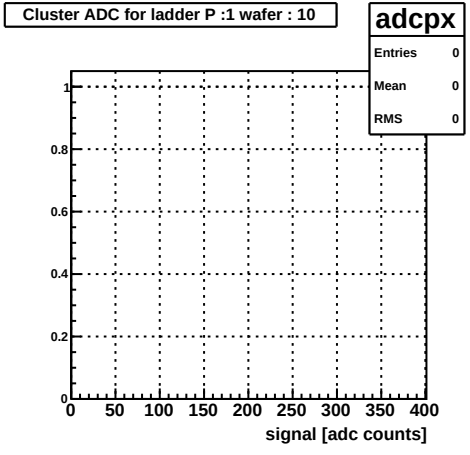

Cluster ADC for ladder P :1 wafer : 9
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

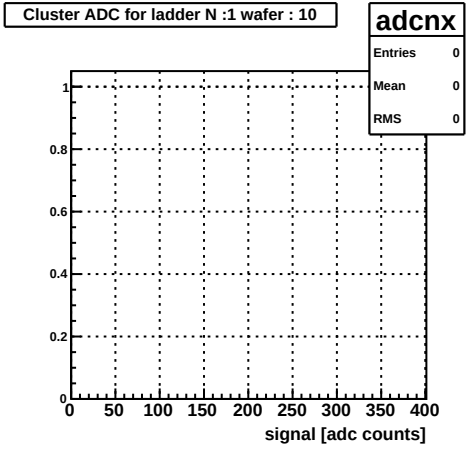

Cluster ADC for ladder N :1 wafer : 9
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

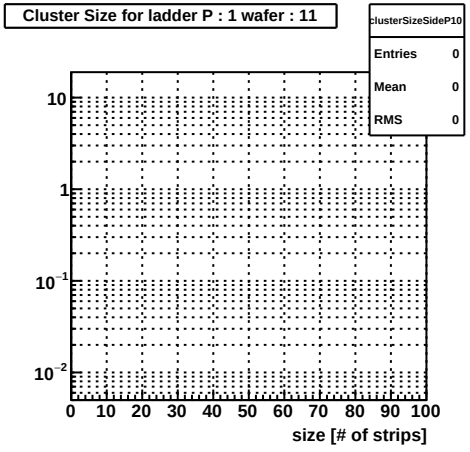

Cluster Size for ladder P : 1 wafer : 10
clusterSizeSideP9
Entries 0
Mean 0
RMS 0
size [# of strips]

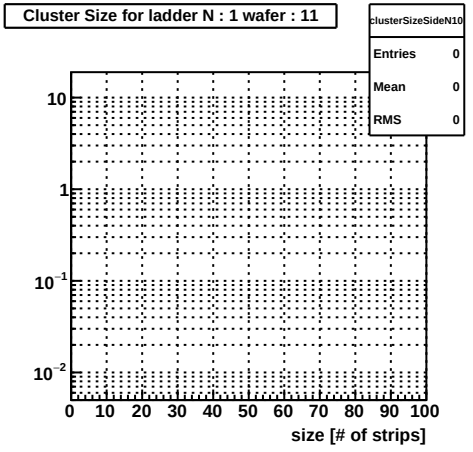

Cluster Size for ladder N : 1 wafer : 10
clusterSizeSideN9
Entries 0
Mean 0
RMS 0
size [# of strips]

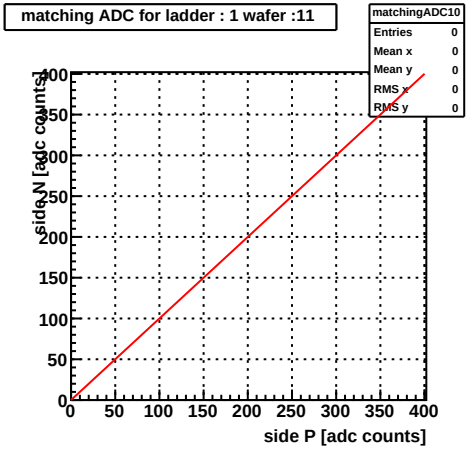

matching ADC for ladder : 1 wafer :10
matchingADC9
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

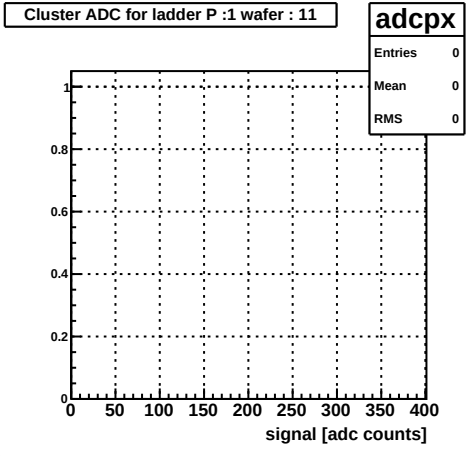

Cluster ADC for ladder P :1 wafer : 10
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

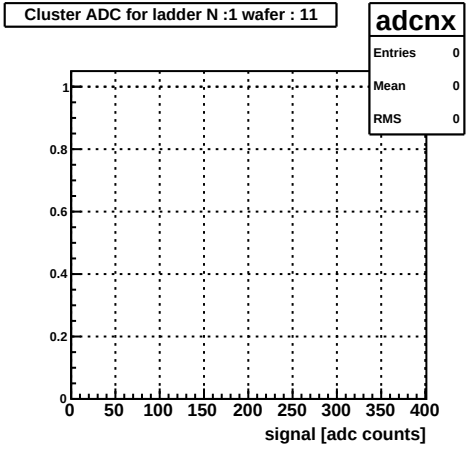

Cluster ADC for ladder N :1 wafer : 10
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

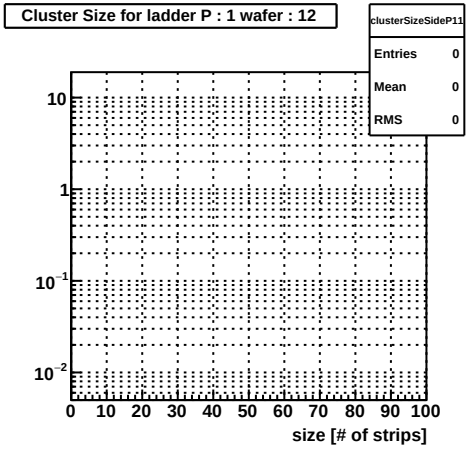

Cluster Size for ladder P : 1 wafer : 11
clusterSizeSideP10
Entries 0
Mean 0
RMS 0
size [# of strips]

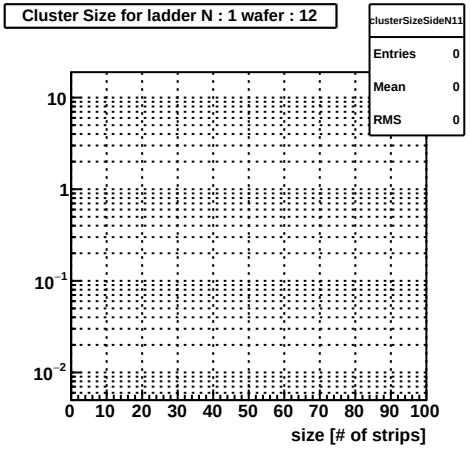

Cluster Size for ladder N : 1 wafer : 11
clusterSizeSideN10
Entries 0
Mean 0
RMS 0
size [# of strips]

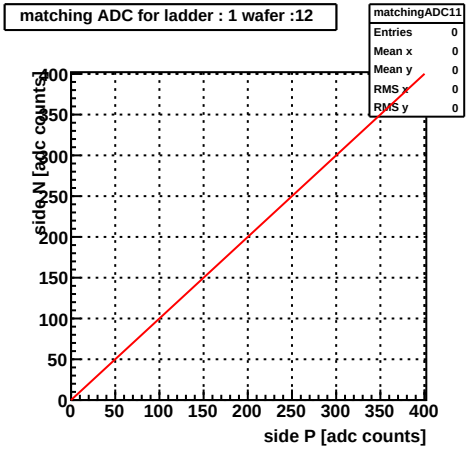

matching ADC for ladder : 1 wafer :11
matchingADC10
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

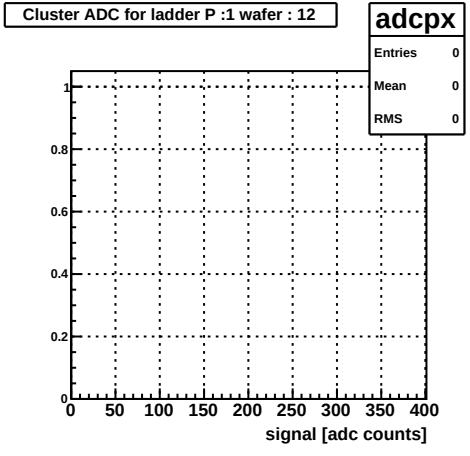

Cluster ADC for ladder P :1 wafer : 11
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

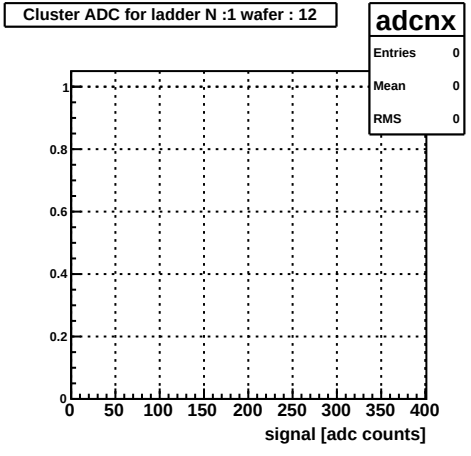

Cluster ADC for ladder N :1 wafer : 11
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

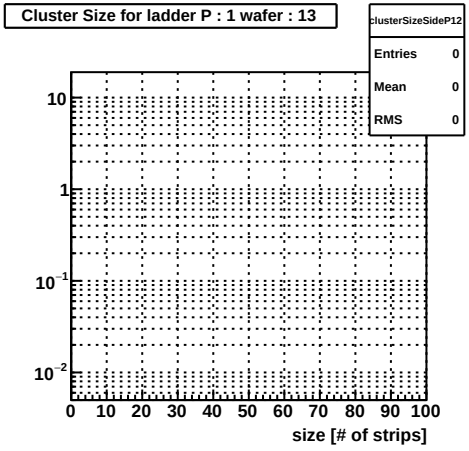

Cluster Size for ladder P : 1 wafer : 12
clusterSizeSideP11
Entries 0
Mean 0
RMS 0
size [# of strips]

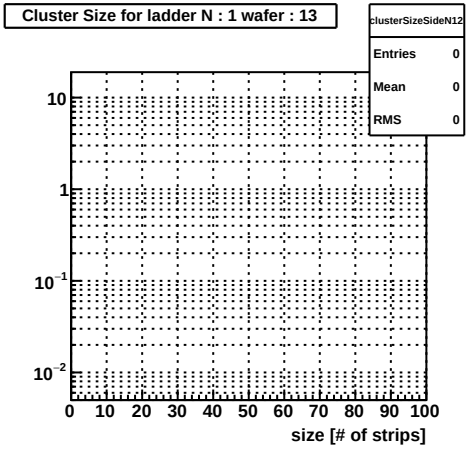

Cluster Size for ladder N : 1 wafer : 12
clusterSizeSideN11
Entries 0
Mean 0
RMS 0
size [# of strips]

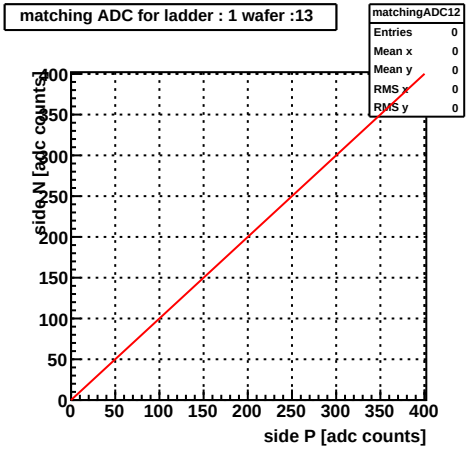

matching ADC for ladder : 1 wafer :12
matchingADC11
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

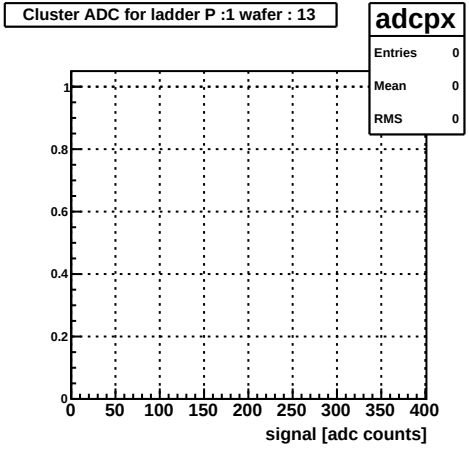

Cluster ADC for ladder P :1 wafer : 12
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

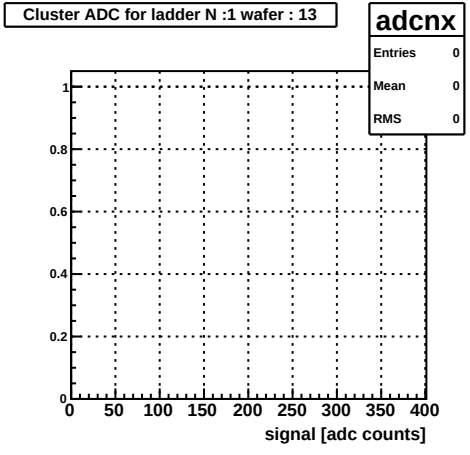

Cluster ADC for ladder N :1 wafer : 12
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

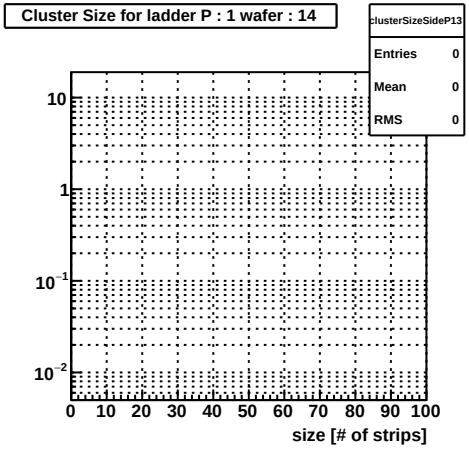

Cluster Size for ladder P : 1 wafer : 13
clusterSizeSideP12
Entries 0
Mean 0
RMS 0
size [# of strips]

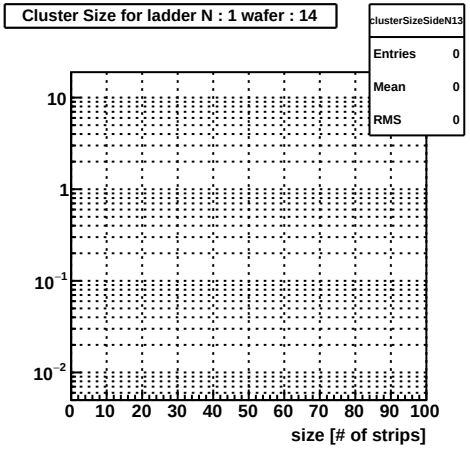

Cluster Size for ladder N : 1 wafer : 13
clusterSizeSideN12
Entries 0
Mean 0
RMS 0
size [# of strips]

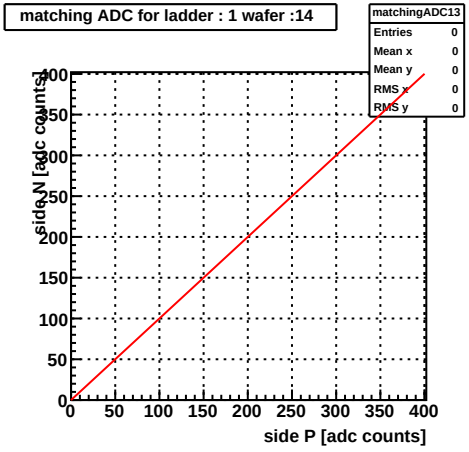

matching ADC for ladder : 1 wafer :13
matchingADC12
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

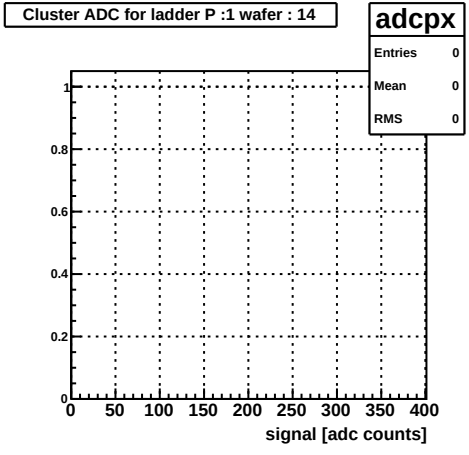

Cluster ADC for ladder P :1 wafer : 13
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

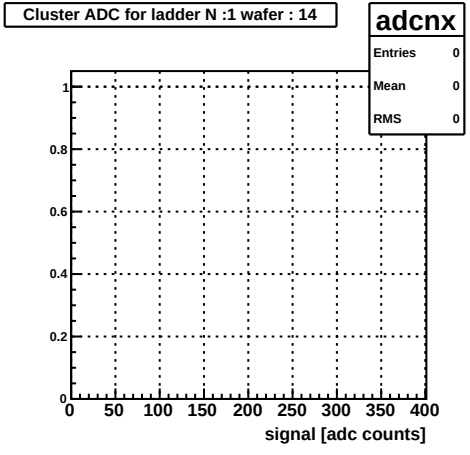

Cluster ADC for ladder N :1 wafer : 13
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

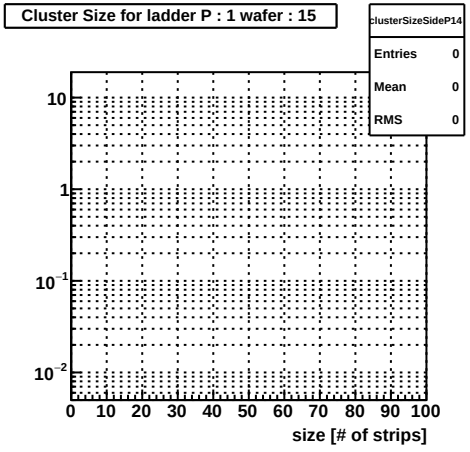

Cluster Size for ladder P : 1 wafer : 14
clusterSizeSideP13
Entries 0
Mean 0
RMS 0
size [# of strips]

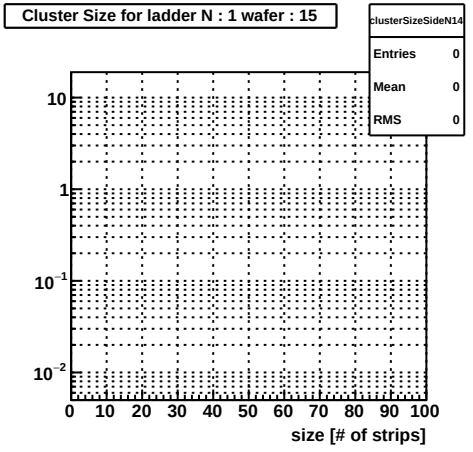

Cluster Size for ladder N : 1 wafer : 14
clusterSizeSideN13
Entries 0
Mean 0
RMS 0
size [# of strips]

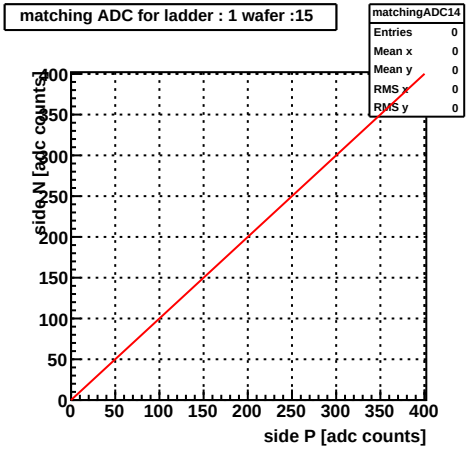

matching ADC for ladder : 1 wafer :14
matchingADC13
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

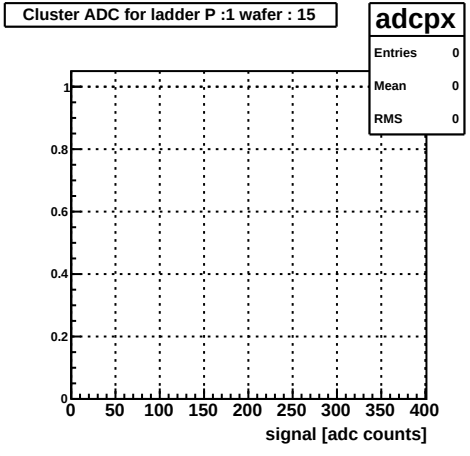

Cluster ADC for ladder P :1 wafer : 14
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

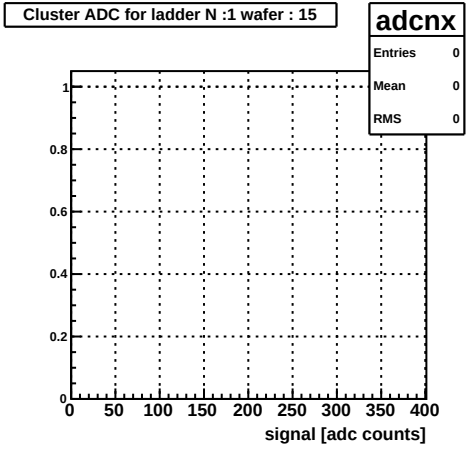

Cluster ADC for ladder N :1 wafer : 14
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

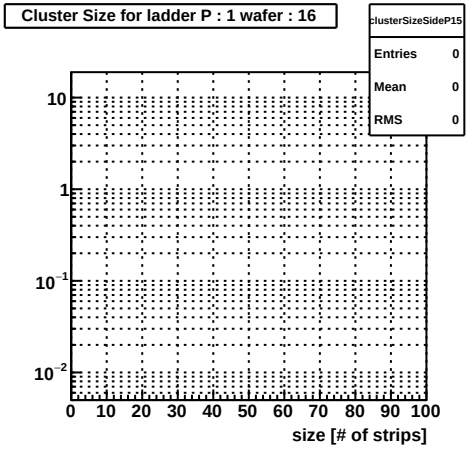

Cluster Size for ladder P : 1 wafer : 15
clusterSizeSideP14
Entries 0
Mean 0
RMS 0
size [# of strips]

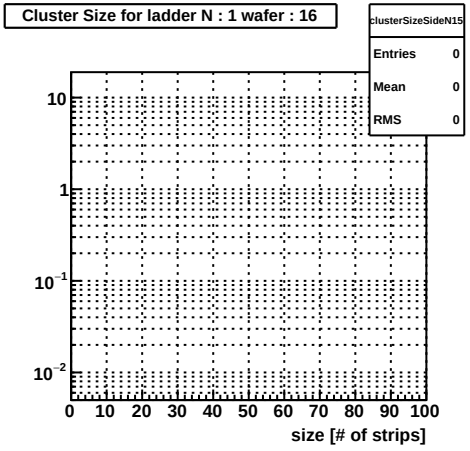

Cluster Size for ladder N : 1 wafer : 15
clusterSizeSideN14
Entries 0
Mean 0
RMS 0
size [# of strips]

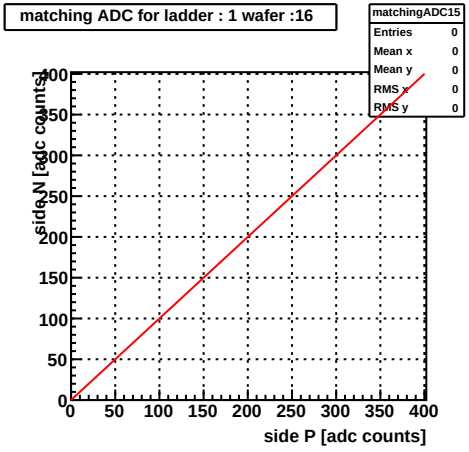

matching ADC for ladder : 1 wafer :15
matchingADC14
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

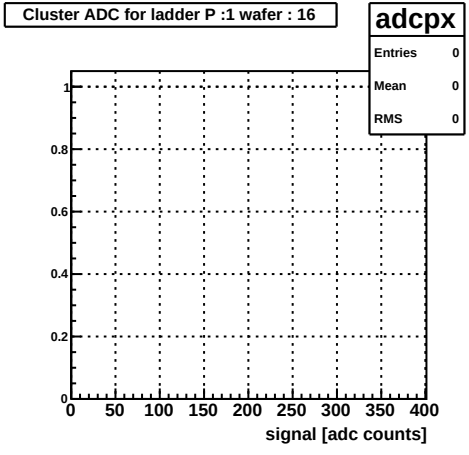

Cluster ADC for ladder P :1 wafer : 15
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

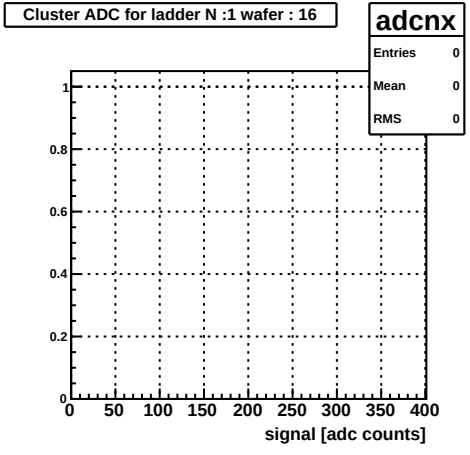

Cluster ADC for ladder N :1 wafer : 15
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

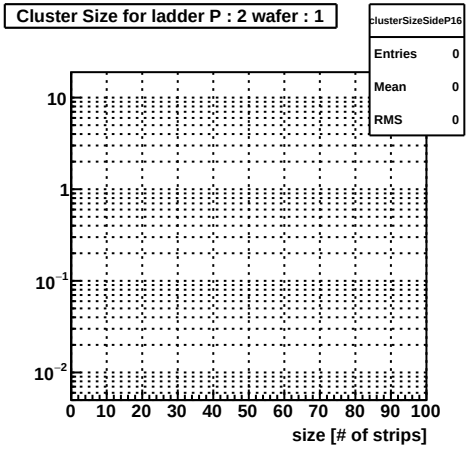

Cluster Size for ladder P : 1 wafer : 16
clusterSizeSideP15
Entries 0
Mean 0
RMS 0
size [# of strips]

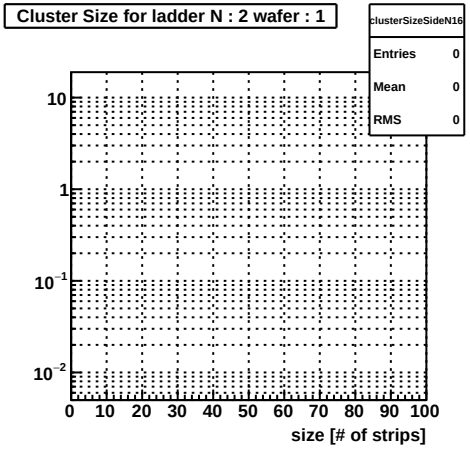

Cluster Size for ladder N : 1 wafer : 16
clusterSizeSideN15
Entries 0
Mean 0
RMS 0
size [# of strips]

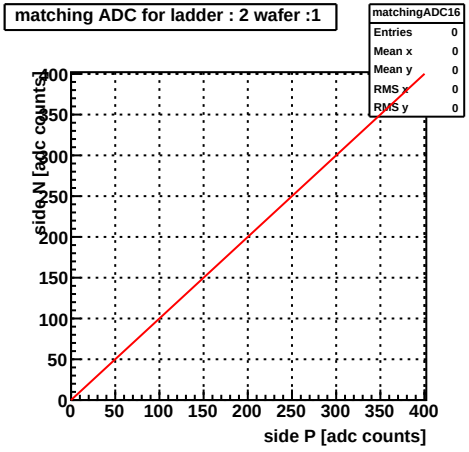

matching ADC for ladder : 1 wafer :16
matchingADC15
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

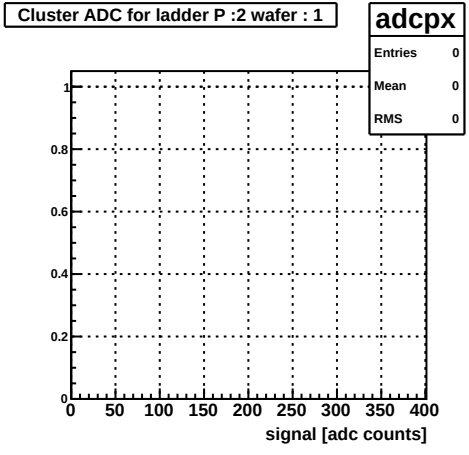

Cluster ADC for ladder P :1 wafer : 16
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

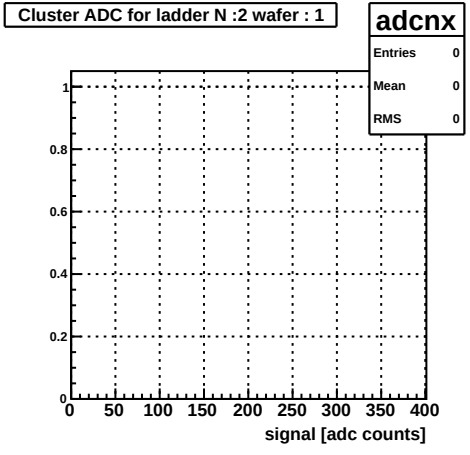

Cluster ADC for ladder N :1 wafer : 16
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

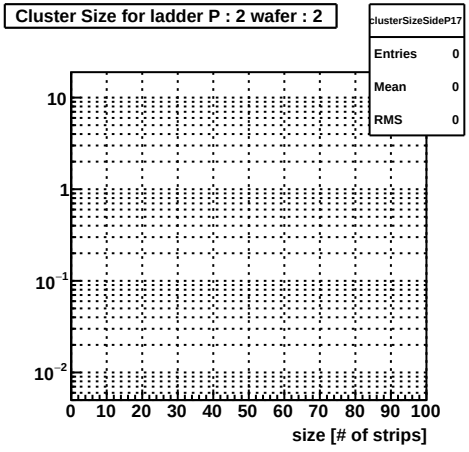

Cluster Size for ladder P : 2 wafer : 1
clusterSizeSideP16
Entries 0
Mean 0
RMS 0
size [# of strips]

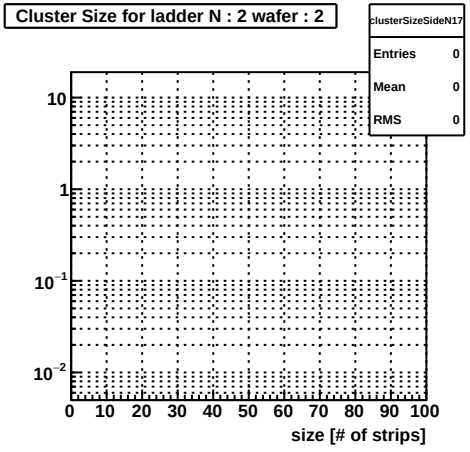

Cluster Size for ladder N : 2 wafer : 1
clusterSizeSideN16
Entries 0
Mean 0
RMS 0
size [# of strips]

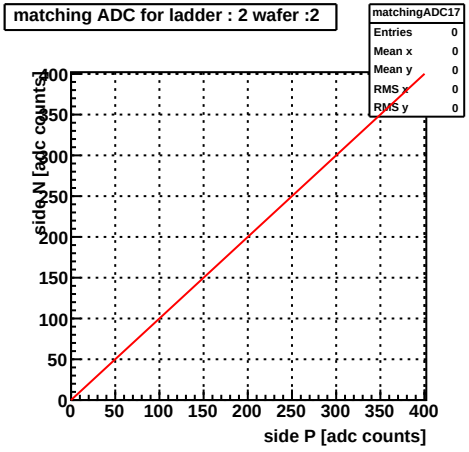

matching ADC for ladder : 2 wafer :1
matchingADC16
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

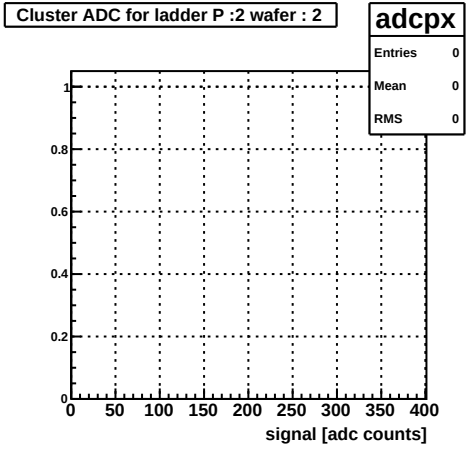

Cluster ADC for ladder P :2 wafer : 1
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

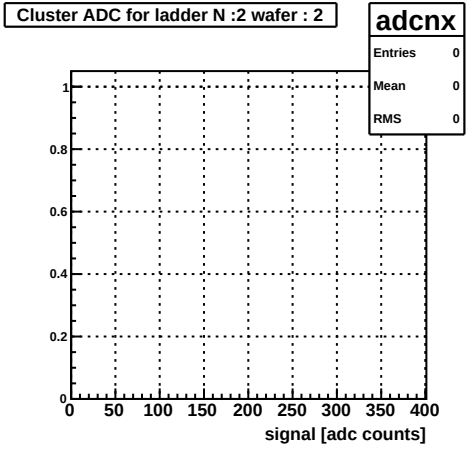

Cluster ADC for ladder N :2 wafer : 1
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

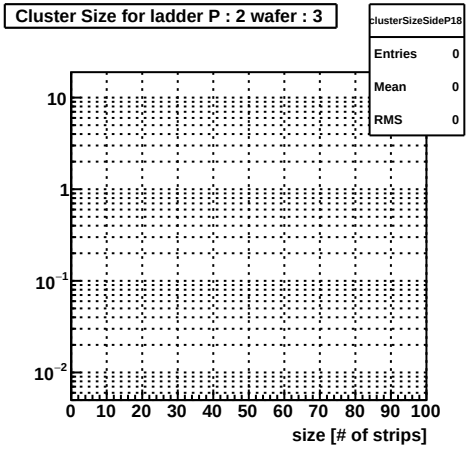

Cluster Size for ladder P : 2 wafer : 2
clusterSizeSideP17
Entries 0
Mean 0
RMS 0
size [# of strips]

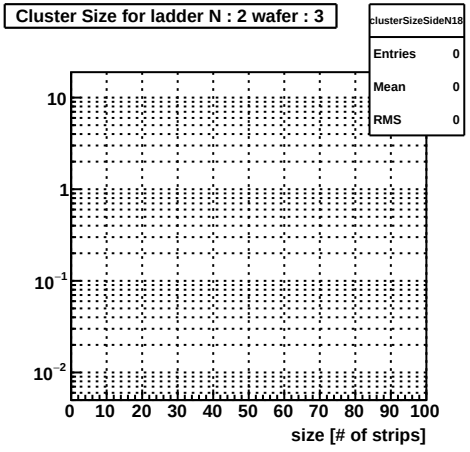

Cluster Size for ladder N : 2 wafer : 2
clusterSizeSideN17
Entries 0
Mean 0
RMS 0
size [# of strips]

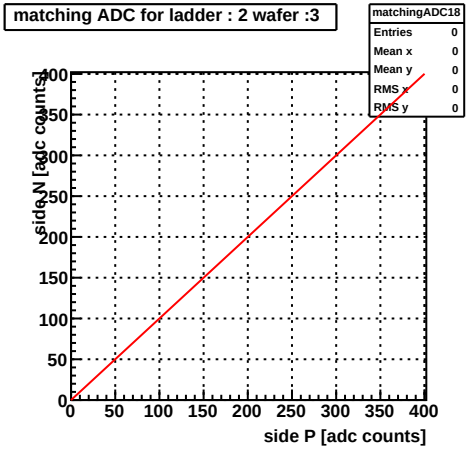

matching ADC for ladder : 2 wafer :2
matchingADC17
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

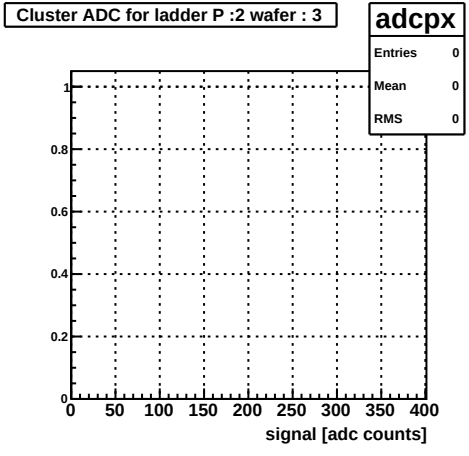

Cluster ADC for ladder P :2 wafer : 2
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

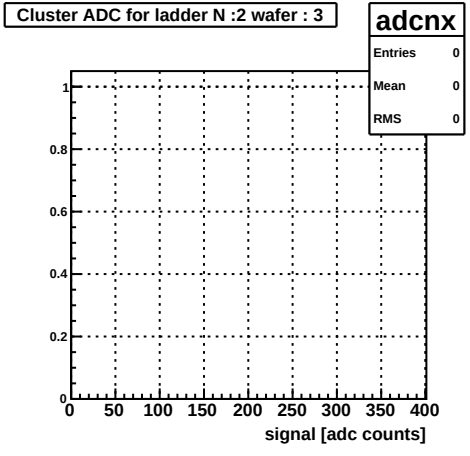

Cluster ADC for ladder N :2 wafer : 2
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

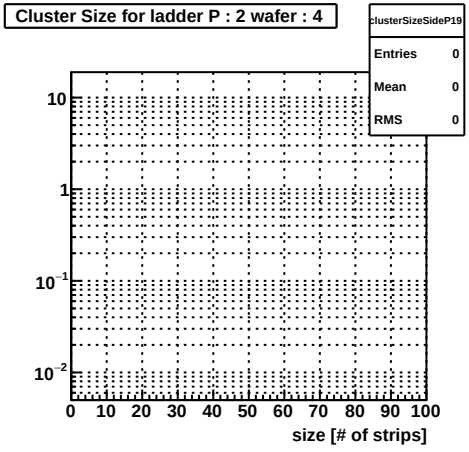

Cluster Size for ladder P : 2 wafer : 3
clusterSizeSideP18
Entries 0
Mean 0
RMS 0
size [# of strips]

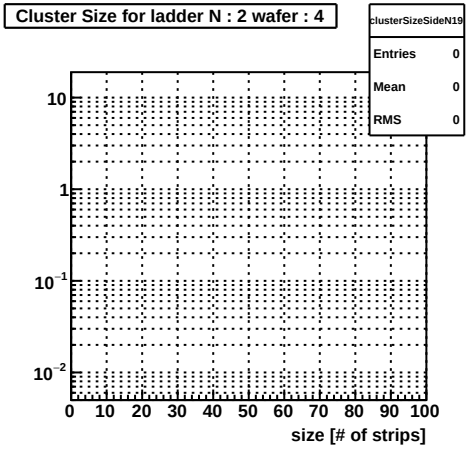

Cluster Size for ladder N : 2 wafer : 3
clusterSizeSideN18
Entries 0
Mean 0
RMS 0
size [# of strips]

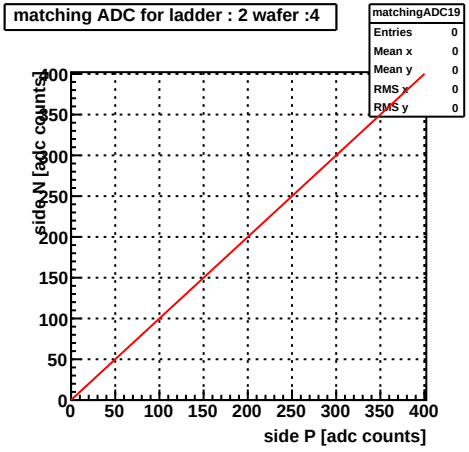

matching ADC for ladder : 2 wafer :3
matchingADC18
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

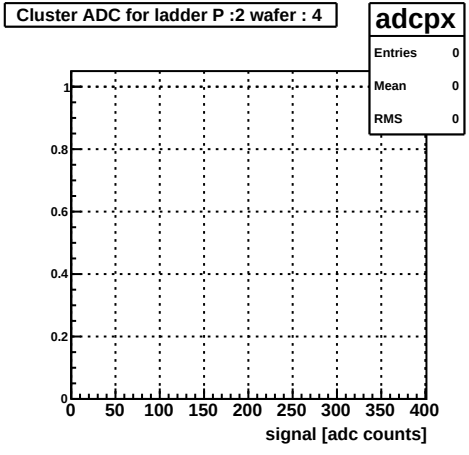

Cluster ADC for ladder P :2 wafer : 3
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

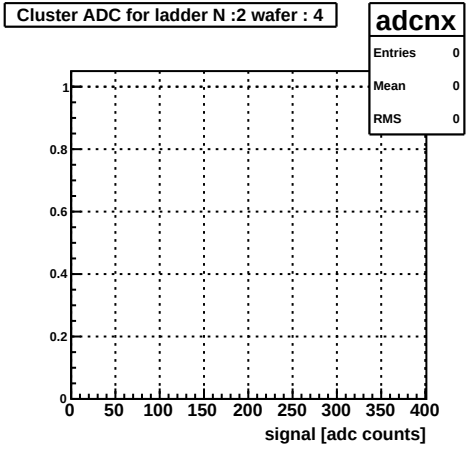

Cluster ADC for ladder N :2 wafer : 3
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

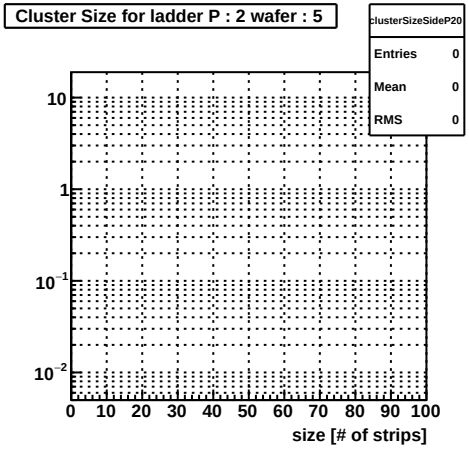

Cluster Size for ladder P : 2 wafer : 4
clusterSizeSideP19
Entries 0
Mean 0
RMS 0
size [# of strips]

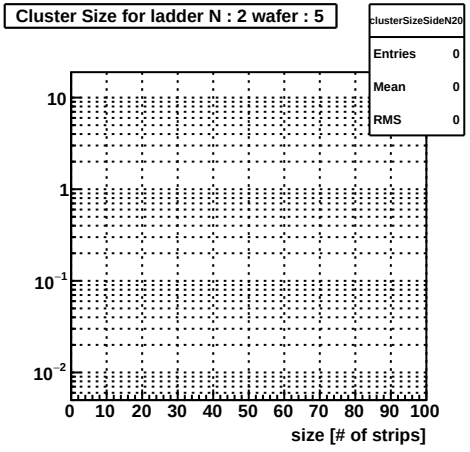

Cluster Size for ladder N : 2 wafer : 4
clusterSizeSideN19
Entries 0
Mean 0
RMS 0
size [# of strips]

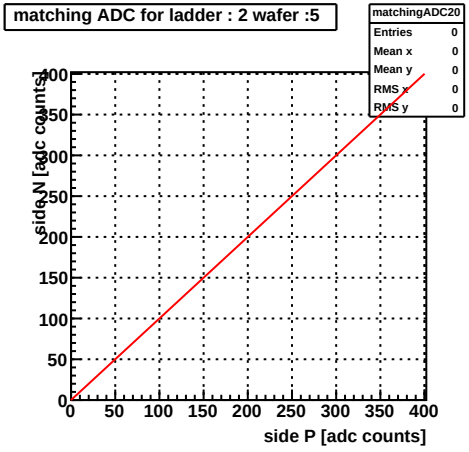

matching ADC for ladder : 2 wafer :4
matchingADC19
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

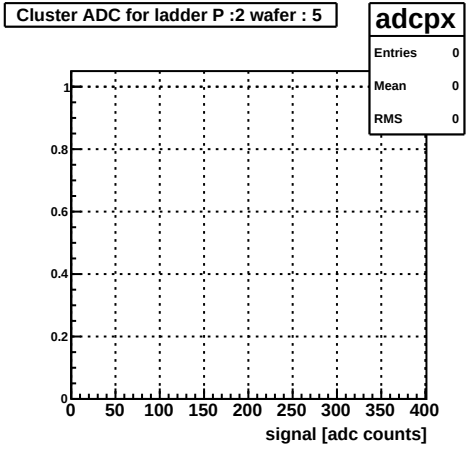

Cluster ADC for ladder P :2 wafer : 4
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

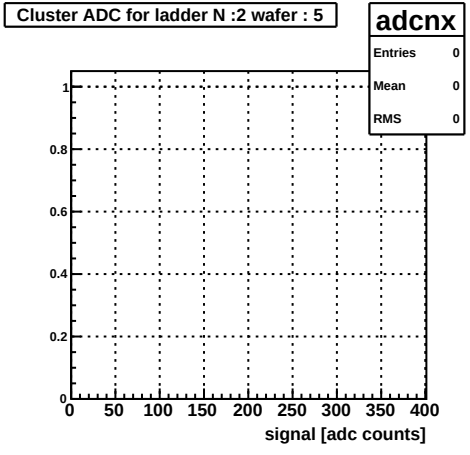

Cluster ADC for ladder N :2 wafer : 4
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

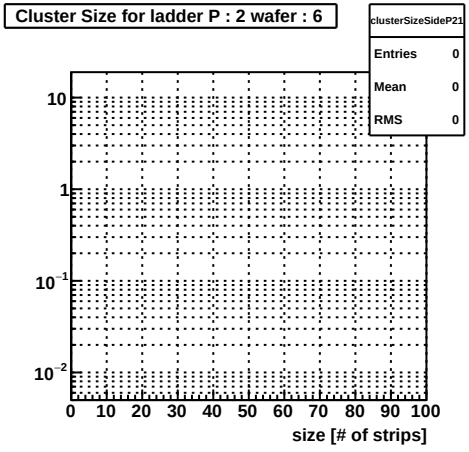

Cluster Size for ladder P : 2 wafer : 5
clusterSizeSideP20
Entries 0
Mean 0
RMS 0
size [# of strips]

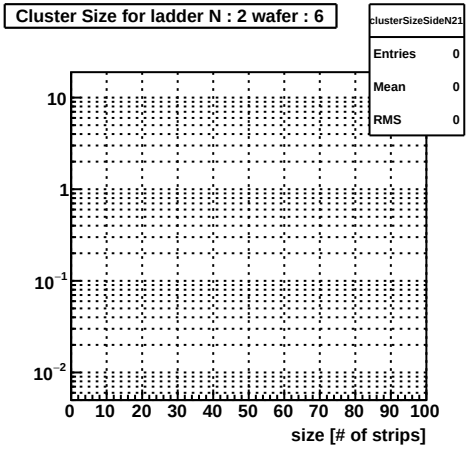

Cluster Size for ladder N : 2 wafer : 5
clusterSizeSideN20
Entries 0
Mean 0
RMS 0
size [# of strips]

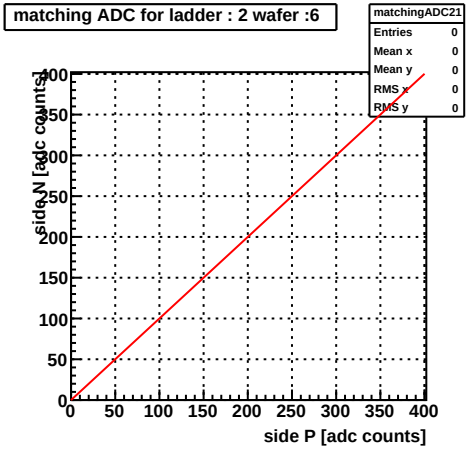

matching ADC for ladder : 2 wafer :5
matchingADC20
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

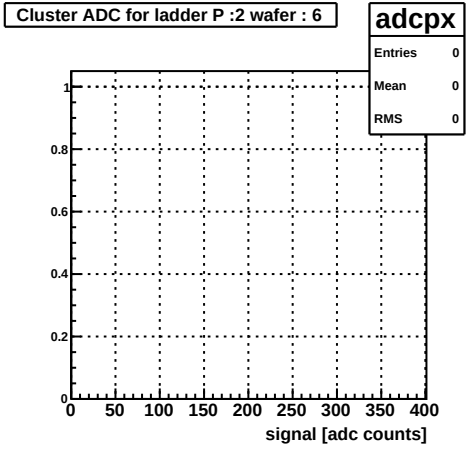

Cluster ADC for ladder P :2 wafer : 5
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

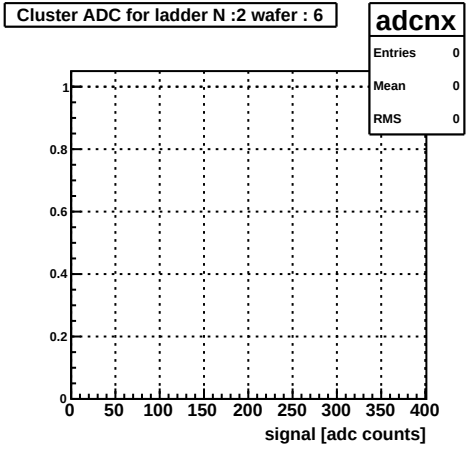

Cluster ADC for ladder N :2 wafer : 5
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

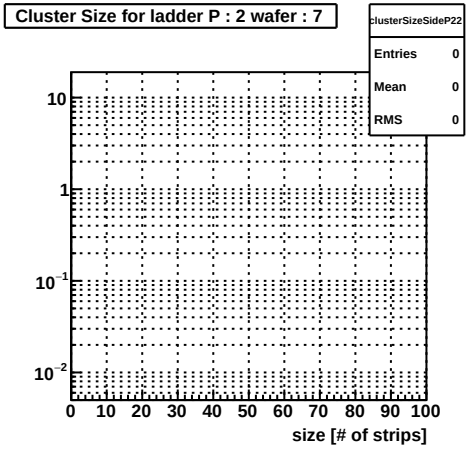

Cluster Size for ladder P : 2 wafer : 6
clusterSizeSideP21
Entries 0
Mean 0
RMS 0
size [# of strips]

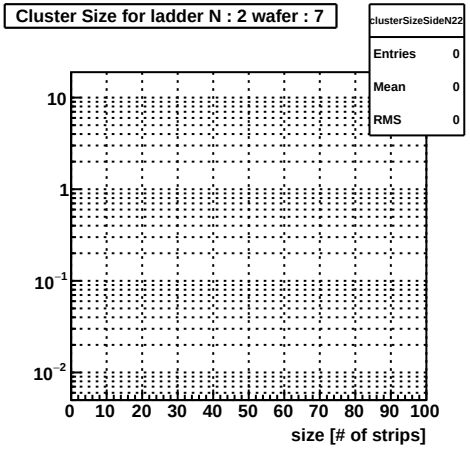

Cluster Size for ladder N : 2 wafer : 6
clusterSizeSideN21
Entries 0
Mean 0
RMS 0
size [# of strips]

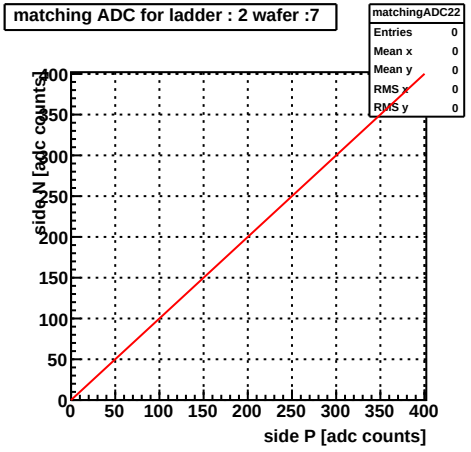

matching ADC for ladder : 2 wafer :6
matchingADC21
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

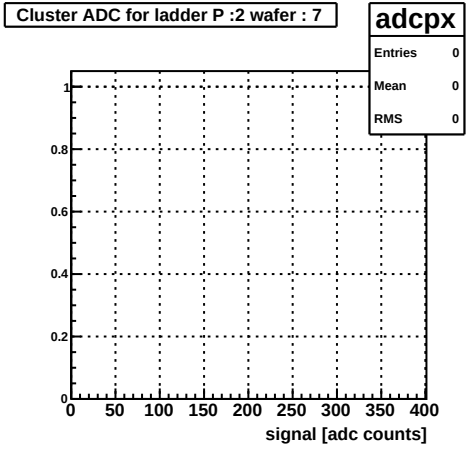

Cluster ADC for ladder P :2 wafer : 6
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

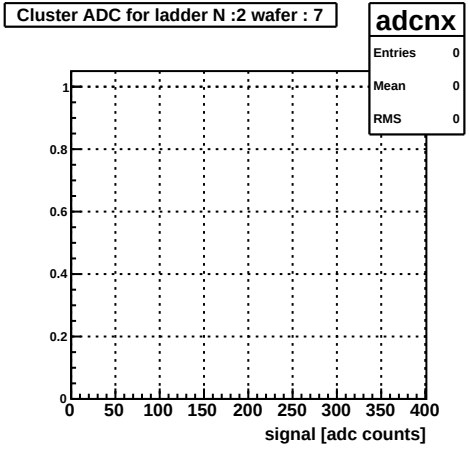

Cluster ADC for ladder N :2 wafer : 6
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

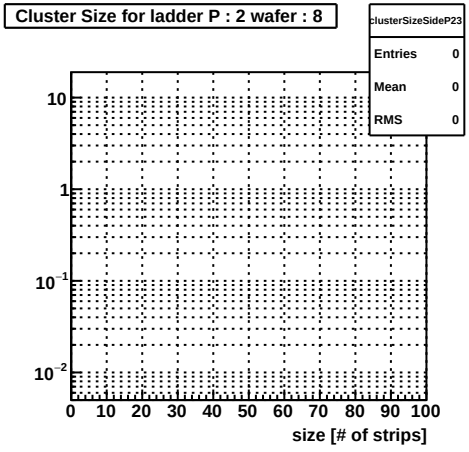

Cluster Size for ladder P : 2 wafer : 7
clusterSizeSideP22
Entries 0
Mean 0
RMS 0
size [# of strips]

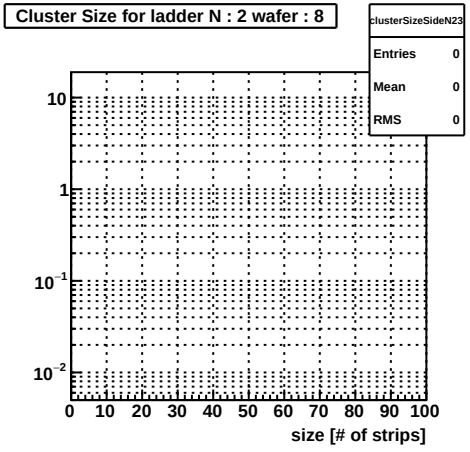

Cluster Size for ladder N : 2 wafer : 7
clusterSizeSideN22
Entries 0
Mean 0
RMS 0
size [# of strips]

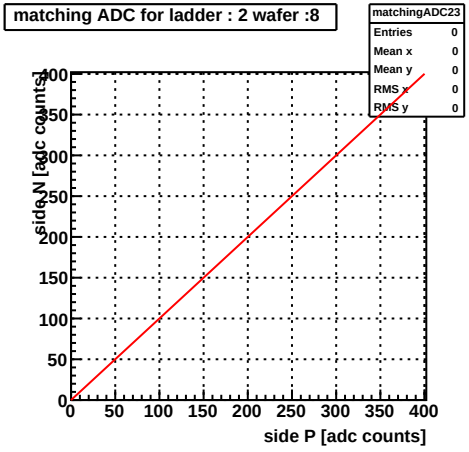

matching ADC for ladder : 2 wafer :7
matchingADC22
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

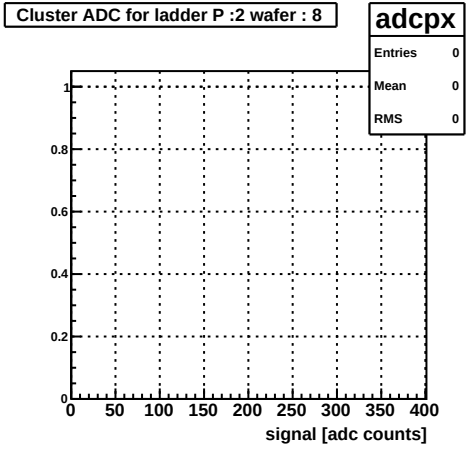

Cluster ADC for ladder P :2 wafer : 7
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

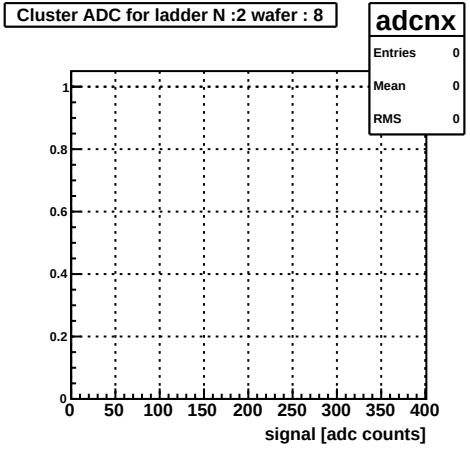

Cluster ADC for ladder N :2 wafer : 7
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

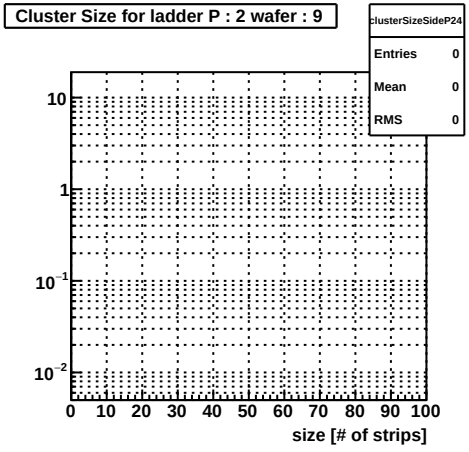

Cluster Size for ladder P : 2 wafer : 8
clusterSizeSideP23
Entries 0
Mean 0
RMS 0
size [# of strips]

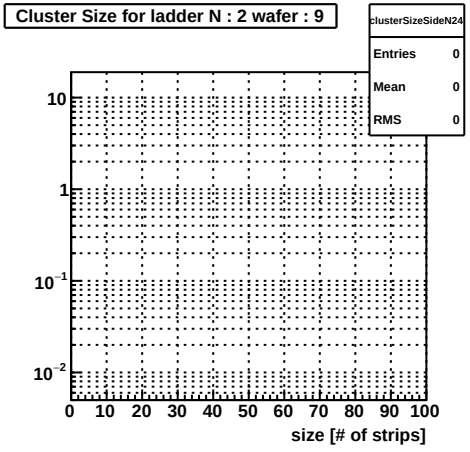

Cluster Size for ladder N : 2 wafer : 8
clusterSizeSideN23
Entries 0
Mean 0
RMS 0
size [# of strips]

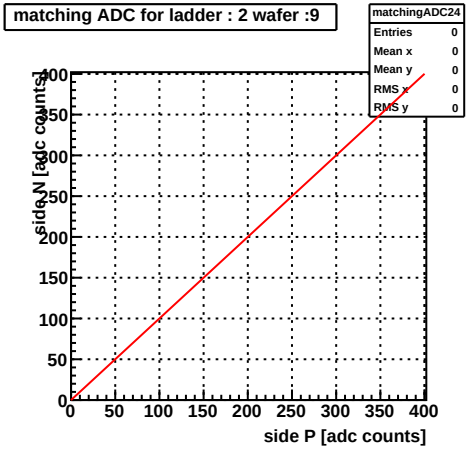

matching ADC for ladder : 2 wafer :8
matchingADC23
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

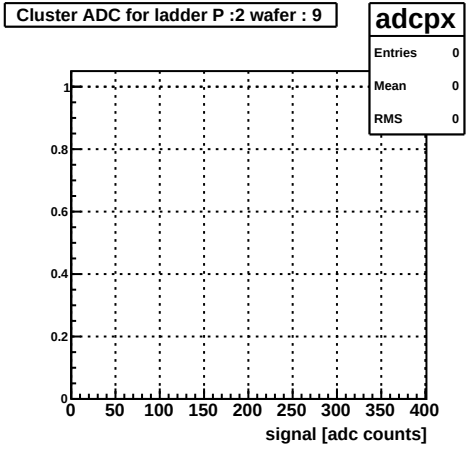

Cluster ADC for ladder P :2 wafer : 8
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

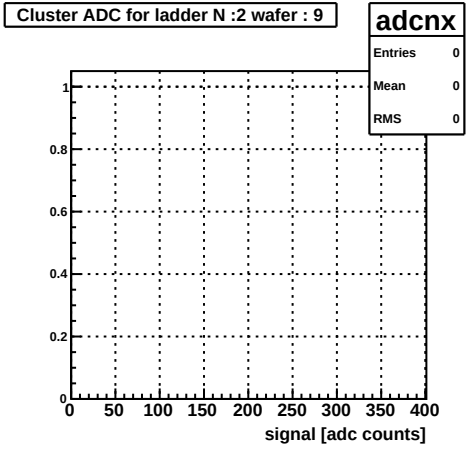

Cluster ADC for ladder N :2 wafer : 8
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

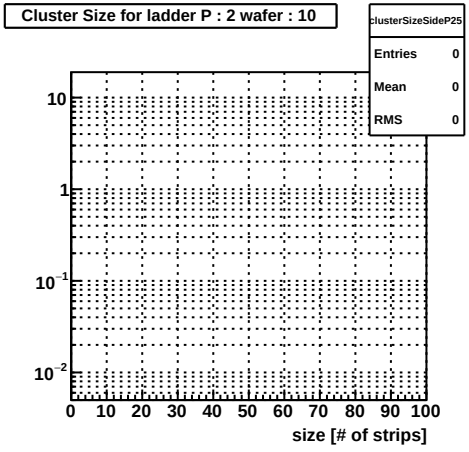

Cluster Size for ladder P : 2 wafer : 9
clusterSizeSideP24
Entries 0
Mean 0
RMS 0
size [# of strips]

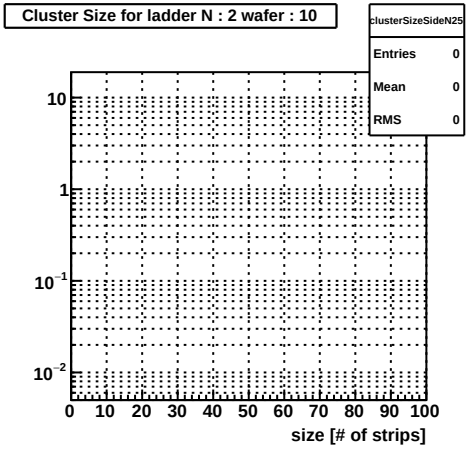

Cluster Size for ladder N : 2 wafer : 9
clusterSizeSideN24
Entries 0
Mean 0
RMS 0
size [# of strips]

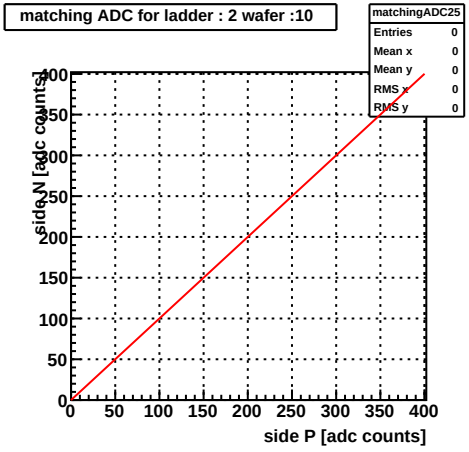

matching ADC for ladder : 2 wafer :9
matchingADC24
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

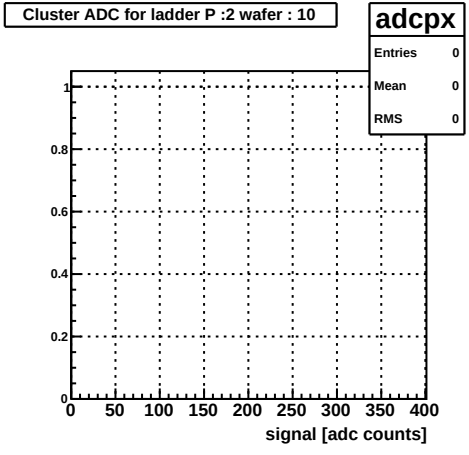

Cluster ADC for ladder P :2 wafer : 9
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

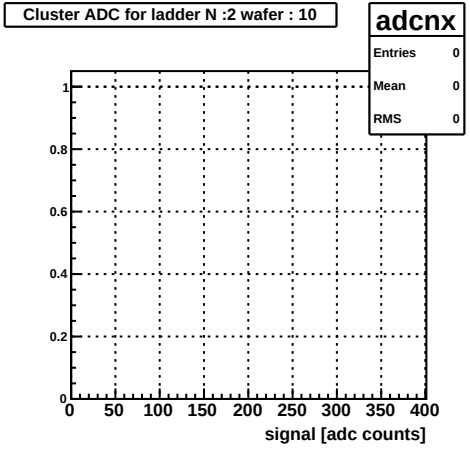

Cluster ADC for ladder N :2 wafer : 9
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

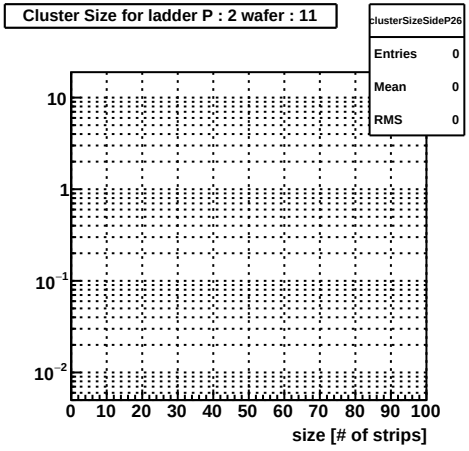

Cluster Size for ladder P : 2 wafer : 10
clusterSizeSideP25
Entries 0
Mean 0
RMS 0
size [# of strips]

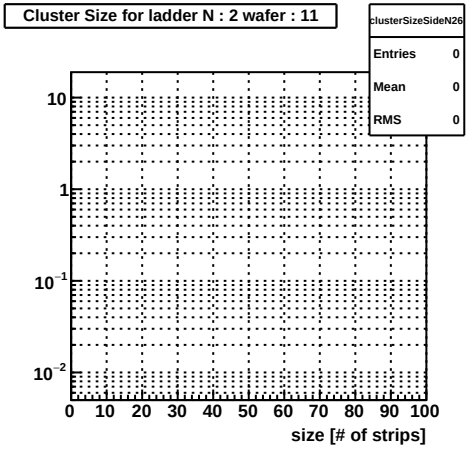

Cluster Size for ladder N : 2 wafer : 10
clusterSizeSideN25
Entries 0
Mean 0
RMS 0
size [# of strips]

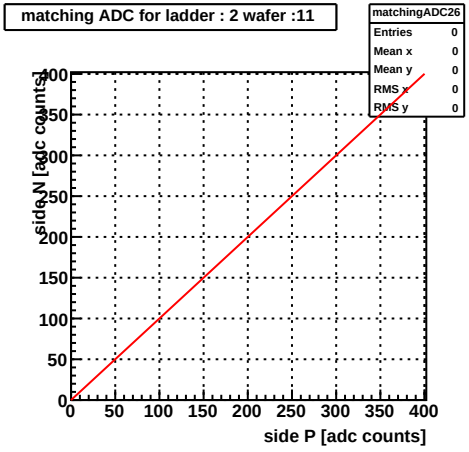

matching ADC for ladder : 2 wafer :10
matchingADC25
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

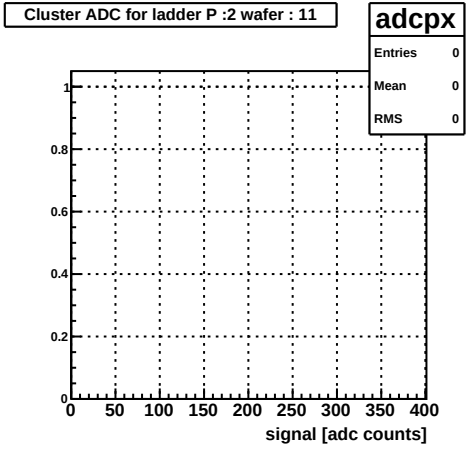

Cluster ADC for ladder P :2 wafer : 10
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

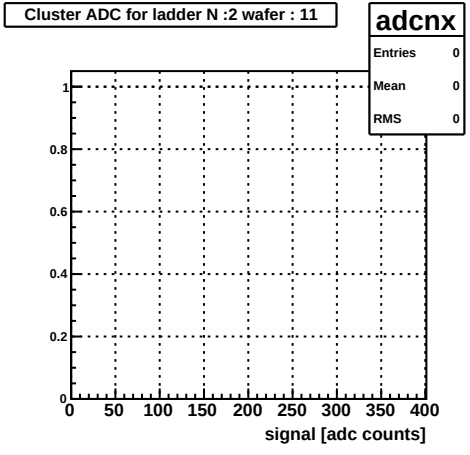

Cluster ADC for ladder N :2 wafer : 10
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

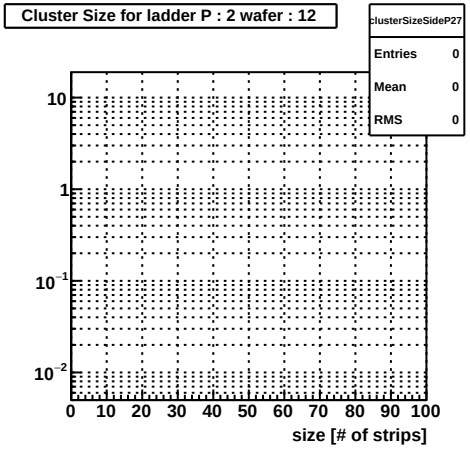

Cluster Size for ladder P : 2 wafer : 11
clusterSizeSideP26
Entries 0
Mean 0
RMS 0
size [# of strips]

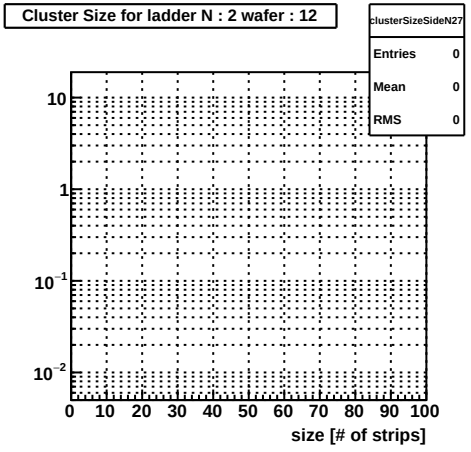

Cluster Size for ladder N : 2 wafer : 11
clusterSizeSideN26
Entries 0
Mean 0
RMS 0
size [# of strips]

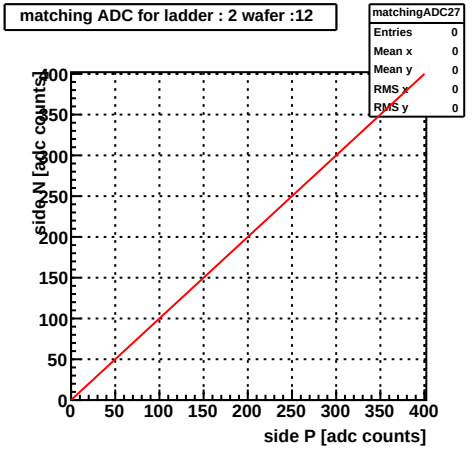

matching ADC for ladder : 2 wafer :11
matchingADC26
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

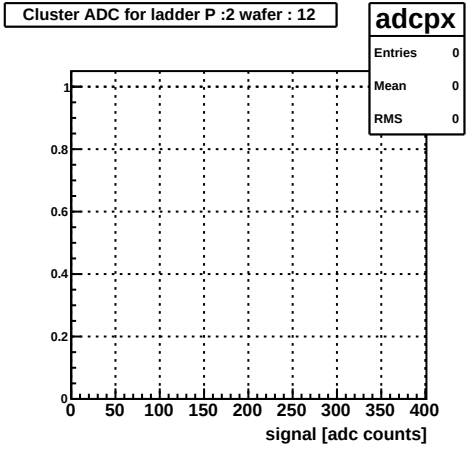

Cluster ADC for ladder P :2 wafer : 11
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

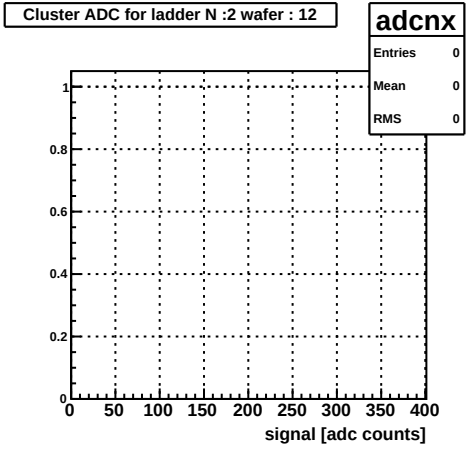

Cluster ADC for ladder N :2 wafer : 11
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

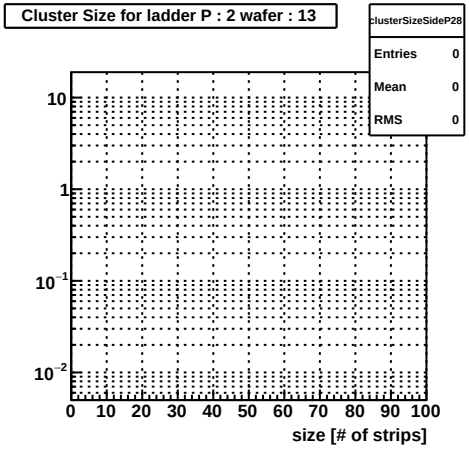

Cluster Size for ladder P : 2 wafer : 12
clusterSizeSideP27
Entries 0
Mean 0
RMS 0
size [# of strips]

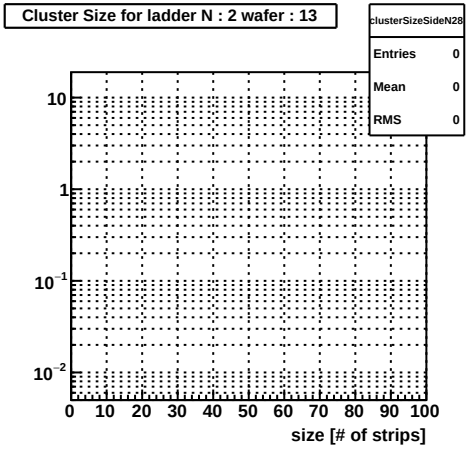

Cluster Size for ladder N : 2 wafer : 12
clusterSizeSideN27
Entries 0
Mean 0
RMS 0
size [# of strips]

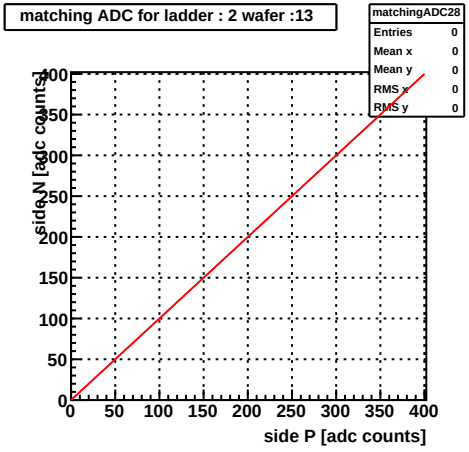

matching ADC for ladder : 2 wafer :12
matchingADC27
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

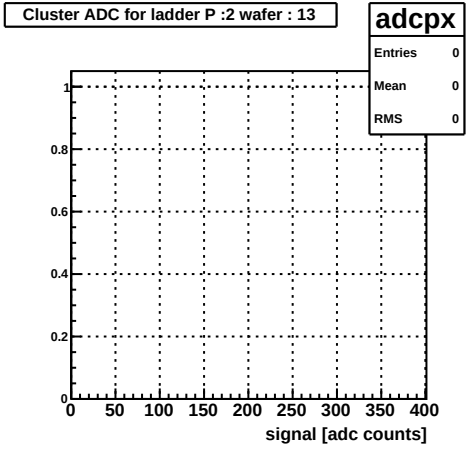

Cluster ADC for ladder P :2 wafer : 12
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

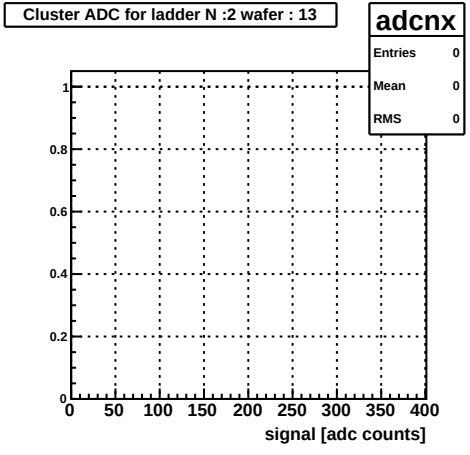

Cluster ADC for ladder N :2 wafer : 12
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

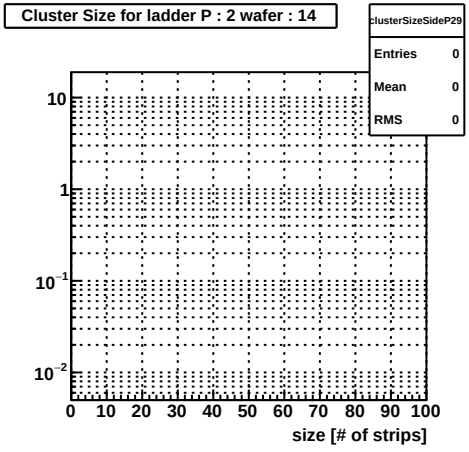

Cluster Size for ladder P : 2 wafer : 13
clusterSizeSideP28
Entries 0
Mean 0
RMS 0
size [# of strips]

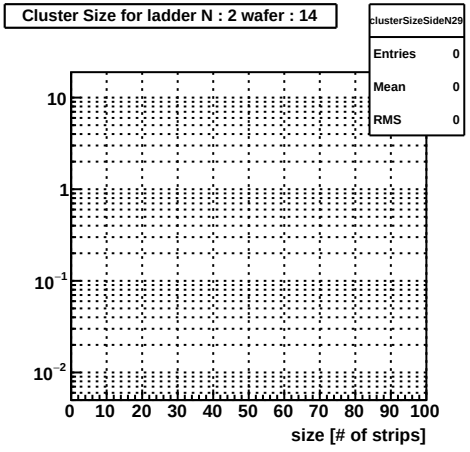

Cluster Size for ladder N : 2 wafer : 13
clusterSizeSideN28
Entries 0
Mean 0
RMS 0
size [# of strips]

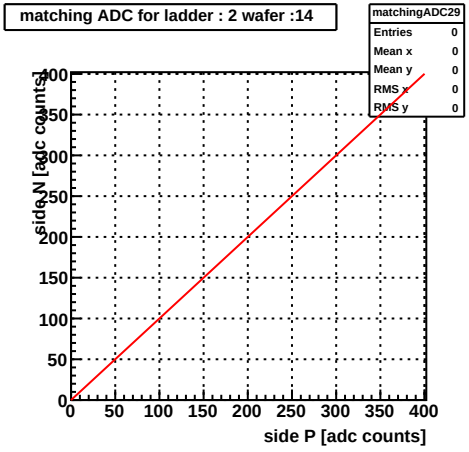

matching ADC for ladder : 2 wafer :13
matchingADC28
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

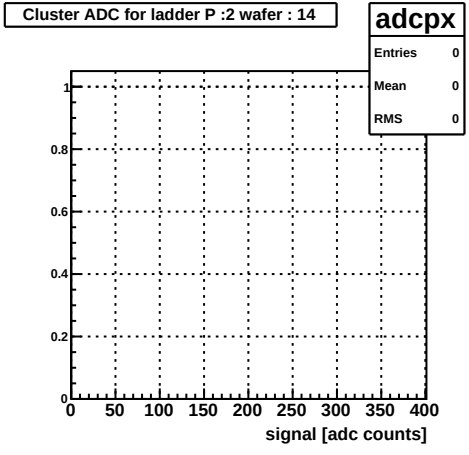

Cluster ADC for ladder P :2 wafer : 13
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

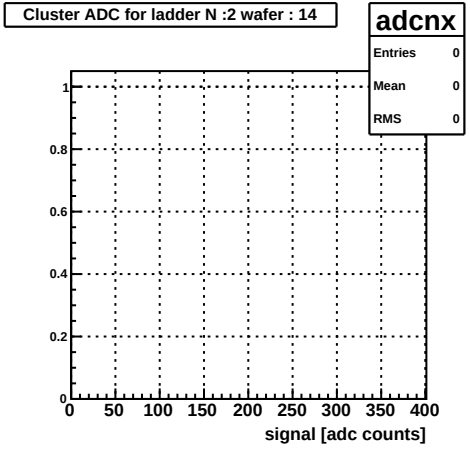

Cluster ADC for ladder N :2 wafer : 13
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

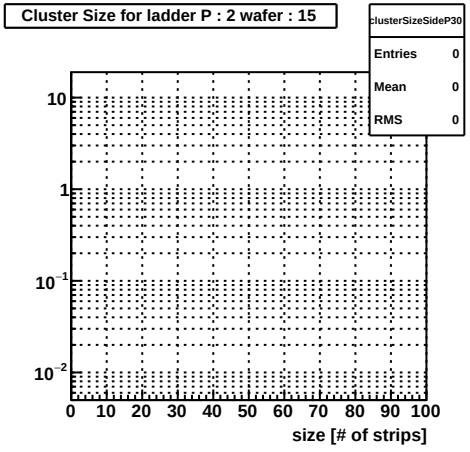

Cluster Size for ladder P : 2 wafer : 14
clusterSizeSideP29
Entries 0
Mean 0
RMS 0
size [# of strips]

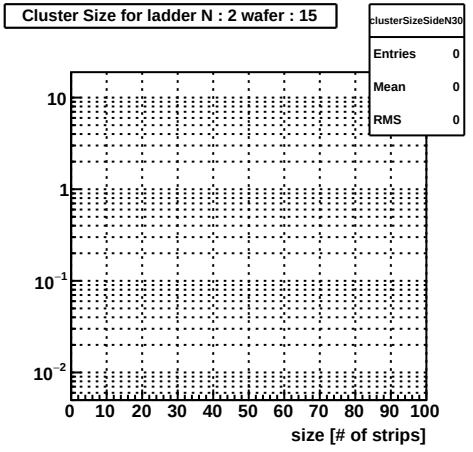

Cluster Size for ladder N : 2 wafer : 14
clusterSizeSideN29
Entries 0
Mean 0
RMS 0
size [# of strips]

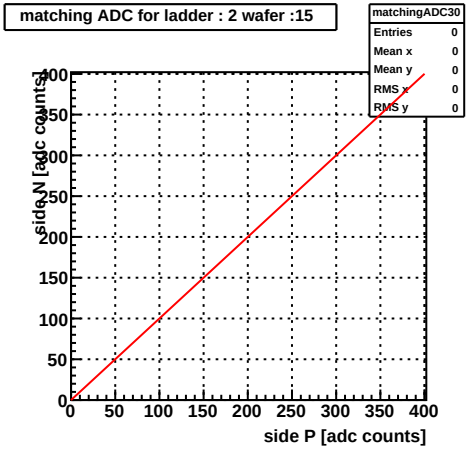

matching ADC for ladder : 2 wafer :14
matchingADC29
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

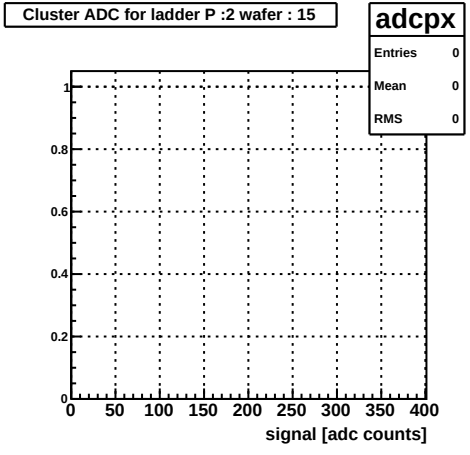

Cluster ADC for ladder P :2 wafer : 14
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

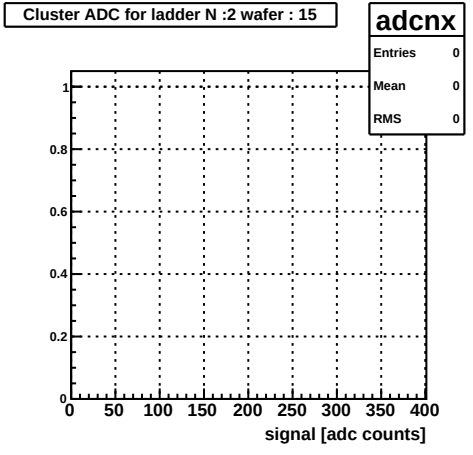

Cluster ADC for ladder N :2 wafer : 14
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

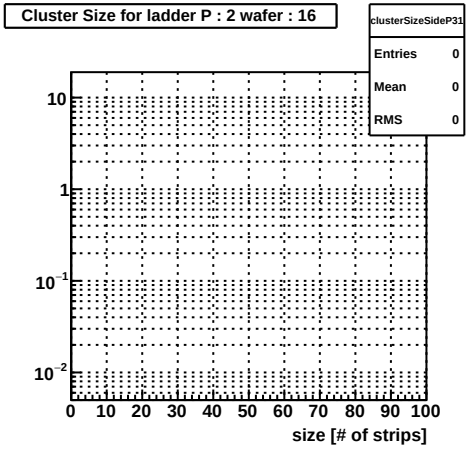

Cluster Size for ladder P : 2 wafer : 15
clusterSizeSideP30
Entries 0
Mean 0
RMS 0
size [# of strips]

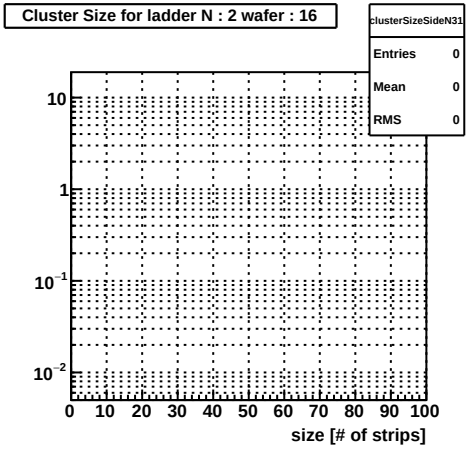

Cluster Size for ladder N : 2 wafer : 15
clusterSizeSideN30
Entries 0
Mean 0
RMS 0
size [# of strips]

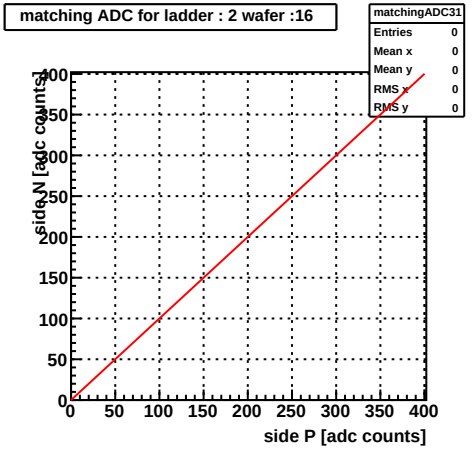

matching ADC for ladder : 2 wafer :15
matchingADC30
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

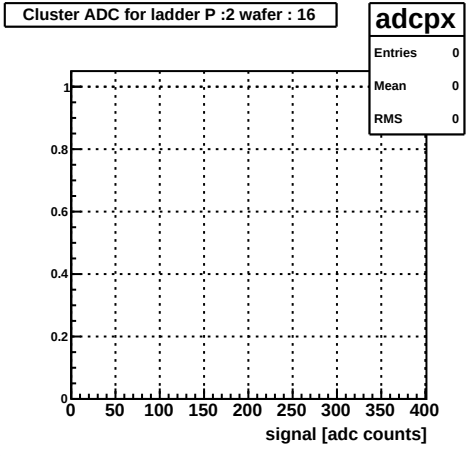

Cluster ADC for ladder P :2 wafer : 15
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

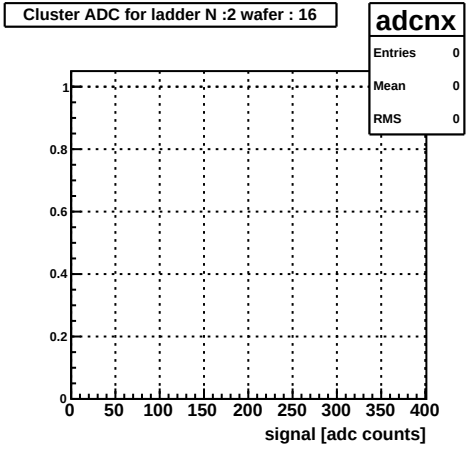

Cluster ADC for ladder N :2 wafer : 15
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

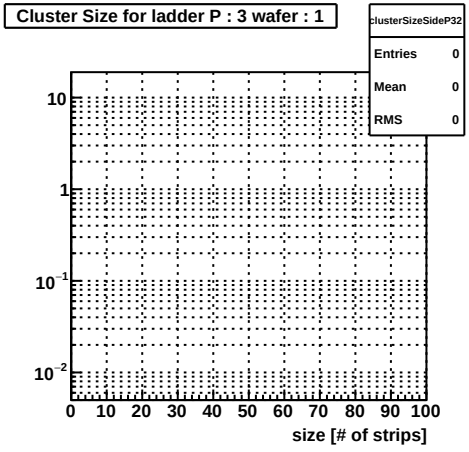

Cluster Size for ladder P : 2 wafer : 16
clusterSizeSideP31
Entries 0
Mean 0
RMS 0
size [# of strips]

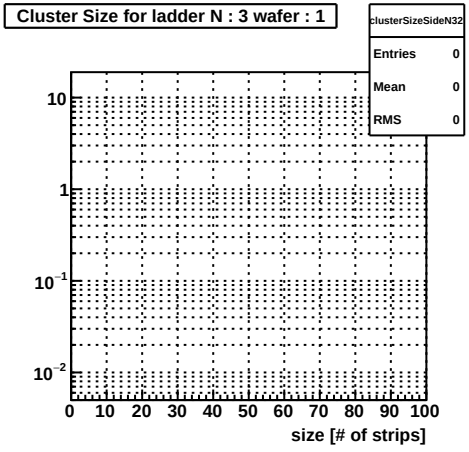

Cluster Size for ladder N : 2 wafer : 16
clusterSizeSideN31
Entries 0
Mean 0
RMS 0
size [# of strips]

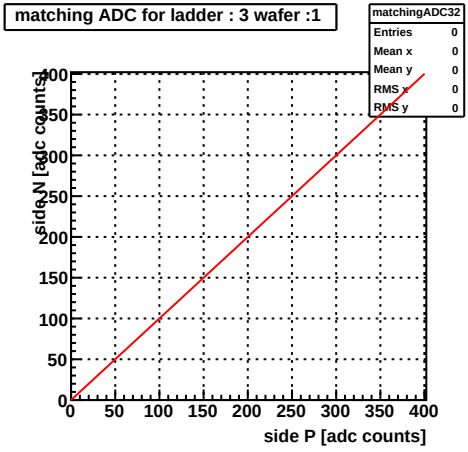

matching ADC for ladder : 2 wafer :16
matchingADC31
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

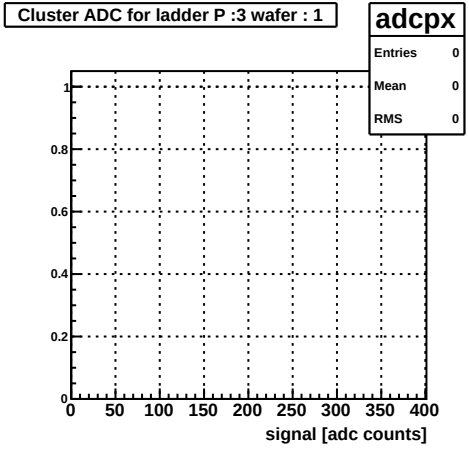

Cluster ADC for ladder P :2 wafer : 16
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

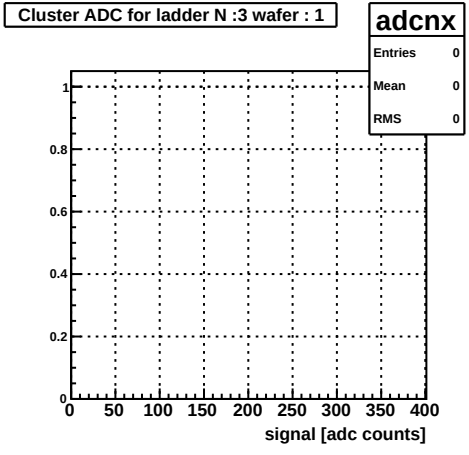

Cluster ADC for ladder N :2 wafer : 16
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

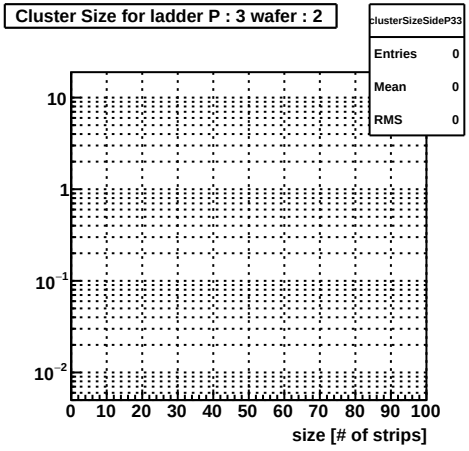

Cluster Size for ladder P : 3 wafer : 1
clusterSizeSideP32
Entries 0
Mean 0
RMS 0
size [# of strips]

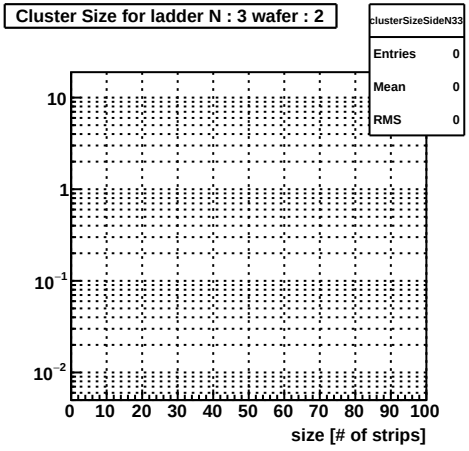

Cluster Size for ladder N : 3 wafer : 1
clusterSizeSideN32
Entries 0
Mean 0
RMS 0
size [# of strips]

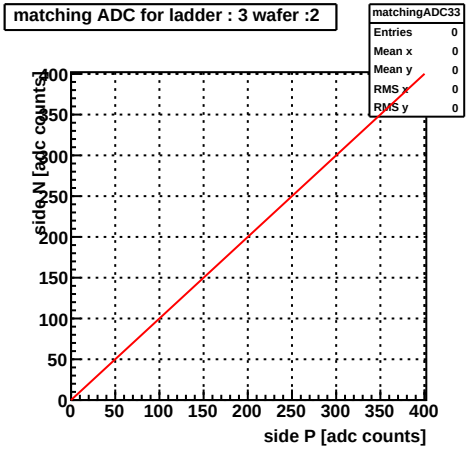

matching ADC for ladder : 3 wafer :1
matchingADC32
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

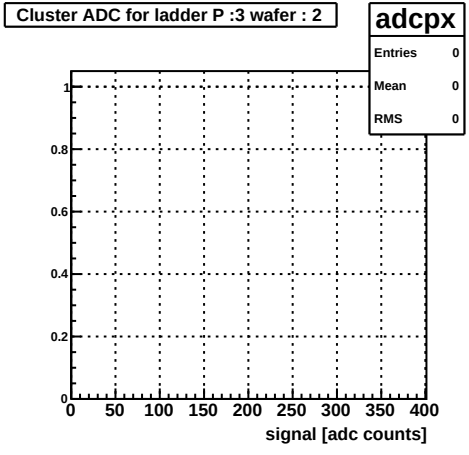

Cluster ADC for ladder P :3 wafer : 1
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

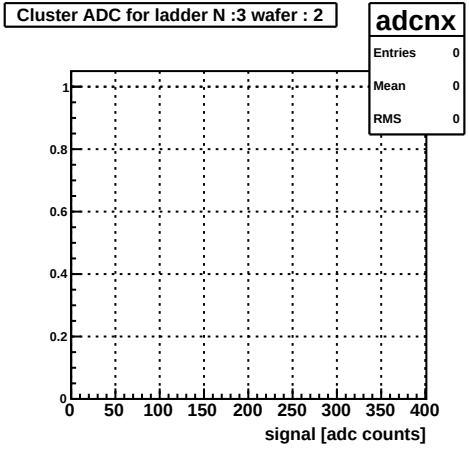

Cluster ADC for ladder N :3 wafer : 1
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

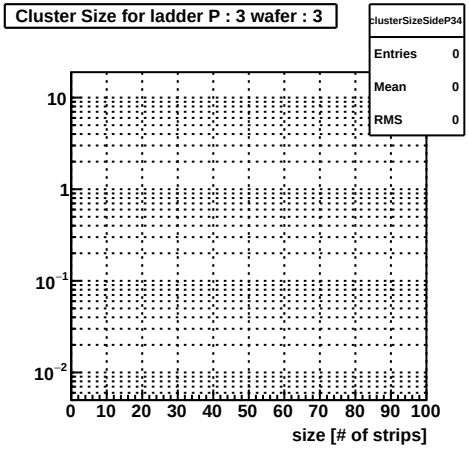

Cluster Size for ladder P : 3 wafer : 2
clusterSizeSideP33
Entries 0
Mean 0
RMS 0
size [# of strips]

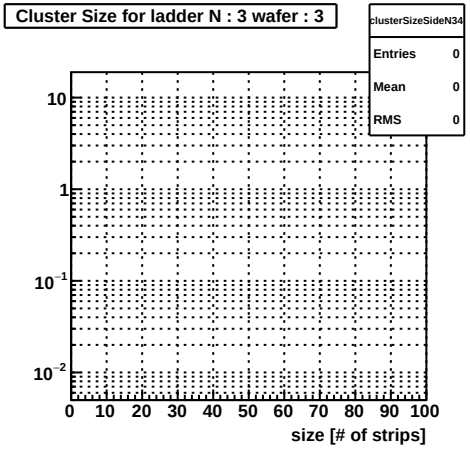

Cluster Size for ladder N : 3 wafer : 2
clusterSizeSideN33
Entries 0
Mean 0
RMS 0
size [# of strips]

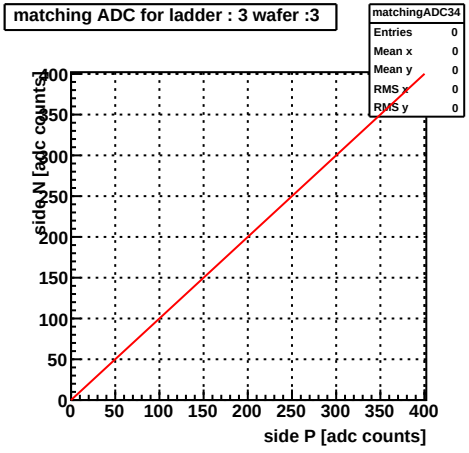

matching ADC for ladder : 3 wafer :2
matchingADC33
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

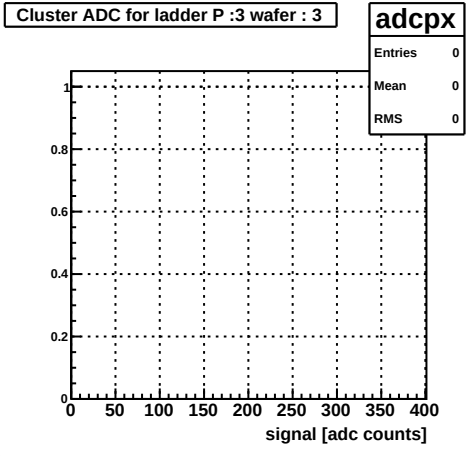

Cluster ADC for ladder P :3 wafer : 2
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

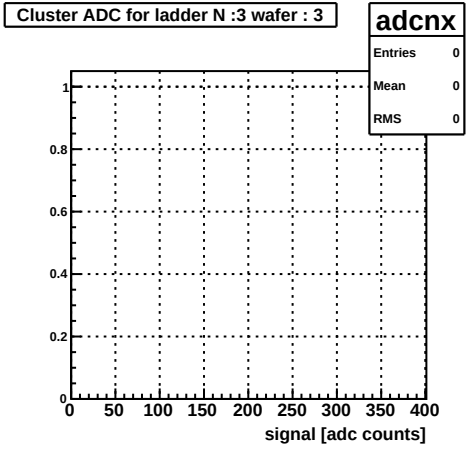

Cluster ADC for ladder N :3 wafer : 2
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

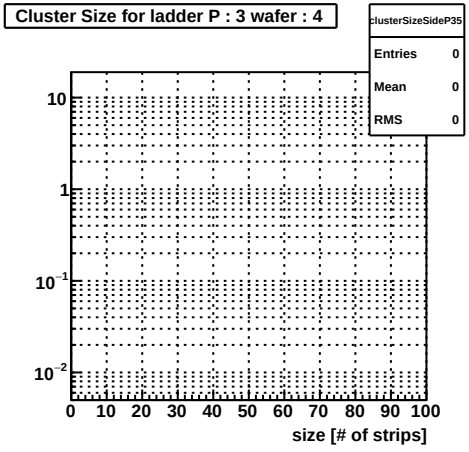

Cluster Size for ladder P : 3 wafer : 3
clusterSizeSideP34
Entries 0
Mean 0
RMS 0
size [# of strips]

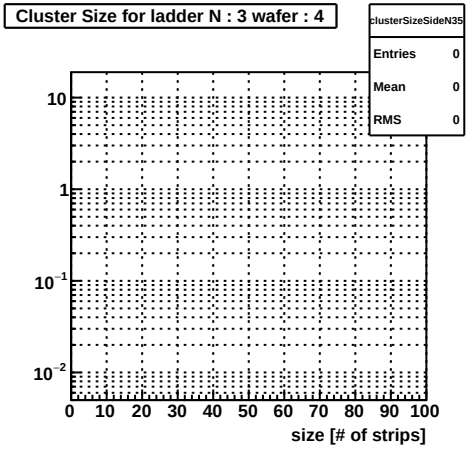

Cluster Size for ladder N : 3 wafer : 3
clusterSizeSideN34
Entries 0
Mean 0
RMS 0
size [# of strips]

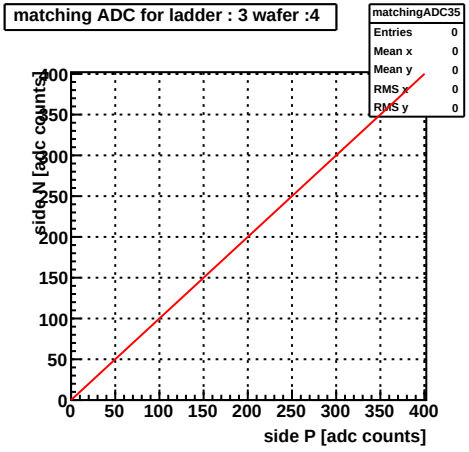

matching ADC for ladder : 3 wafer :3
matchingADC34
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

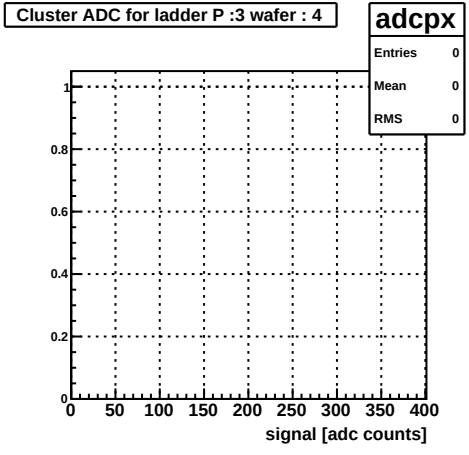

Cluster ADC for ladder P :3 wafer : 3
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

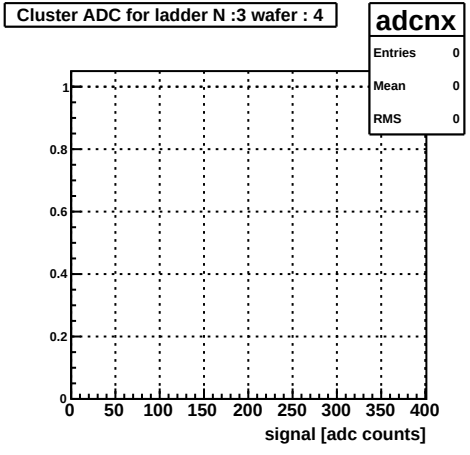

Cluster ADC for ladder N :3 wafer : 3
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

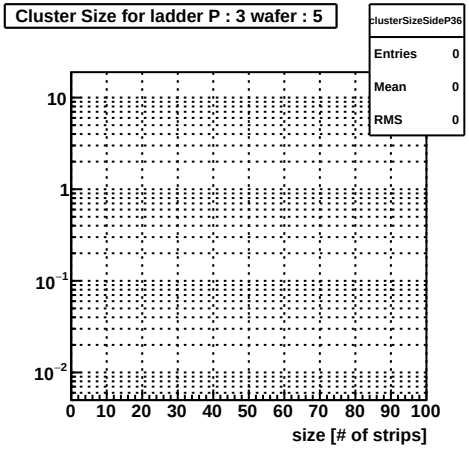

Cluster Size for ladder P : 3 wafer : 4
clusterSizeSideP35
Entries 0
Mean 0
RMS 0
size [# of strips]

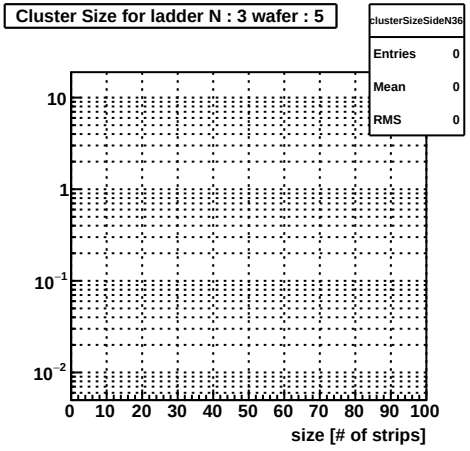

Cluster Size for ladder N : 3 wafer : 4
clusterSizeSideN35
Entries 0
Mean 0
RMS 0
size [# of strips]

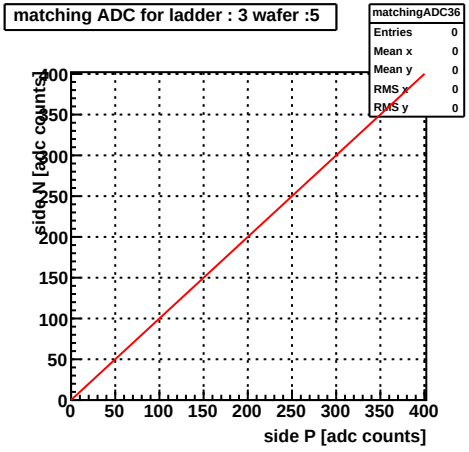

matching ADC for ladder : 3 wafer :4
matchingADC35
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

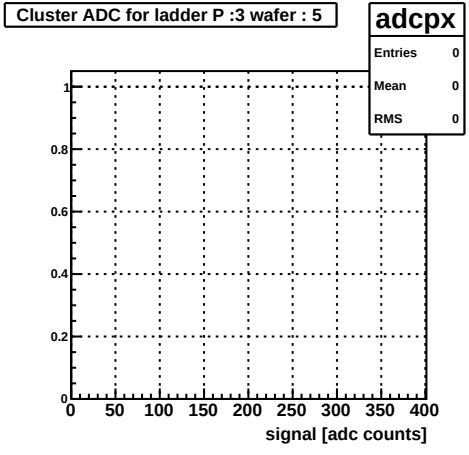

Cluster ADC for ladder P :3 wafer : 4
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

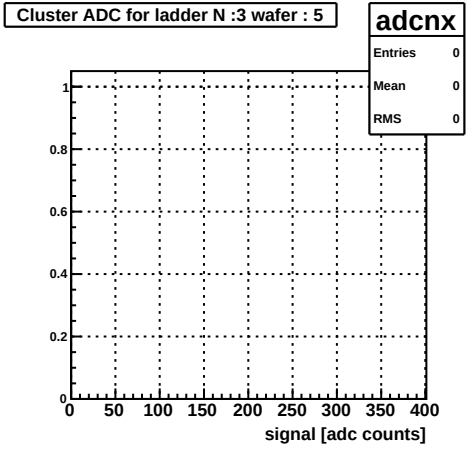

Cluster ADC for ladder N :3 wafer : 4
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

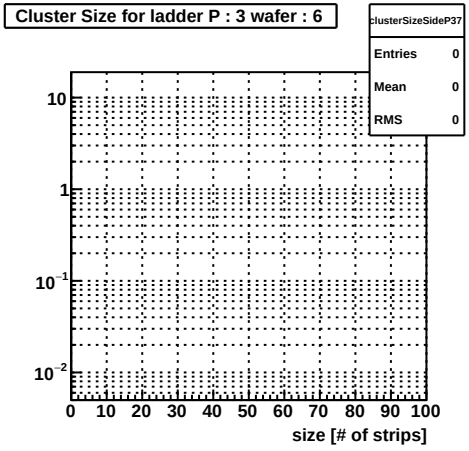

Cluster Size for ladder P : 3 wafer : 5
clusterSizeSideP36
Entries 0
Mean 0
RMS 0
size [# of strips]

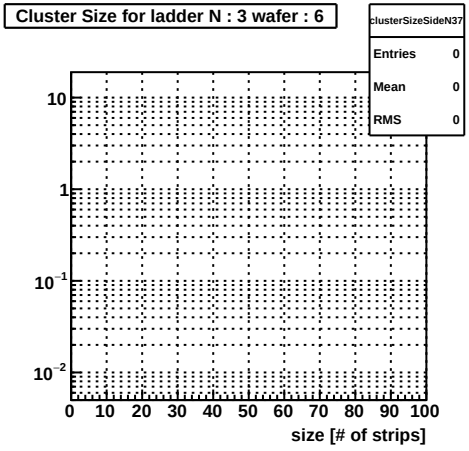

Cluster Size for ladder N : 3 wafer : 5
clusterSizeSideN36
Entries 0
Mean 0
RMS 0
size [# of strips]

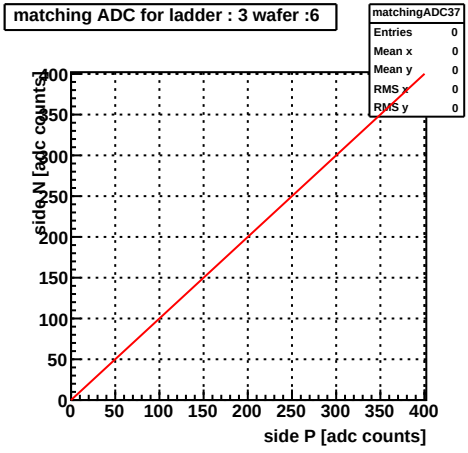

matching ADC for ladder : 3 wafer :5
matchingADC36
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

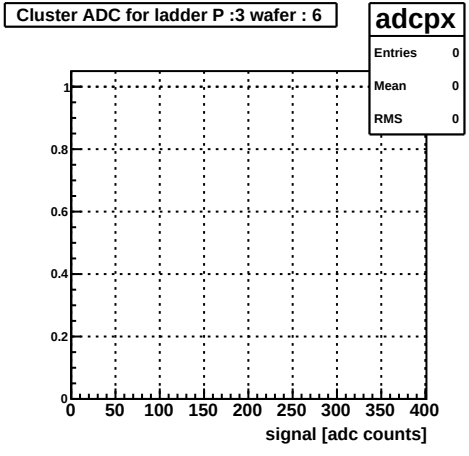

Cluster ADC for ladder P :3 wafer : 5
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

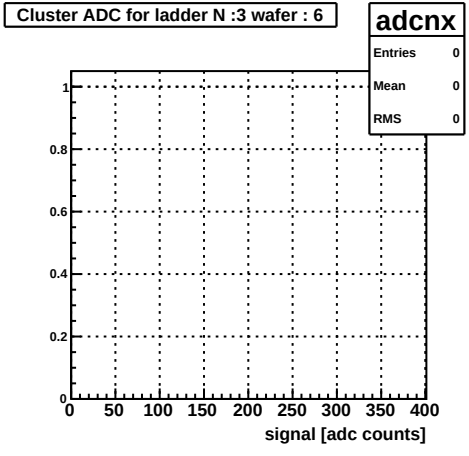

Cluster ADC for ladder N :3 wafer : 5
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

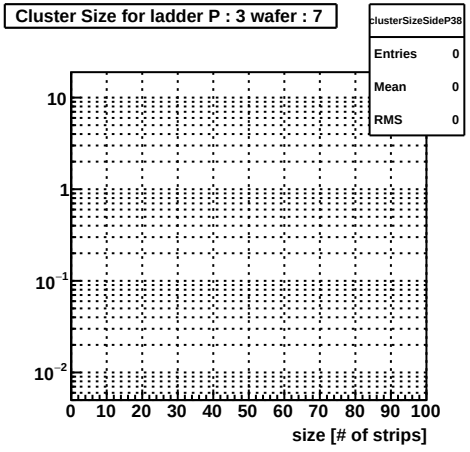

Cluster Size for ladder P : 3 wafer : 6
clusterSizeSideP37
Entries 0
Mean 0
RMS 0
size [# of strips]

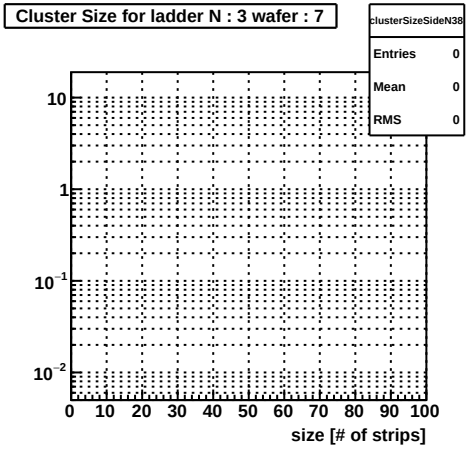

Cluster Size for ladder N : 3 wafer : 6
clusterSizeSideN37
Entries 0
Mean 0
RMS 0
size [# of strips]

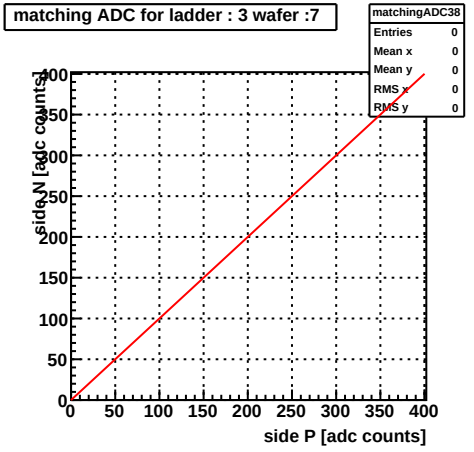

matching ADC for ladder : 3 wafer :6
matchingADC37
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

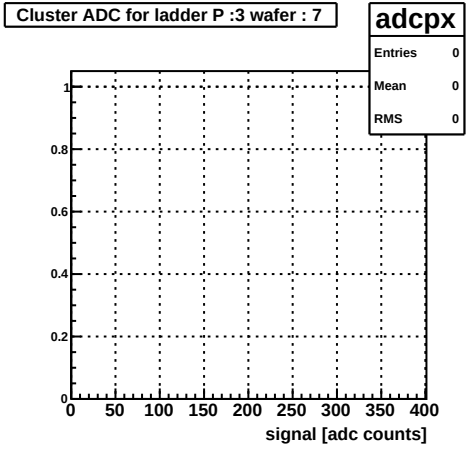

Cluster ADC for ladder P :3 wafer : 6
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

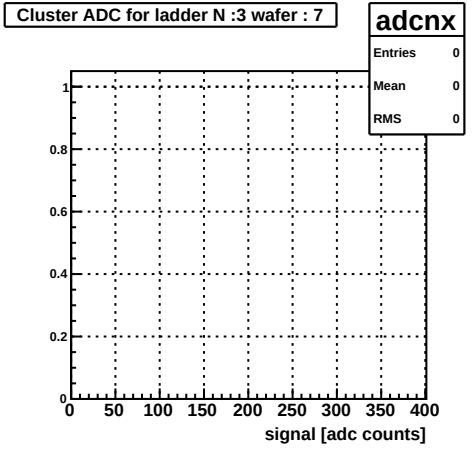

Cluster ADC for ladder N :3 wafer : 6
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

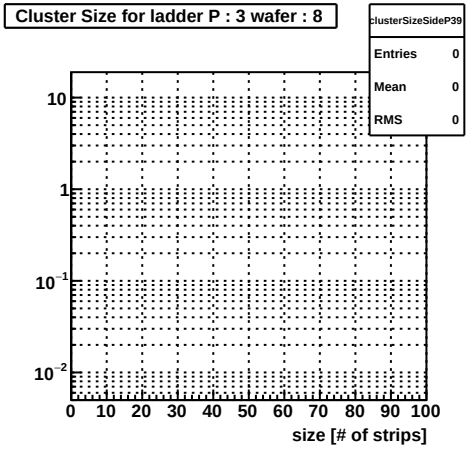

Cluster Size for ladder P : 3 wafer : 7
clusterSizeSideP38
Entries 0
Mean 0
RMS 0
size [# of strips]

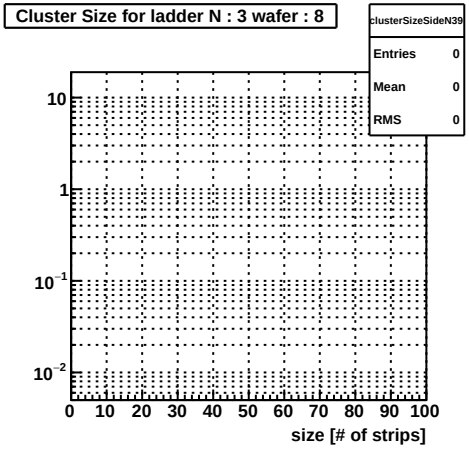

Cluster Size for ladder N : 3 wafer : 7
clusterSizeSideN38
Entries 0
Mean 0
RMS 0
size [# of strips]

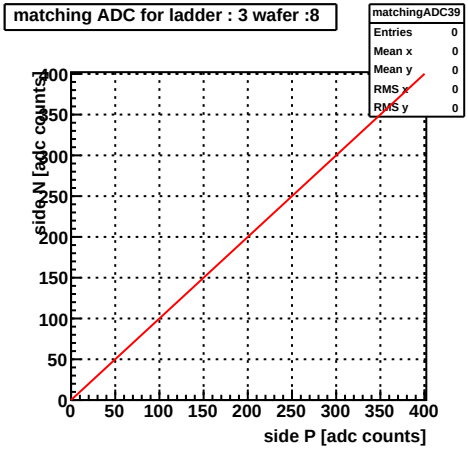

matching ADC for ladder : 3 wafer :7
matchingADC38
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

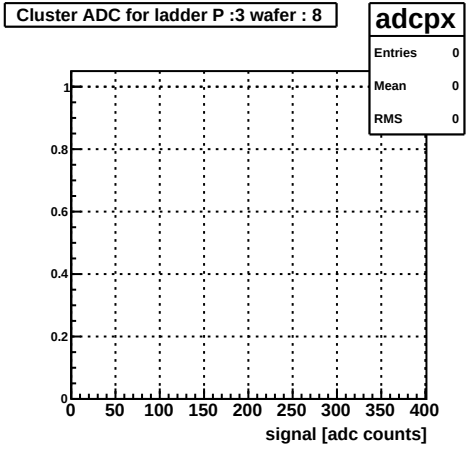

Cluster ADC for ladder P :3 wafer : 7
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

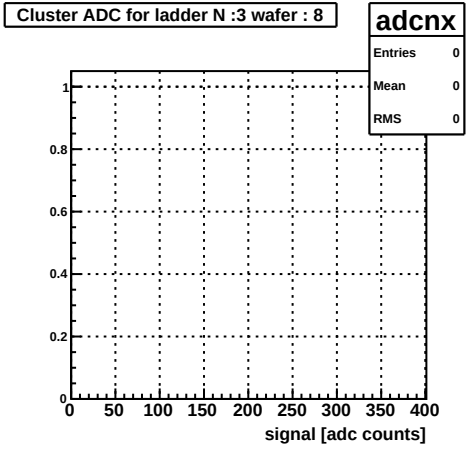

Cluster ADC for ladder N :3 wafer : 7
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

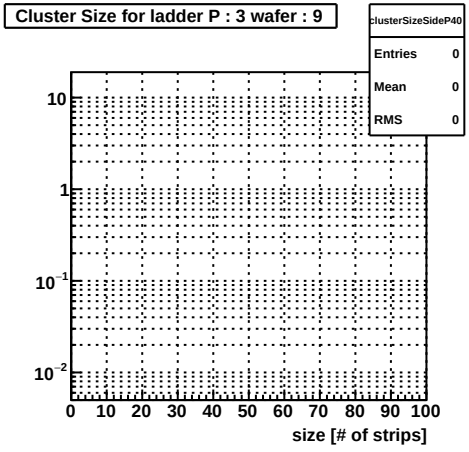

Cluster Size for ladder P : 3 wafer : 8
clusterSizeSideP39
Entries 0
Mean 0
RMS 0
size [# of strips]

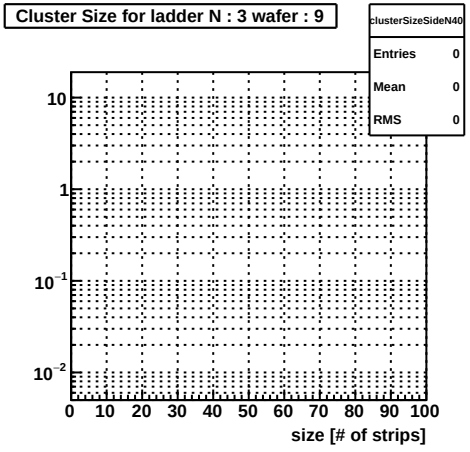

Cluster Size for ladder N : 3 wafer : 8
clusterSizeSideN39
Entries 0
Mean 0
RMS 0
size [# of strips]

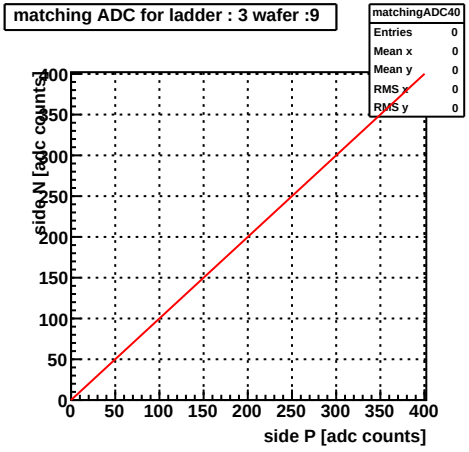

matching ADC for ladder : 3 wafer :8
matchingADC39
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

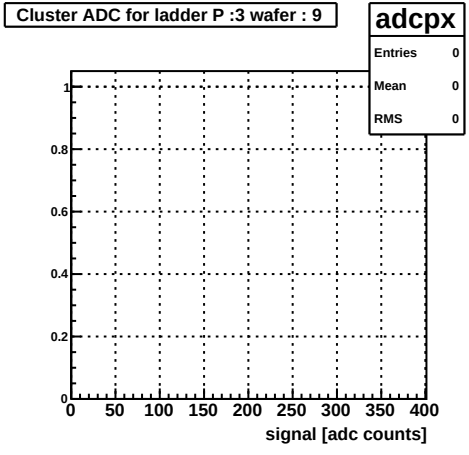

Cluster ADC for ladder P :3 wafer : 8
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

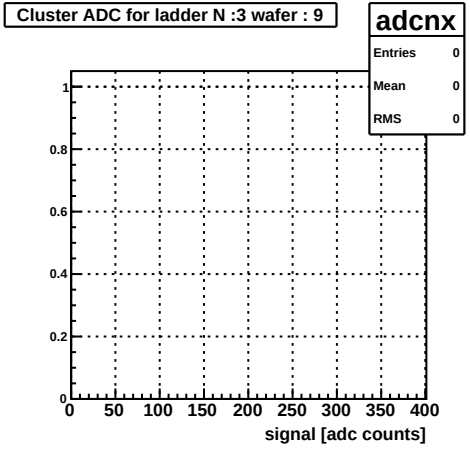

Cluster ADC for ladder N :3 wafer : 8
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

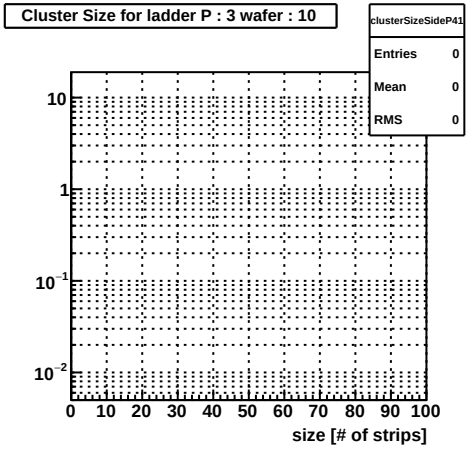

Cluster Size for ladder P : 3 wafer : 9
clusterSizeSideP40
Entries 0
Mean 0
RMS 0
size [# of strips]

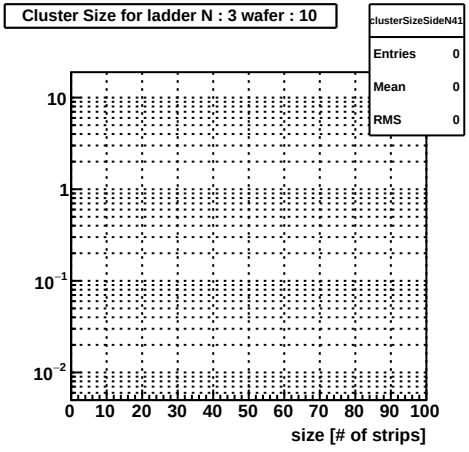

Cluster Size for ladder N : 3 wafer : 9
clusterSizeSideN40
Entries 0
Mean 0
RMS 0
size [# of strips]

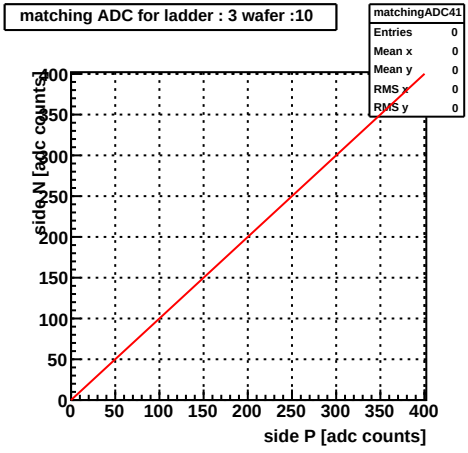

matching ADC for ladder : 3 wafer :9
matchingADC40
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

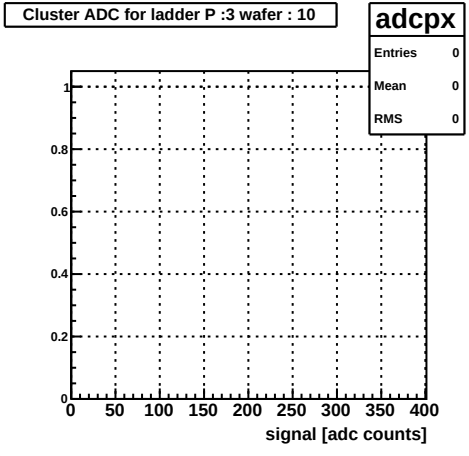

Cluster ADC for ladder P :3 wafer : 9
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

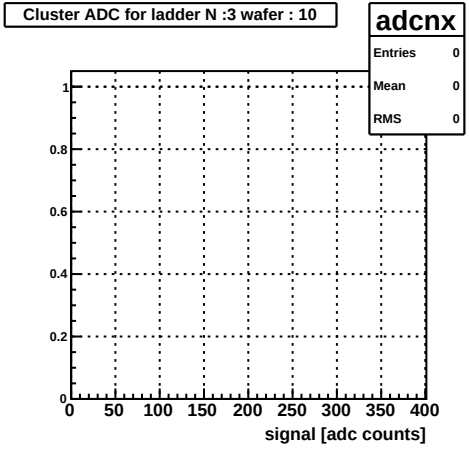

Cluster ADC for ladder N :3 wafer : 9
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

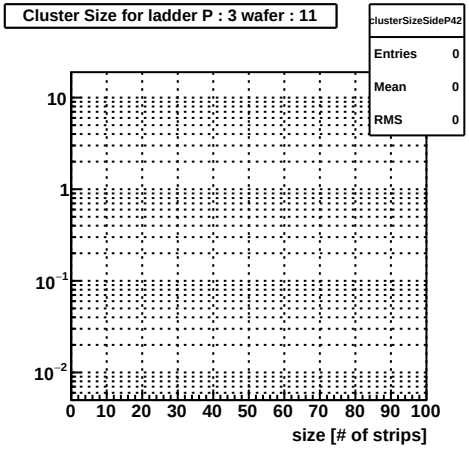

Cluster Size for ladder P : 3 wafer : 10
clusterSizeSideP41
Entries 0
Mean 0
RMS 0
size [# of strips]

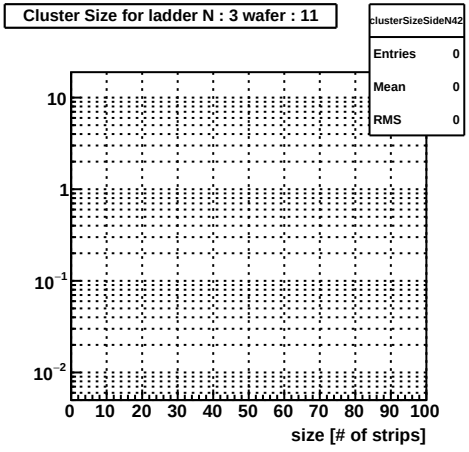

Cluster Size for ladder N : 3 wafer : 10
clusterSizeSideN41
Entries 0
Mean 0
RMS 0
size [# of strips]

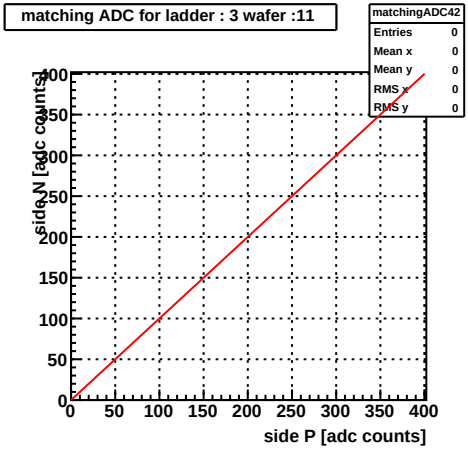

matching ADC for ladder : 3 wafer :10
matchingADC41
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

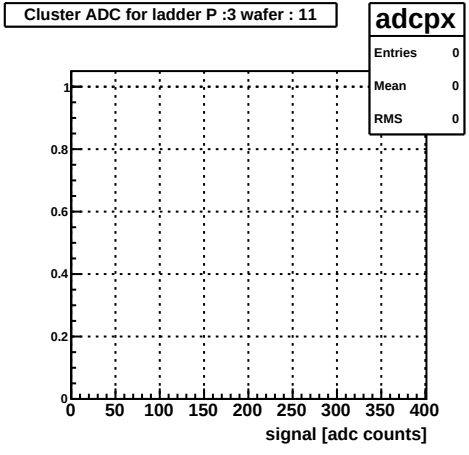

Cluster ADC for ladder P :3 wafer : 10
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

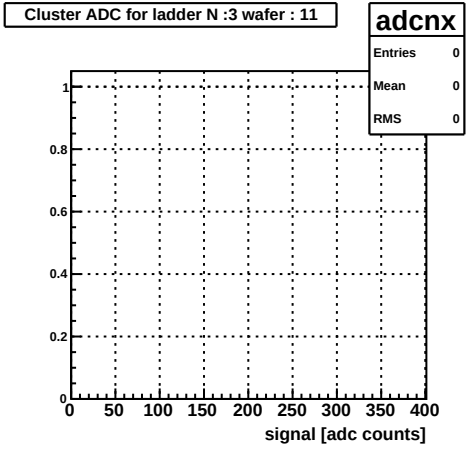

Cluster ADC for ladder N :3 wafer : 10
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

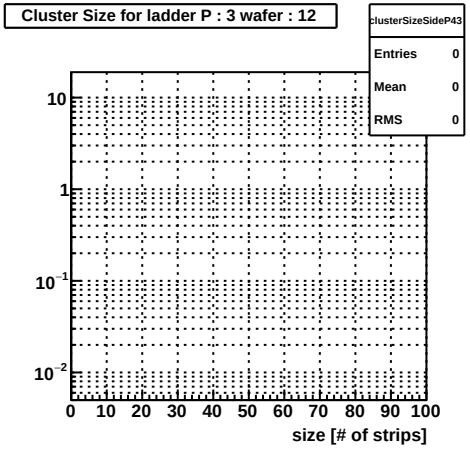

Cluster Size for ladder P : 3 wafer : 11
clusterSizeSideP42
Entries 0
Mean 0
RMS 0
size [# of strips]

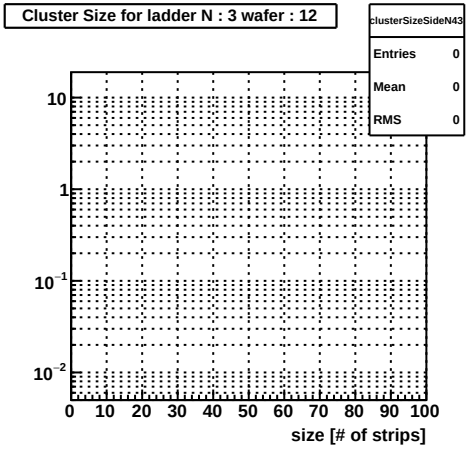

Cluster Size for ladder N : 3 wafer : 11
clusterSizeSideN42
Entries 0
Mean 0
RMS 0
size [# of strips]

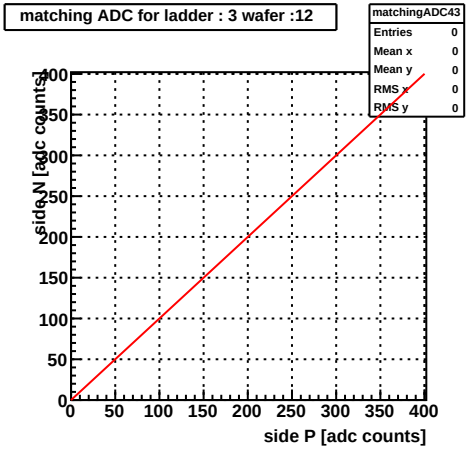

matching ADC for ladder : 3 wafer :11
matchingADC42
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

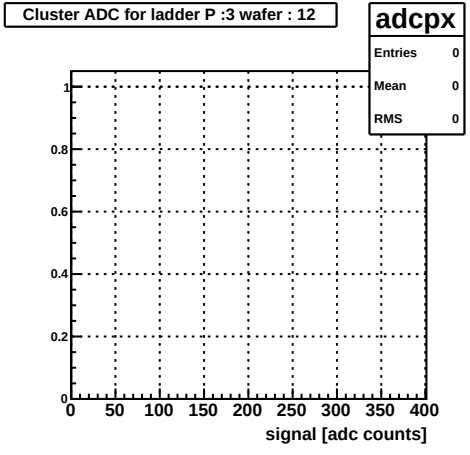

Cluster ADC for ladder P :3 wafer : 11
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

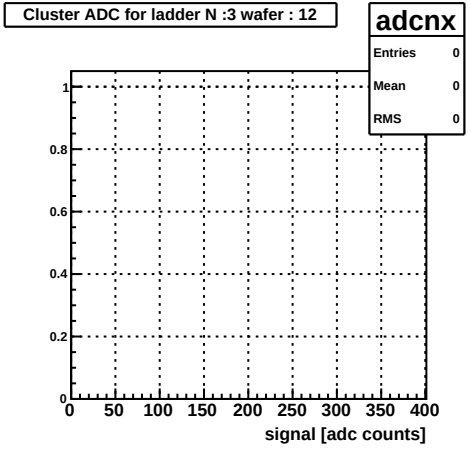

Cluster ADC for ladder N :3 wafer : 11
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

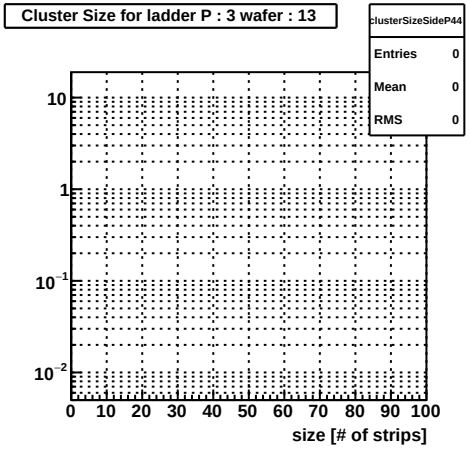

Cluster Size for ladder P : 3 wafer : 12
clusterSizeSideP43
Entries 0
Mean 0
RMS 0
size [# of strips]

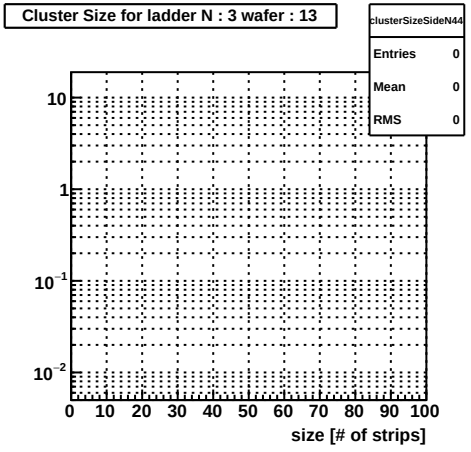

Cluster Size for ladder N : 3 wafer : 12
clusterSizeSideN43
Entries 0
Mean 0
RMS 0
size [# of strips]

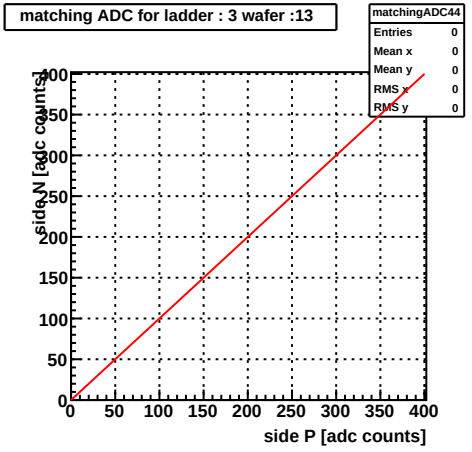

matching ADC for ladder : 3 wafer :12
matchingADC43
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

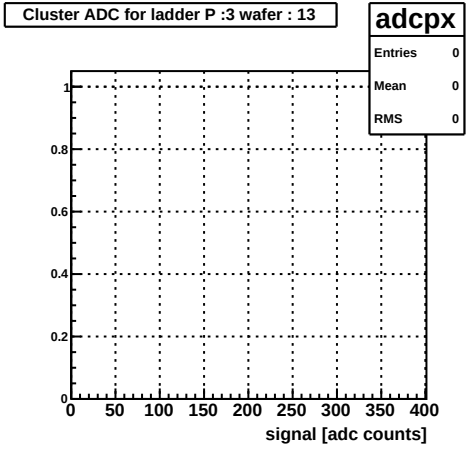

Cluster ADC for ladder P :3 wafer : 12
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

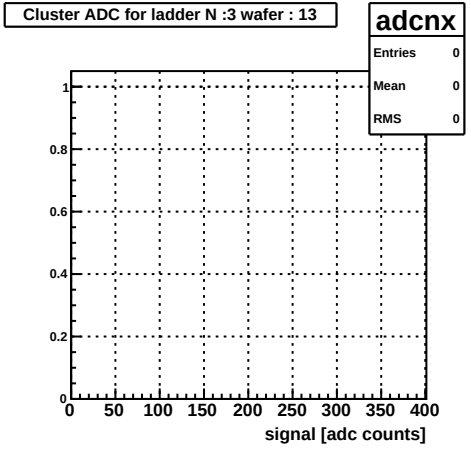

Cluster ADC for ladder N :3 wafer : 12
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

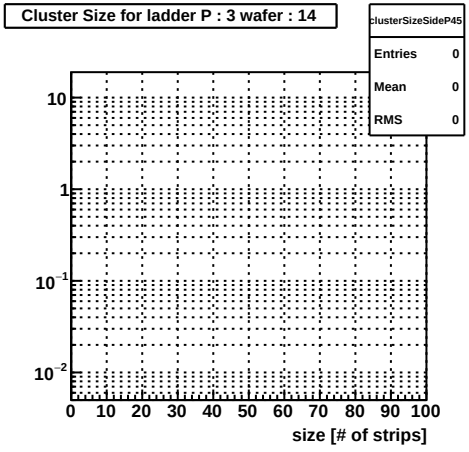

Cluster Size for ladder P : 3 wafer : 13
clusterSizeSideP44
Entries 0
Mean 0
RMS 0
size [# of strips]

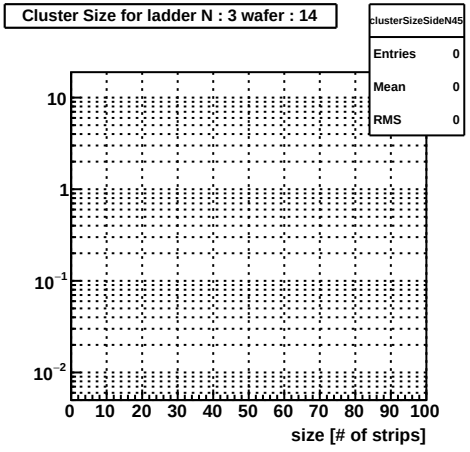

Cluster Size for ladder N : 3 wafer : 13
clusterSizeSideN44
Entries 0
Mean 0
RMS 0
size [# of strips]

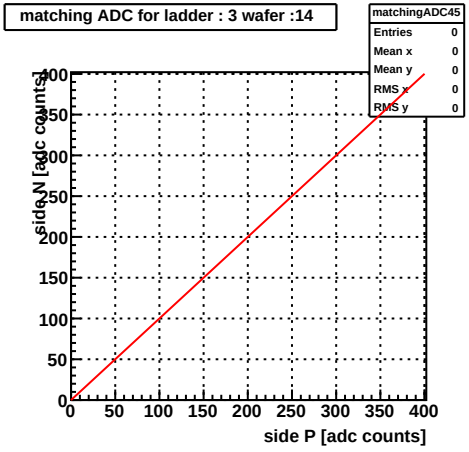

matching ADC for ladder : 3 wafer :13
matchingADC44
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

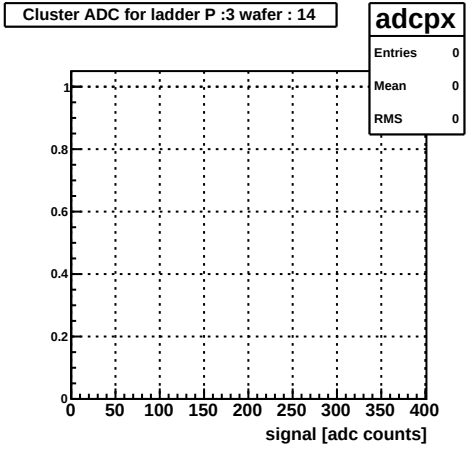

Cluster ADC for ladder P :3 wafer : 13
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

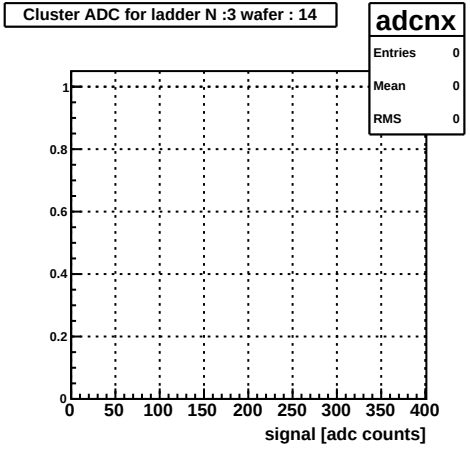

Cluster ADC for ladder N :3 wafer : 13
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

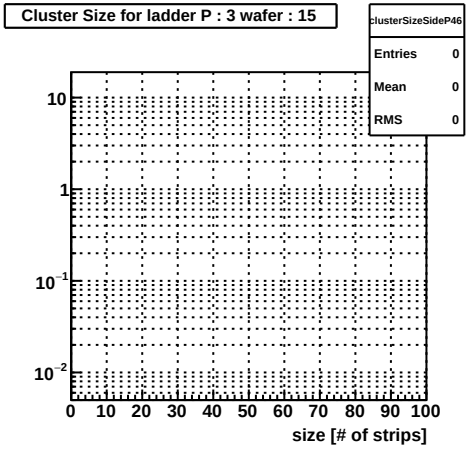

Cluster Size for ladder P : 3 wafer : 14
clusterSizeSideP45
Entries 0
Mean 0
RMS 0
size [# of strips]

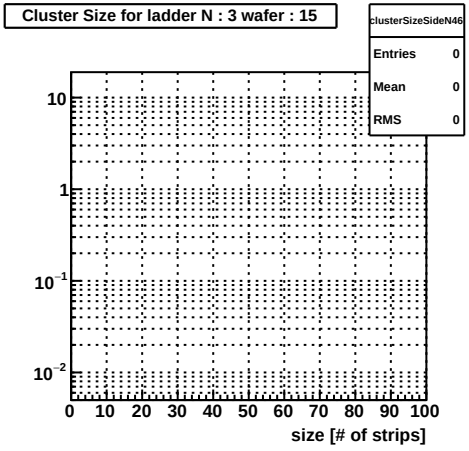

Cluster Size for ladder N : 3 wafer : 14
clusterSizeSideN45
Entries 0
Mean 0
RMS 0
size [# of strips]

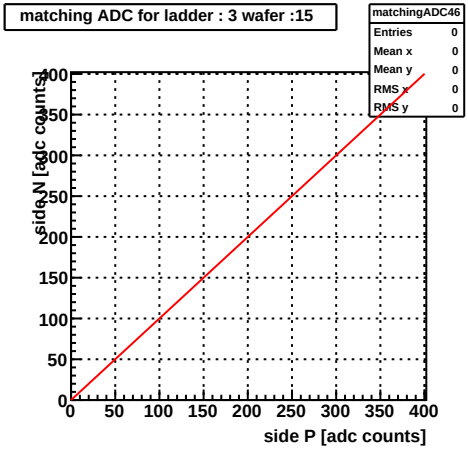

matching ADC for ladder : 3 wafer :14
matchingADC45
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

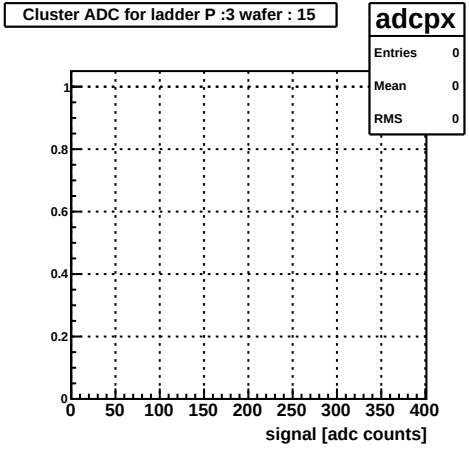

Cluster ADC for ladder P :3 wafer : 14
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

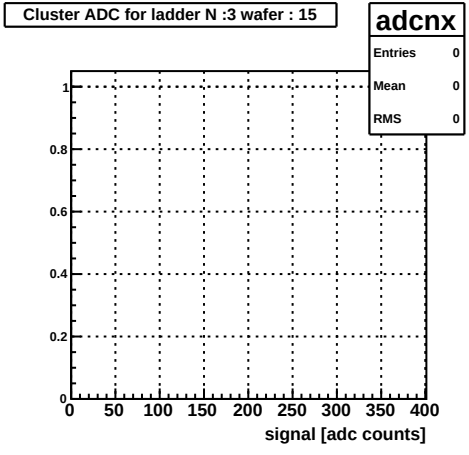

Cluster ADC for ladder N :3 wafer : 14
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

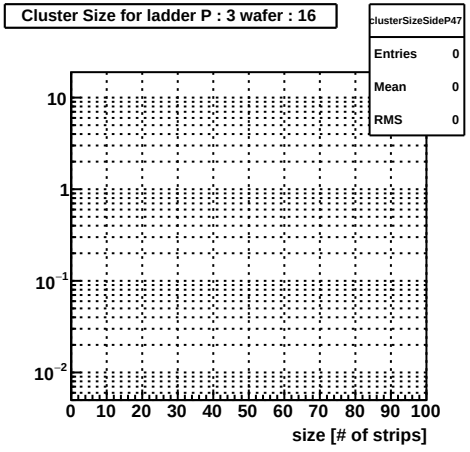

Cluster Size for ladder P : 3 wafer : 15
clusterSizeSideP46
Entries 0
Mean 0
RMS 0
size [# of strips]

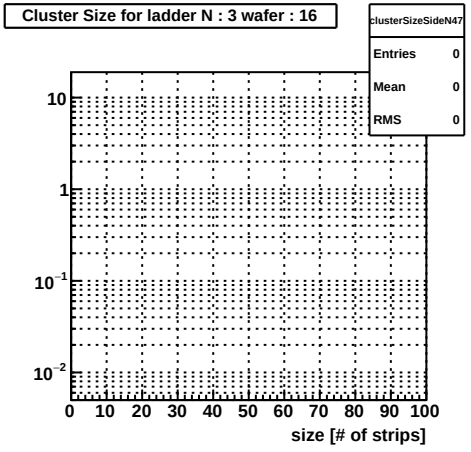

Cluster Size for ladder N : 3 wafer : 15
clusterSizeSideN46
Entries 0
Mean 0
RMS 0
size [# of strips]

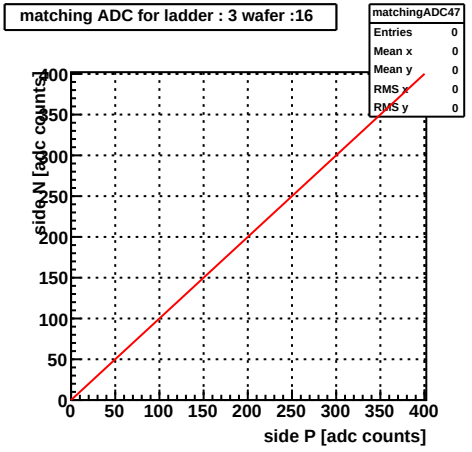

matching ADC for ladder : 3 wafer :15
matchingADC46
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

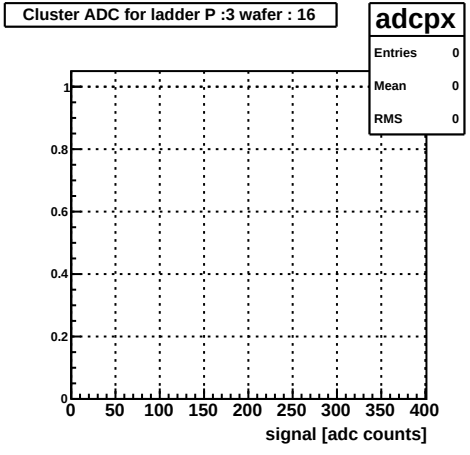

Cluster ADC for ladder P :3 wafer : 15
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

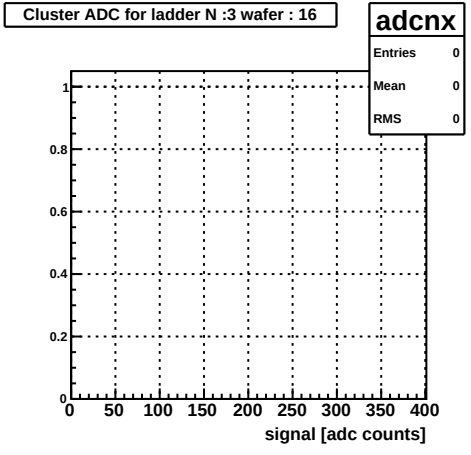

Cluster ADC for ladder N :3 wafer : 15
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

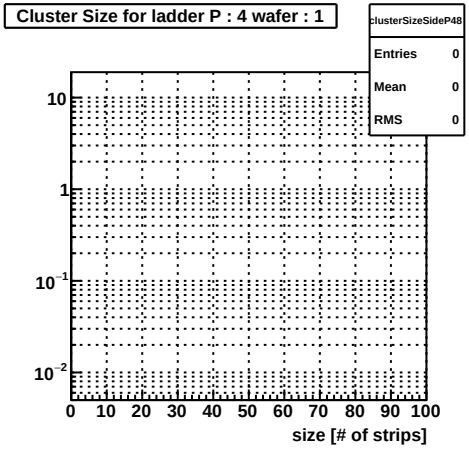

Cluster Size for ladder P : 3 wafer : 16
clusterSizeSideP47
Entries 0
Mean 0
RMS 0
size [# of strips]

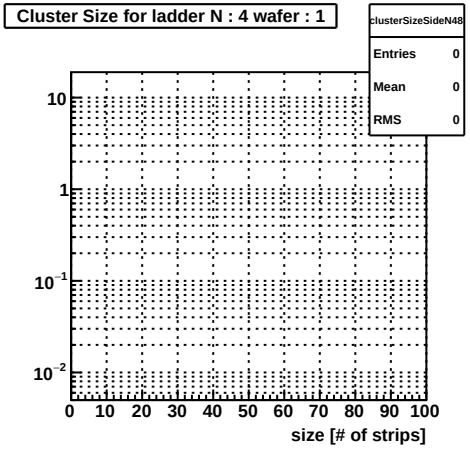

Cluster Size for ladder N : 3 wafer : 16
clusterSizeSideN47
Entries 0
Mean 0
RMS 0
size [# of strips]

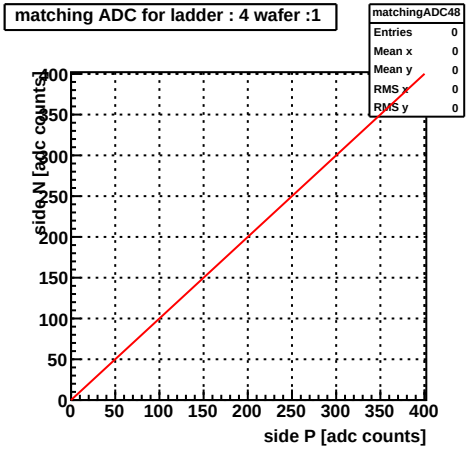

matching ADC for ladder : 3 wafer :16
matchingADC47
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

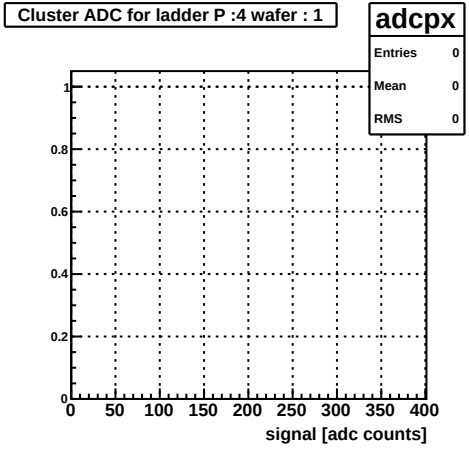

Cluster ADC for ladder P :3 wafer : 16
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

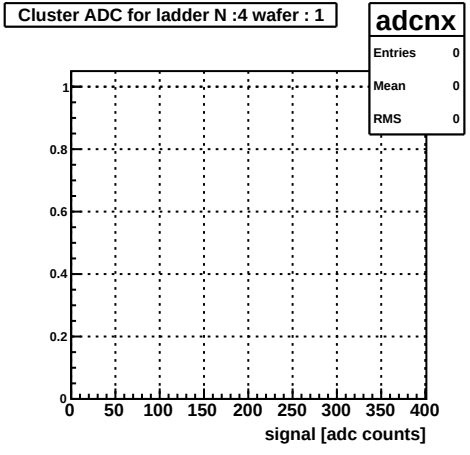

Cluster ADC for ladder N :3 wafer : 16
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

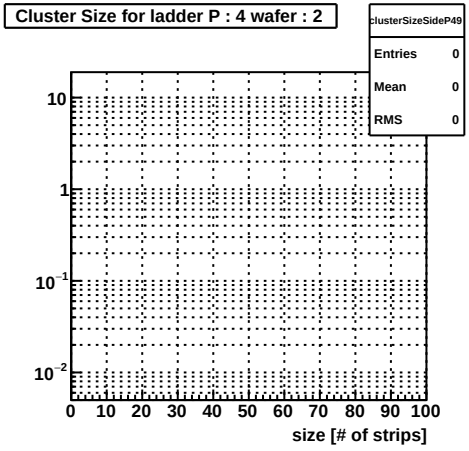

Cluster Size for ladder P : 4 wafer : 1
clusterSizeSideP48
Entries 0
Mean 0
RMS 0
size [# of strips]

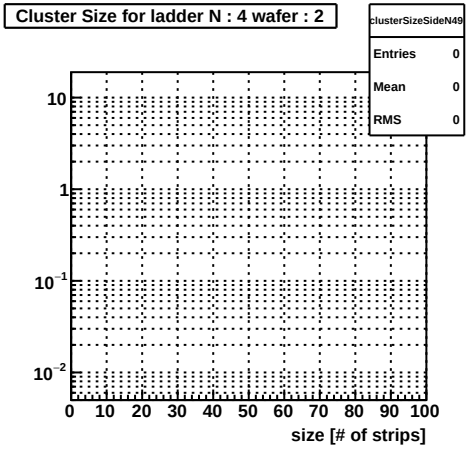

Cluster Size for ladder N : 4 wafer : 1
clusterSizeSideN48
Entries 0
Mean 0
RMS 0
size [# of strips]

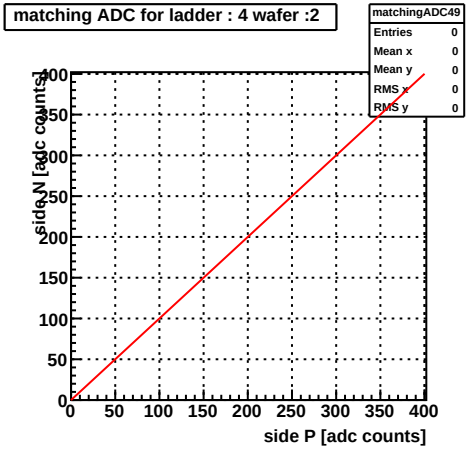

matching ADC for ladder : 4 wafer :1
matchingADC48
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

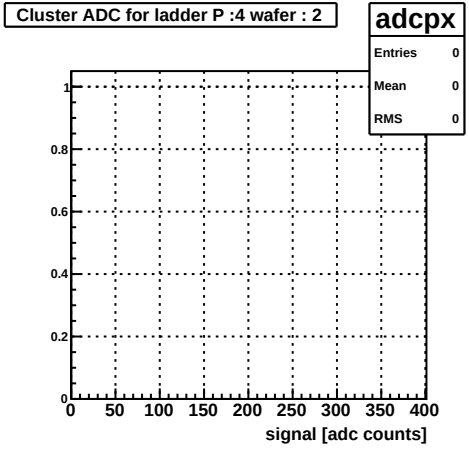

Cluster ADC for ladder P :4 wafer : 1
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

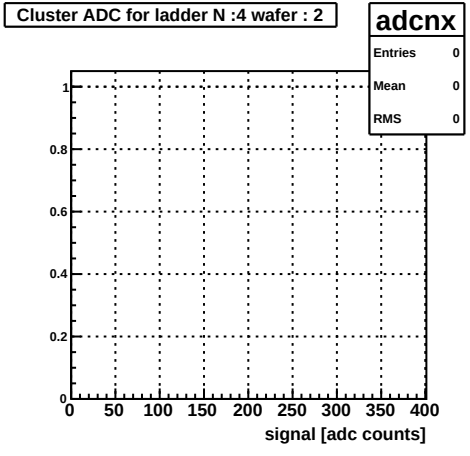

Cluster ADC for ladder N :4 wafer : 1
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

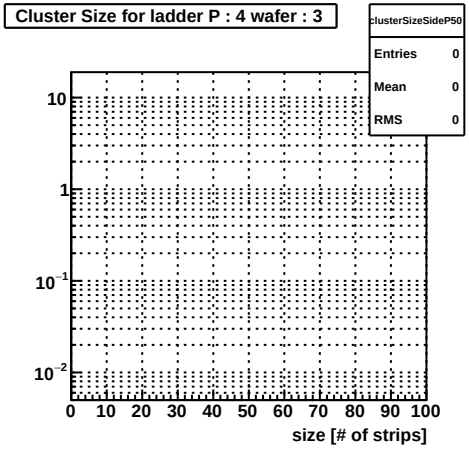

Cluster Size for ladder P : 4 wafer : 2
clusterSizeSideP49
Entries 0
Mean 0
RMS 0
size [# of strips]

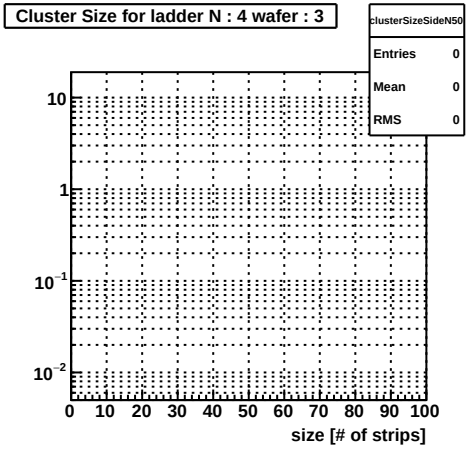

Cluster Size for ladder N : 4 wafer : 2
clusterSizeSideN49
Entries 0
Mean 0
RMS 0
size [# of strips]

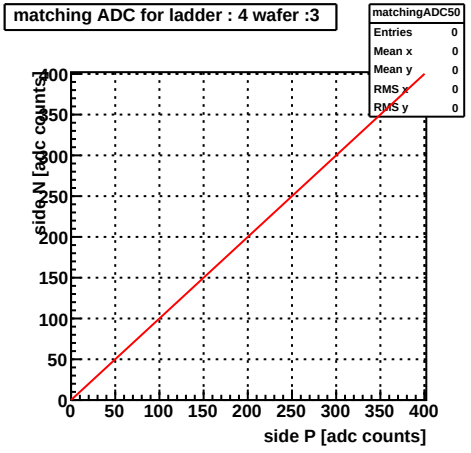

matching ADC for ladder : 4 wafer :2
matchingADC49
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

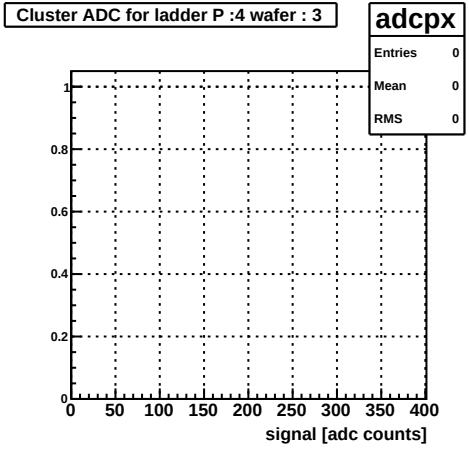

Cluster ADC for ladder P :4 wafer : 2
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

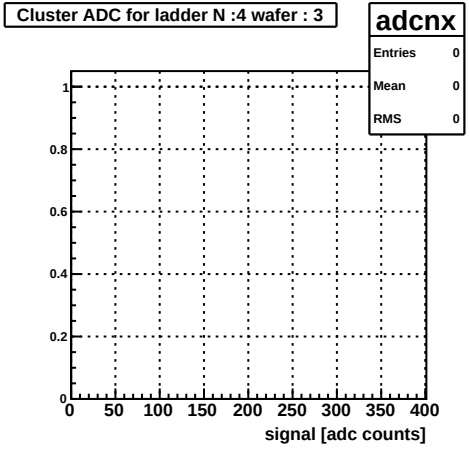

Cluster ADC for ladder N :4 wafer : 2
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

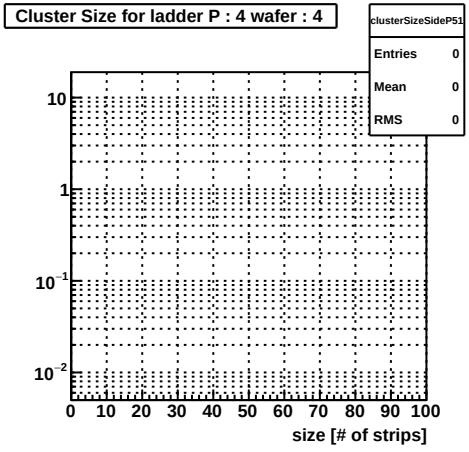

Cluster Size for ladder P : 4 wafer : 3
clusterSizeSideP50
Entries 0
Mean 0
RMS 0
size [# of strips]

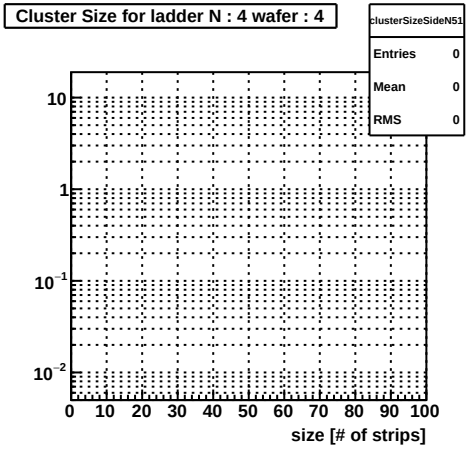

Cluster Size for ladder N : 4 wafer : 3
clusterSizeSideN50
Entries 0
Mean 0
RMS 0
size [# of strips]

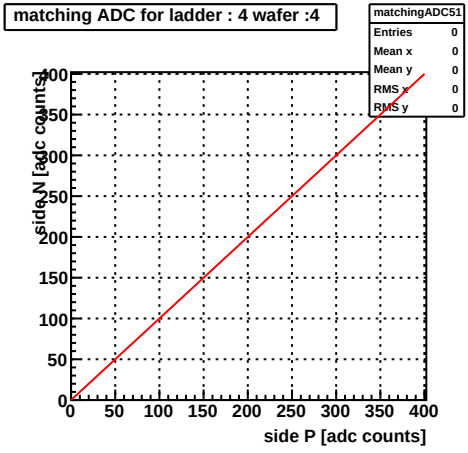

matching ADC for ladder : 4 wafer :3
matchingADC50
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

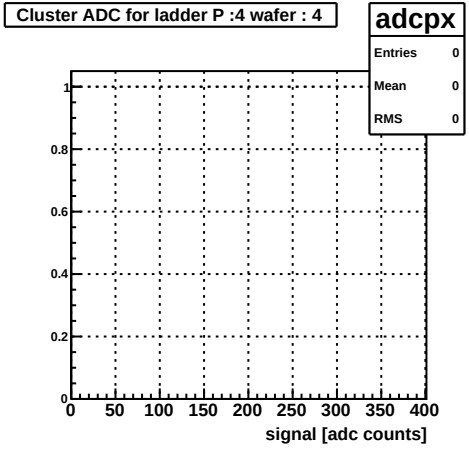

Cluster ADC for ladder P :4 wafer : 3
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

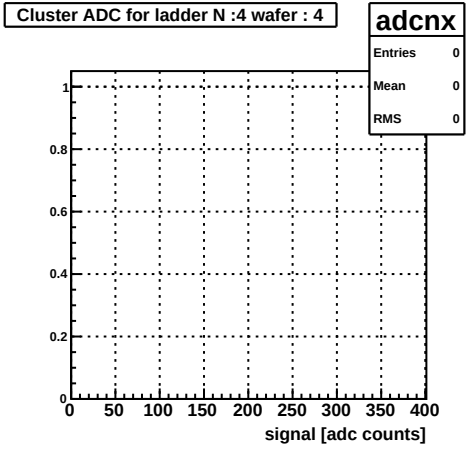

Cluster ADC for ladder N :4 wafer : 3
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

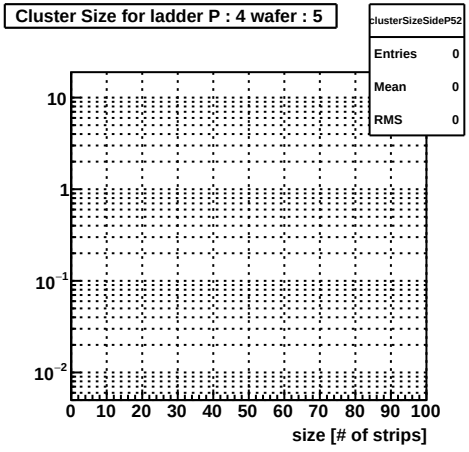

Cluster Size for ladder P : 4 wafer : 4
clusterSizeSideP51
Entries 0
Mean 0
RMS 0
size [# of strips]

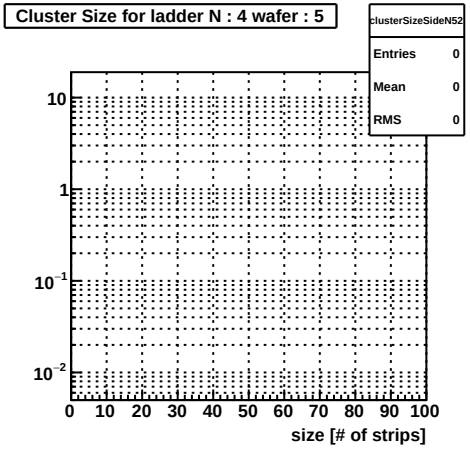

Cluster Size for ladder N : 4 wafer : 4
clusterSizeSideN51
Entries 0
Mean 0
RMS 0
size [# of strips]

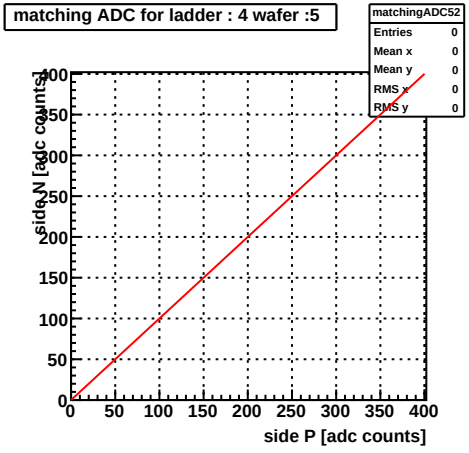

matching ADC for ladder : 4 wafer :4
matchingADC51
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

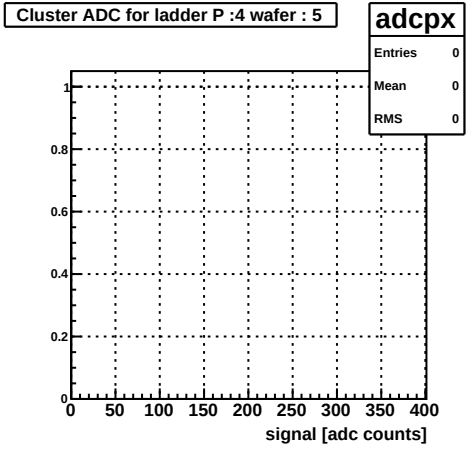

Cluster ADC for ladder P :4 wafer : 4
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

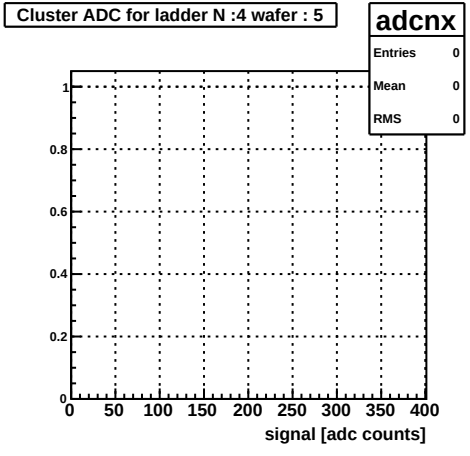

Cluster ADC for ladder N :4 wafer : 4
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

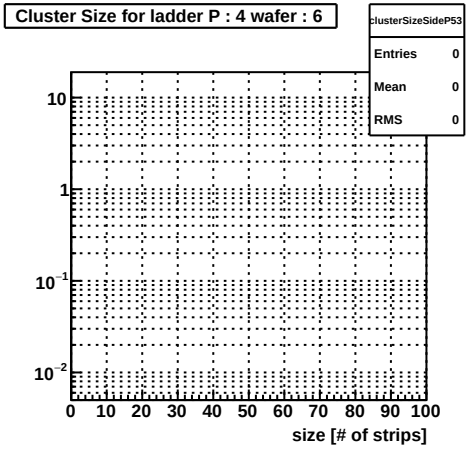

Cluster Size for ladder P : 4 wafer : 5
clusterSizeSideP52
Entries 0
Mean 0
RMS 0
size [# of strips]

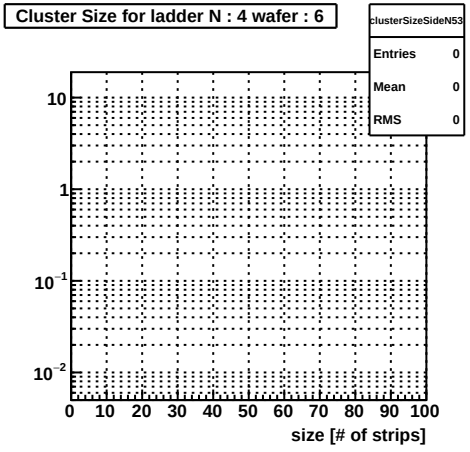

Cluster Size for ladder N : 4 wafer : 5
clusterSizeSideN52
Entries 0
Mean 0
RMS 0
size [# of strips]

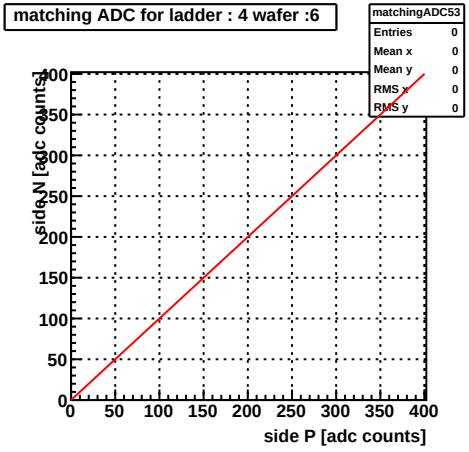

matching ADC for ladder : 4 wafer :5
matchingADC52
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

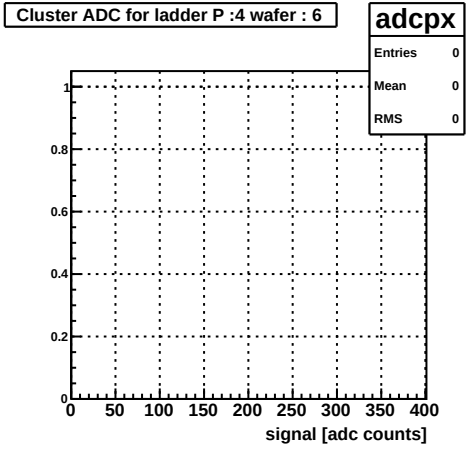

Cluster ADC for ladder P :4 wafer : 5
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

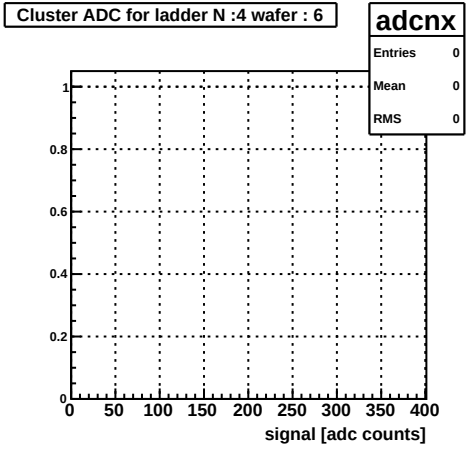

Cluster ADC for ladder N :4 wafer : 5
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

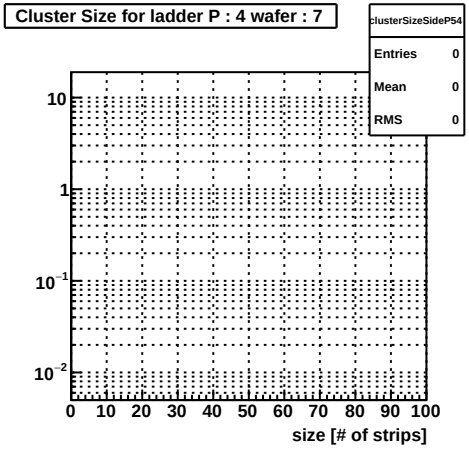

Cluster Size for ladder P : 4 wafer : 6
clusterSizeSideP53
Entries 0
Mean 0
RMS 0
size [# of strips]

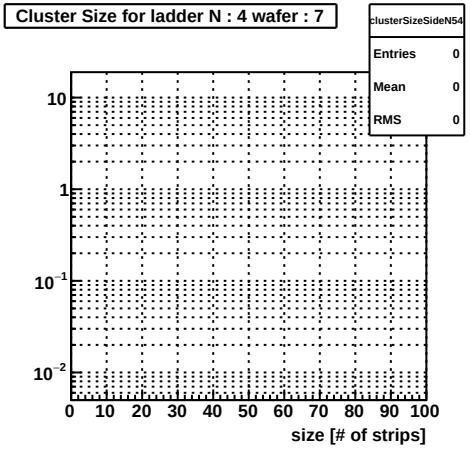

Cluster Size for ladder N : 4 wafer : 6
clusterSizeSideN53
Entries 0
Mean 0
RMS 0
size [# of strips]

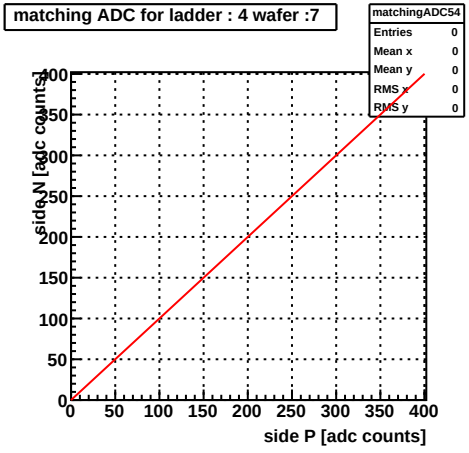

matching ADC for ladder : 4 wafer :6
matchingADC53
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

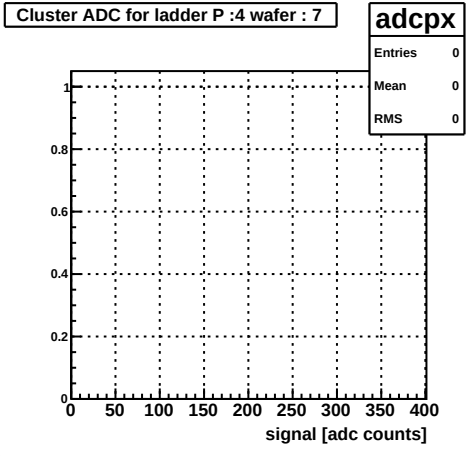

Cluster ADC for ladder P :4 wafer : 6
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

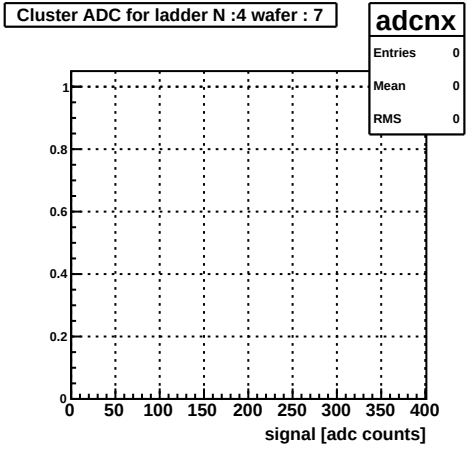

Cluster ADC for ladder N :4 wafer : 6
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

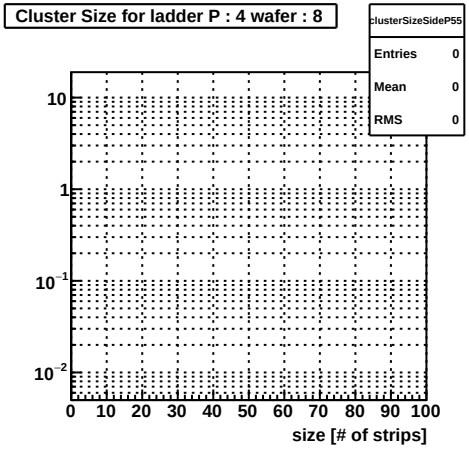

Cluster Size for ladder P : 4 wafer : 7
clusterSizeSideP54
Entries 0
Mean 0
RMS 0
size [# of strips]

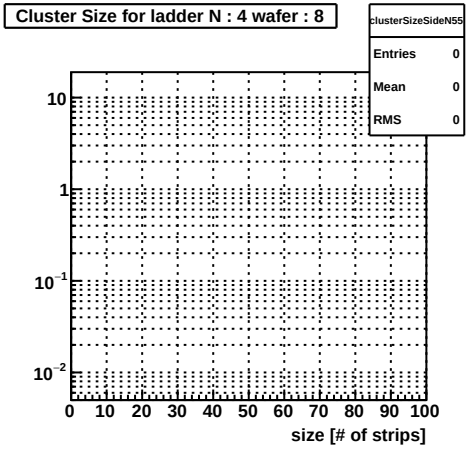

Cluster Size for ladder N : 4 wafer : 7
clusterSizeSideN54
Entries 0
Mean 0
RMS 0
size [# of strips]

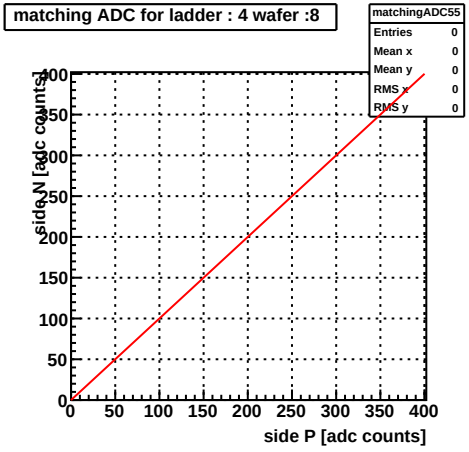

matching ADC for ladder : 4 wafer :7
matchingADC54
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

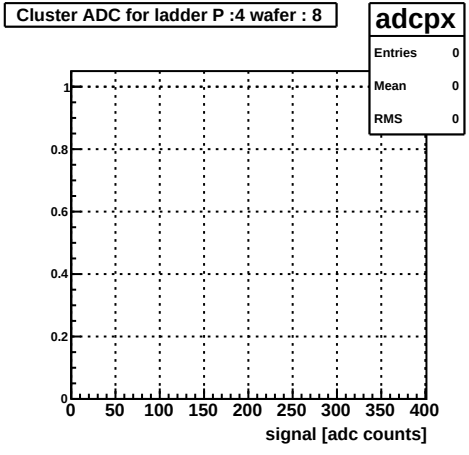

Cluster ADC for ladder P :4 wafer : 7
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

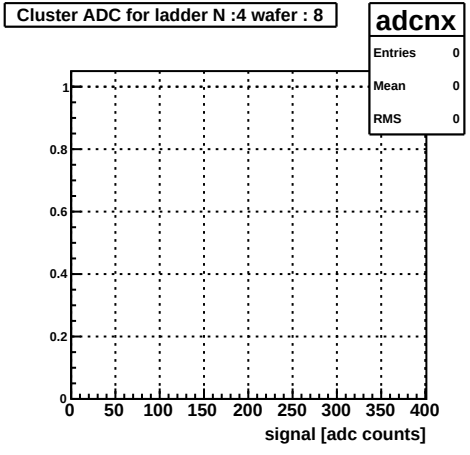

Cluster ADC for ladder N :4 wafer : 7
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

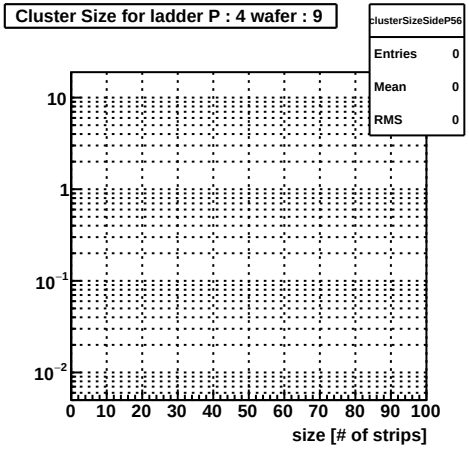

Cluster Size for ladder P : 4 wafer : 8
clusterSizeSideP55
Entries 0
Mean 0
RMS 0
size [# of strips]

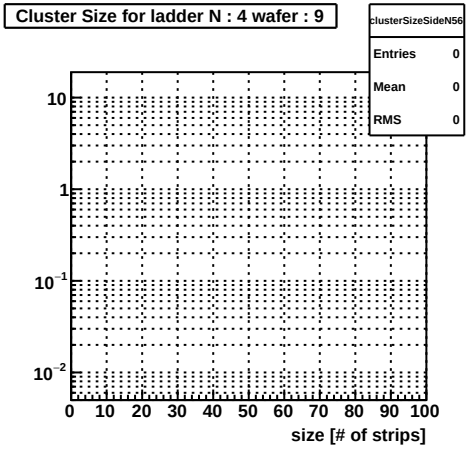

Cluster Size for ladder N : 4 wafer : 8
clusterSizeSideN55
Entries 0
Mean 0
RMS 0
size [# of strips]

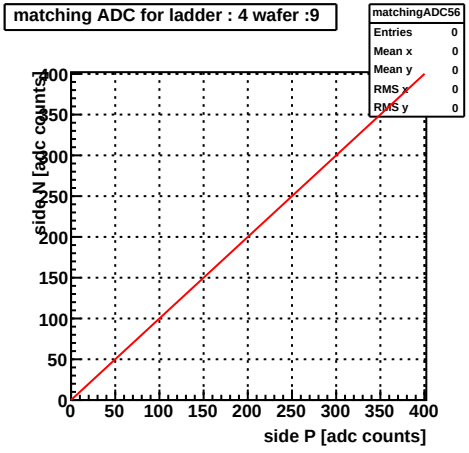

matching ADC for ladder : 4 wafer :8
matchingADC55
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

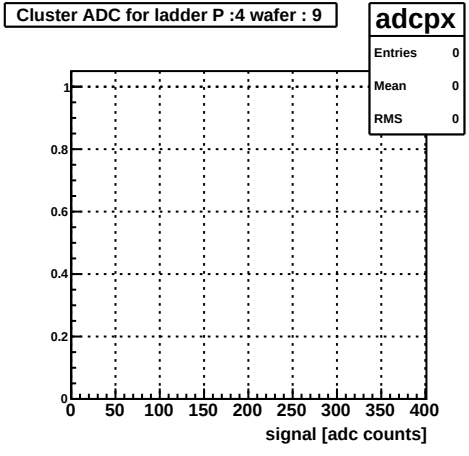

Cluster ADC for ladder P :4 wafer : 8
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

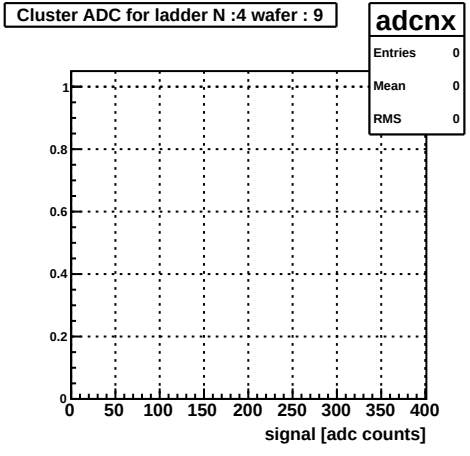

Cluster ADC for ladder N :4 wafer : 8
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

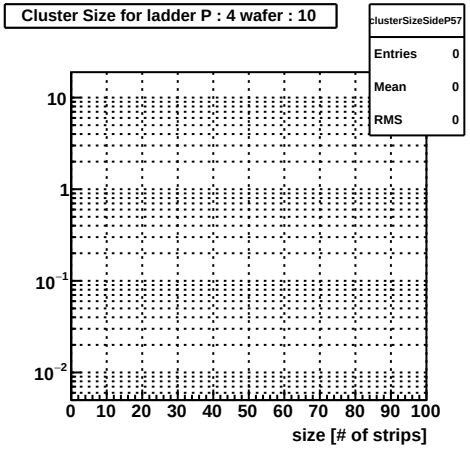

Cluster Size for ladder P : 4 wafer : 9
clusterSizeSideP56
Entries 0
Mean 0
RMS 0
size [# of strips]

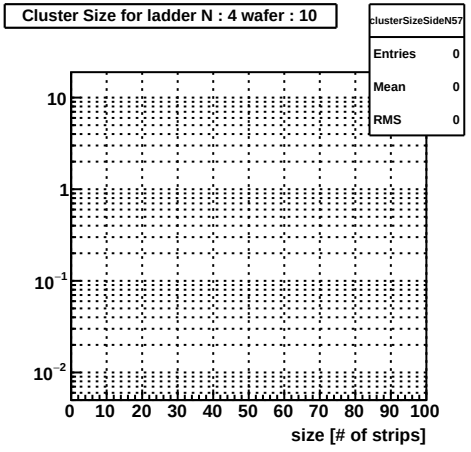

Cluster Size for ladder N : 4 wafer : 9
clusterSizeSideN56
Entries 0
Mean 0
RMS 0
size [# of strips]

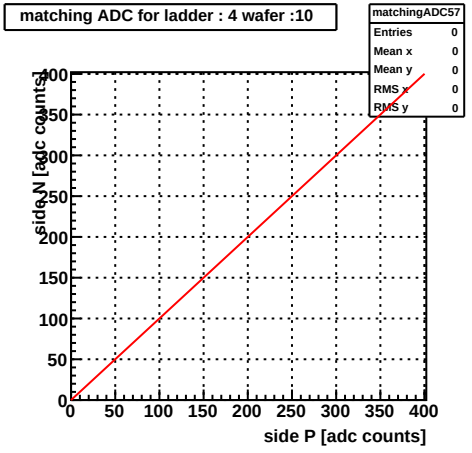

matching ADC for ladder : 4 wafer :9
matchingADC56
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

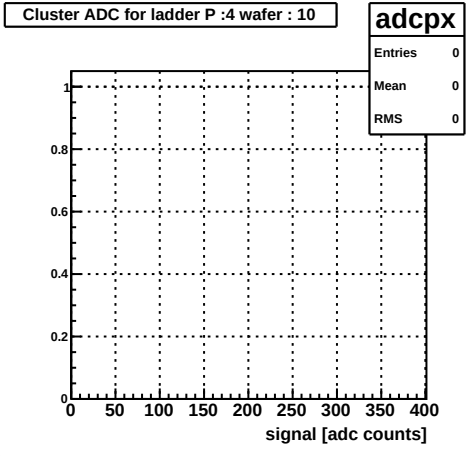

Cluster ADC for ladder P :4 wafer : 9
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

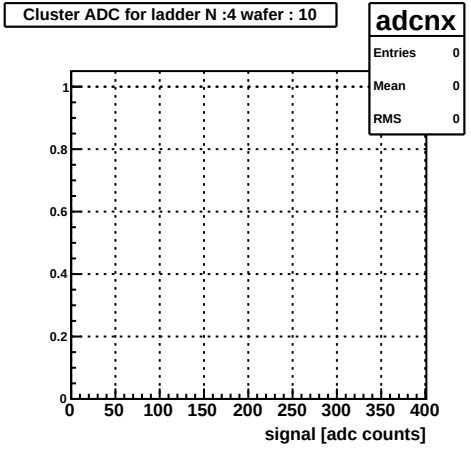

Cluster ADC for ladder N :4 wafer : 9
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

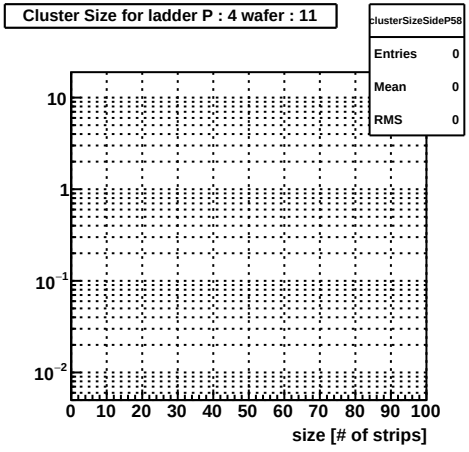

Cluster Size for ladder P : 4 wafer : 10
clusterSizeSideP57
Entries 0
Mean 0
RMS 0
size [# of strips]

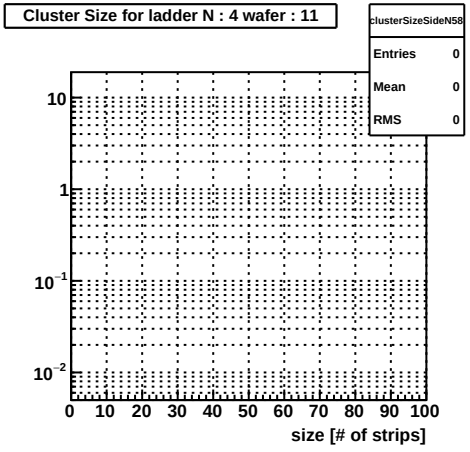

Cluster Size for ladder N : 4 wafer : 10
clusterSizeSideN57
Entries 0
Mean 0
RMS 0
size [# of strips]

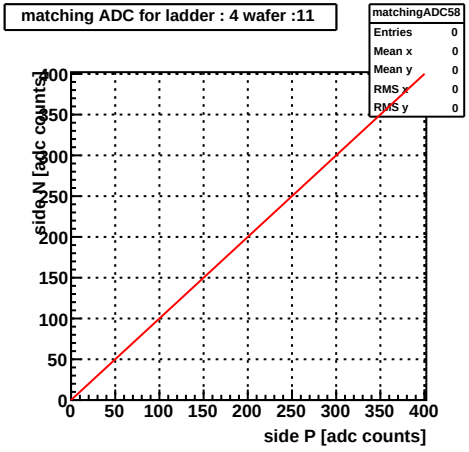

matching ADC for ladder : 4 wafer :10
matchingADC57
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

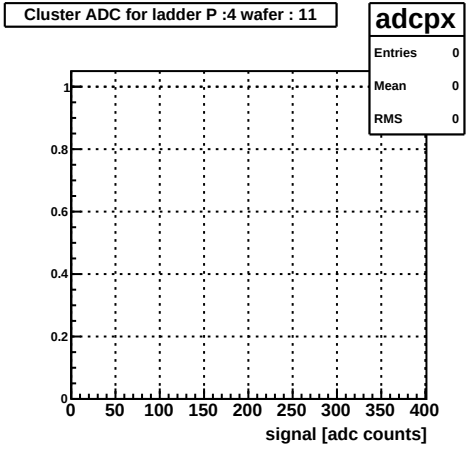

Cluster ADC for ladder P :4 wafer : 10
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

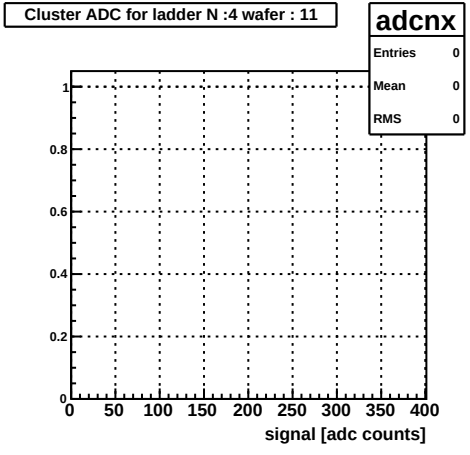

Cluster ADC for ladder N :4 wafer : 10
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

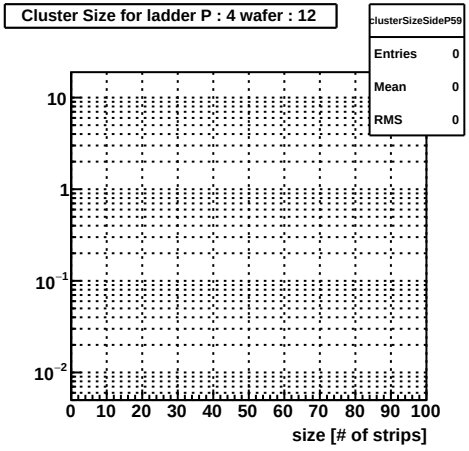

Cluster Size for ladder P : 4 wafer : 11
clusterSizeSideP58
Entries 0
Mean 0
RMS 0
size [# of strips]

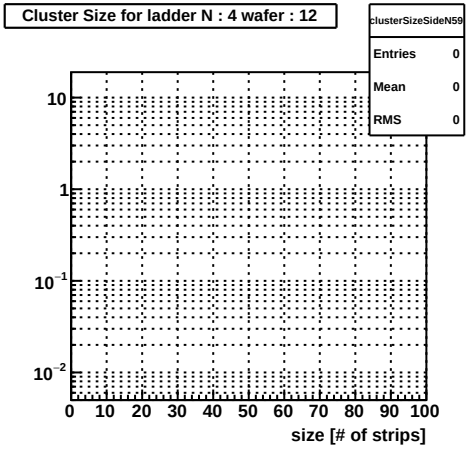

Cluster Size for ladder N : 4 wafer : 11
clusterSizeSideN58
Entries 0
Mean 0
RMS 0
size [# of strips]

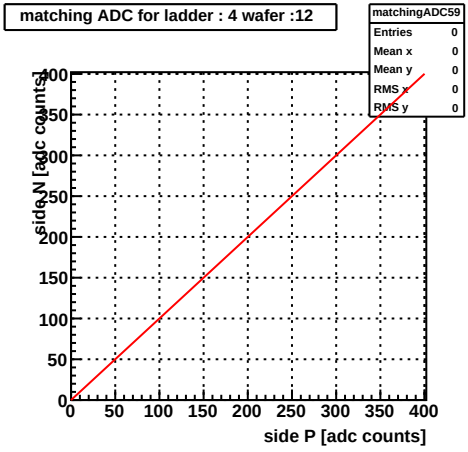

matching ADC for ladder : 4 wafer :11
matchingADC58
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

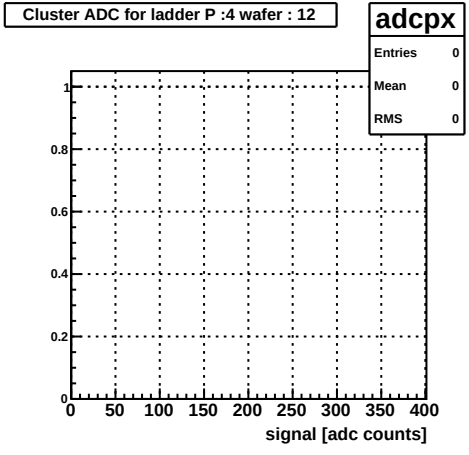

Cluster ADC for ladder P :4 wafer : 11
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

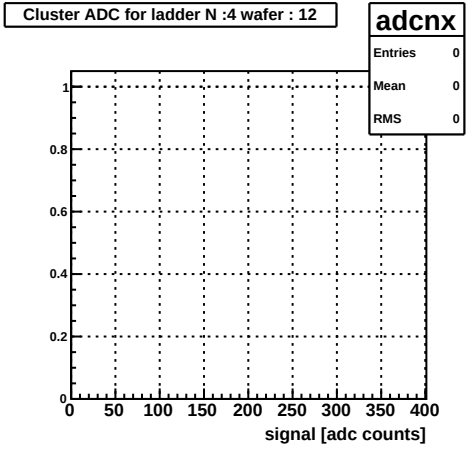

Cluster ADC for ladder N :4 wafer : 11
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

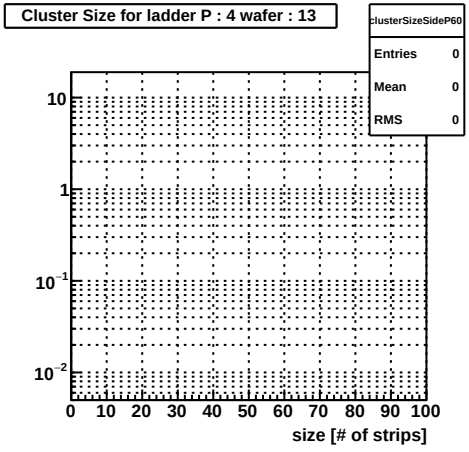

Cluster Size for ladder P : 4 wafer : 12
clusterSizeSideP59
Entries 0
Mean 0
RMS 0
size [# of strips]

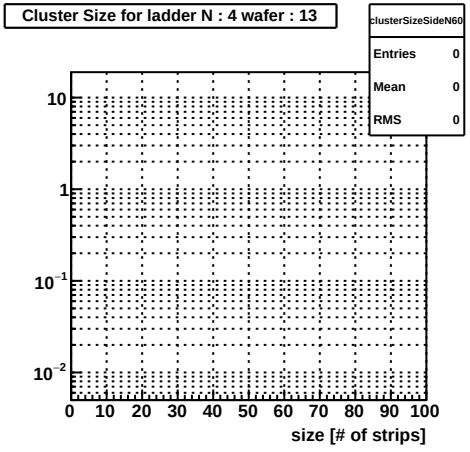

Cluster Size for ladder N : 4 wafer : 12
clusterSizeSideN59
Entries 0
Mean 0
RMS 0
size [# of strips]

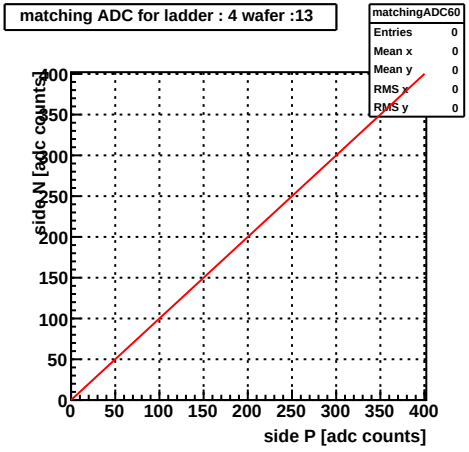

matching ADC for ladder : 4 wafer :12
matchingADC59
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

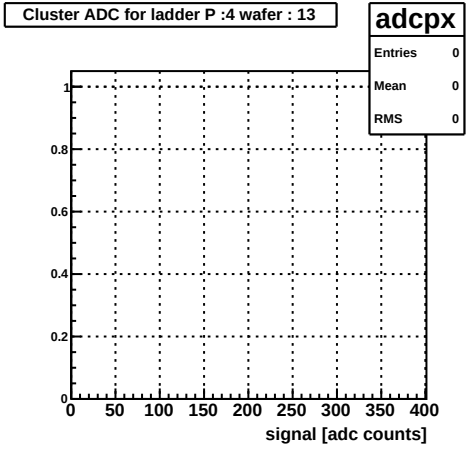

Cluster ADC for ladder P :4 wafer : 12
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

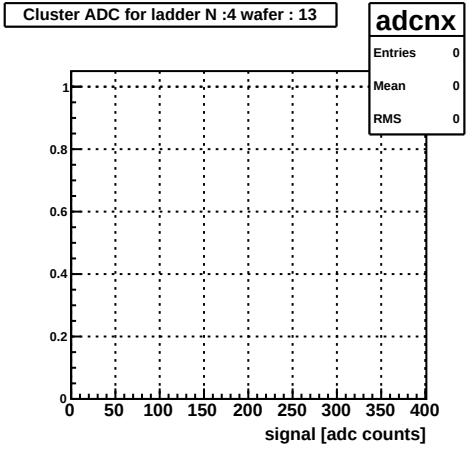

Cluster ADC for ladder N :4 wafer : 12
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

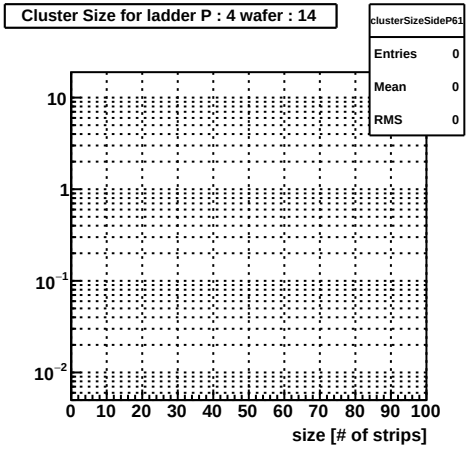

Cluster Size for ladder P : 4 wafer : 13
clusterSizeSideP60
Entries 0
Mean 0
RMS 0
size [# of strips]

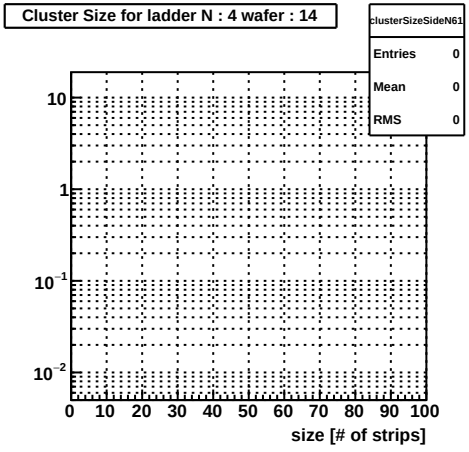

Cluster Size for ladder N : 4 wafer : 13
clusterSizeSideN60
Entries 0
Mean 0
RMS 0
size [# of strips]

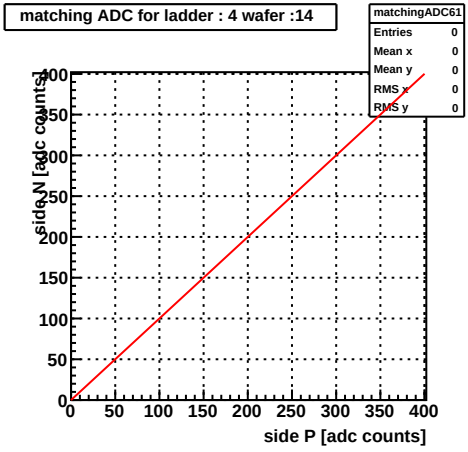

matching ADC for ladder : 4 wafer :13
matchingADC60
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

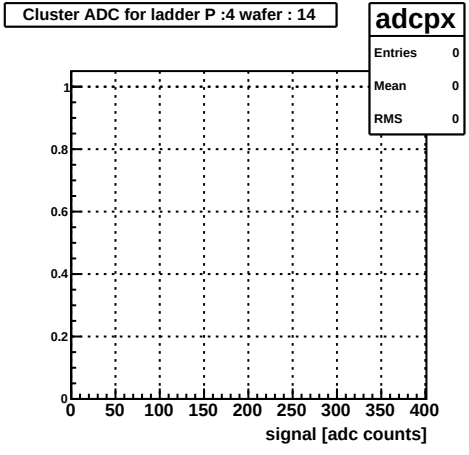

Cluster ADC for ladder P :4 wafer : 13
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

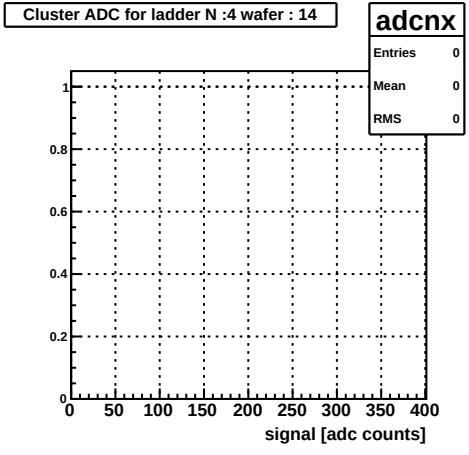

Cluster ADC for ladder N :4 wafer : 13
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

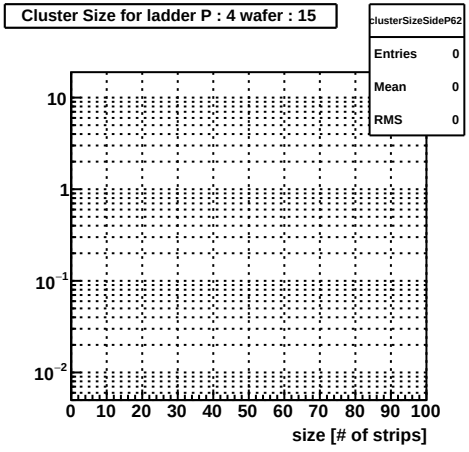

Cluster Size for ladder P : 4 wafer : 14
clusterSizeSideP61
Entries 0
Mean 0
RMS 0
size [# of strips]

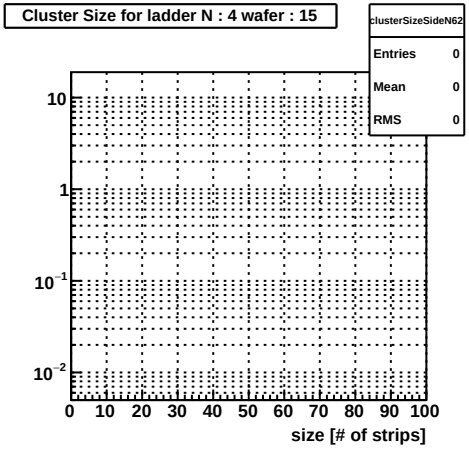

Cluster Size for ladder N : 4 wafer : 14
clusterSizeSideN61
Entries 0
Mean 0
RMS 0
size [# of strips]

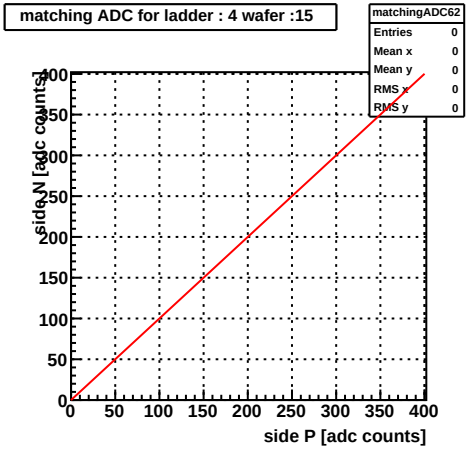

matching ADC for ladder : 4 wafer :14
matchingADC61
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

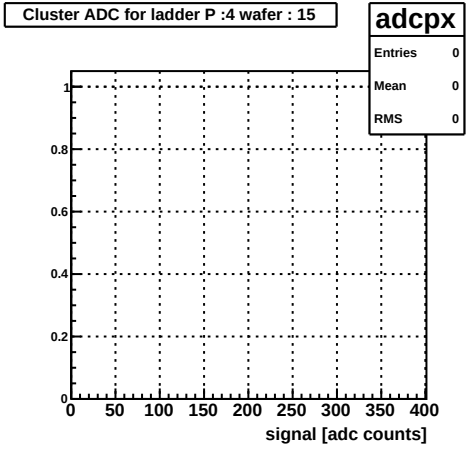

Cluster ADC for ladder P :4 wafer : 14
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

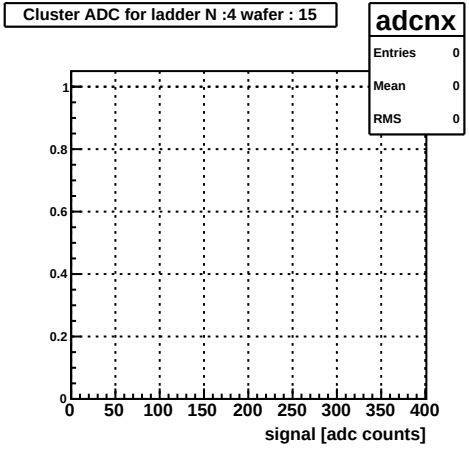

Cluster ADC for ladder N :4 wafer : 14
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

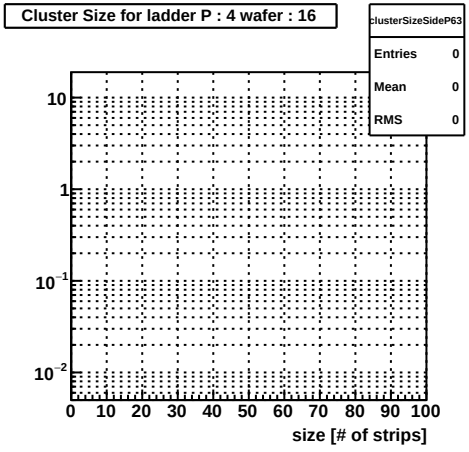

Cluster Size for ladder P : 4 wafer : 15
clusterSizeSideP62
Entries 0
Mean 0
RMS 0
size [# of strips]

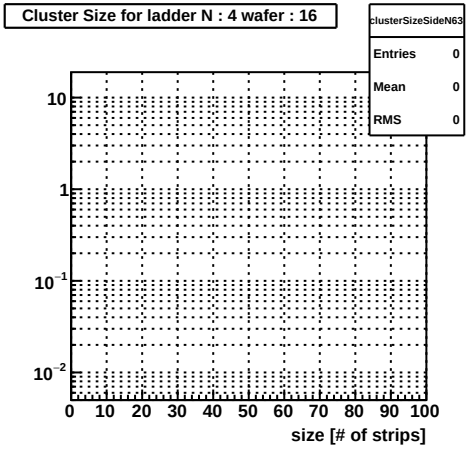

Cluster Size for ladder N : 4 wafer : 15
clusterSizeSideN62
Entries 0
Mean 0
RMS 0
size [# of strips]

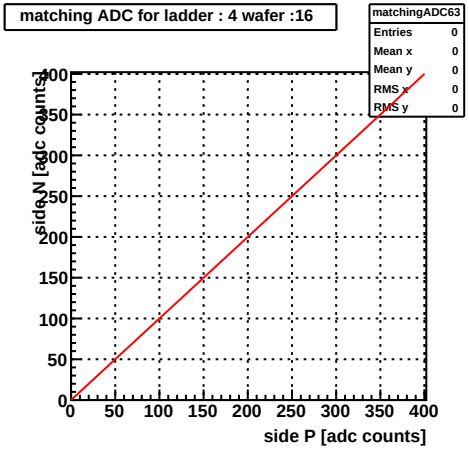

matching ADC for ladder : 4 wafer :15
matchingADC62
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

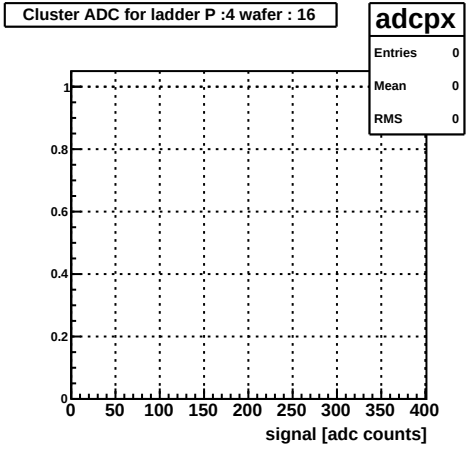

Cluster ADC for ladder P :4 wafer : 15
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

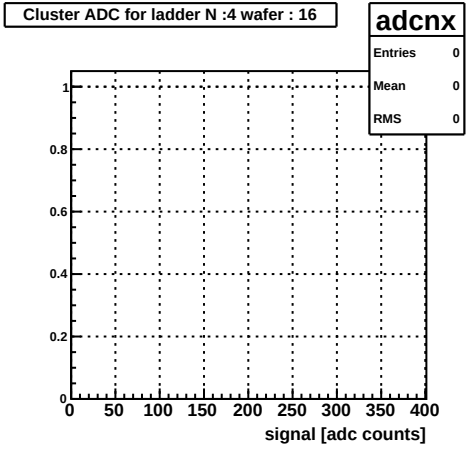

Cluster ADC for ladder N :4 wafer : 15
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

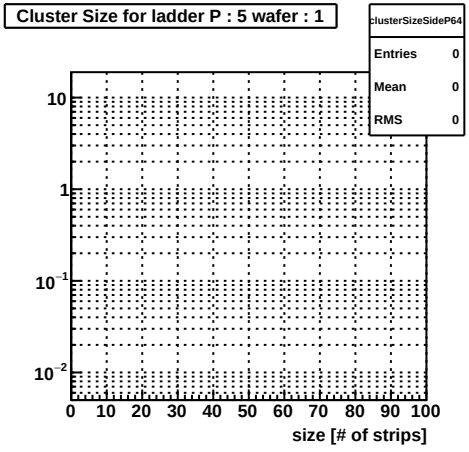

Cluster Size for ladder P : 4 wafer : 16
clusterSizeSideP63
Entries 0
Mean 0
RMS 0
size [# of strips]

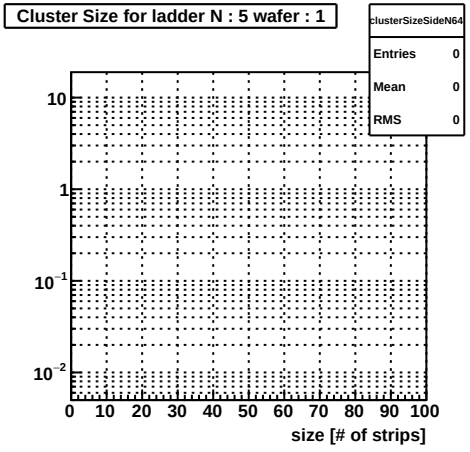

Cluster Size for ladder N : 4 wafer : 16
clusterSizeSideN63
Entries 0
Mean 0
RMS 0
size [# of strips]

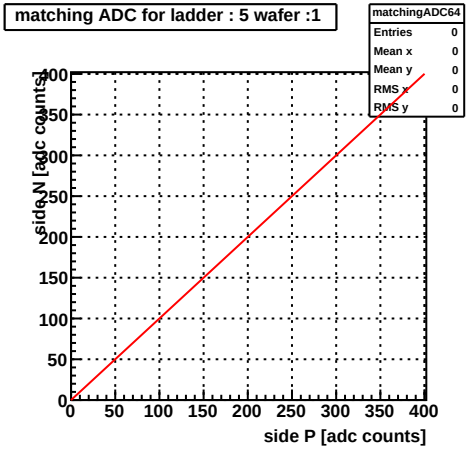

matching ADC for ladder : 4 wafer :16
matchingADC63
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

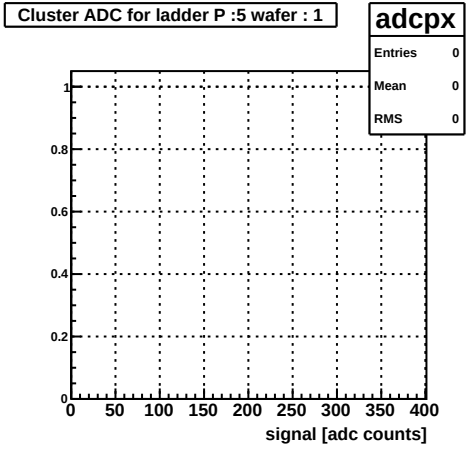

Cluster ADC for ladder P :4 wafer : 16
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

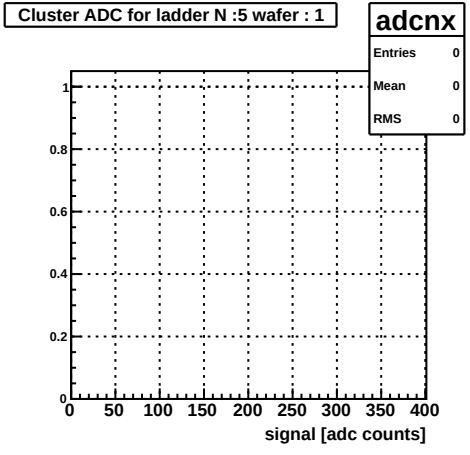

Cluster ADC for ladder N :4 wafer : 16
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

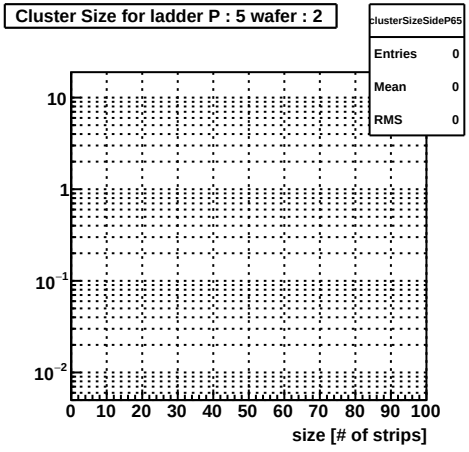

Cluster Size for ladder P : 5 wafer : 1
clusterSizeSideP64
Entries 0
Mean 0
RMS 0
size [# of strips]

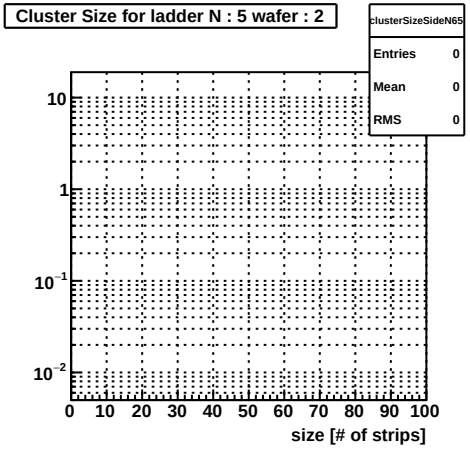

Cluster Size for ladder N : 5 wafer : 1
clusterSizeSideN64
Entries 0
Mean 0
RMS 0
size [# of strips]

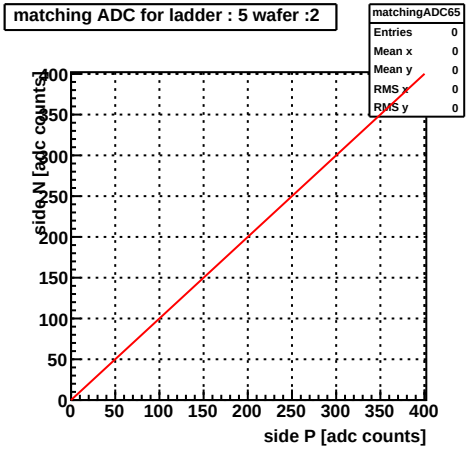

matching ADC for ladder : 5 wafer :1
matchingADC64
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

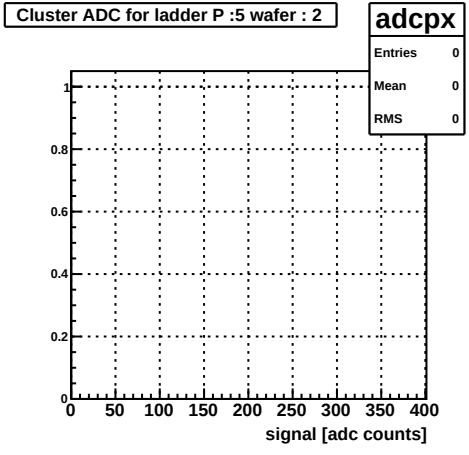

Cluster ADC for ladder P :5 wafer : 1
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

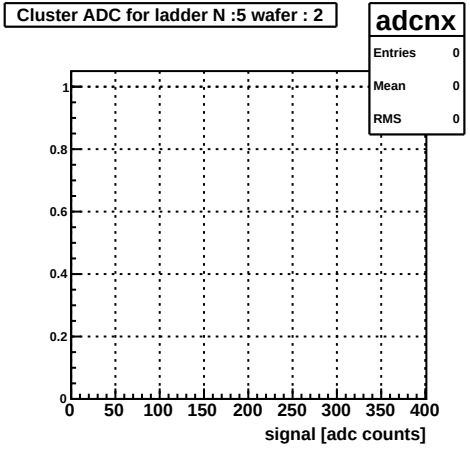

Cluster ADC for ladder N :5 wafer : 1
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

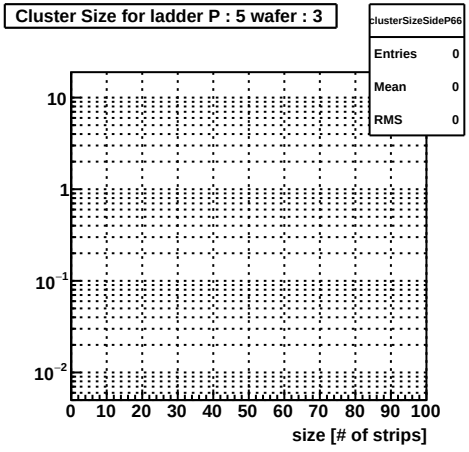

Cluster Size for ladder P : 5 wafer : 2
clusterSizeSideP65
Entries 0
Mean 0
RMS 0
size [# of strips]

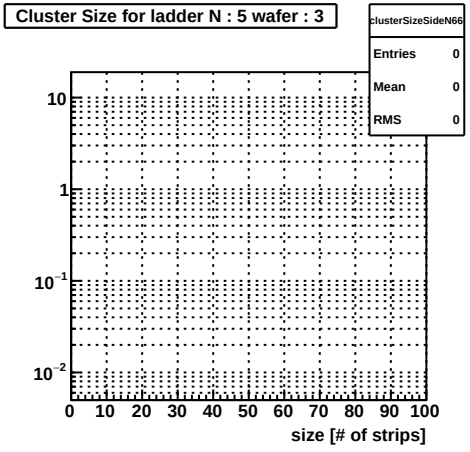

Cluster Size for ladder N : 5 wafer : 2
clusterSizeSideN65
Entries 0
Mean 0
RMS 0
size [# of strips]

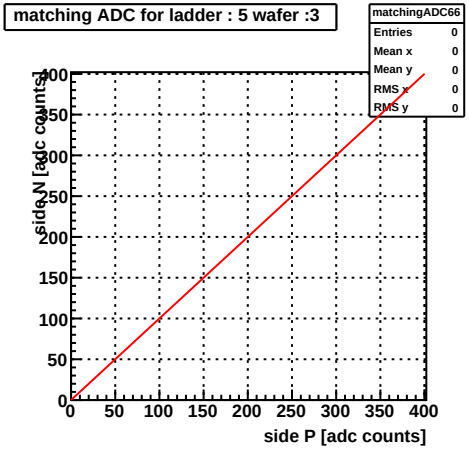

matching ADC for ladder : 5 wafer :2
matchingADC65
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

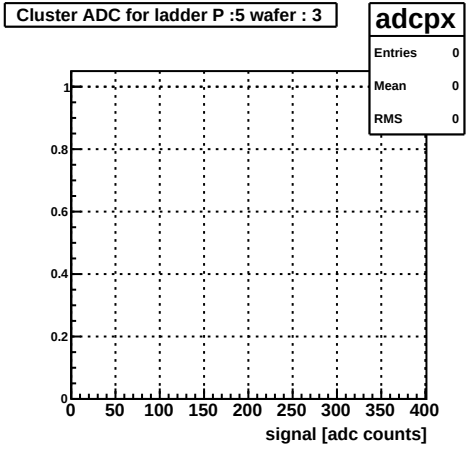

Cluster ADC for ladder P :5 wafer : 2
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

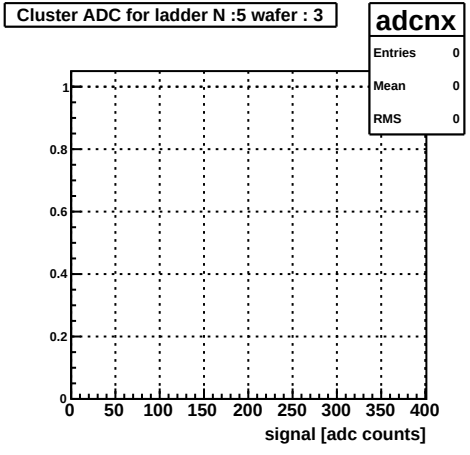

Cluster ADC for ladder N :5 wafer : 2
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

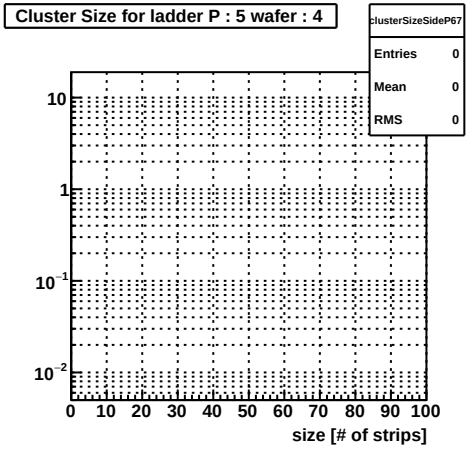

Cluster Size for ladder P : 5 wafer : 3
clusterSizeSideP66
Entries 0
Mean 0
RMS 0
size [# of strips]

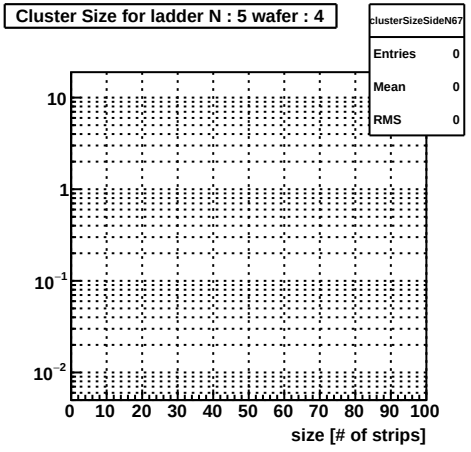

Cluster Size for ladder N : 5 wafer : 3
clusterSizeSideN66
Entries 0
Mean 0
RMS 0
size [# of strips]

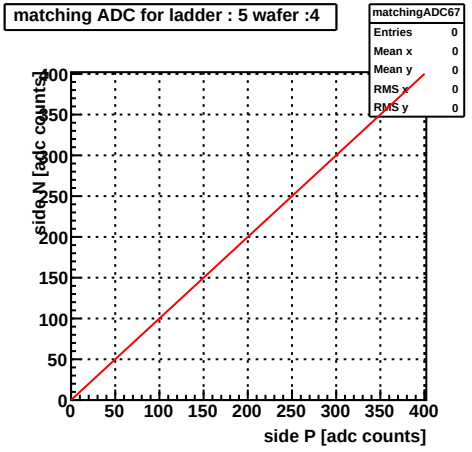

matching ADC for ladder : 5 wafer :3
matchingADC66
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

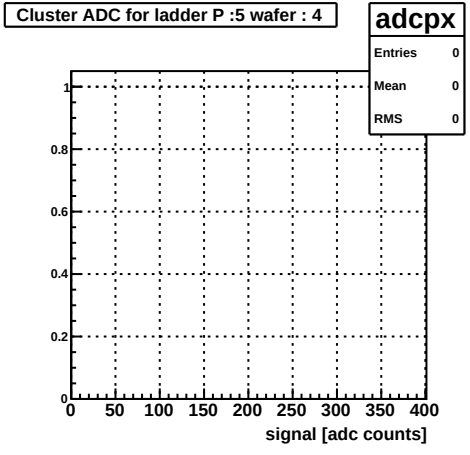

Cluster ADC for ladder P :5 wafer : 3
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

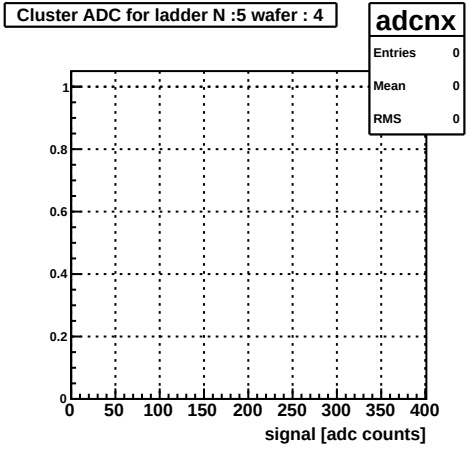

Cluster ADC for ladder N :5 wafer : 3
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

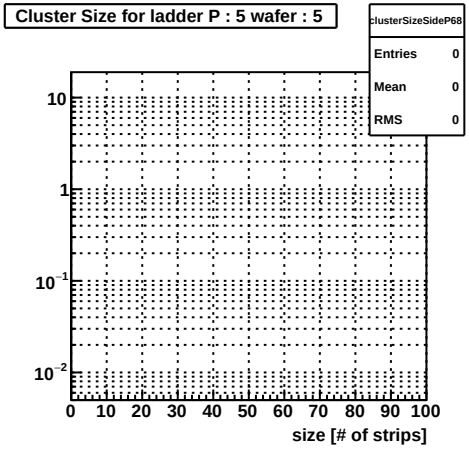

Cluster Size for ladder P : 5 wafer : 4
clusterSizeSideP67
Entries 0
Mean 0
RMS 0
size [# of strips]

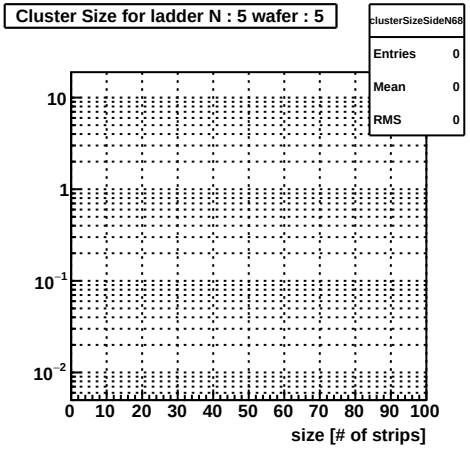

Cluster Size for ladder N : 5 wafer : 4
clusterSizeSideN67
Entries 0
Mean 0
RMS 0
size [# of strips]

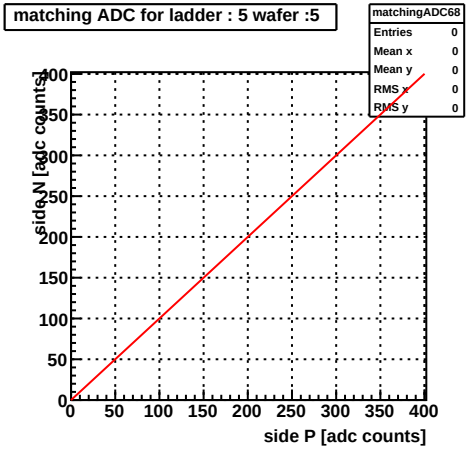

matching ADC for ladder : 5 wafer :4
matchingADC67
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

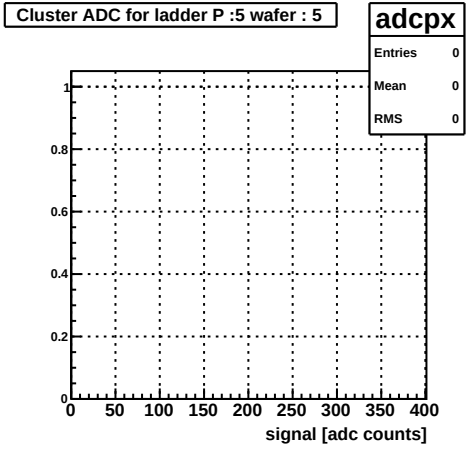

Cluster ADC for ladder P :5 wafer : 4
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

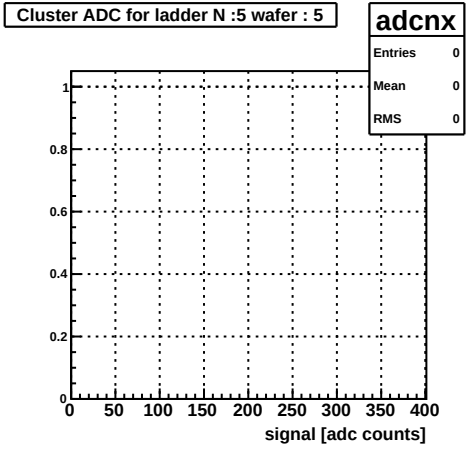

Cluster ADC for ladder N :5 wafer : 4
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

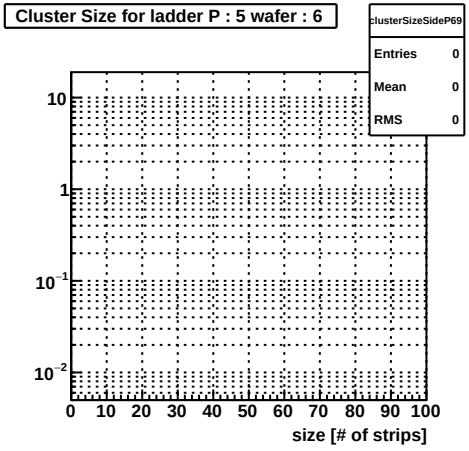

Cluster Size for ladder P : 5 wafer : 5
clusterSizeSideP68
Entries 0
Mean 0
RMS 0
size [# of strips]

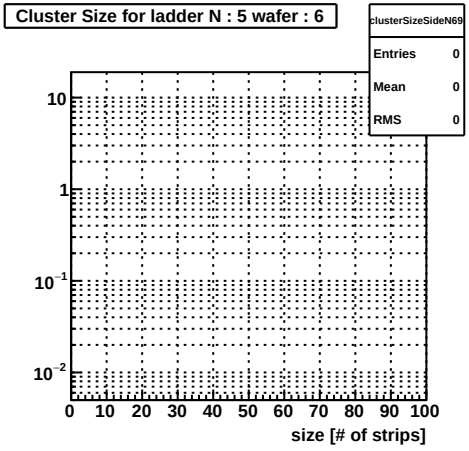

Cluster Size for ladder N : 5 wafer : 5
clusterSizeSideN68
Entries 0
Mean 0
RMS 0
size [# of strips]

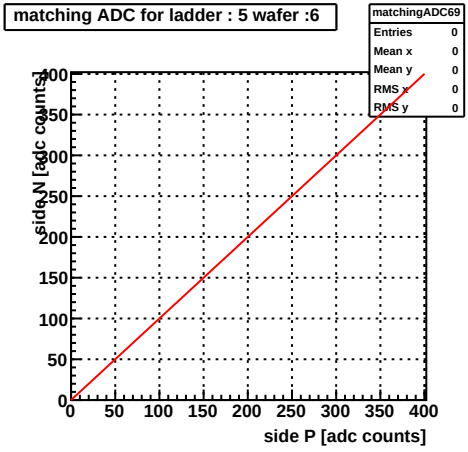

matching ADC for ladder : 5 wafer :5
matchingADC68
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

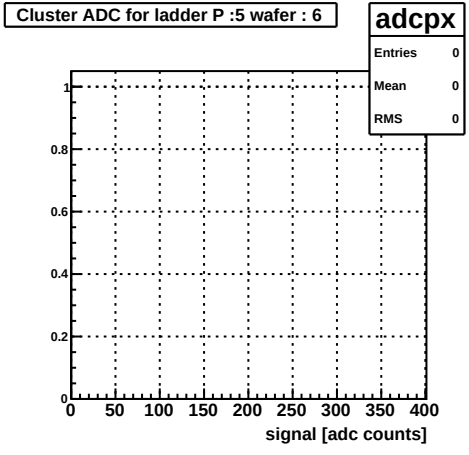

Cluster ADC for ladder P :5 wafer : 5
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

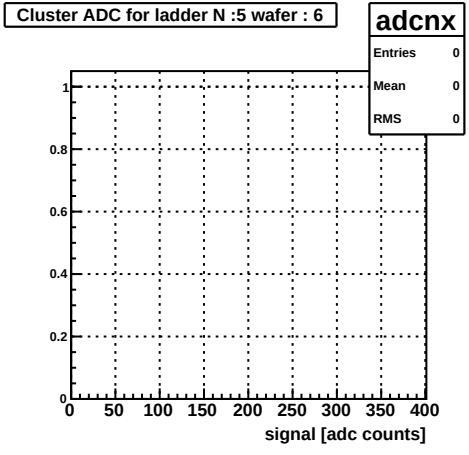

Cluster ADC for ladder N :5 wafer : 5
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

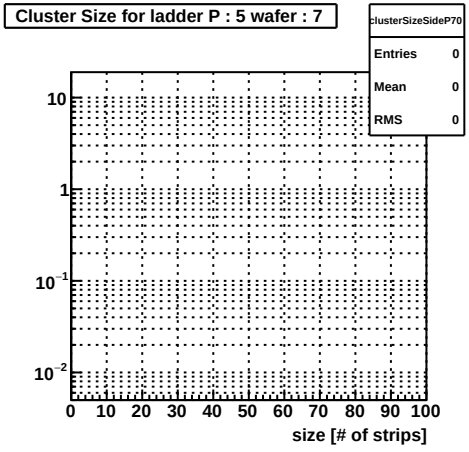

Cluster Size for ladder P : 5 wafer : 6
clusterSizeSideP69
Entries 0
Mean 0
RMS 0
size [# of strips]

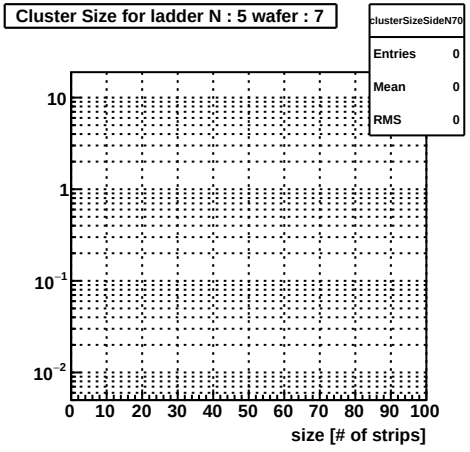

Cluster Size for ladder N : 5 wafer : 6
clusterSizeSideN69
Entries 0
Mean 0
RMS 0
size [# of strips]

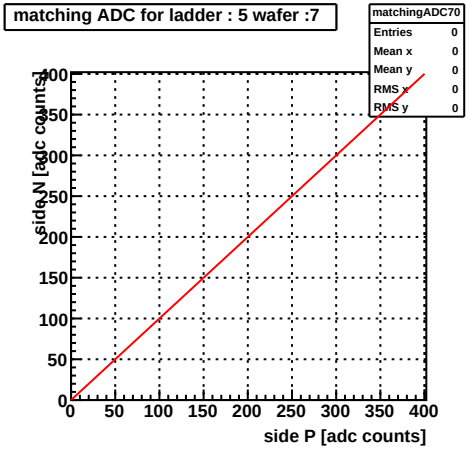

matching ADC for ladder : 5 wafer :6
matchingADC69
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

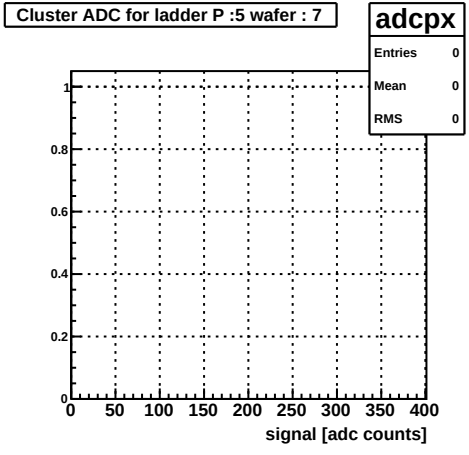

Cluster ADC for ladder P :5 wafer : 6
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

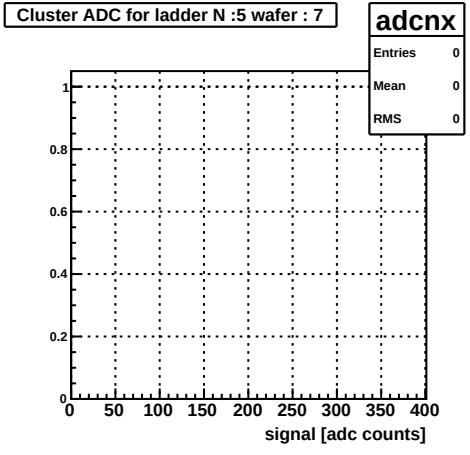

Cluster ADC for ladder N :5 wafer : 6
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

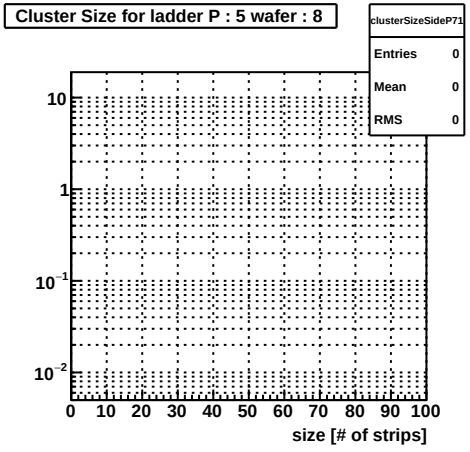

Cluster Size for ladder P : 5 wafer : 7
clusterSizeSideP70
Entries 0
Mean 0
RMS 0
size [# of strips]

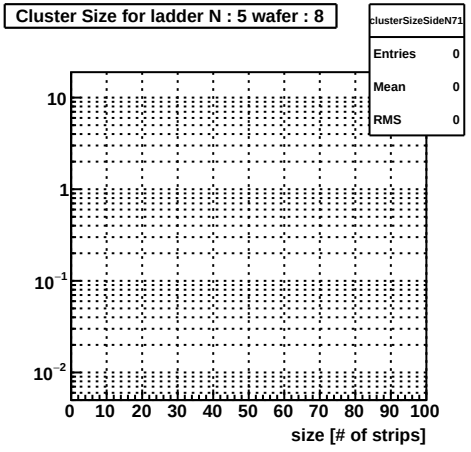

Cluster Size for ladder N : 5 wafer : 7
clusterSizeSideN70
Entries 0
Mean 0
RMS 0
size [# of strips]

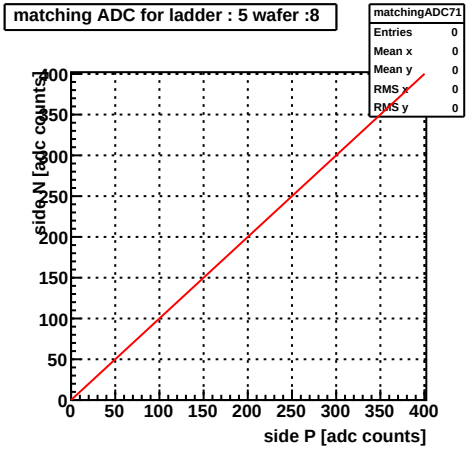

matching ADC for ladder : 5 wafer :7
matchingADC70
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

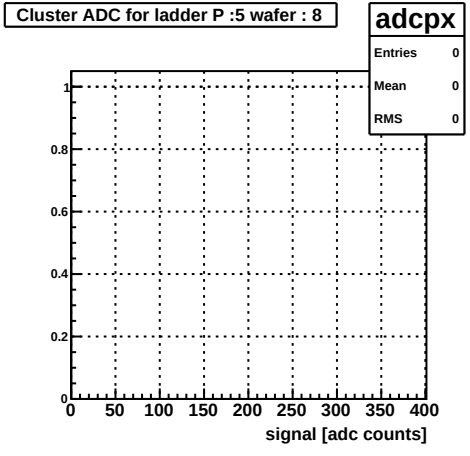

Cluster ADC for ladder P :5 wafer : 7
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

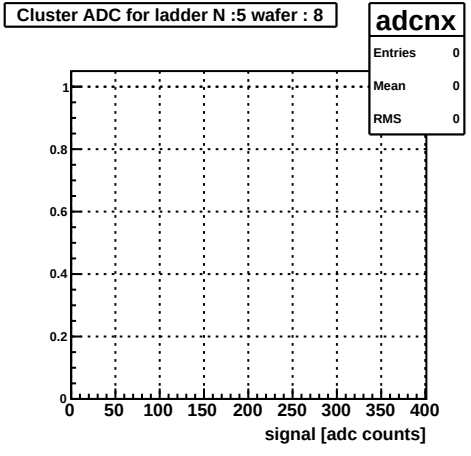

Cluster ADC for ladder N :5 wafer : 7
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

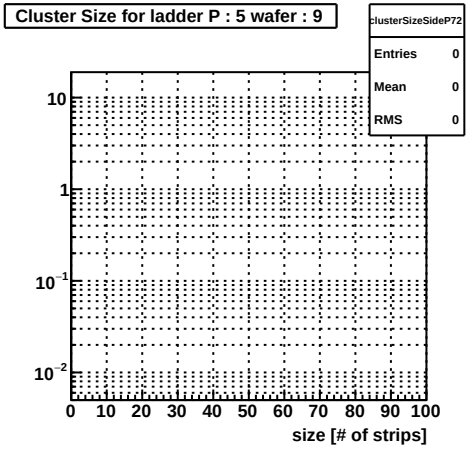

Cluster Size for ladder P : 5 wafer : 8
clusterSizeSideP71
Entries 0
Mean 0
RMS 0
size [# of strips]

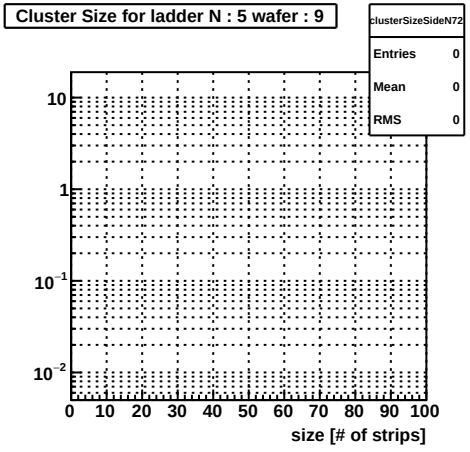

Cluster Size for ladder N : 5 wafer : 8
clusterSizeSideN71
Entries 0
Mean 0
RMS 0
size [# of strips]

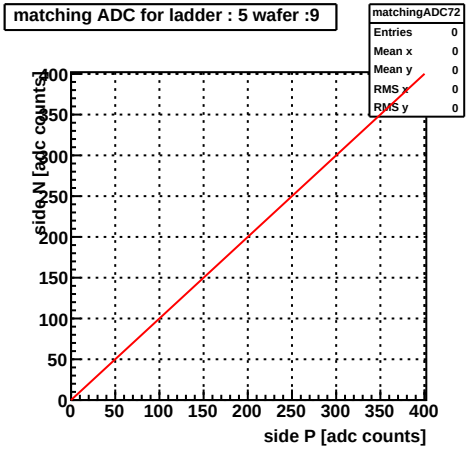

matching ADC for ladder : 5 wafer :8
matchingADC71
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

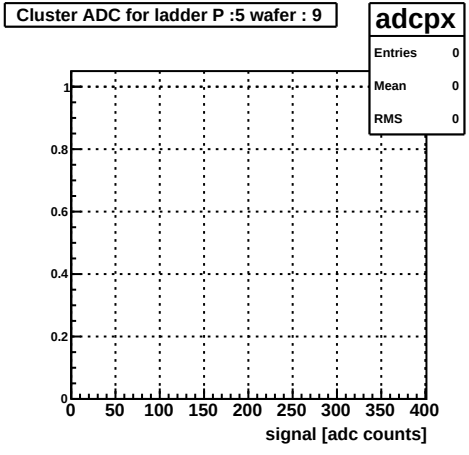

Cluster ADC for ladder P :5 wafer : 8
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

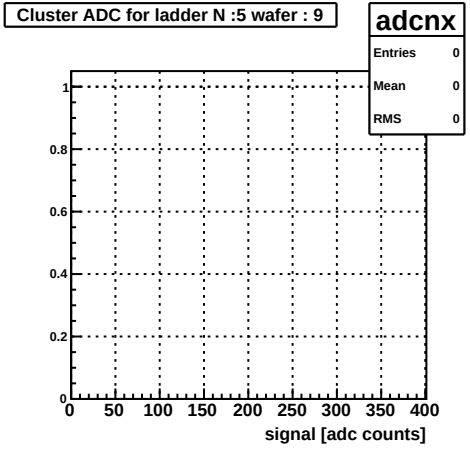

Cluster ADC for ladder N :5 wafer : 8
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

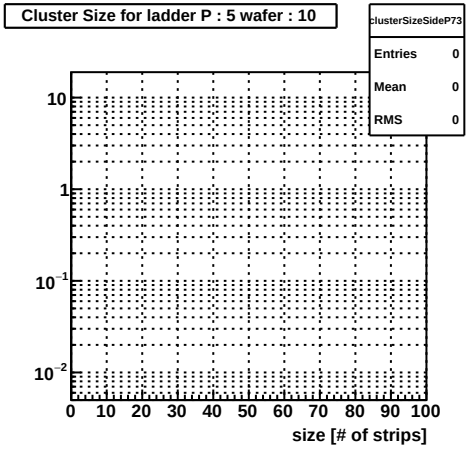

Cluster Size for ladder P : 5 wafer : 9
clusterSizeSideP72
Entries 0
Mean 0
RMS 0
size [# of strips]

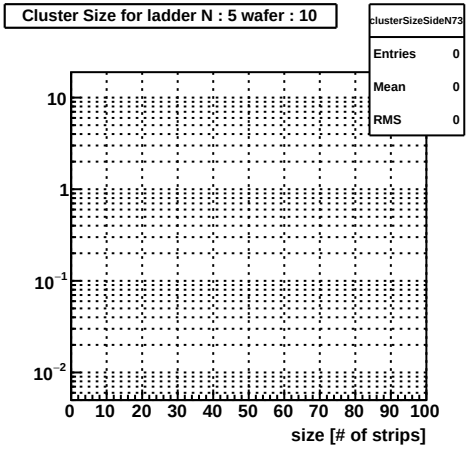

Cluster Size for ladder N : 5 wafer : 9
clusterSizeSideN72
Entries 0
Mean 0
RMS 0
size [# of strips]

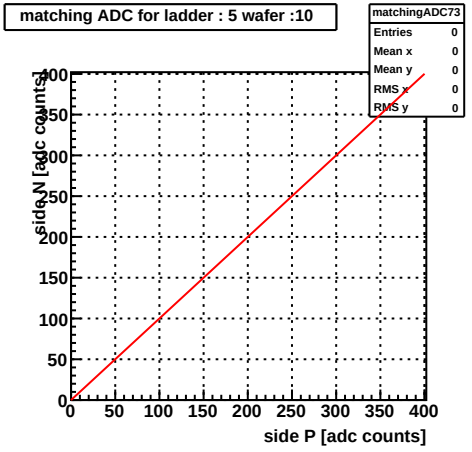

matching ADC for ladder : 5 wafer :9
matchingADC72
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

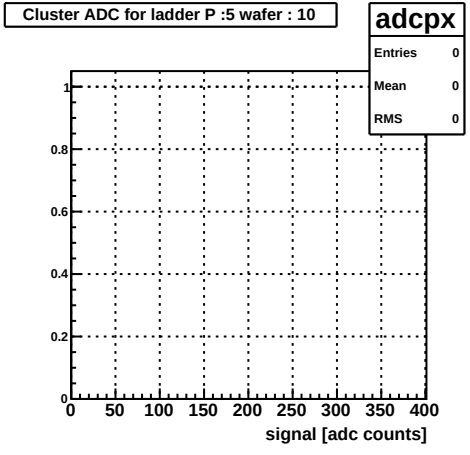

Cluster ADC for ladder P :5 wafer : 9
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

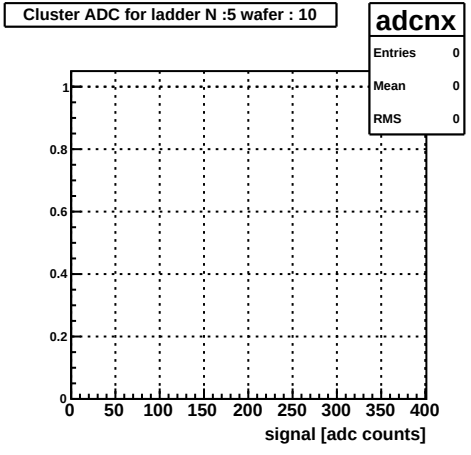

Cluster ADC for ladder N :5 wafer : 9
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

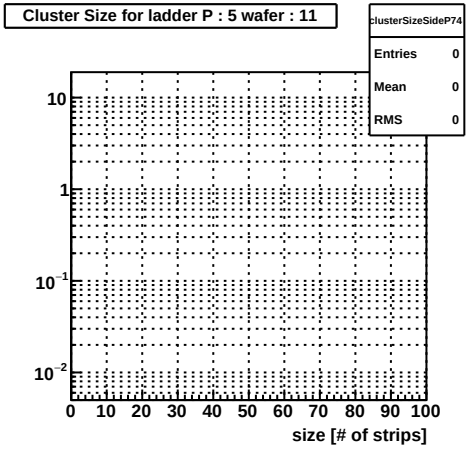

Cluster Size for ladder P : 5 wafer : 10
clusterSizeSideP73
Entries 0
Mean 0
RMS 0
size [# of strips]

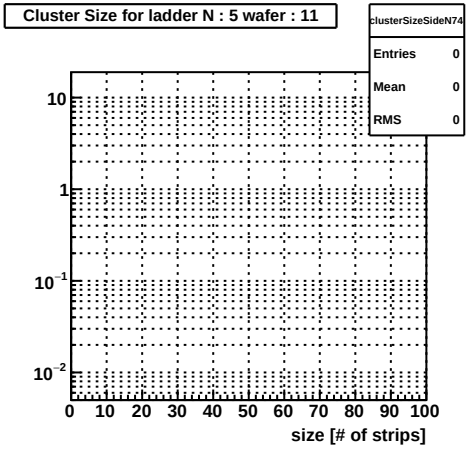

Cluster Size for ladder N : 5 wafer : 10
clusterSizeSideN73
Entries 0
Mean 0
RMS 0
size [# of strips]

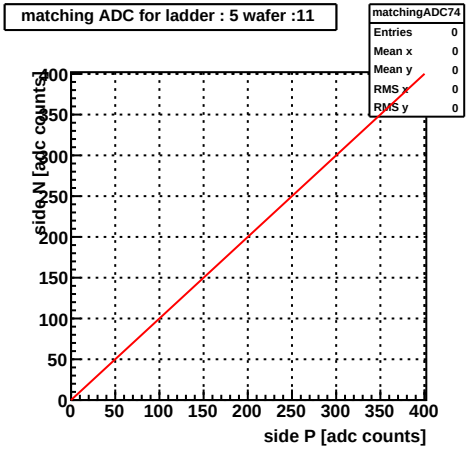

matching ADC for ladder : 5 wafer :10
matchingADC73
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

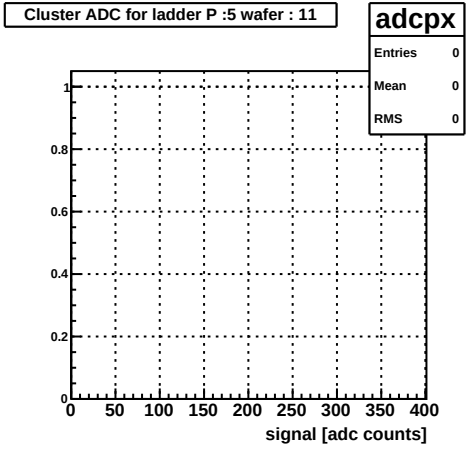

Cluster ADC for ladder P :5 wafer : 10
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

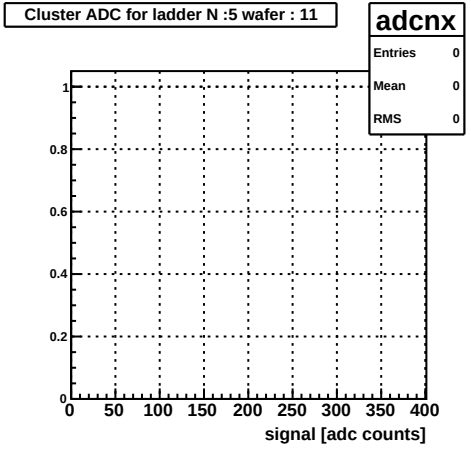

Cluster ADC for ladder N :5 wafer : 10
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

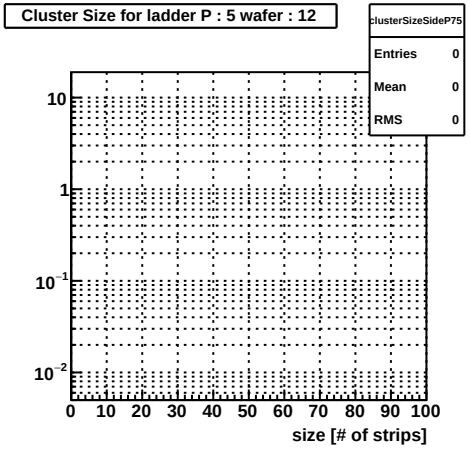

Cluster Size for ladder P : 5 wafer : 11
clusterSizeSideP74
Entries 0
Mean 0
RMS 0
size [# of strips]

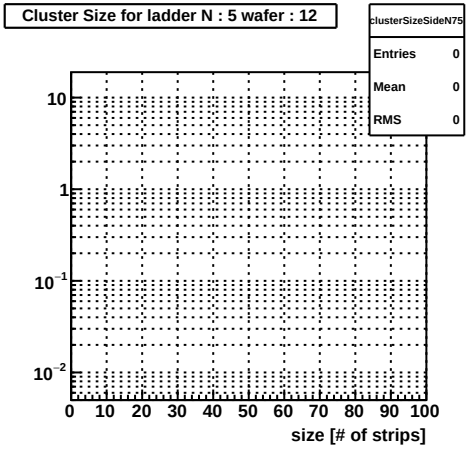

Cluster Size for ladder N : 5 wafer : 11
clusterSizeSideN74
Entries 0
Mean 0
RMS 0
size [# of strips]

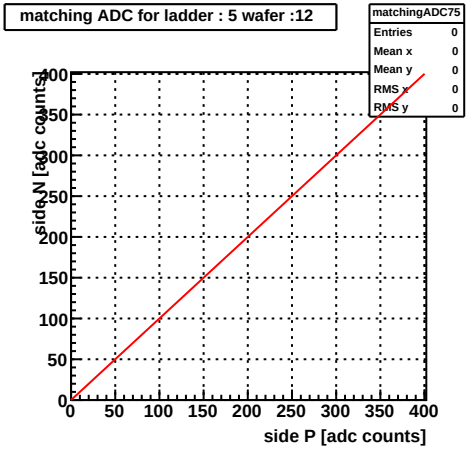

matching ADC for ladder : 5 wafer :11
matchingADC74
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

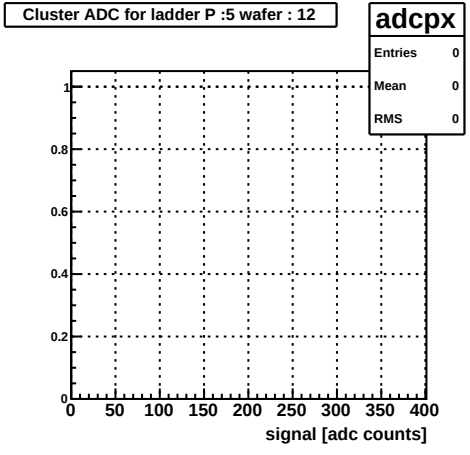

Cluster ADC for ladder P :5 wafer : 11
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

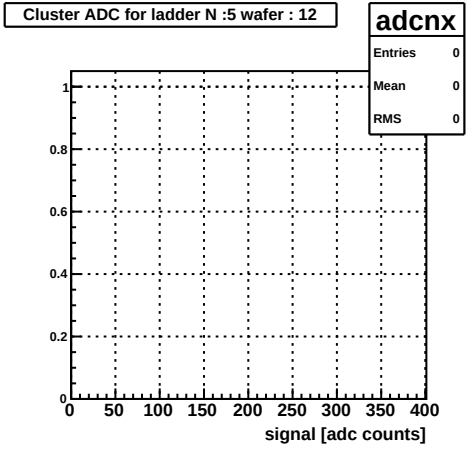

Cluster ADC for ladder N :5 wafer : 11
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

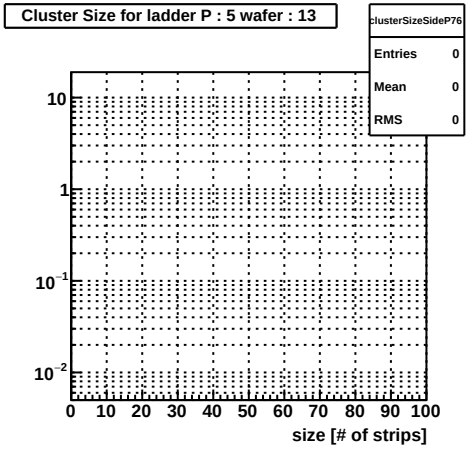

Cluster Size for ladder P : 5 wafer : 12
clusterSizeSideP75
Entries 0
Mean 0
RMS 0
size [# of strips]

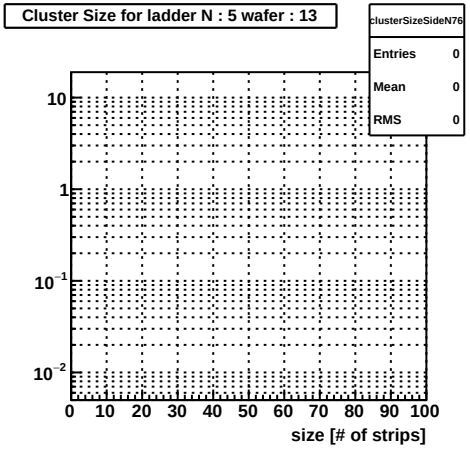

Cluster Size for ladder N : 5 wafer : 12
clusterSizeSideN75
Entries 0
Mean 0
RMS 0
size [# of strips]

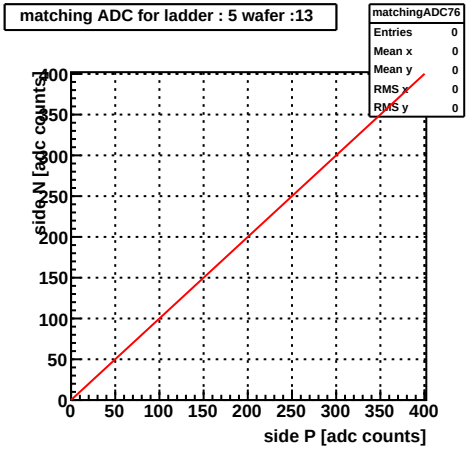

matching ADC for ladder : 5 wafer :12
matchingADC75
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

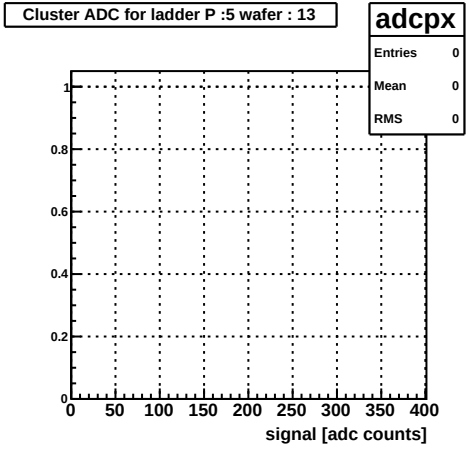

Cluster ADC for ladder P :5 wafer : 12
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

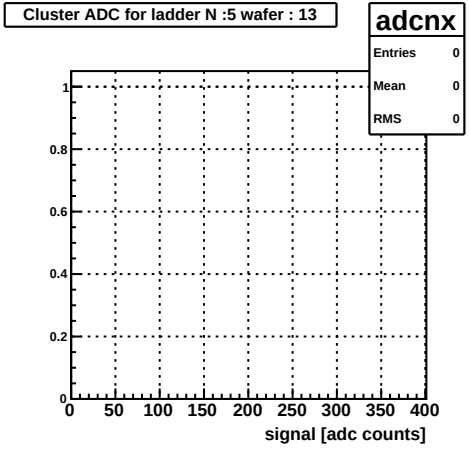

Cluster ADC for ladder N :5 wafer : 12
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

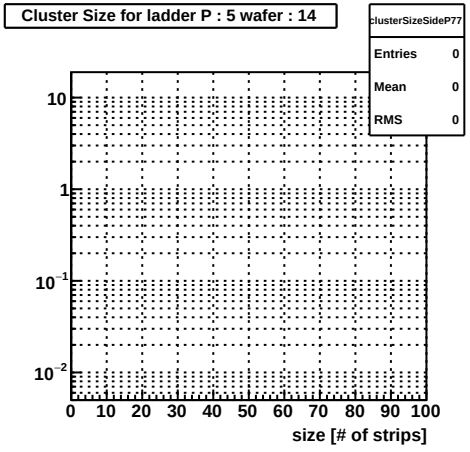

Cluster Size for ladder P : 5 wafer : 13
clusterSizeSideP76
Entries 0
Mean 0
RMS 0
size [# of strips]

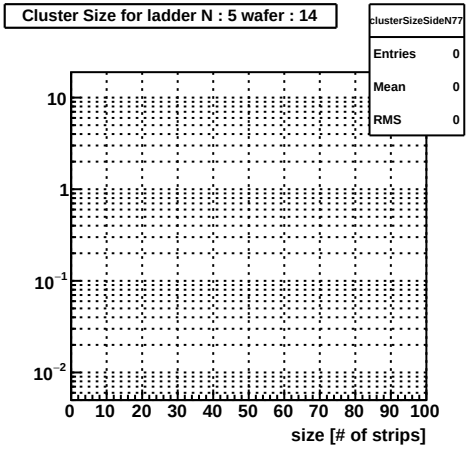

Cluster Size for ladder N : 5 wafer : 13
clusterSizeSideN76
Entries 0
Mean 0
RMS 0
size [# of strips]

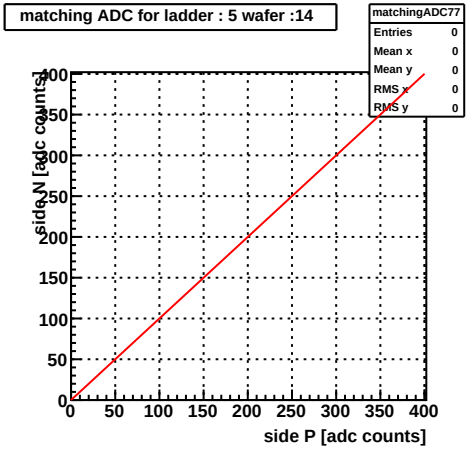

matching ADC for ladder : 5 wafer :13
matchingADC76
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

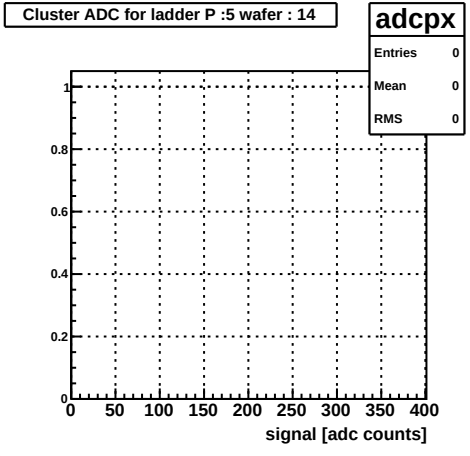

Cluster ADC for ladder P :5 wafer : 13
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

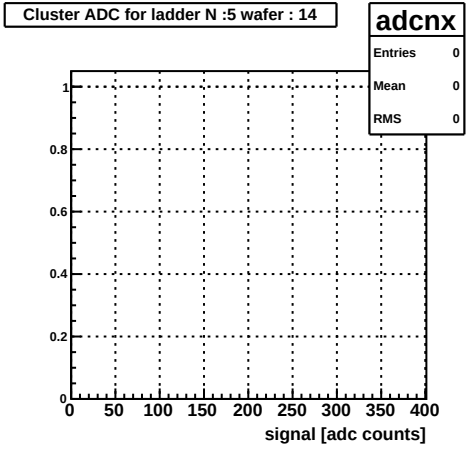

Cluster ADC for ladder N :5 wafer : 13
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

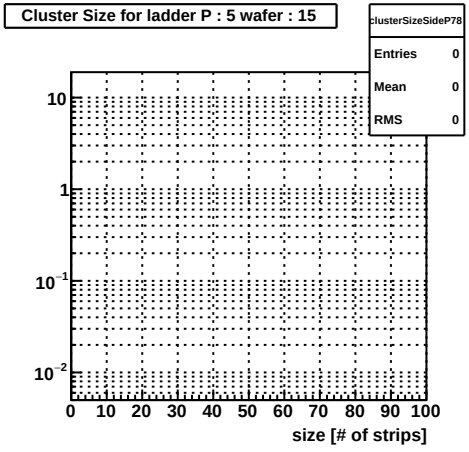

Cluster Size for ladder P : 5 wafer : 14
clusterSizeSideP77
Entries 0
Mean 0
RMS 0
size [# of strips]

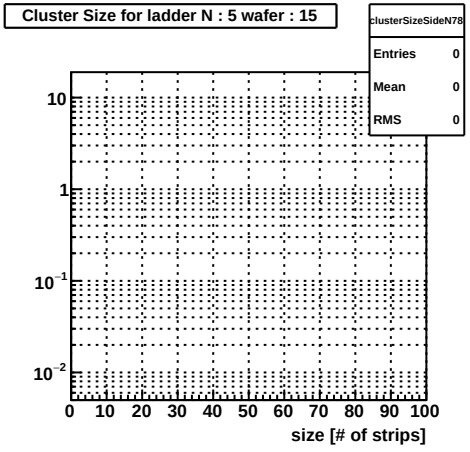

Cluster Size for ladder N : 5 wafer : 14
clusterSizeSideN77
Entries 0
Mean 0
RMS 0
size [# of strips]

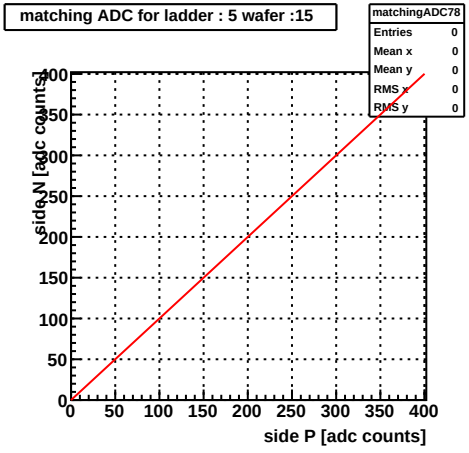

matching ADC for ladder : 5 wafer :14
matchingADC77
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

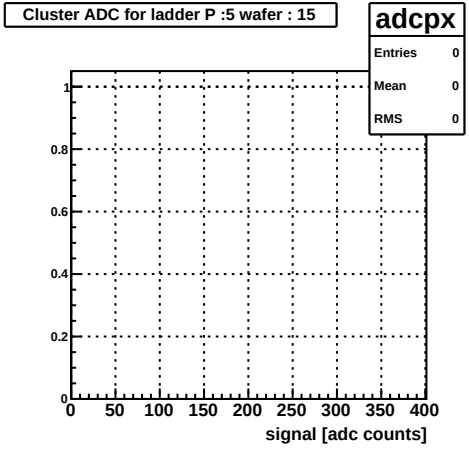

Cluster ADC for ladder P :5 wafer : 14
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

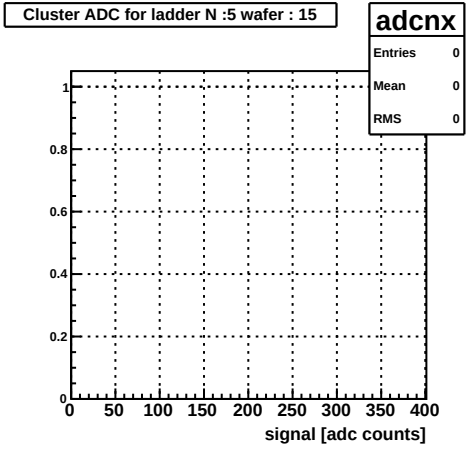

Cluster ADC for ladder N :5 wafer : 14
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

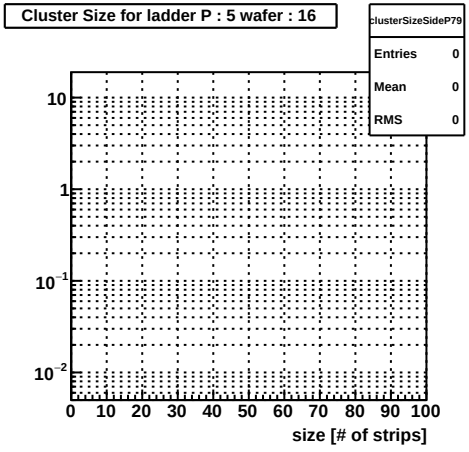

Cluster Size for ladder P : 5 wafer : 15
clusterSizeSideP78
Entries 0
Mean 0
RMS 0
size [# of strips]

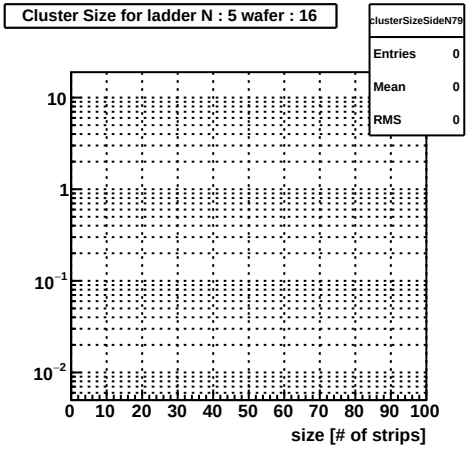

Cluster Size for ladder N : 5 wafer : 15
clusterSizeSideN78
Entries 0
Mean 0
RMS 0
size [# of strips]

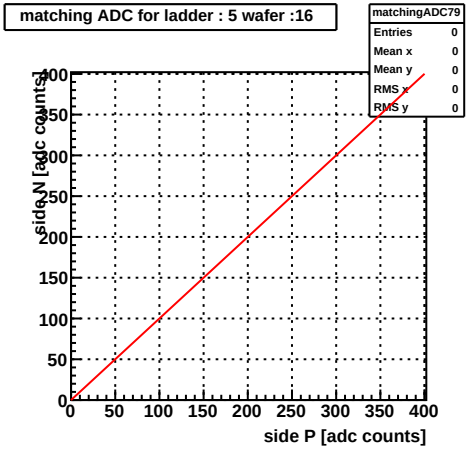

matching ADC for ladder : 5 wafer :15
matchingADC78
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

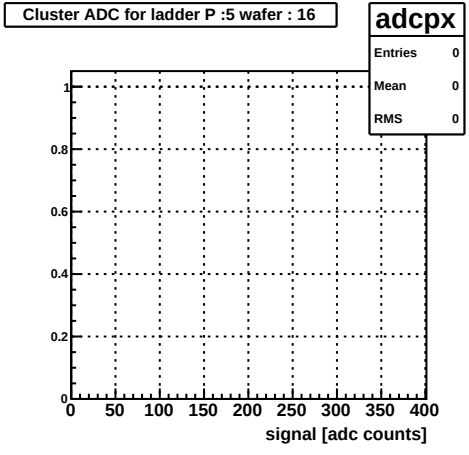

Cluster ADC for ladder P :5 wafer : 15
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

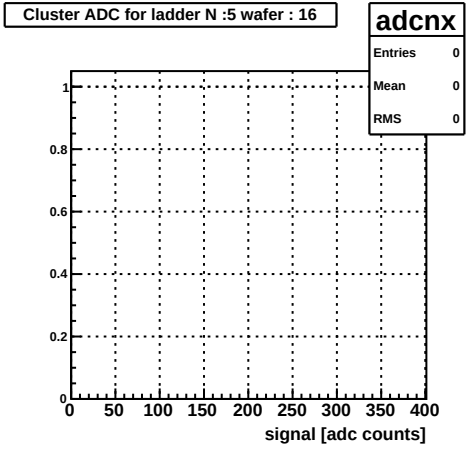

Cluster ADC for ladder N :5 wafer : 15
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

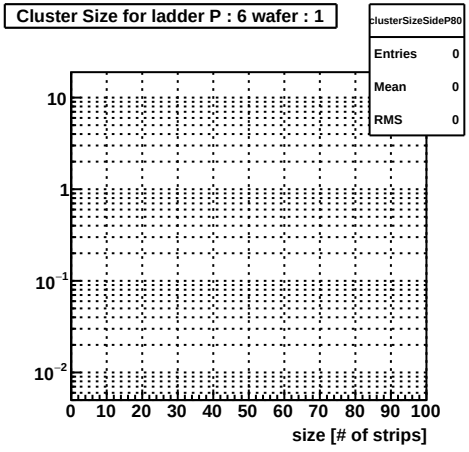

Cluster Size for ladder P : 5 wafer : 16
clusterSizeSideP79
Entries 0
Mean 0
RMS 0
size [# of strips]

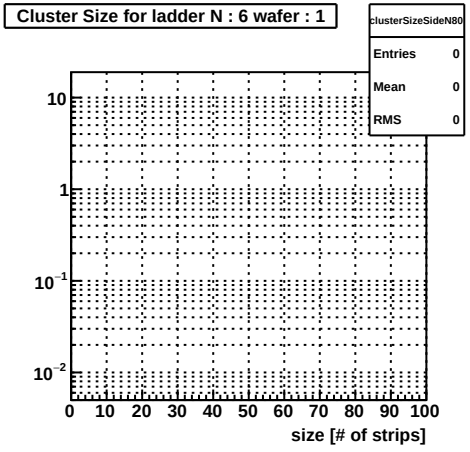

Cluster Size for ladder N : 5 wafer : 16
clusterSizeSideN79
Entries 0
Mean 0
RMS 0
size [# of strips]

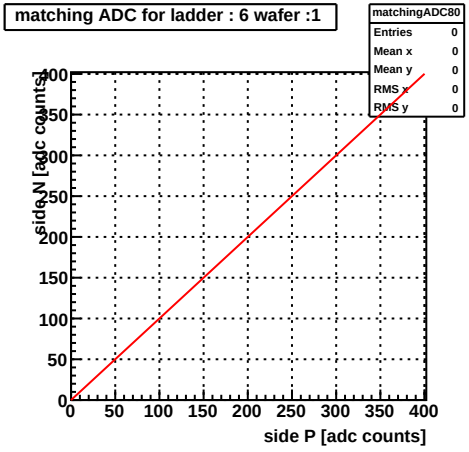

matching ADC for ladder : 5 wafer :16
matchingADC79
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

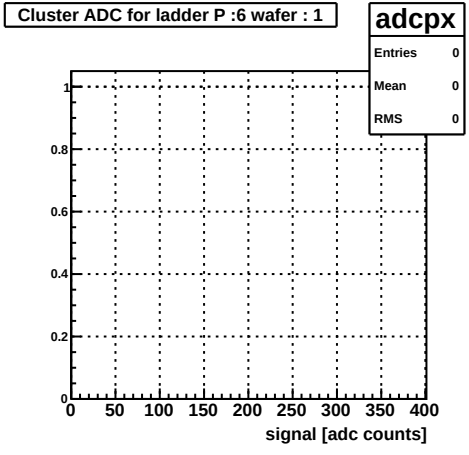

Cluster ADC for ladder P :5 wafer : 16
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

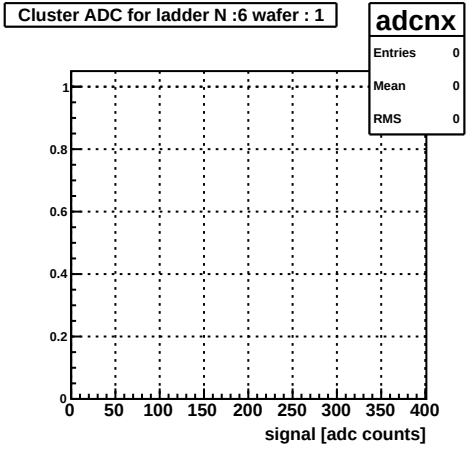

Cluster ADC for ladder N :5 wafer : 16
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

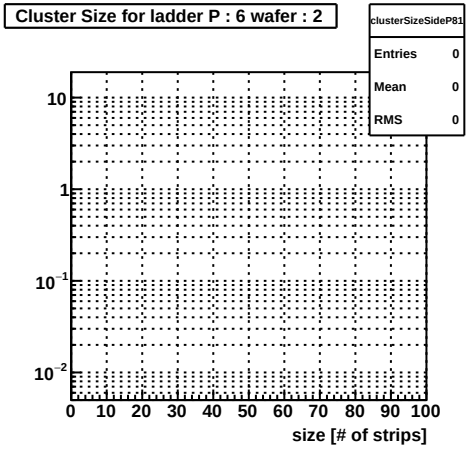

Cluster Size for ladder P : 6 wafer : 1
clusterSizeSideP80
Entries 0
Mean 0
RMS 0
size [# of strips]

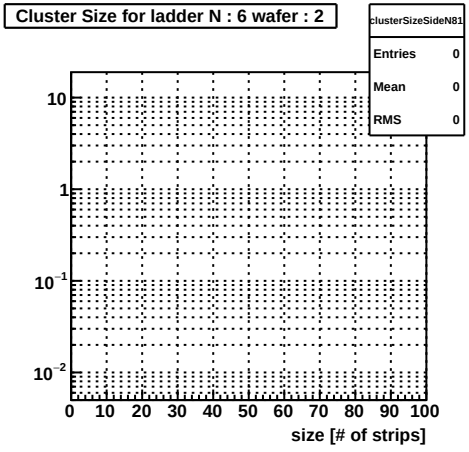

Cluster Size for ladder N : 6 wafer : 1
clusterSizeSideN80
Entries 0
Mean 0
RMS 0
size [# of strips]

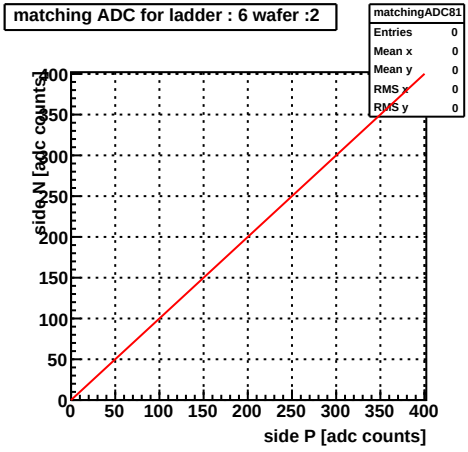

matching ADC for ladder : 6 wafer :1
matchingADC80
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

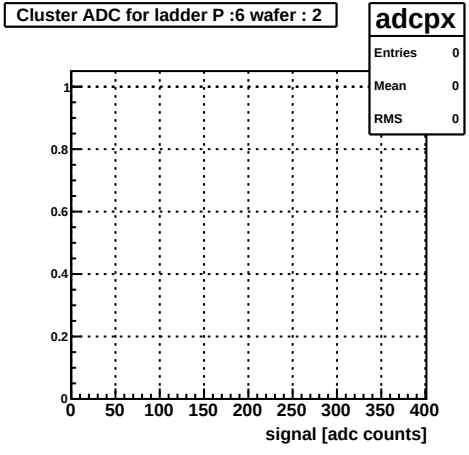

Cluster ADC for ladder P :6 wafer : 1
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

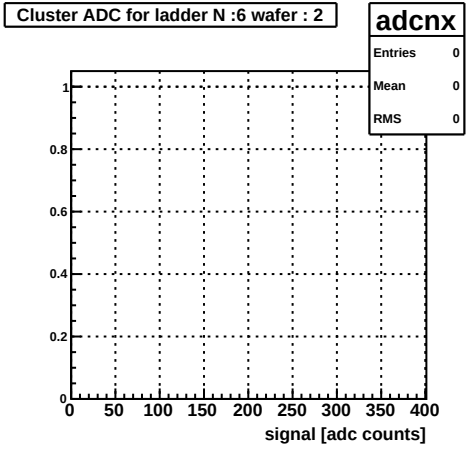

Cluster ADC for ladder N :6 wafer : 1
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

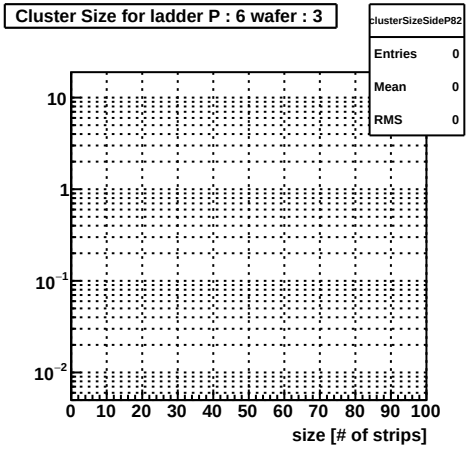

Cluster Size for ladder P : 6 wafer : 2
clusterSizeSideP81
Entries 0
Mean 0
RMS 0
size [# of strips]

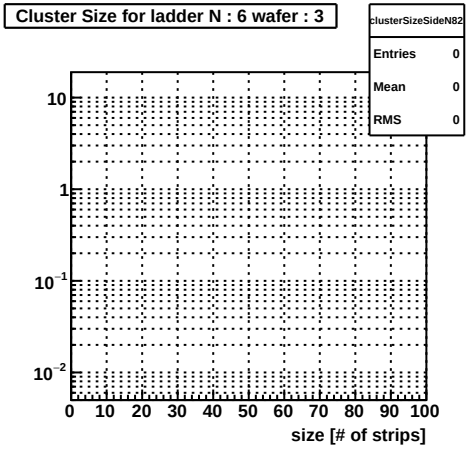

Cluster Size for ladder N : 6 wafer : 2
clusterSizeSideN81
Entries 0
Mean 0
RMS 0
size [# of strips]

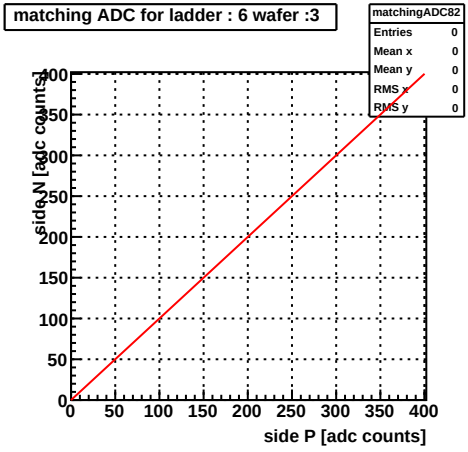

matching ADC for ladder : 6 wafer :2
matchingADC81
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

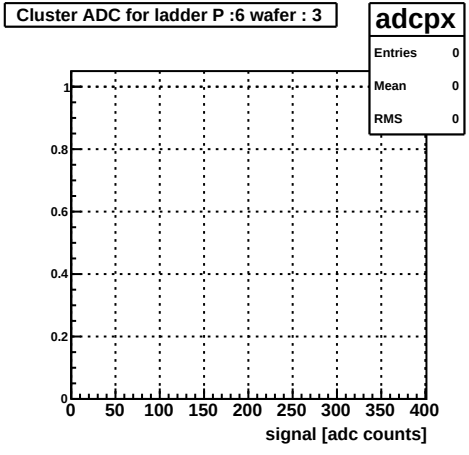

Cluster ADC for ladder P :6 wafer : 2
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

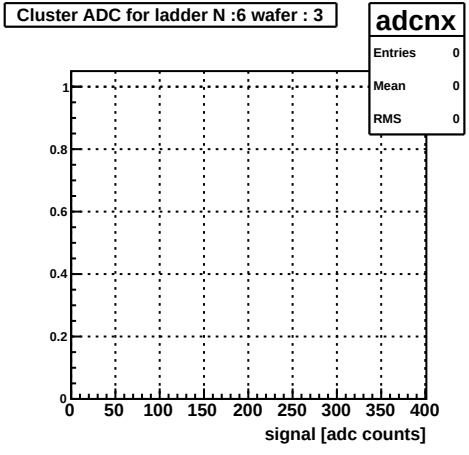

Cluster ADC for ladder N :6 wafer : 2
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

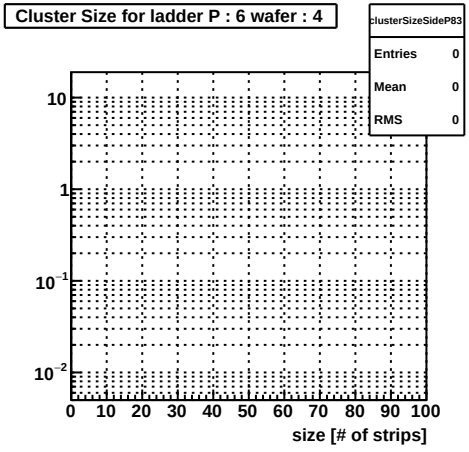

Cluster Size for ladder P : 6 wafer : 3
clusterSizeSideP82
Entries 0
Mean 0
RMS 0
size [# of strips]

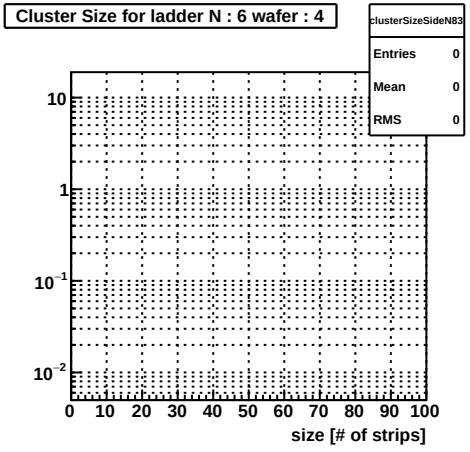

Cluster Size for ladder N : 6 wafer : 3
clusterSizeSideN82
Entries 0
Mean 0
RMS 0
size [# of strips]

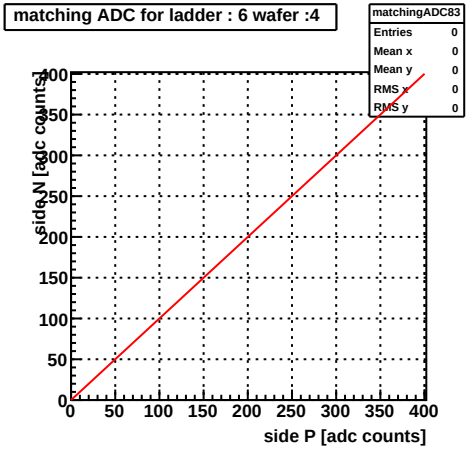

matching ADC for ladder : 6 wafer :3
matchingADC82
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

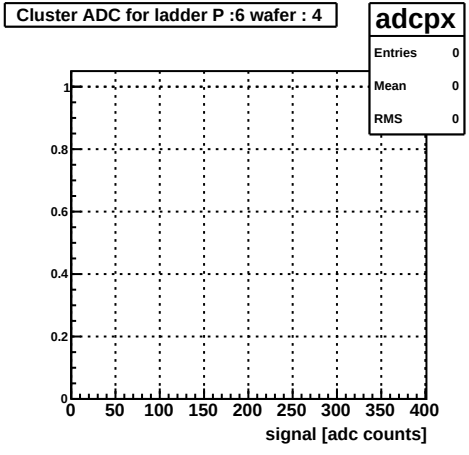

Cluster ADC for ladder P :6 wafer : 3
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

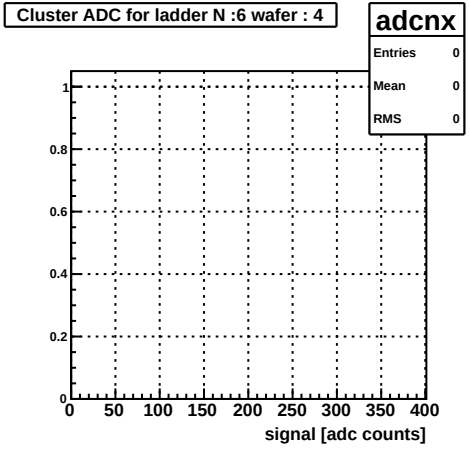

Cluster ADC for ladder N :6 wafer : 3
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

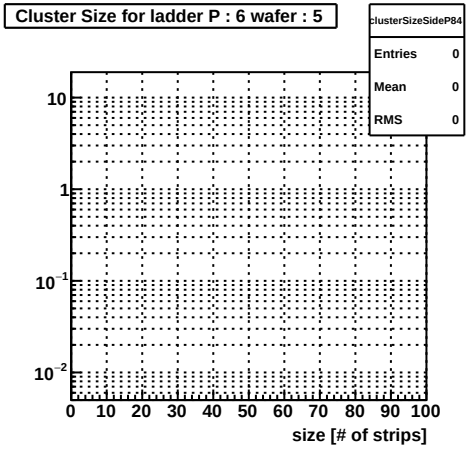

Cluster Size for ladder P : 6 wafer : 4
clusterSizeSideP83
Entries 0
Mean 0
RMS 0
size [# of strips]

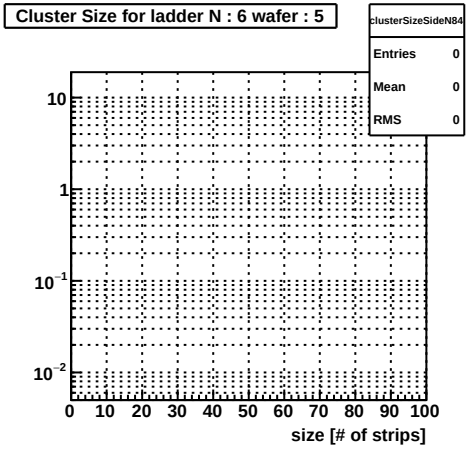

Cluster Size for ladder N : 6 wafer : 4
clusterSizeSideN83
Entries 0
Mean 0
RMS 0
size [# of strips]

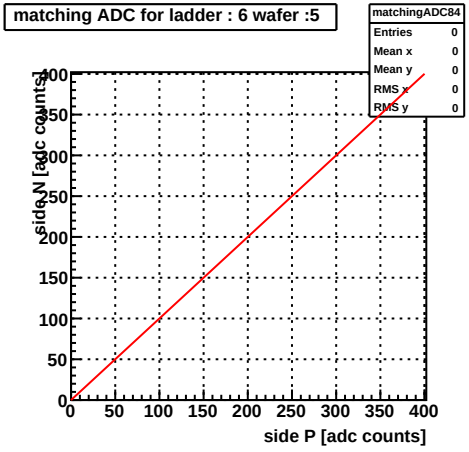

matching ADC for ladder : 6 wafer :4
matchingADC83
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

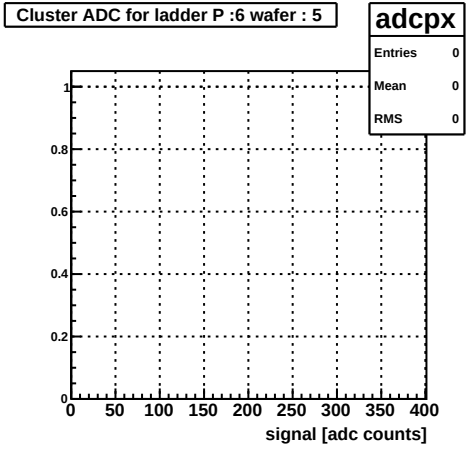

Cluster ADC for ladder P :6 wafer : 4
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

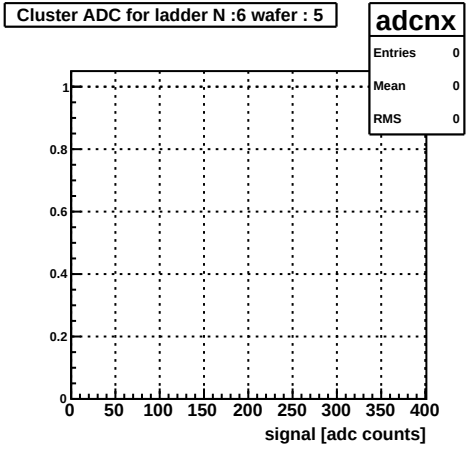

Cluster ADC for ladder N :6 wafer : 4
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

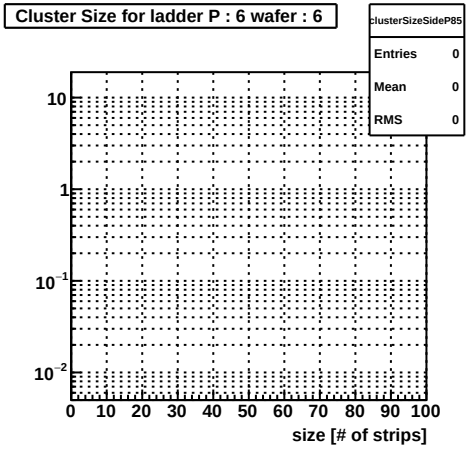

Cluster Size for ladder P : 6 wafer : 5
clusterSizeSideP84
Entries 0
Mean 0
RMS 0
size [# of strips]

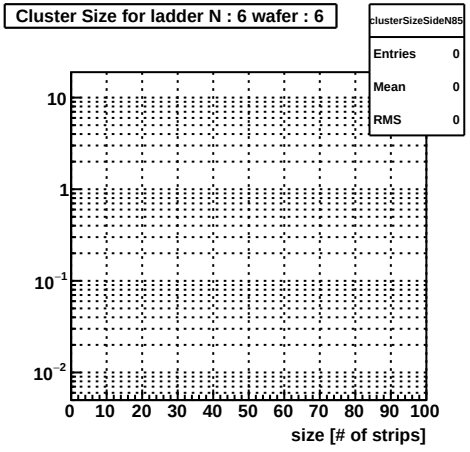

Cluster Size for ladder N : 6 wafer : 5
clusterSizeSideN84
Entries 0
Mean 0
RMS 0
size [# of strips]

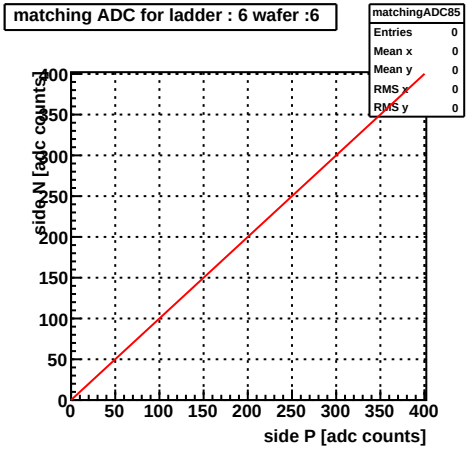

matching ADC for ladder : 6 wafer :5
matchingADC84
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

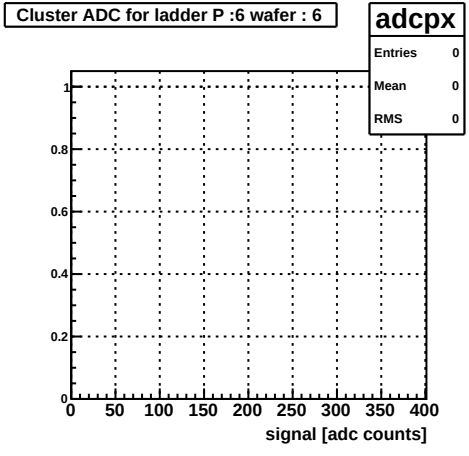

Cluster ADC for ladder P :6 wafer : 5
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

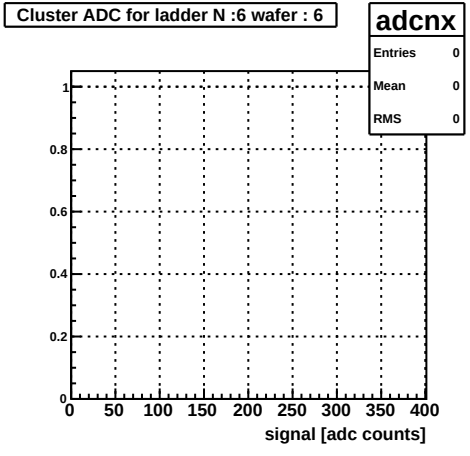

Cluster ADC for ladder N :6 wafer : 5
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

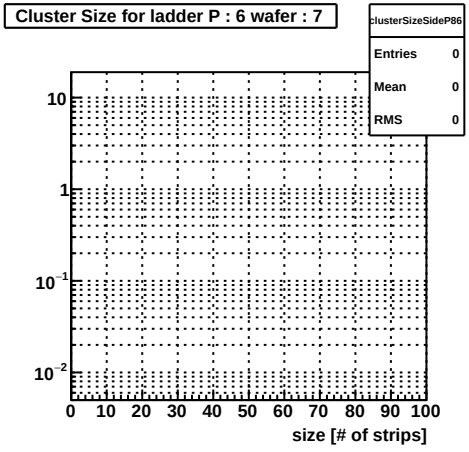

Cluster Size for ladder P : 6 wafer : 6
clusterSizeSideP85
Entries 0
Mean 0
RMS 0
size [# of strips]

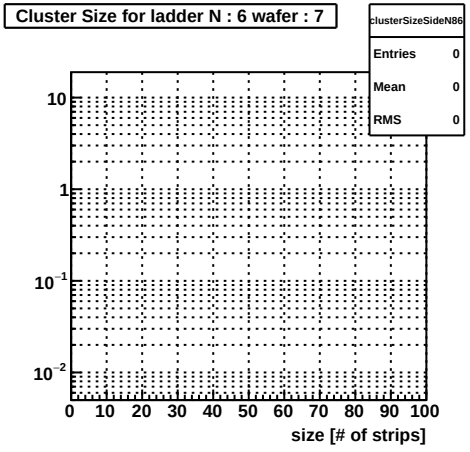

Cluster Size for ladder N : 6 wafer : 6
clusterSizeSideN85
Entries 0
Mean 0
RMS 0
size [# of strips]

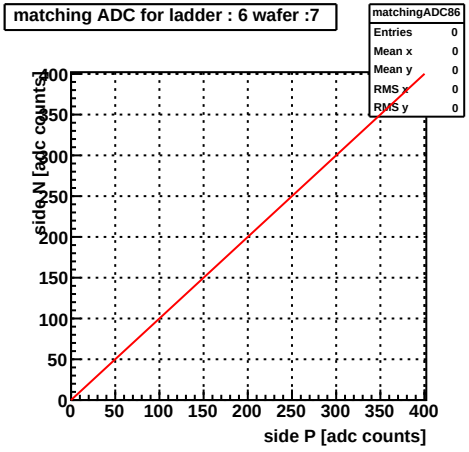

matching ADC for ladder : 6 wafer :6
matchingADC85
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

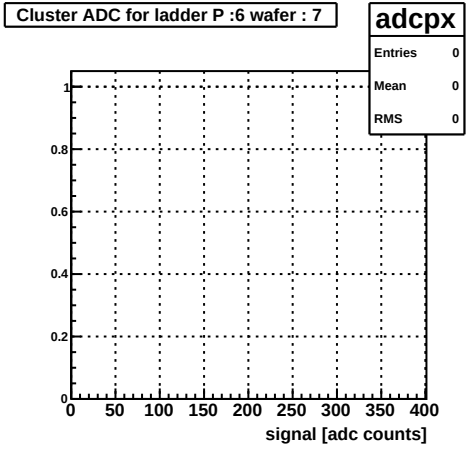

Cluster ADC for ladder P :6 wafer : 6
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

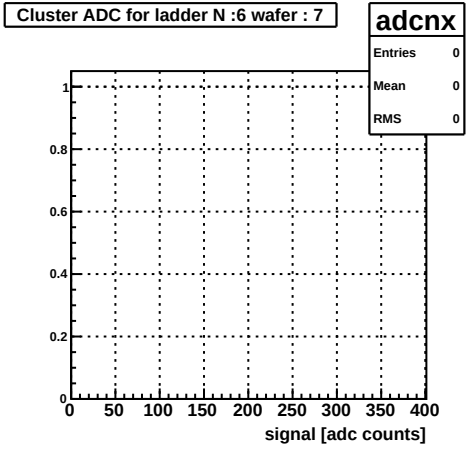

Cluster ADC for ladder N :6 wafer : 6
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

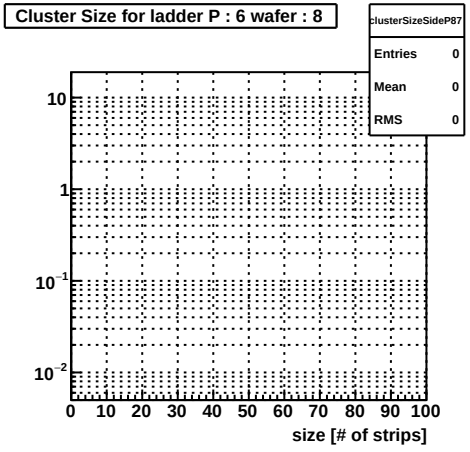

Cluster Size for ladder P : 6 wafer : 7
clusterSizeSideP86
Entries 0
Mean 0
RMS 0
size [# of strips]

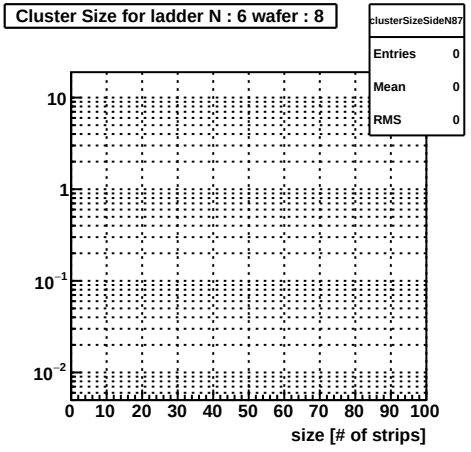

Cluster Size for ladder N : 6 wafer : 7
clusterSizeSideN86
Entries 0
Mean 0
RMS 0
size [# of strips]

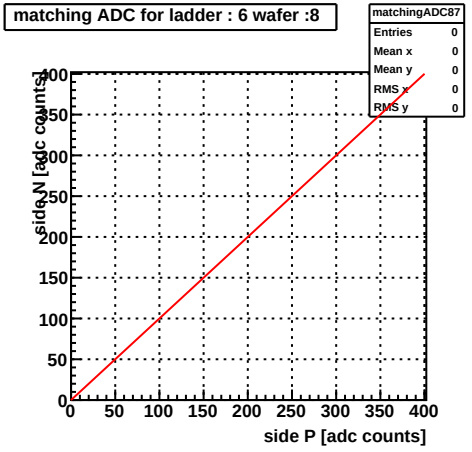

matching ADC for ladder : 6 wafer :7
matchingADC86
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

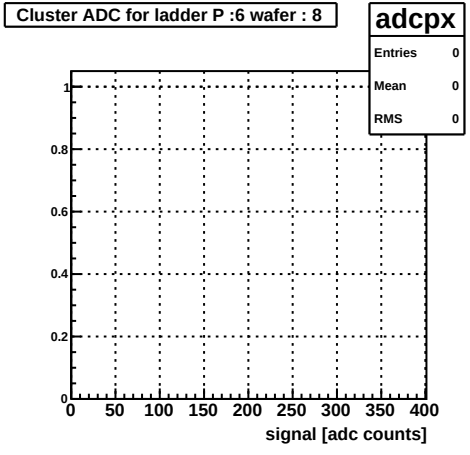

Cluster ADC for ladder P :6 wafer : 7
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

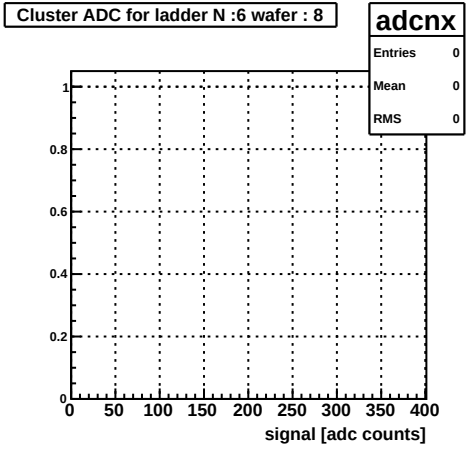

Cluster ADC for ladder N :6 wafer : 7
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

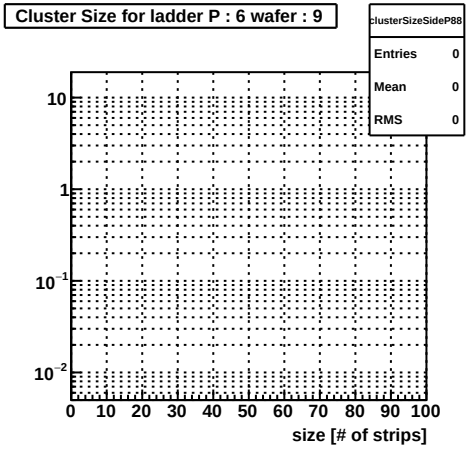

Cluster Size for ladder P : 6 wafer : 8
clusterSizeSideP87
Entries 0
Mean 0
RMS 0
size [# of strips]

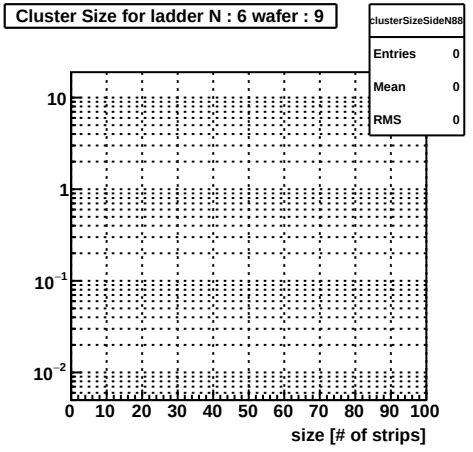

Cluster Size for ladder N : 6 wafer : 8
clusterSizeSideN87
Entries 0
Mean 0
RMS 0
size [# of strips]

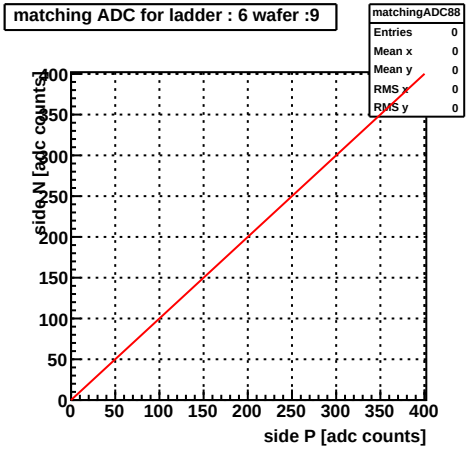

matching ADC for ladder : 6 wafer :8
matchingADC87
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

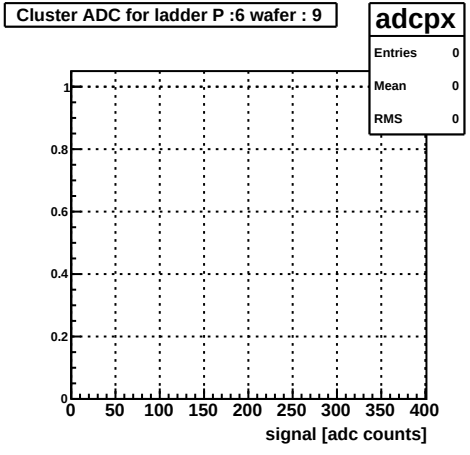

Cluster ADC for ladder P :6 wafer : 8
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

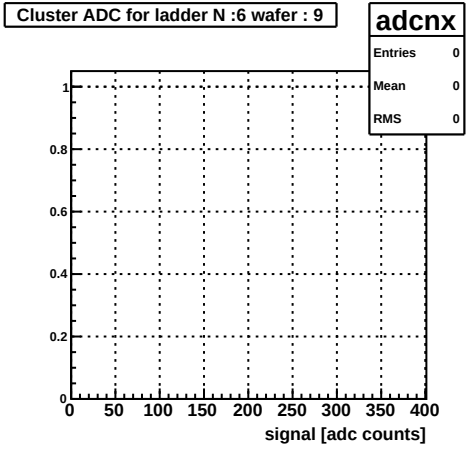

Cluster ADC for ladder N :6 wafer : 8
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

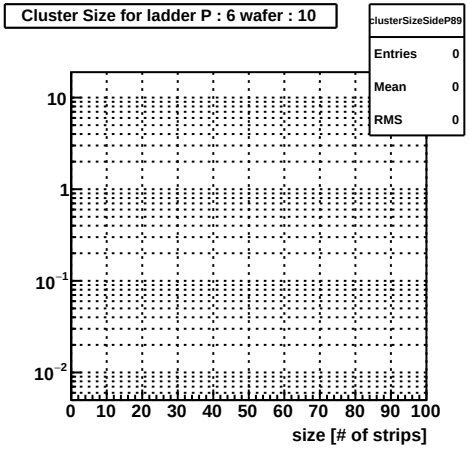

Cluster Size for ladder P : 6 wafer : 9
clusterSizeSideP88
Entries 0
Mean 0
RMS 0
size [# of strips]

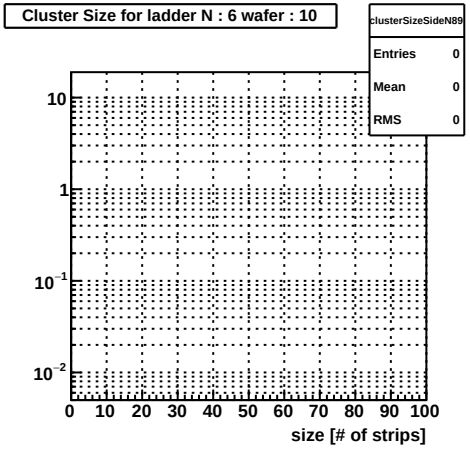

Cluster Size for ladder N : 6 wafer : 9
clusterSizeSideN88
Entries 0
Mean 0
RMS 0
size [# of strips]

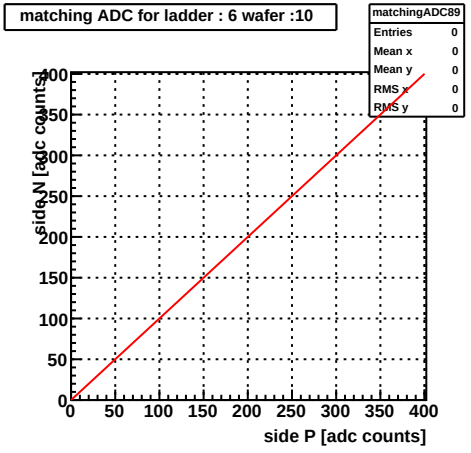

matching ADC for ladder : 6 wafer :9
matchingADC88
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

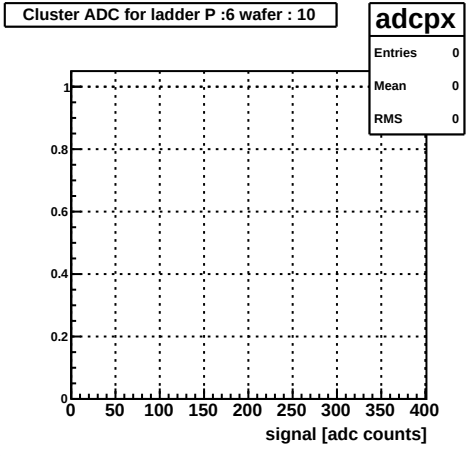

Cluster ADC for ladder P :6 wafer : 9
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

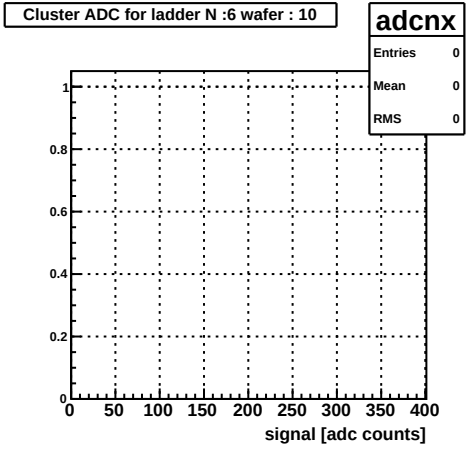

Cluster ADC for ladder N :6 wafer : 9
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

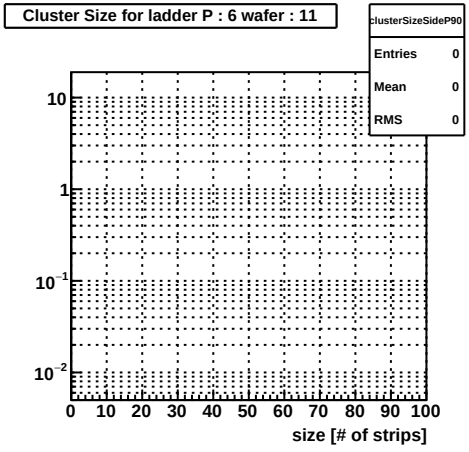

Cluster Size for ladder P : 6 wafer : 10
clusterSizeSideP89
Entries 0
Mean 0
RMS 0
size [# of strips]

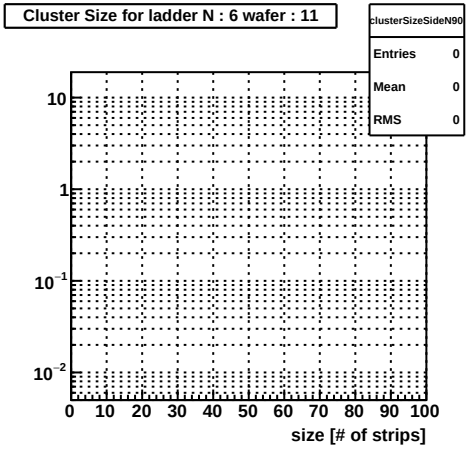

Cluster Size for ladder N : 6 wafer : 10
clusterSizeSideN89
Entries 0
Mean 0
RMS 0
size [# of strips]

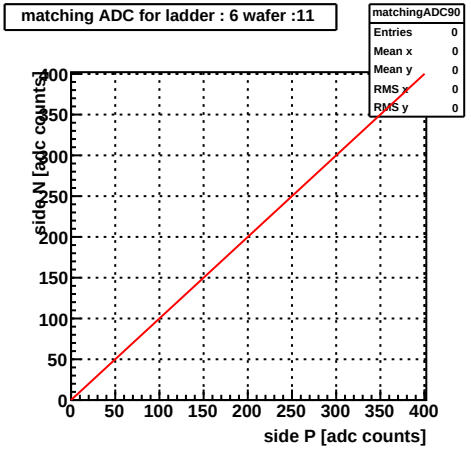

matching ADC for ladder : 6 wafer :10
matchingADC89
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

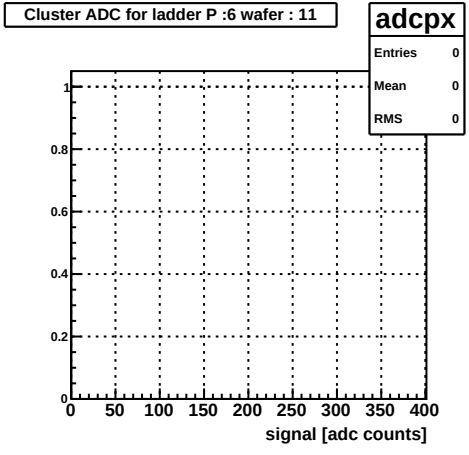

Cluster ADC for ladder P :6 wafer : 10
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

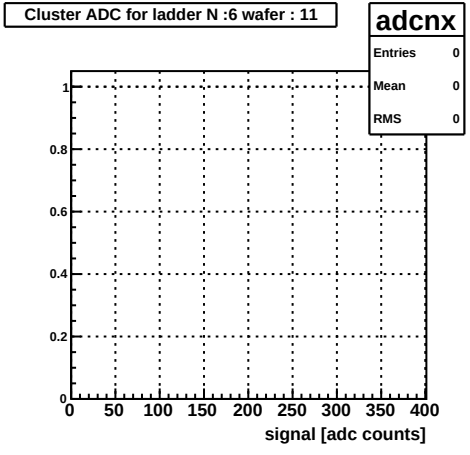

Cluster ADC for ladder N :6 wafer : 10
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

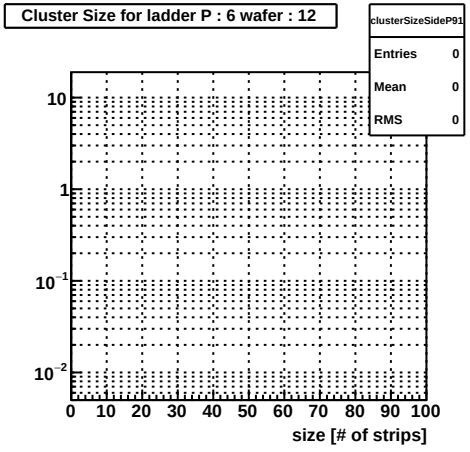

Cluster Size for ladder P : 6 wafer : 11
clusterSizeSideP90
Entries 0
Mean 0
RMS 0
size [# of strips]

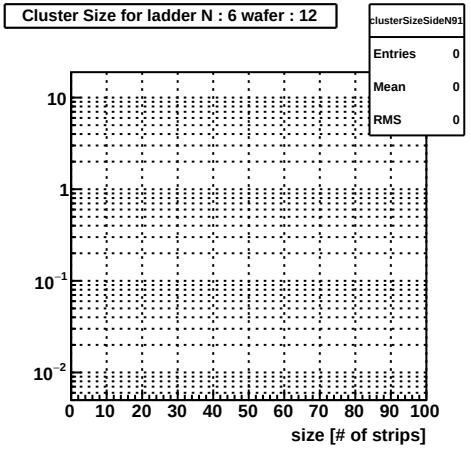

Cluster Size for ladder N : 6 wafer : 11
clusterSizeSideN90
Entries 0
Mean 0
RMS 0
size [# of strips]

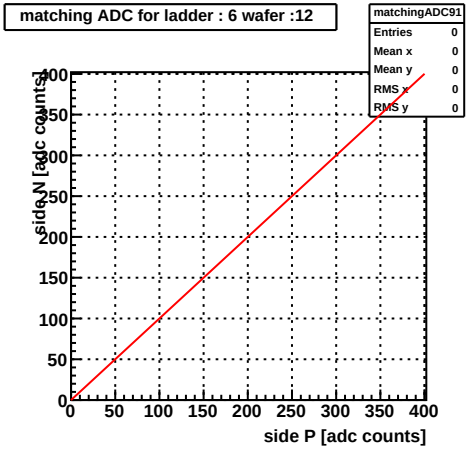

matching ADC for ladder : 6 wafer :11
matchingADC90
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

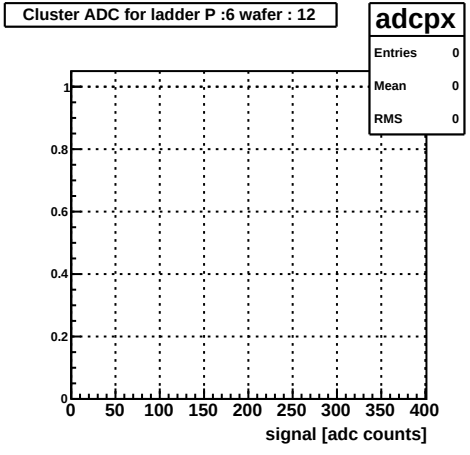

Cluster ADC for ladder P :6 wafer : 11
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

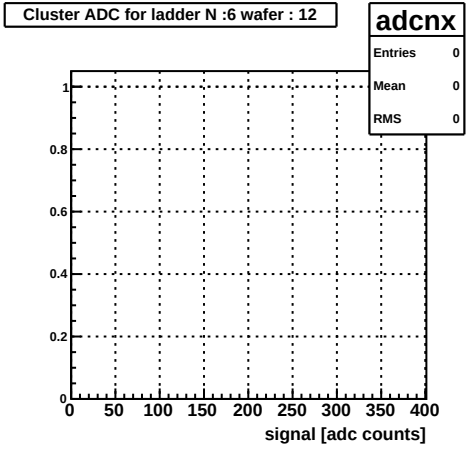

Cluster ADC for ladder N :6 wafer : 11
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

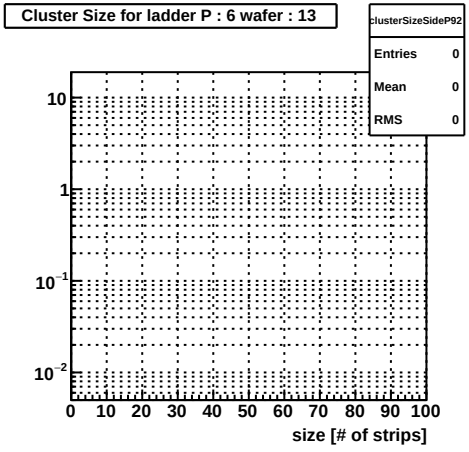

Cluster Size for ladder P : 6 wafer : 12
clusterSizeSideP91
Entries 0
Mean 0
RMS 0
size [# of strips]

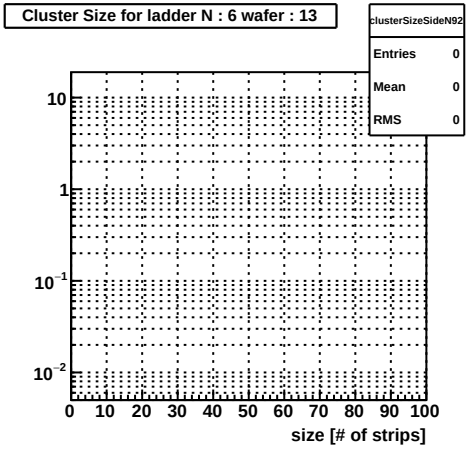

Cluster Size for ladder N : 6 wafer : 12
clusterSizeSideN91
Entries 0
Mean 0
RMS 0
size [# of strips]

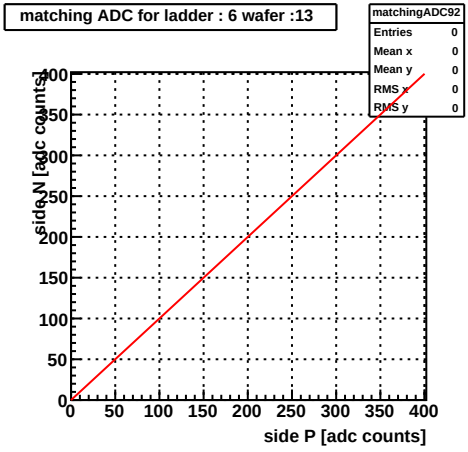

matching ADC for ladder : 6 wafer :12
matchingADC91
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

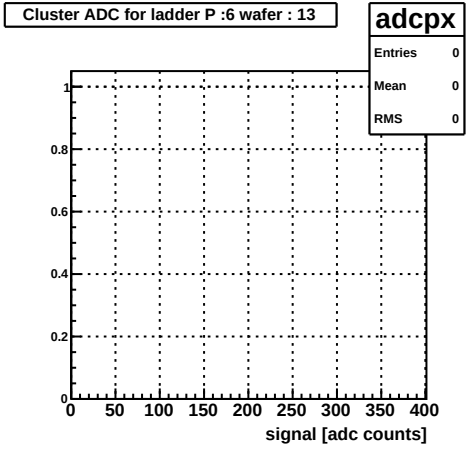

Cluster ADC for ladder P :6 wafer : 12
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

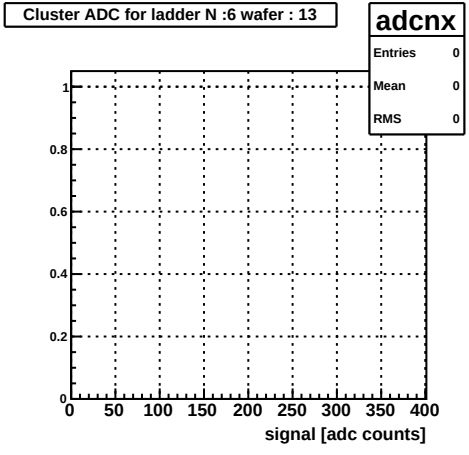

Cluster ADC for ladder N :6 wafer : 12
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

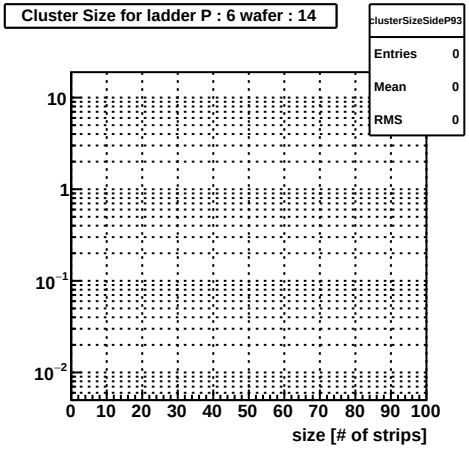

Cluster Size for ladder P : 6 wafer : 13
clusterSizeSideP92
Entries 0
Mean 0
RMS 0
size [# of strips]

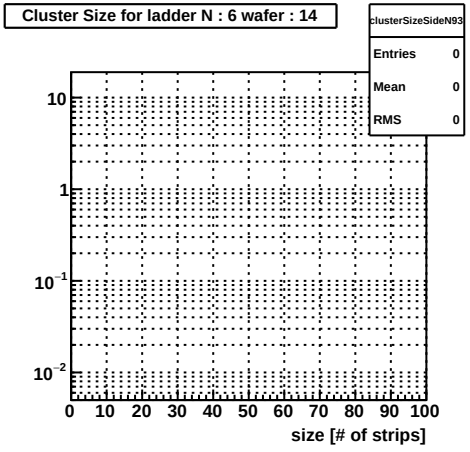

Cluster Size for ladder N : 6 wafer : 13
clusterSizeSideN92
Entries 0
Mean 0
RMS 0
size [# of strips]

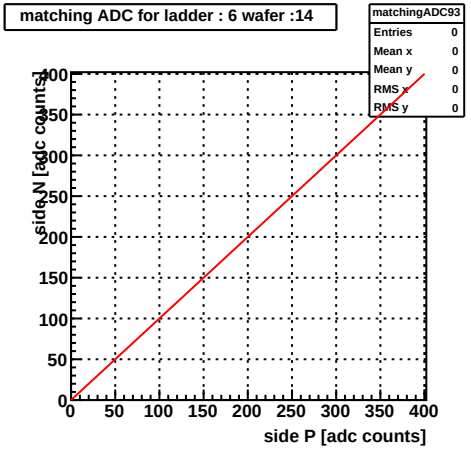

matching ADC for ladder : 6 wafer :13
matchingADC92
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

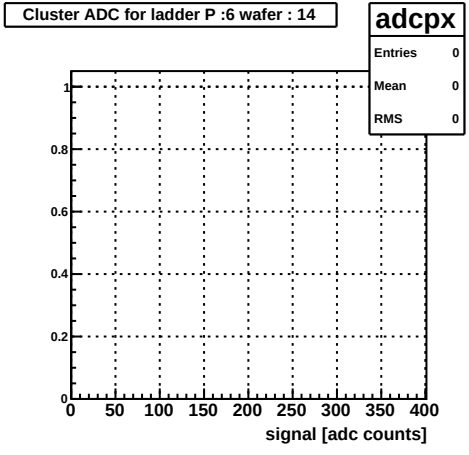

Cluster ADC for ladder P :6 wafer : 13
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

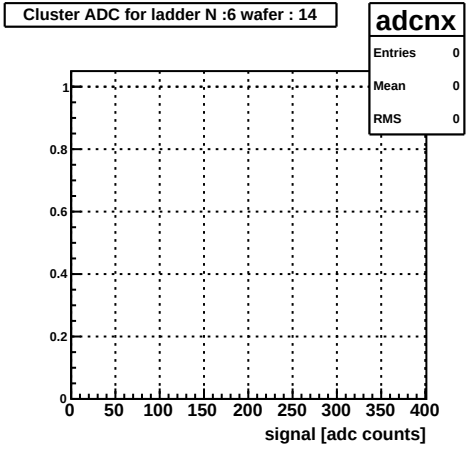

Cluster ADC for ladder N :6 wafer : 13
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

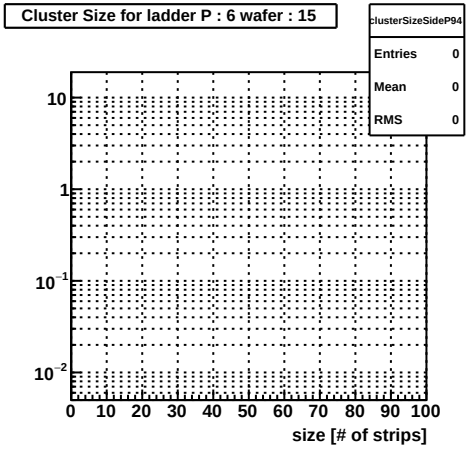

Cluster Size for ladder P : 6 wafer : 14
clusterSizeSideP93
Entries 0
Mean 0
RMS 0
size [# of strips]

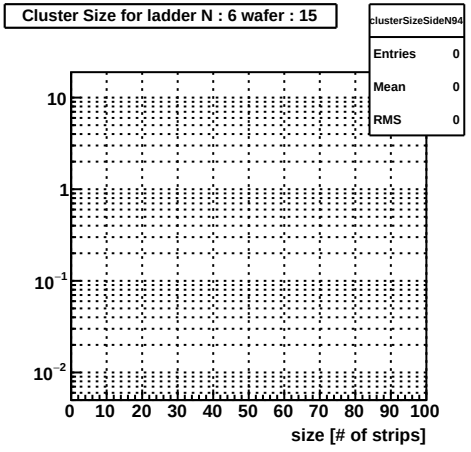

Cluster Size for ladder N : 6 wafer : 14
clusterSizeSideN93
Entries 0
Mean 0
RMS 0
size [# of strips]

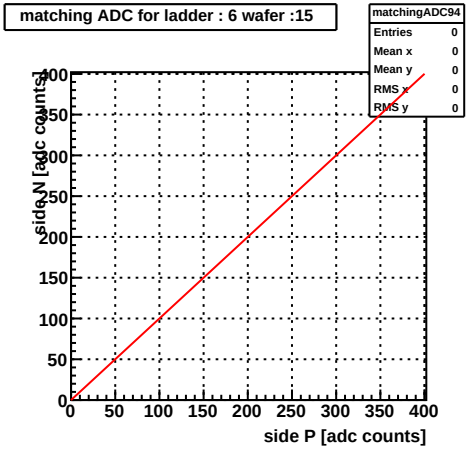

matching ADC for ladder : 6 wafer :14
matchingADC93
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

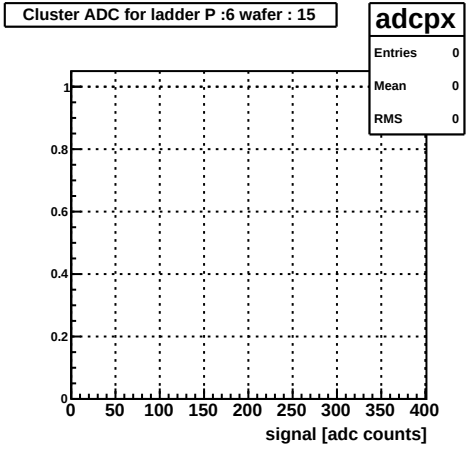

Cluster ADC for ladder P :6 wafer : 14
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

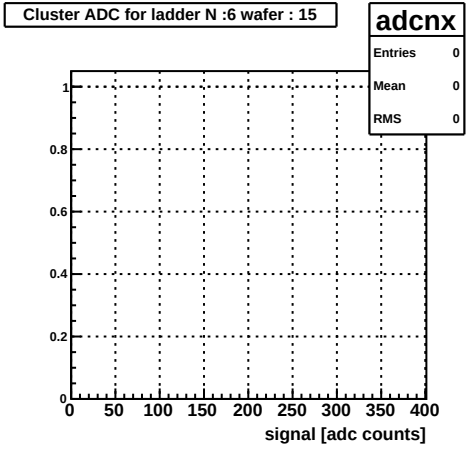

Cluster ADC for ladder N :6 wafer : 14
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

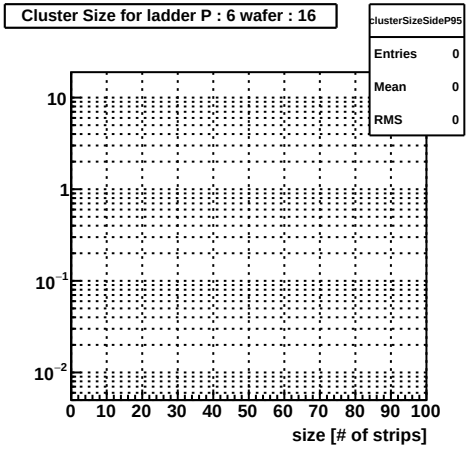

Cluster Size for ladder P : 6 wafer : 15
clusterSizeSideP94
Entries 0
Mean 0
RMS 0
size [# of strips]

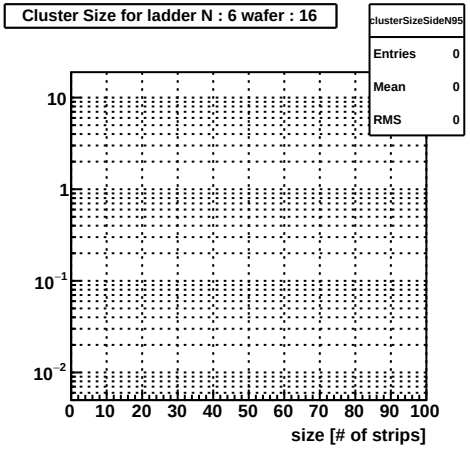

Cluster Size for ladder N : 6 wafer : 15
clusterSizeSideN94
Entries 0
Mean 0
RMS 0
size [# of strips]

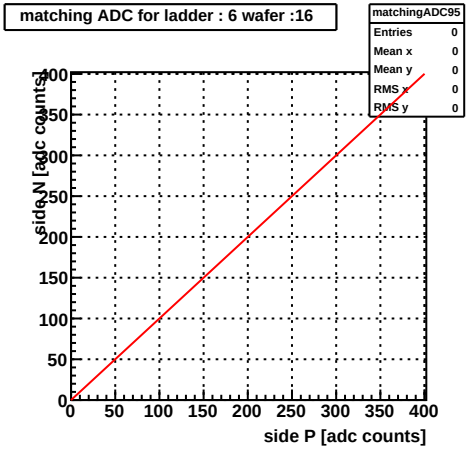

matching ADC for ladder : 6 wafer :15
matchingADC94
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

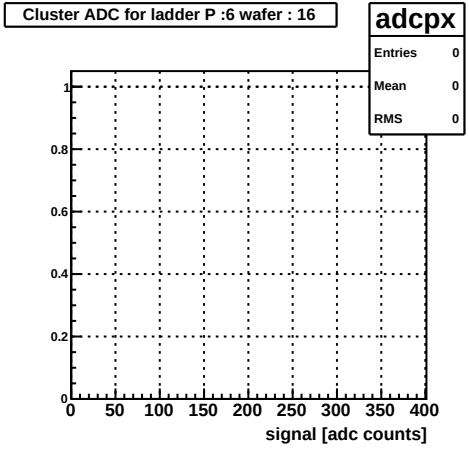

Cluster ADC for ladder P :6 wafer : 15
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

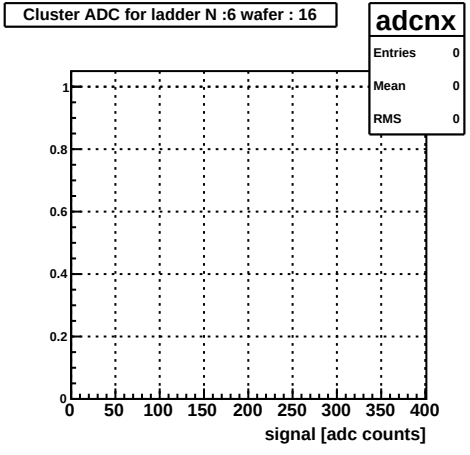

Cluster ADC for ladder N :6 wafer : 15
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

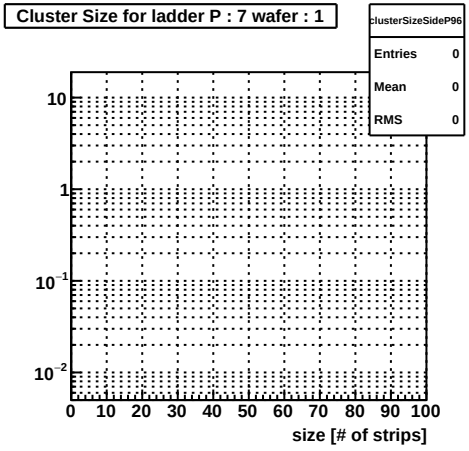

Cluster Size for ladder P : 6 wafer : 16
clusterSizeSideP95
Entries 0
Mean 0
RMS 0
size [# of strips]

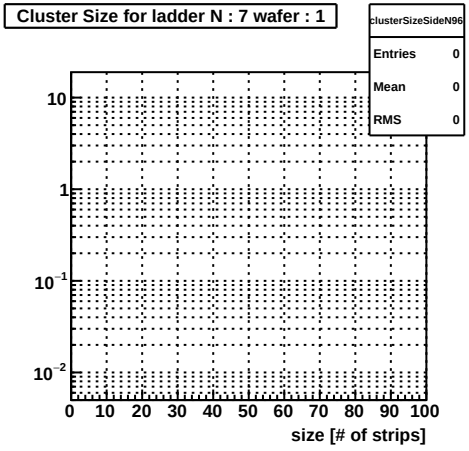

Cluster Size for ladder N : 6 wafer : 16
clusterSizeSideN95
Entries 0
Mean 0
RMS 0
size [# of strips]

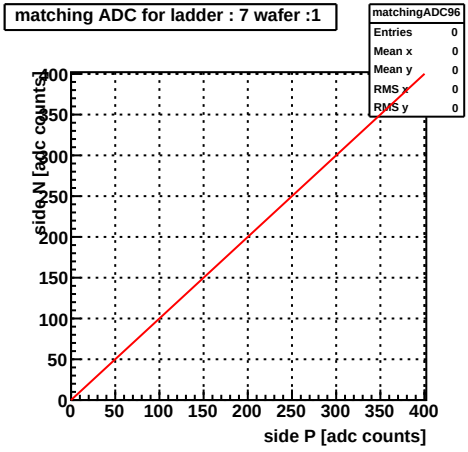

matching ADC for ladder : 6 wafer :16
matchingADC95
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

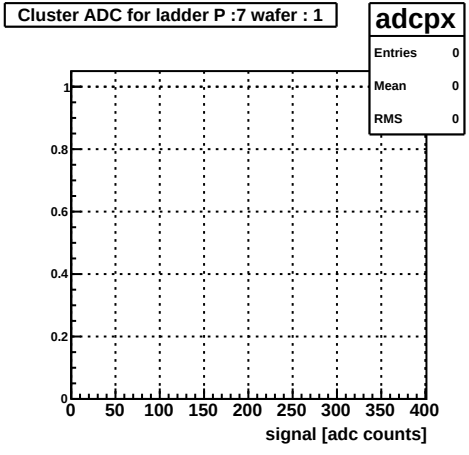

Cluster ADC for ladder P :6 wafer : 16
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

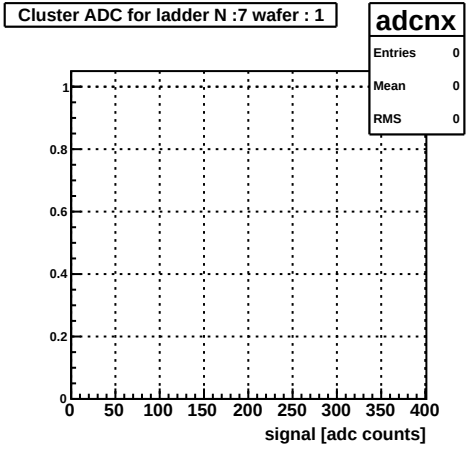

Cluster ADC for ladder N :6 wafer : 16
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

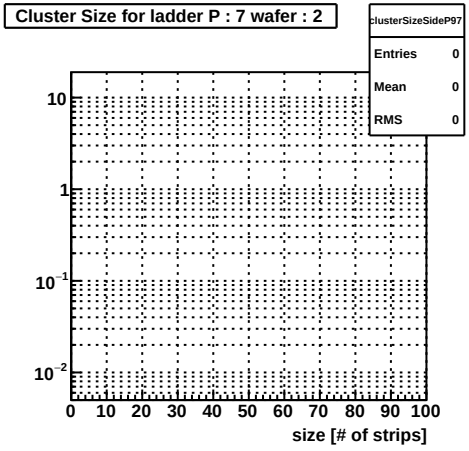

Cluster Size for ladder P : 7 wafer : 1
clusterSizeSideP96
Entries 0
Mean 0
RMS 0
size [# of strips]

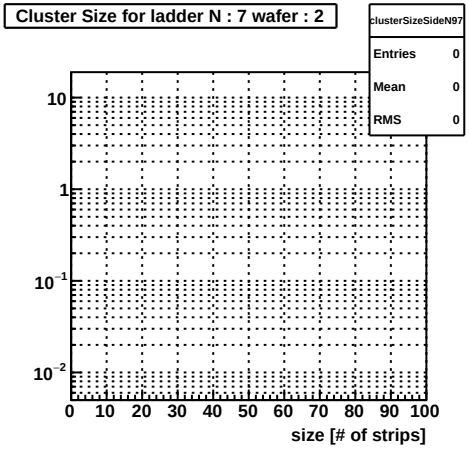

Cluster Size for ladder N : 7 wafer : 1
clusterSizeSideN96
Entries 0
Mean 0
RMS 0
size [# of strips]

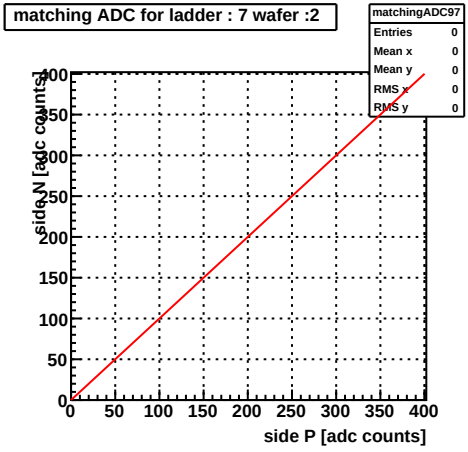

matching ADC for ladder : 7 wafer :1
matchingADC96
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

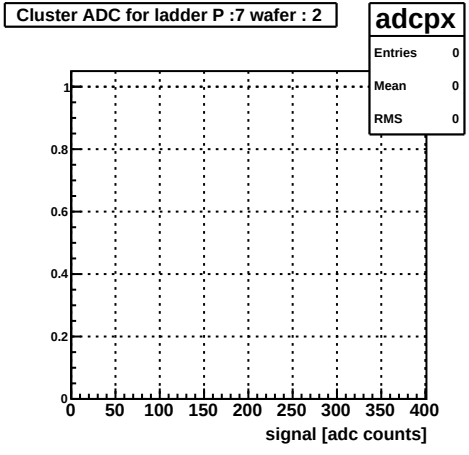

Cluster ADC for ladder P :7 wafer : 1
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

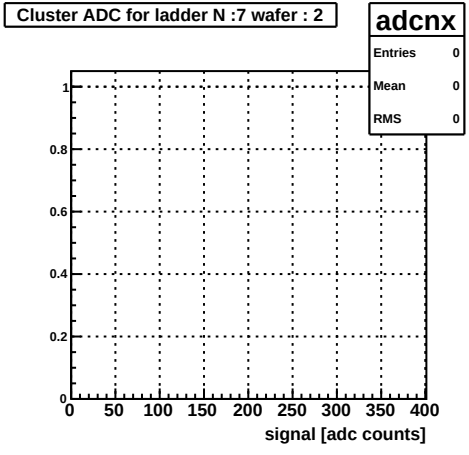

Cluster ADC for ladder N :7 wafer : 1
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

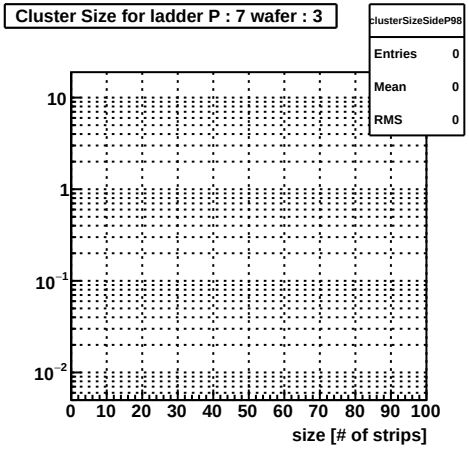

Cluster Size for ladder P : 7 wafer : 2
clusterSizeSideP97
Entries 0
Mean 0
RMS 0
size [# of strips]

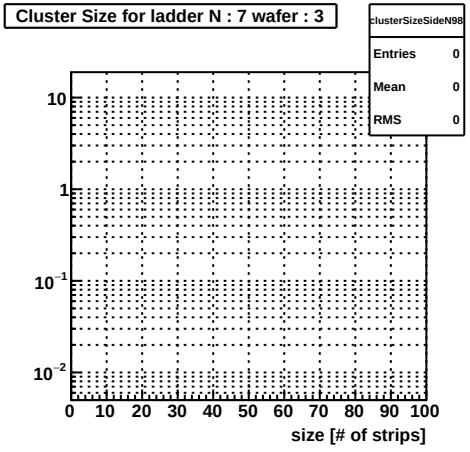

Cluster Size for ladder N : 7 wafer : 2
clusterSizeSideN97
Entries 0
Mean 0
RMS 0
size [# of strips]

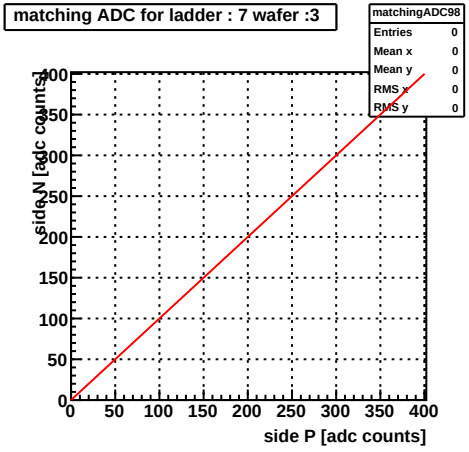

matching ADC for ladder : 7 wafer :2
matchingADC97
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

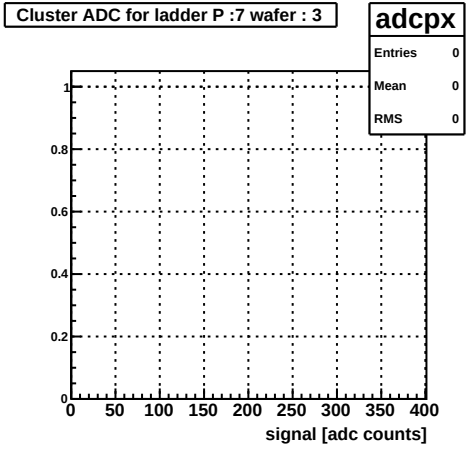

Cluster ADC for ladder P :7 wafer : 2
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

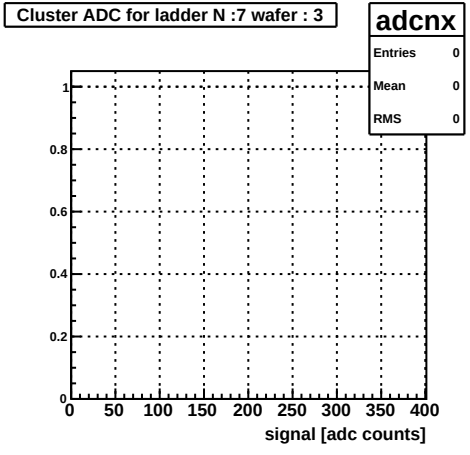

Cluster ADC for ladder N :7 wafer : 2
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

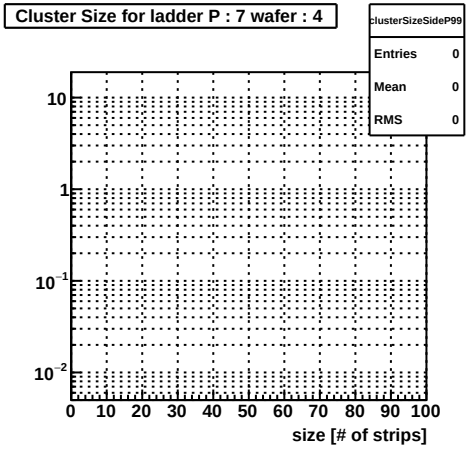

Cluster Size for ladder P : 7 wafer : 3
clusterSizeSideP98
Entries 0
Mean 0
RMS 0
size [# of strips]

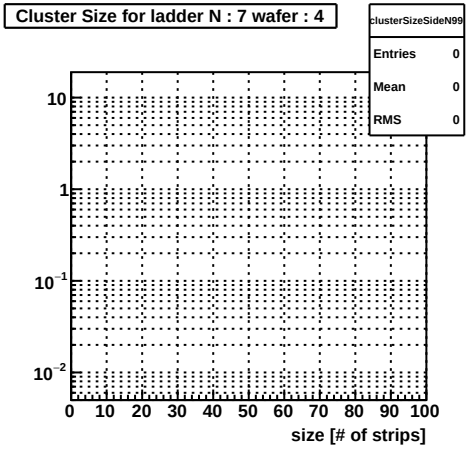

Cluster Size for ladder N : 7 wafer : 3
clusterSizeSideN98
Entries 0
Mean 0
RMS 0
size [# of strips]

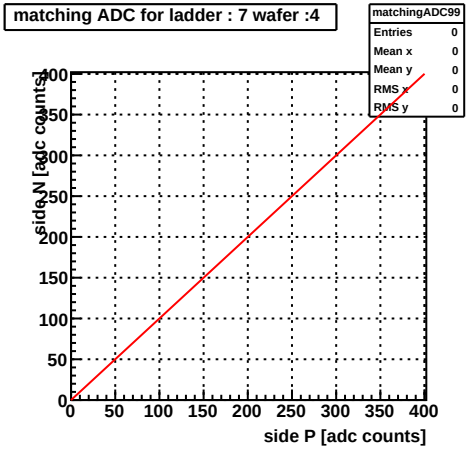

matching ADC for ladder : 7 wafer :3
matchingADC98
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

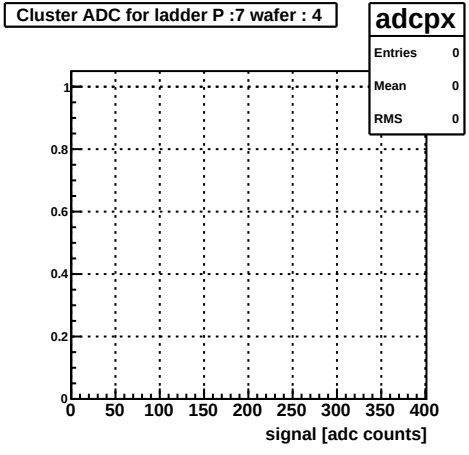

Cluster ADC for ladder P :7 wafer : 3
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

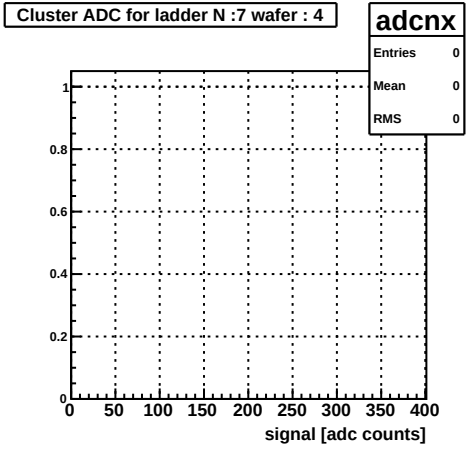

Cluster ADC for ladder N :7 wafer : 3
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

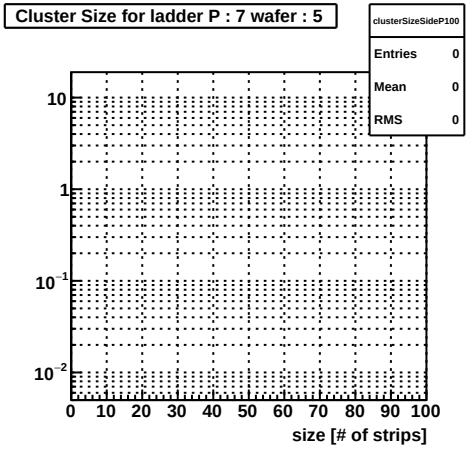

Cluster Size for ladder P : 7 wafer : 4
clusterSizeSideP99
Entries 0
Mean 0
RMS 0
size [# of strips]

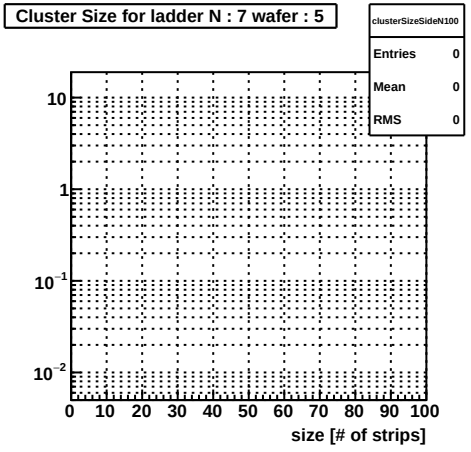

Cluster Size for ladder N : 7 wafer : 4
clusterSizeSideN99
Entries 0
Mean 0
RMS 0
size [# of strips]

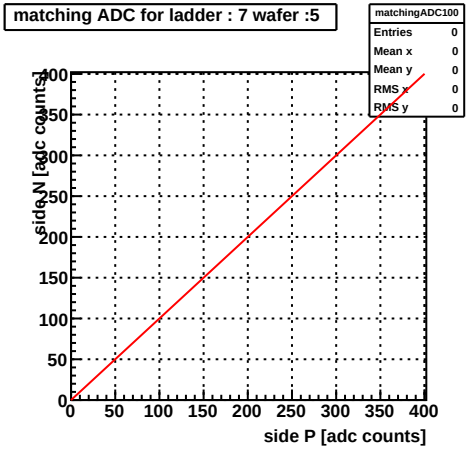

matching ADC for ladder : 7 wafer :4
matchingADC99
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

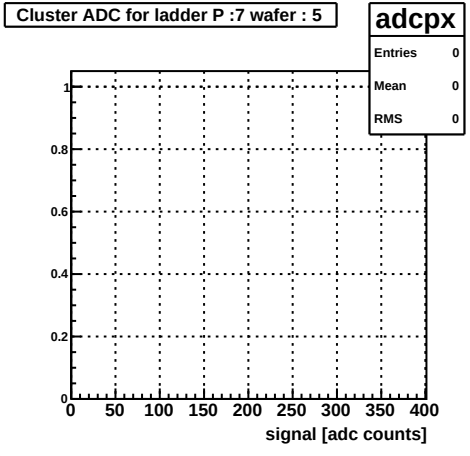

Cluster ADC for ladder P :7 wafer : 4
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

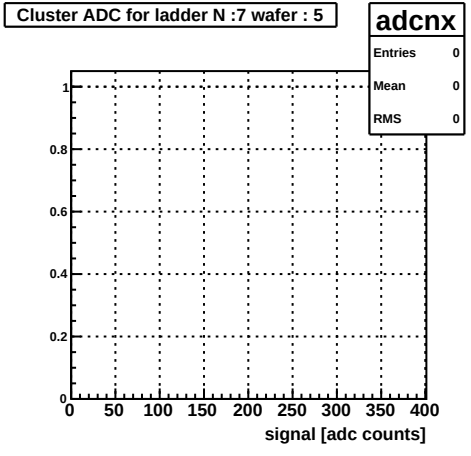

Cluster ADC for ladder N :7 wafer : 4
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

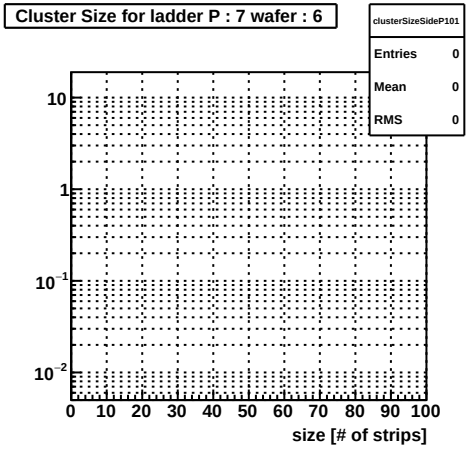

Cluster Size for ladder P : 7 wafer : 5
clusterSizeSideP100
Entries 0
Mean 0
RMS 0
size [# of strips]

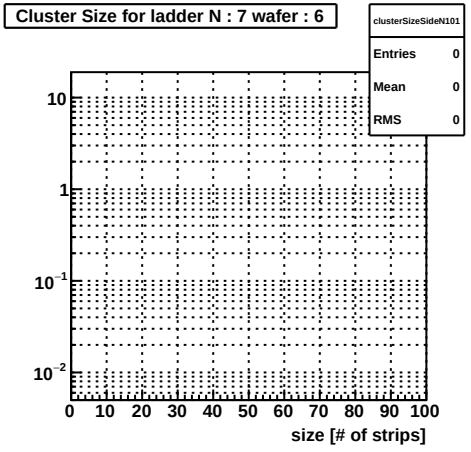

Cluster Size for ladder N : 7 wafer : 5
clusterSizeSideN100
Entries 0
Mean 0
RMS 0
size [# of strips]

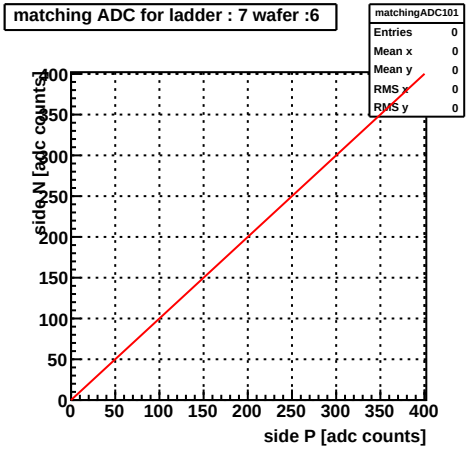

matching ADC for ladder : 7 wafer :5
matchingADC100
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

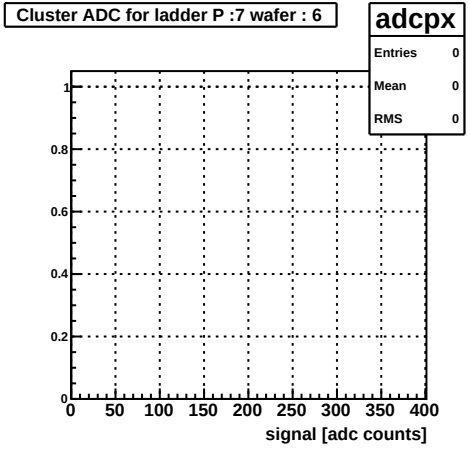

Cluster ADC for ladder P :7 wafer : 5
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

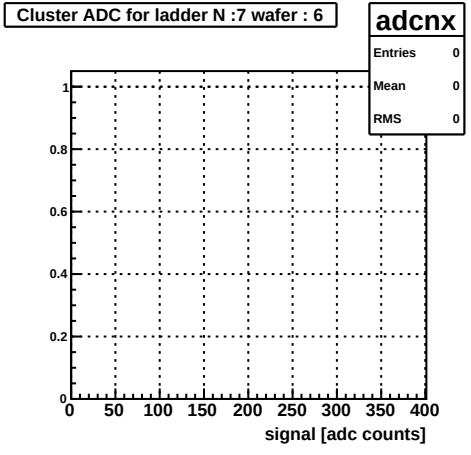

Cluster ADC for ladder N :7 wafer : 5
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

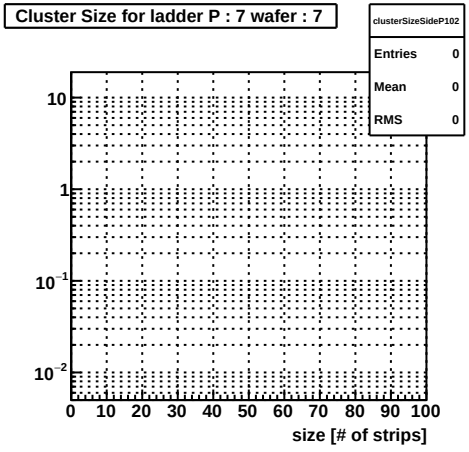

Cluster Size for ladder P : 7 wafer : 6
clusterSizeSideP101
Entries 0
Mean 0
RMS 0
size [# of strips]

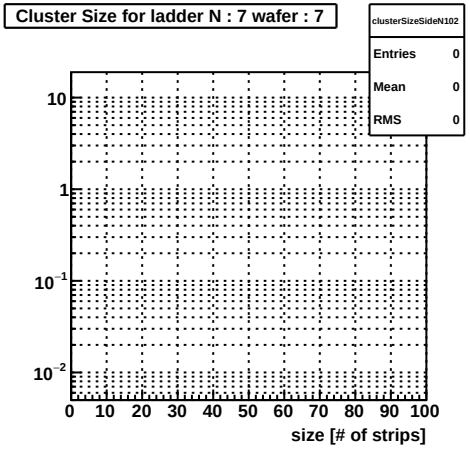

Cluster Size for ladder N : 7 wafer : 6
clusterSizeSideN101
Entries 0
Mean 0
RMS 0
size [# of strips]

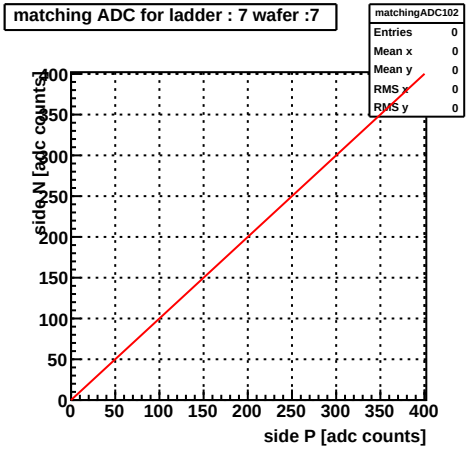

matching ADC for ladder : 7 wafer :6
matchingADC101
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

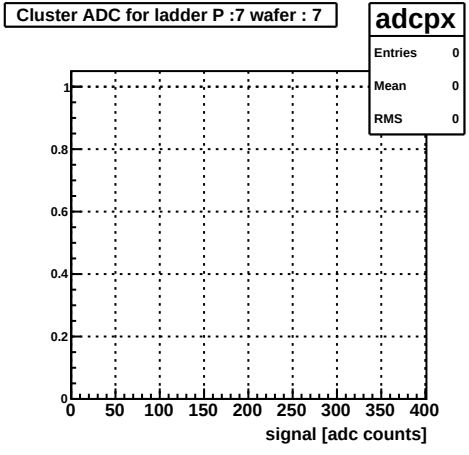

Cluster ADC for ladder P :7 wafer : 6
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

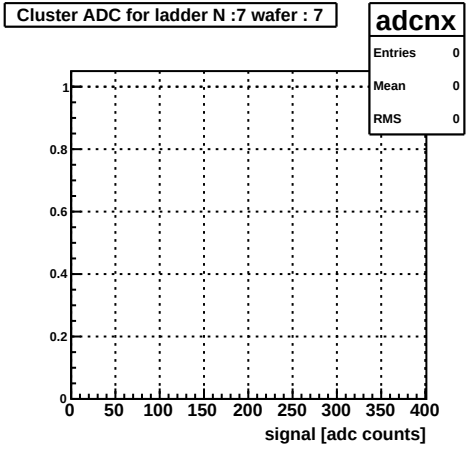

Cluster ADC for ladder N :7 wafer : 6
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

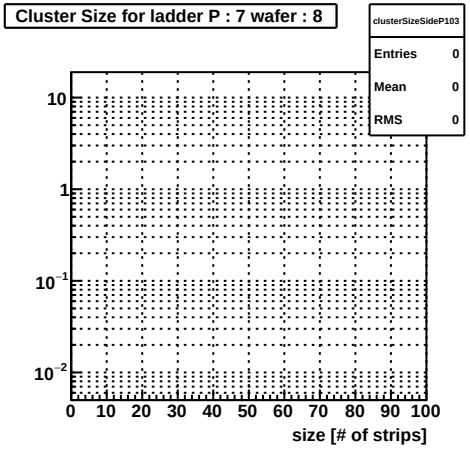

Cluster Size for ladder P : 7 wafer : 7
clusterSizeSideP102
Entries 0
Mean 0
RMS 0
size [# of strips]

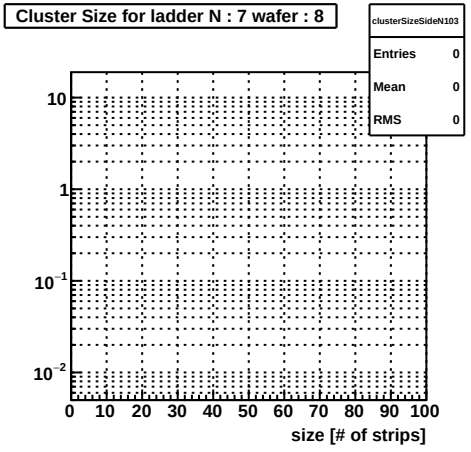

Cluster Size for ladder N : 7 wafer : 7
clusterSizeSideN102
Entries 0
Mean 0
RMS 0
size [# of strips]

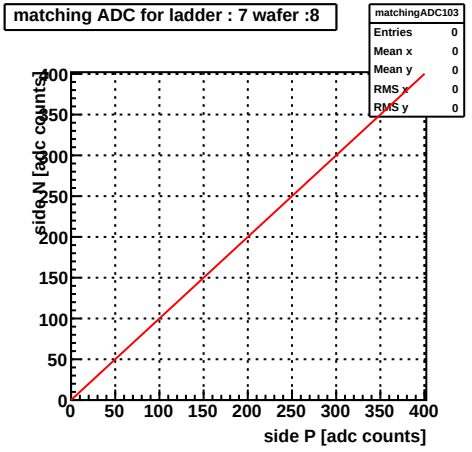

matching ADC for ladder : 7 wafer :7
matchingADC102
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

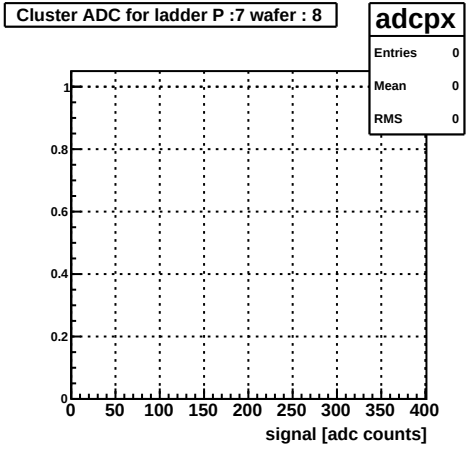

Cluster ADC for ladder P :7 wafer : 7
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

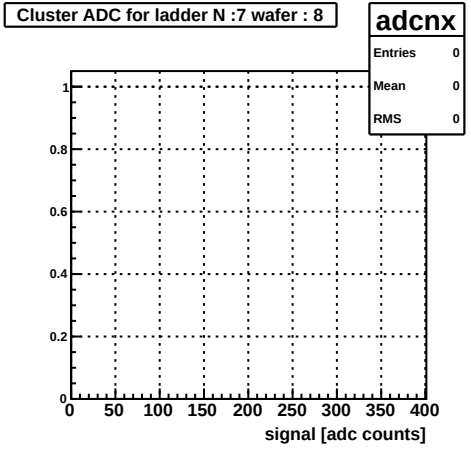

Cluster ADC for ladder N :7 wafer : 7
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

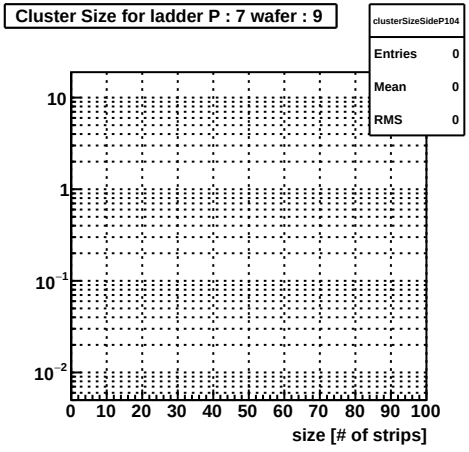

Cluster Size for ladder P : 7 wafer : 8
clusterSizeSideP103
Entries 0
Mean 0
RMS 0
size [# of strips]

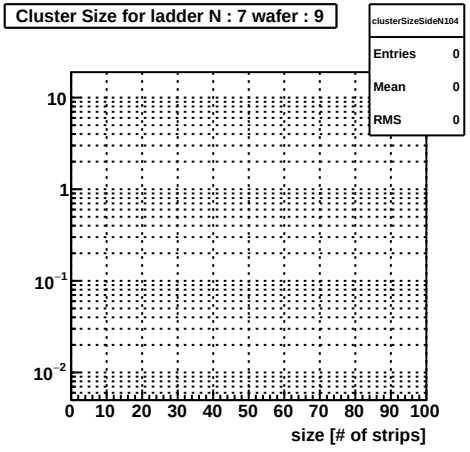

Cluster Size for ladder N : 7 wafer : 8
clusterSizeSideN103
Entries 0
Mean 0
RMS 0
size [# of strips]

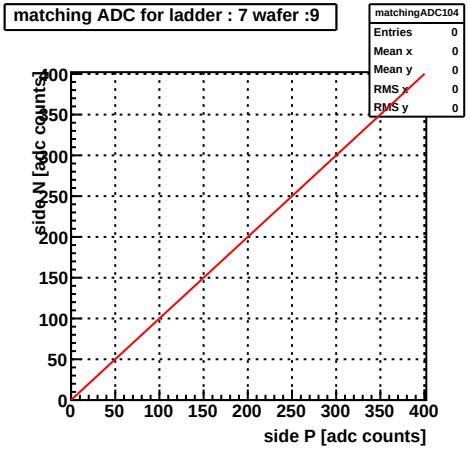

matching ADC for ladder : 7 wafer :8
matchingADC103
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

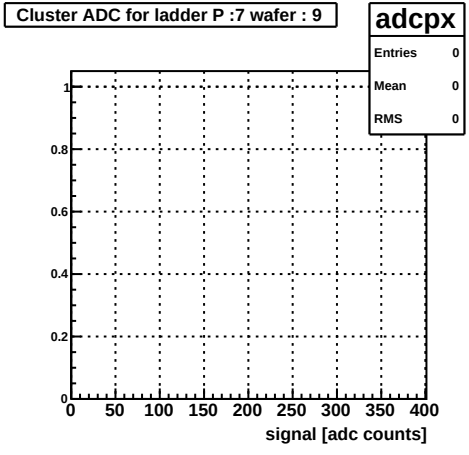

Cluster ADC for ladder P :7 wafer : 8
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

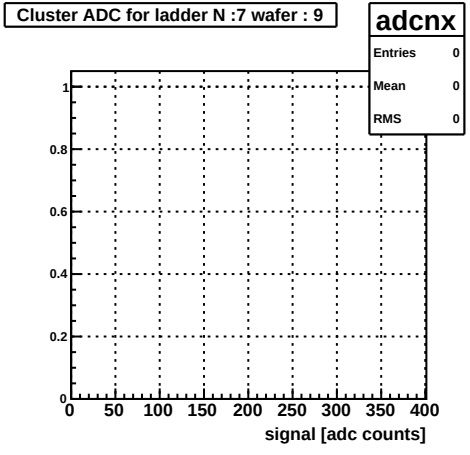

Cluster ADC for ladder N :7 wafer : 8
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

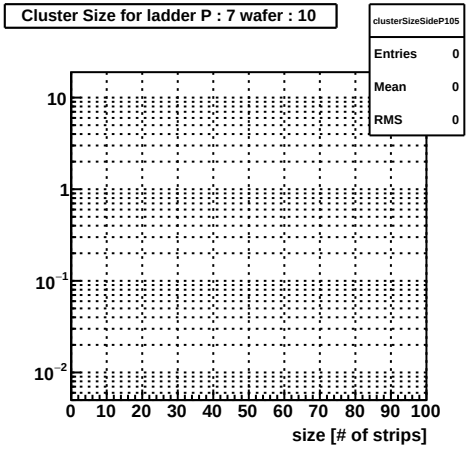

Cluster Size for ladder P : 7 wafer : 9
clusterSizeSideP104
Entries 0
Mean 0
RMS 0
size [# of strips]

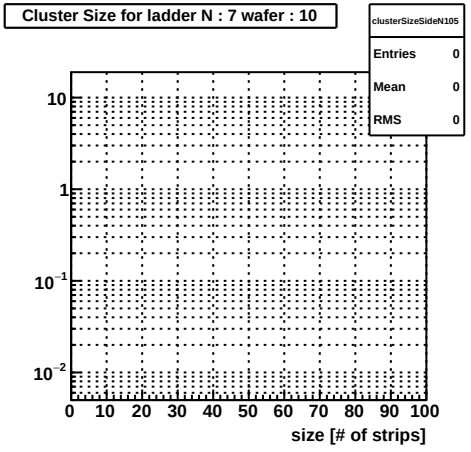

Cluster Size for ladder N : 7 wafer : 9
clusterSizeSideN104
Entries 0
Mean 0
RMS 0
size [# of strips]

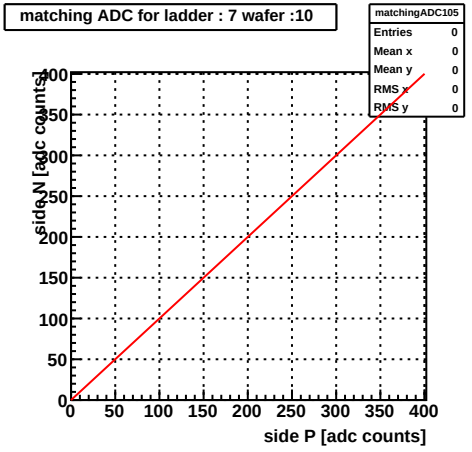

matching ADC for ladder : 7 wafer :9
matchingADC104
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

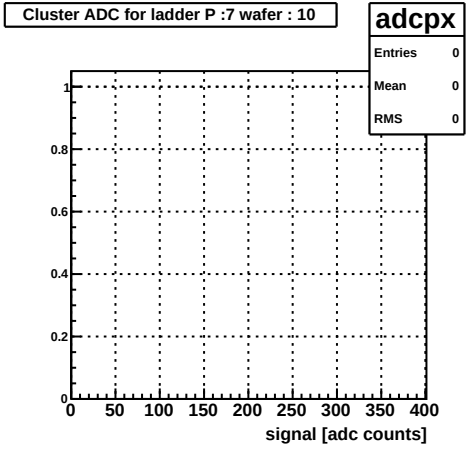

Cluster ADC for ladder P :7 wafer : 9
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

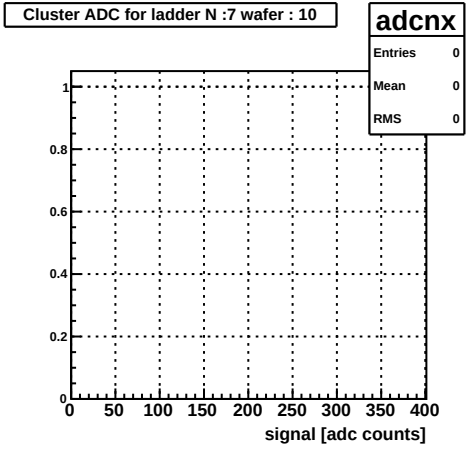

Cluster ADC for ladder N :7 wafer : 9
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

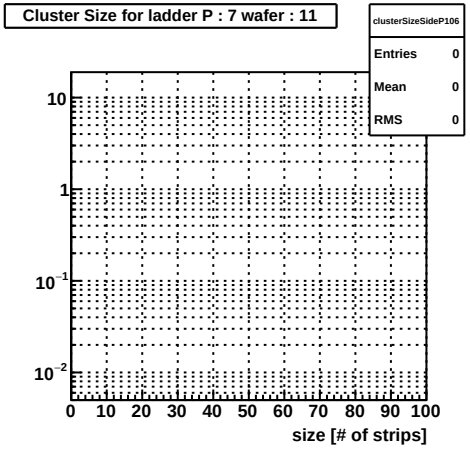

Cluster Size for ladder P : 7 wafer : 10
clusterSizeSideP105
Entries 0
Mean 0
RMS 0
size [# of strips]

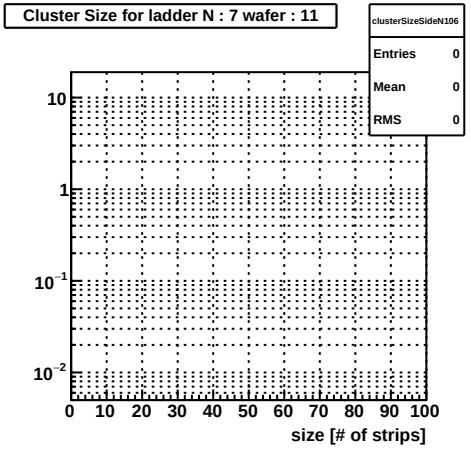

Cluster Size for ladder N : 7 wafer : 10
clusterSizeSideN105
Entries 0
Mean 0
RMS 0
size [# of strips]

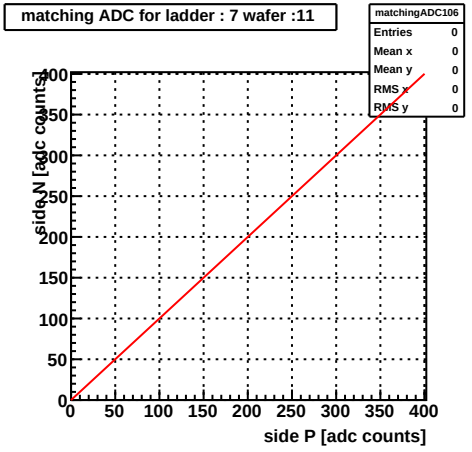

matching ADC for ladder : 7 wafer :10
matchingADC105
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

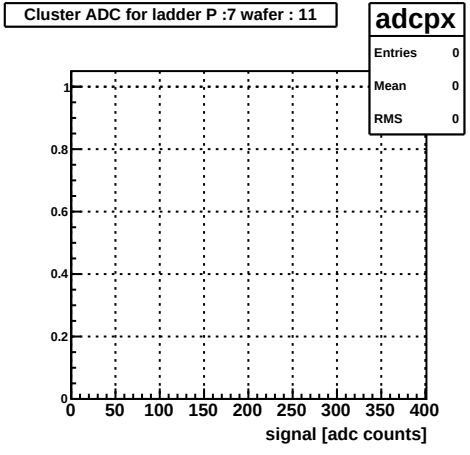

Cluster ADC for ladder P :7 wafer : 10
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

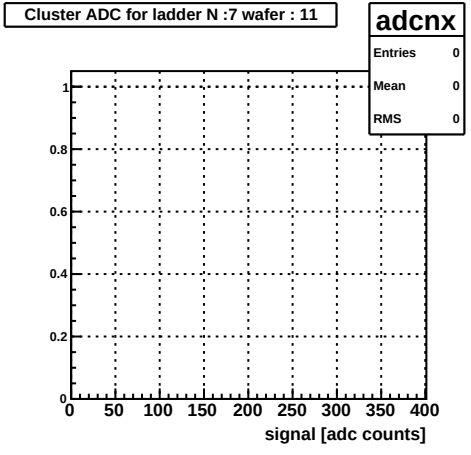

Cluster ADC for ladder N :7 wafer : 10
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

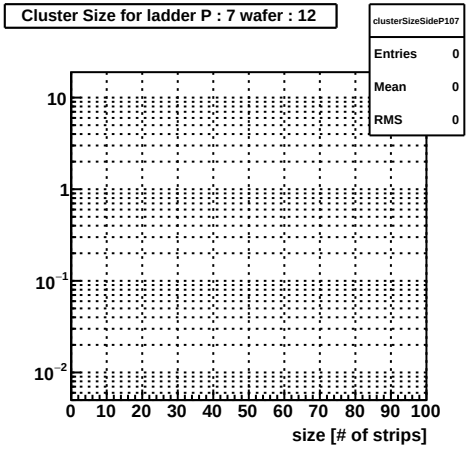

Cluster Size for ladder P : 7 wafer : 11
clusterSizeSideP106
Entries 0
Mean 0
RMS 0
size [# of strips]

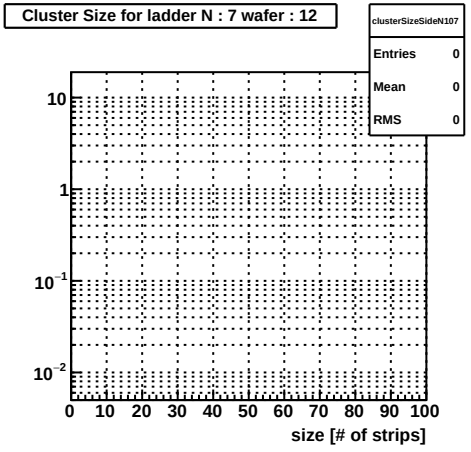

Cluster Size for ladder N : 7 wafer : 11
clusterSizeSideN106
Entries 0
Mean 0
RMS 0
size [# of strips]

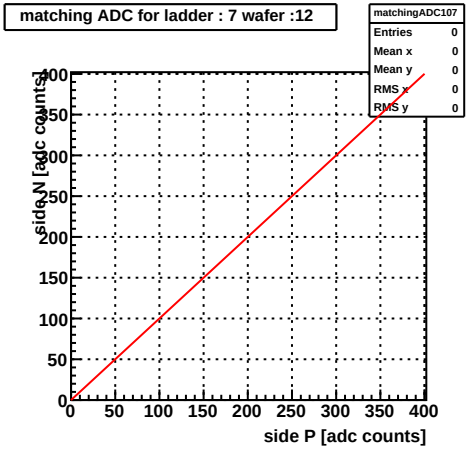

matching ADC for ladder : 7 wafer :11
matchingADC106
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

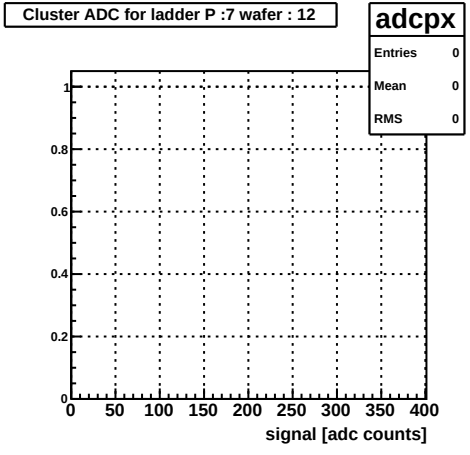

Cluster ADC for ladder P :7 wafer : 11
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

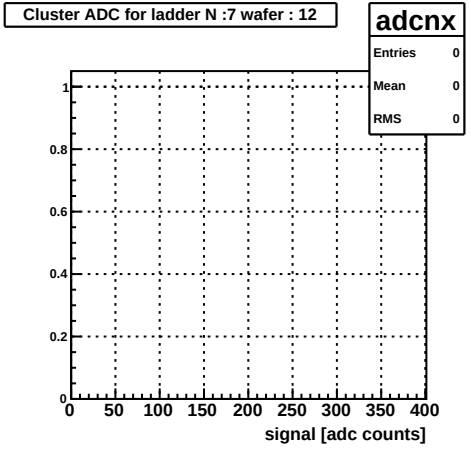

Cluster ADC for ladder N :7 wafer : 11
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

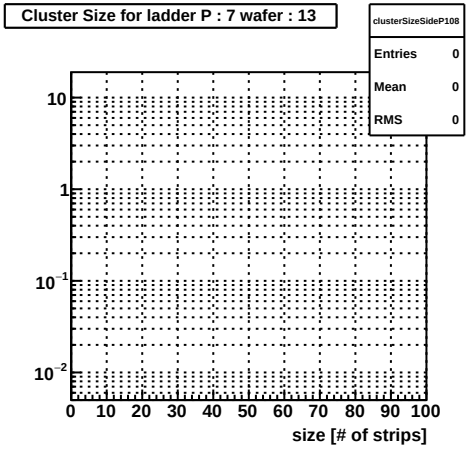

Cluster Size for ladder P : 7 wafer : 12
clusterSizeSideP107
Entries 0
Mean 0
RMS 0
size [# of strips]

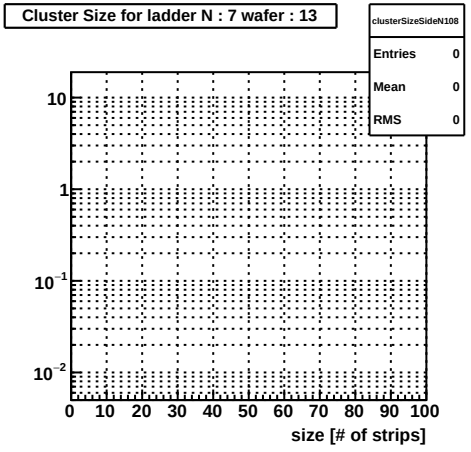

Cluster Size for ladder N : 7 wafer : 12
clusterSizeSideN107
Entries 0
Mean 0
RMS 0
size [# of strips]

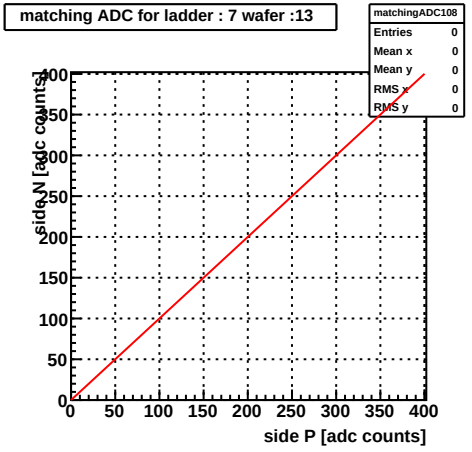

matching ADC for ladder : 7 wafer :12
matchingADC107
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

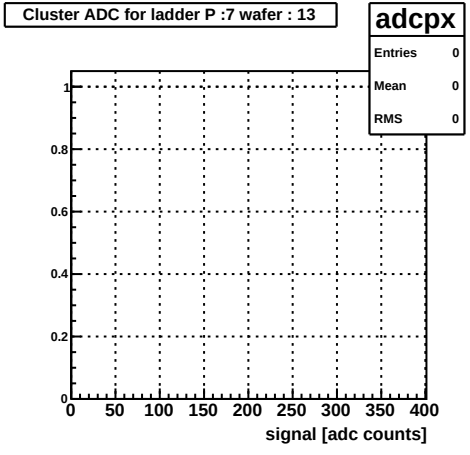

Cluster ADC for ladder P :7 wafer : 12
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

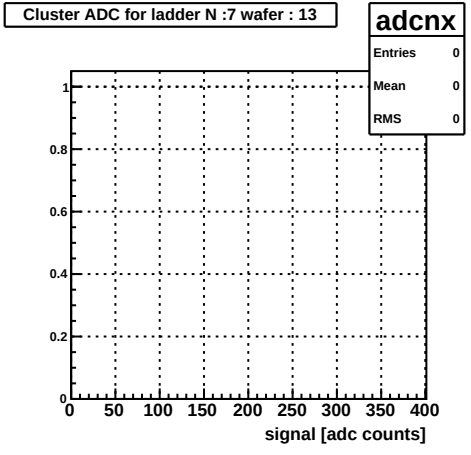

Cluster ADC for ladder N :7 wafer : 12
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

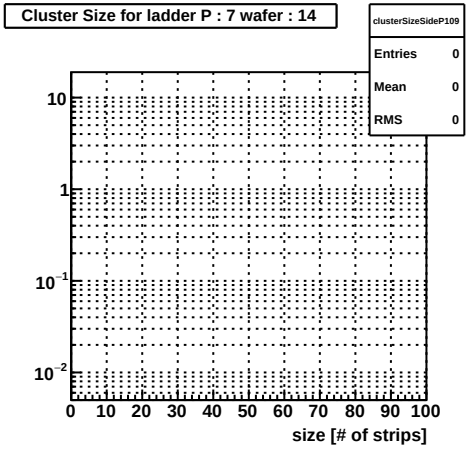

Cluster Size for ladder P : 7 wafer : 13
clusterSizeSideP108
Entries 0
Mean 0
RMS 0
size [# of strips]

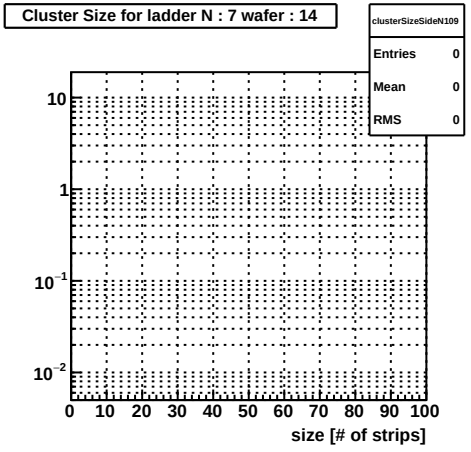

Cluster Size for ladder N : 7 wafer : 13
clusterSizeSideN108
Entries 0
Mean 0
RMS 0
size [# of strips]

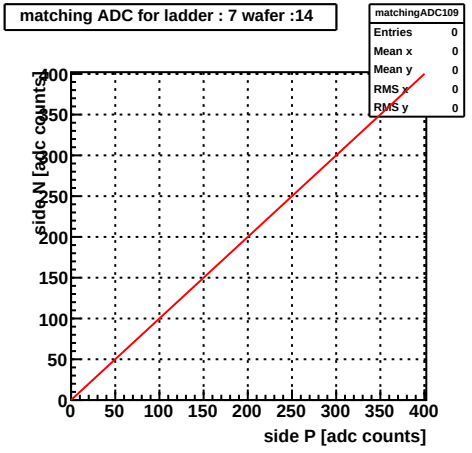

matching ADC for ladder : 7 wafer :13
matchingADC108
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

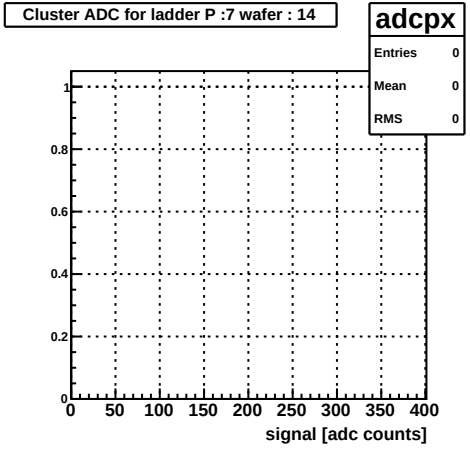

Cluster ADC for ladder P :7 wafer : 13
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

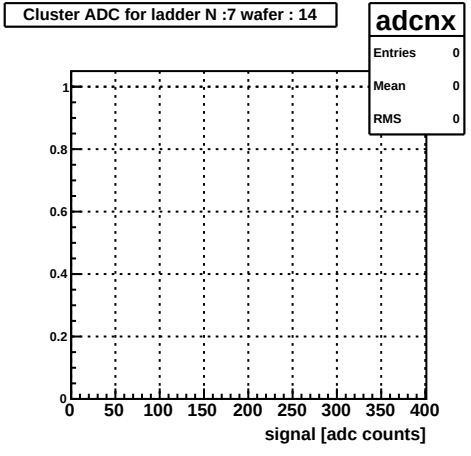

Cluster ADC for ladder N :7 wafer : 13
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

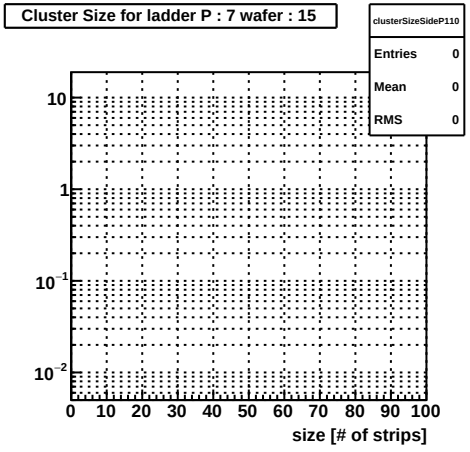

Cluster Size for ladder P : 7 wafer : 14
clusterSizeSideP109
Entries 0
Mean 0
RMS 0
size [# of strips]

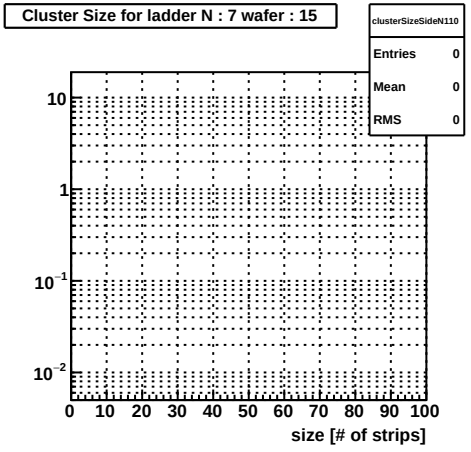

Cluster Size for ladder N : 7 wafer : 14
clusterSizeSideN109
Entries 0
Mean 0
RMS 0
size [# of strips]

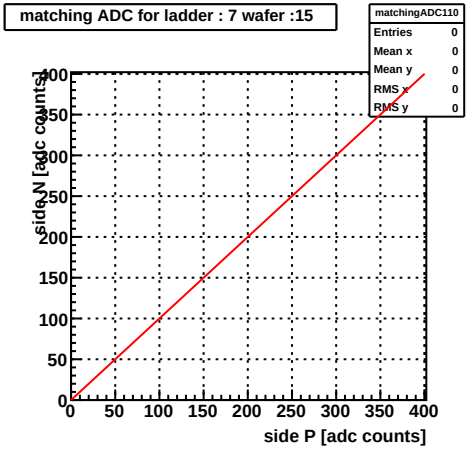

matching ADC for ladder : 7 wafer :14
matchingADC109
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

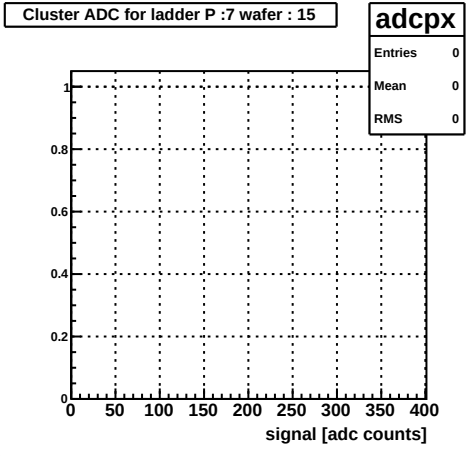

Cluster ADC for ladder P :7 wafer : 14
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

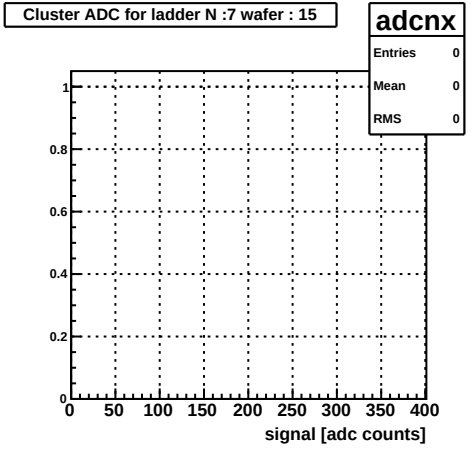

Cluster ADC for ladder N :7 wafer : 14
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

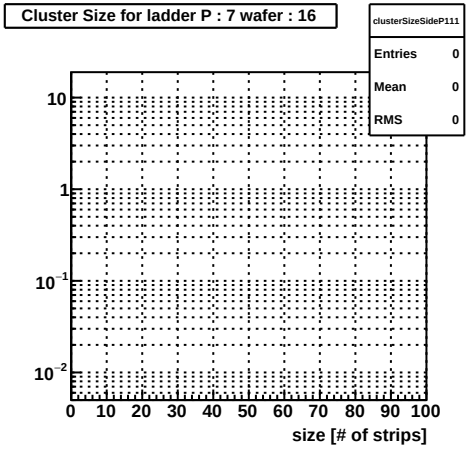

Cluster Size for ladder P : 7 wafer : 15
clusterSizeSideP110
Entries 0
Mean 0
RMS 0
size [# of strips]

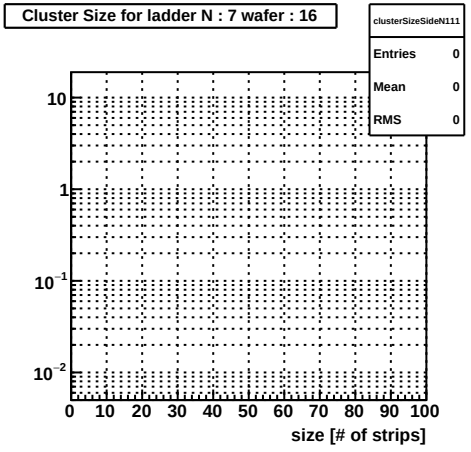

Cluster Size for ladder N : 7 wafer : 15
clusterSizeSideN110
Entries 0
Mean 0
RMS 0
size [# of strips]

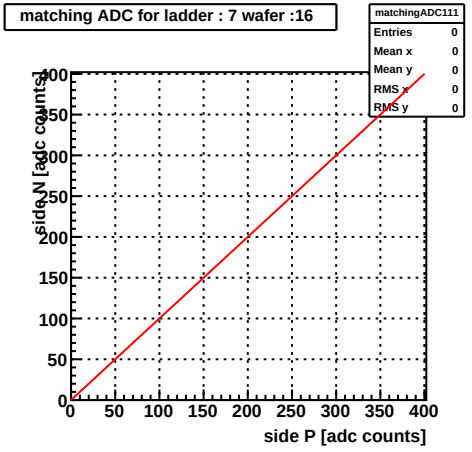

matching ADC for ladder : 7 wafer :15
matchingADC110
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

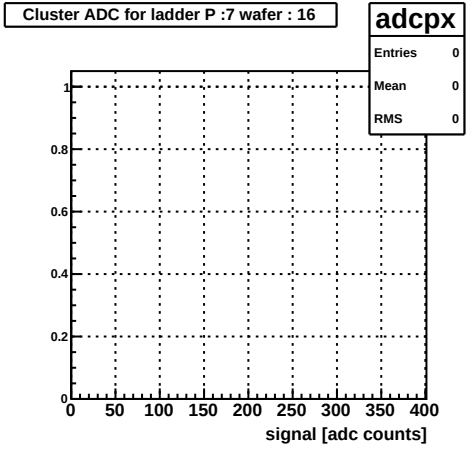

Cluster ADC for ladder P :7 wafer : 15
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

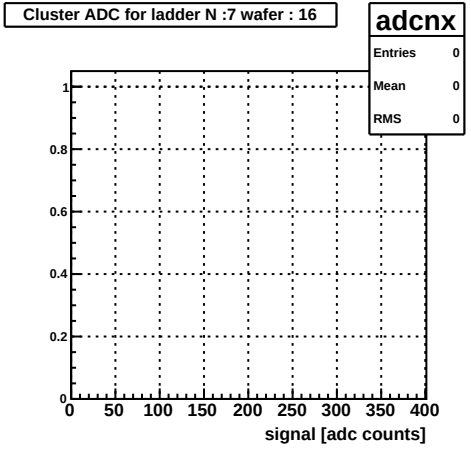

Cluster ADC for ladder N :7 wafer : 15
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

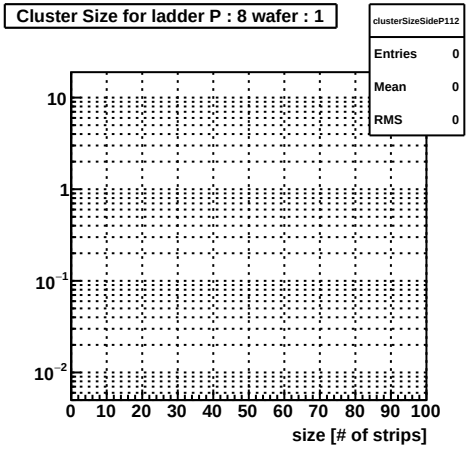

Cluster Size for ladder P : 7 wafer : 16
clusterSizeSideP111
Entries 0
Mean 0
RMS 0
size [# of strips]

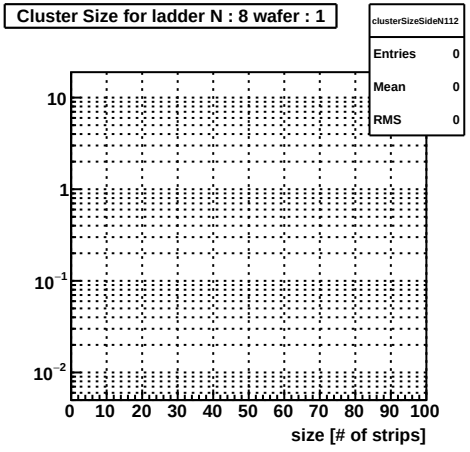

Cluster Size for ladder N : 7 wafer : 16
clusterSizeSideN111
Entries 0
Mean 0
RMS 0
size [# of strips]

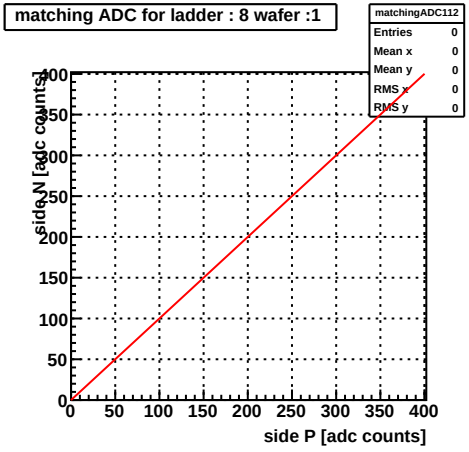

matching ADC for ladder : 7 wafer :16
matchingADC111
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

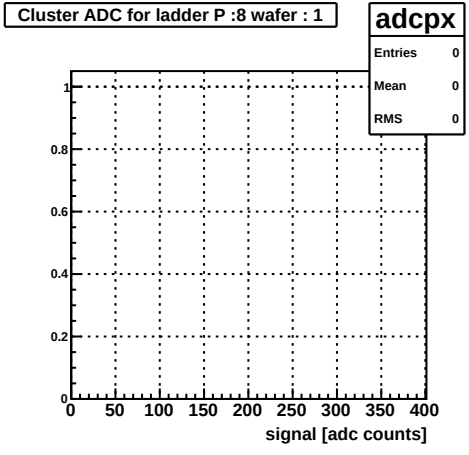

Cluster ADC for ladder P :7 wafer : 16
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

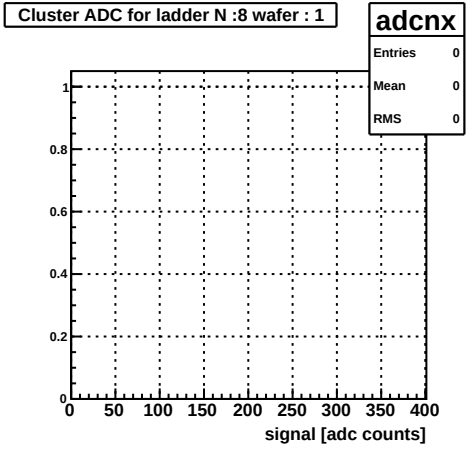

Cluster ADC for ladder N :7 wafer : 16
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

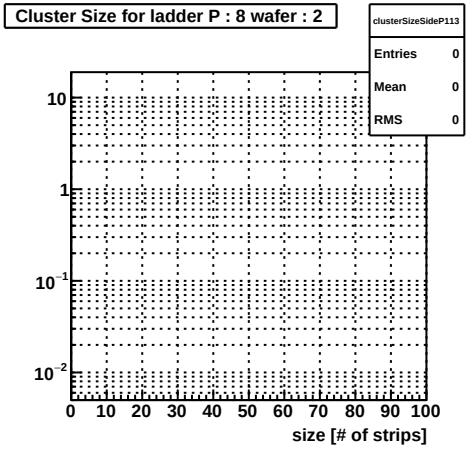

Cluster Size for ladder P : 8 wafer : 1
clusterSizeSideP112
Entries 0
Mean 0
RMS 0
size [# of strips]

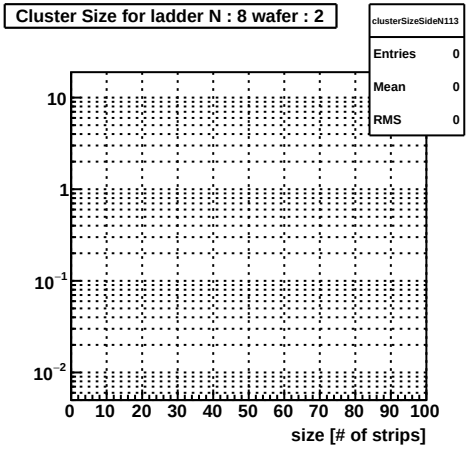

Cluster Size for ladder N : 8 wafer : 1
clusterSizeSideN112
Entries 0
Mean 0
RMS 0
size [# of strips]

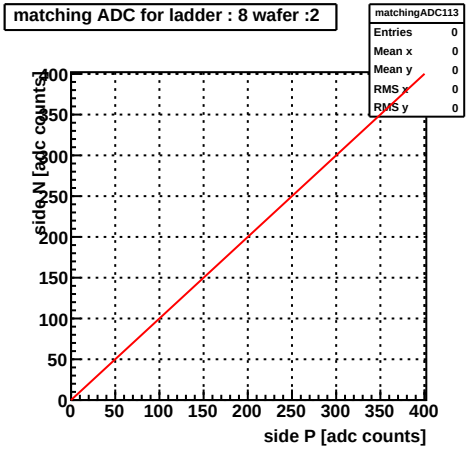

matching ADC for ladder : 8 wafer :1
matchingADC112
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

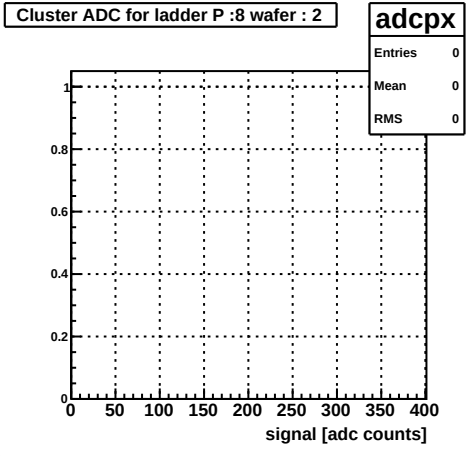

Cluster ADC for ladder P :8 wafer : 1
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

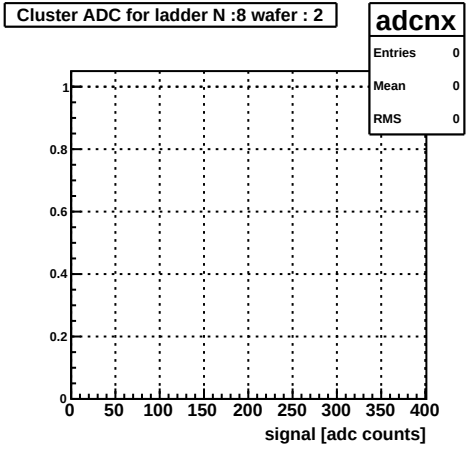

Cluster ADC for ladder N :8 wafer : 1
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

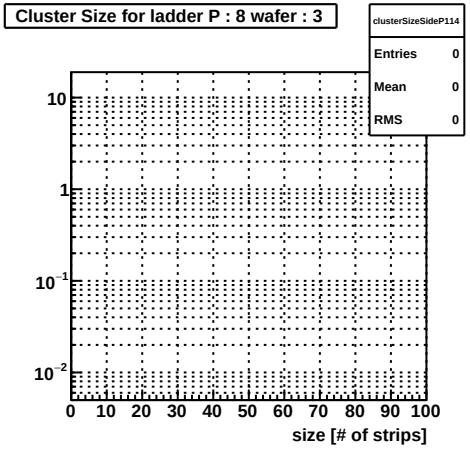

Cluster Size for ladder P : 8 wafer : 2
clusterSizeSideP113
Entries 0
Mean 0
RMS 0
size [# of strips]

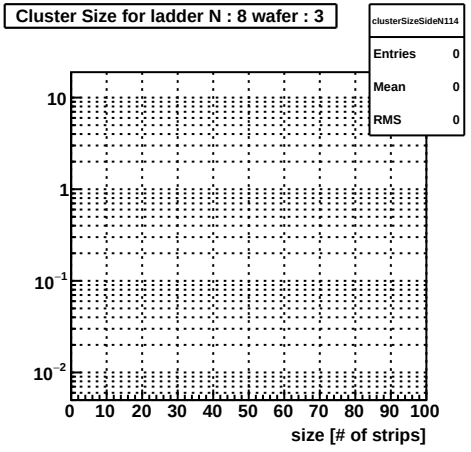

Cluster Size for ladder N : 8 wafer : 2
clusterSizeSideN113
Entries 0
Mean 0
RMS 0
size [# of strips]

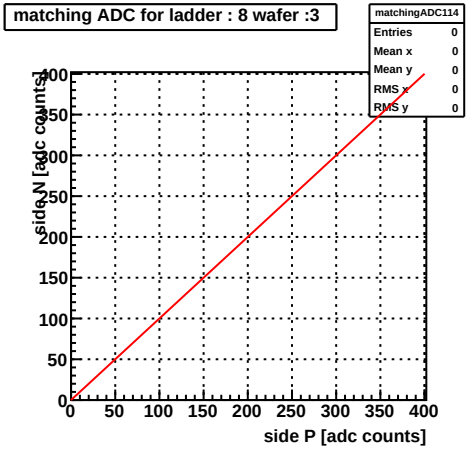

matching ADC for ladder : 8 wafer :2
matchingADC113
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

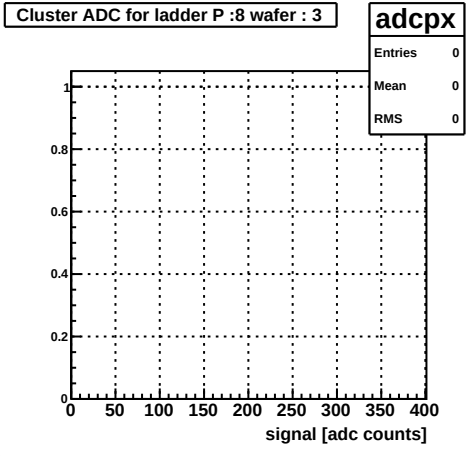

Cluster ADC for ladder P :8 wafer : 2
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

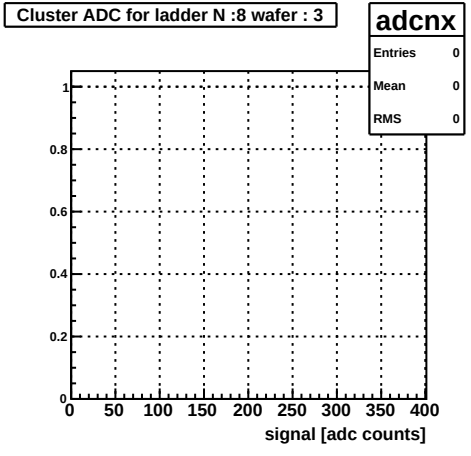

Cluster ADC for ladder N :8 wafer : 2
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

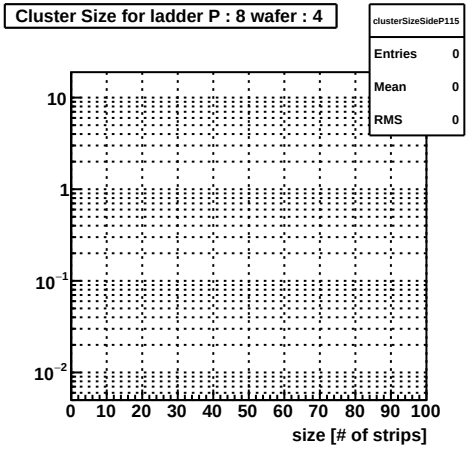

Cluster Size for ladder P : 8 wafer : 3
clusterSizeSideP114
Entries 0
Mean 0
RMS 0
size [# of strips]

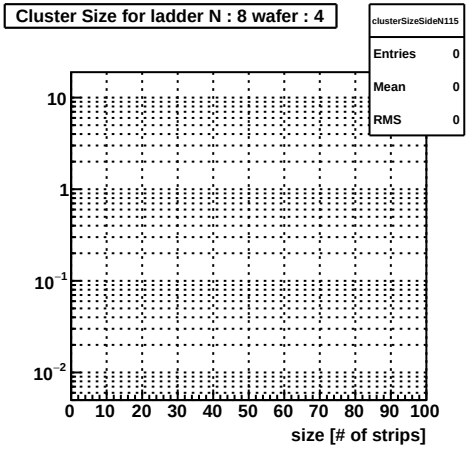

Cluster Size for ladder N : 8 wafer : 3
clusterSizeSideN114
Entries 0
Mean 0
RMS 0
size [# of strips]

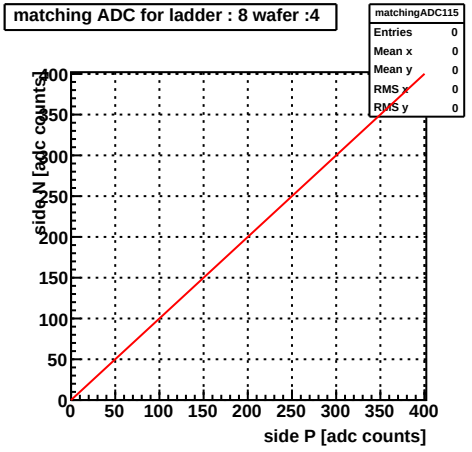

matching ADC for ladder : 8 wafer :3
matchingADC114
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

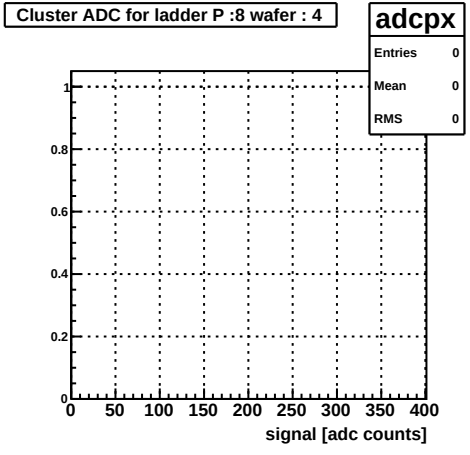

Cluster ADC for ladder P :8 wafer : 3
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

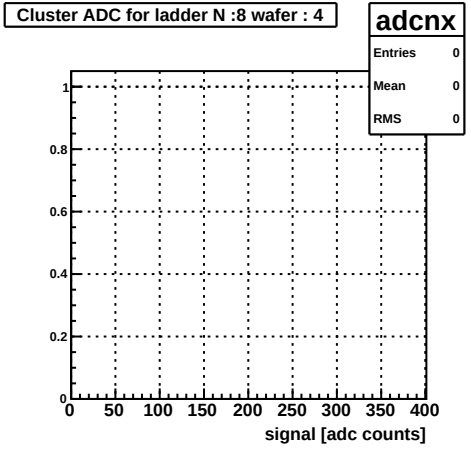

Cluster ADC for ladder N :8 wafer : 3
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

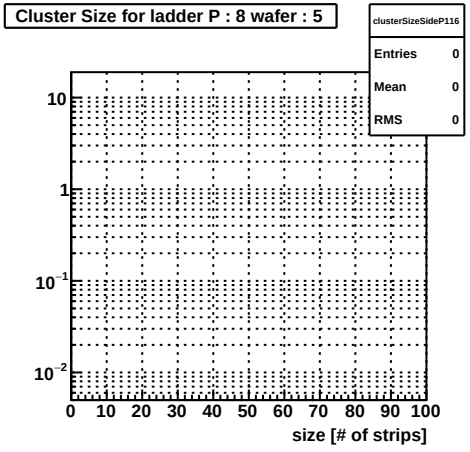

Cluster Size for ladder P : 8 wafer : 4
clusterSizeSideP115
Entries 0
Mean 0
RMS 0
size [# of strips]

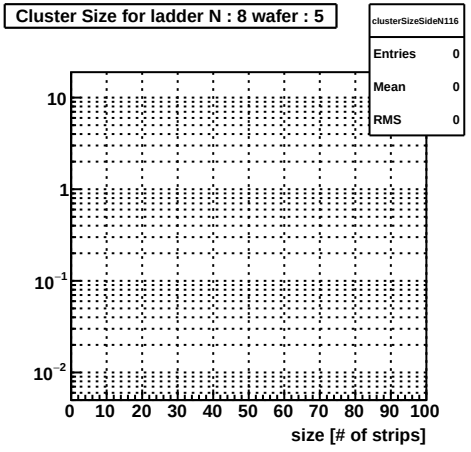

Cluster Size for ladder N : 8 wafer : 4
clusterSizeSideN115
Entries 0
Mean 0
RMS 0
size [# of strips]

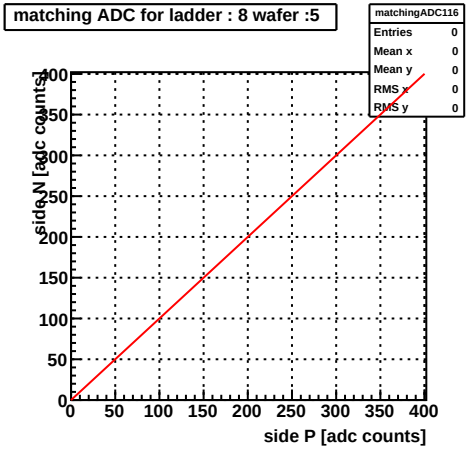

matching ADC for ladder : 8 wafer :4
matchingADC115
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

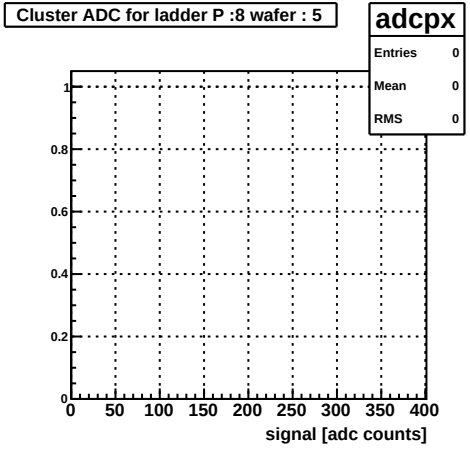

Cluster ADC for ladder P :8 wafer : 4
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

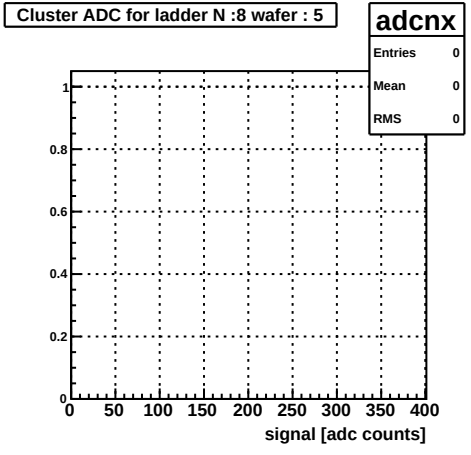

Cluster ADC for ladder N :8 wafer : 4
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

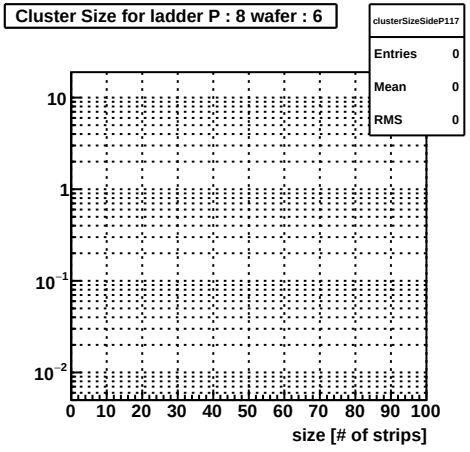

Cluster Size for ladder P : 8 wafer : 5
clusterSizeSideP116
Entries 0
Mean 0
RMS 0
size [# of strips]

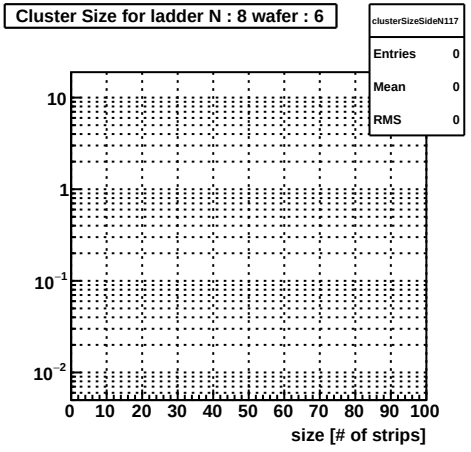

Cluster Size for ladder N : 8 wafer : 5
clusterSizeSideN116
Entries 0
Mean 0
RMS 0
size [# of strips]

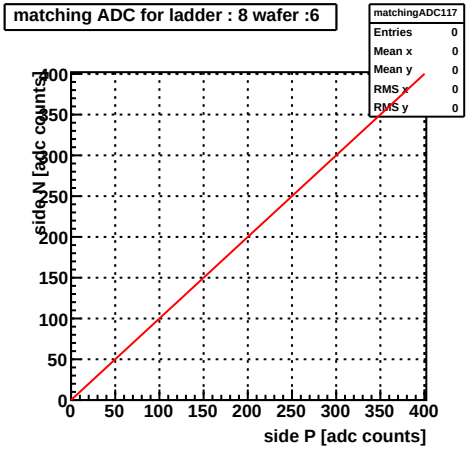

matching ADC for ladder : 8 wafer :5
matchingADC116
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

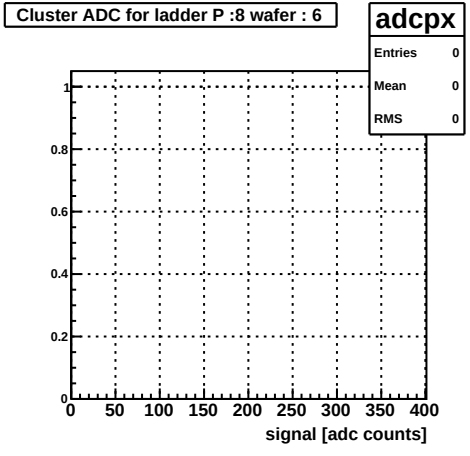

Cluster ADC for ladder P :8 wafer : 5
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

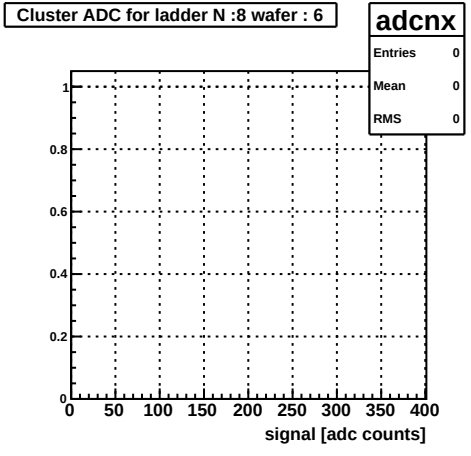

Cluster ADC for ladder N :8 wafer : 5
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

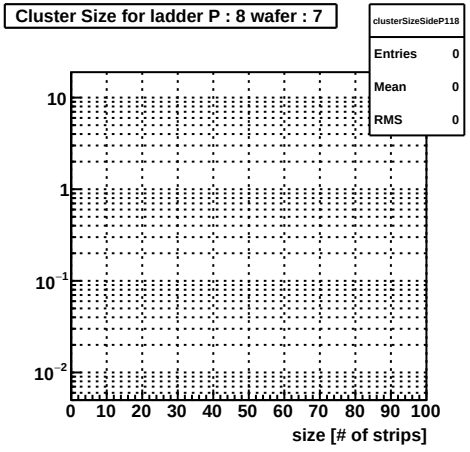

Cluster Size for ladder P : 8 wafer : 6
clusterSizeSideP117
Entries 0
Mean 0
RMS 0
size [# of strips]

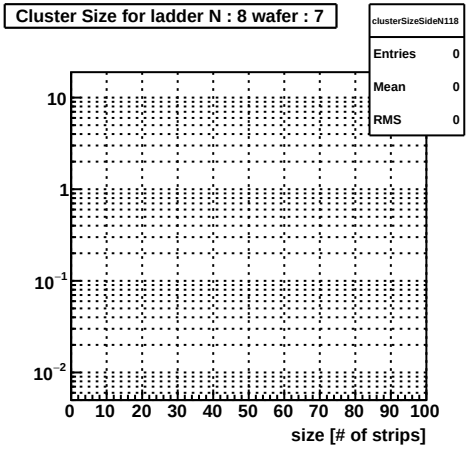

Cluster Size for ladder N : 8 wafer : 6
clusterSizeSideN117
Entries 0
Mean 0
RMS 0
size [# of strips]

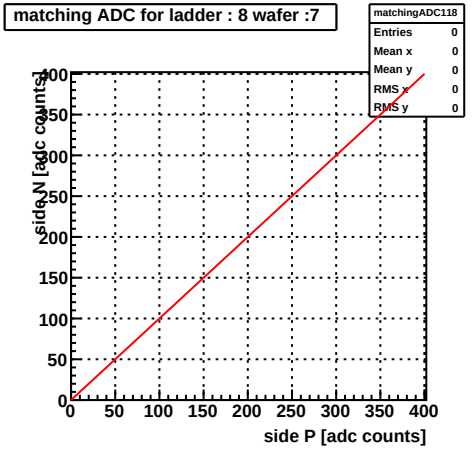

matching ADC for ladder : 8 wafer :6
matchingADC117
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

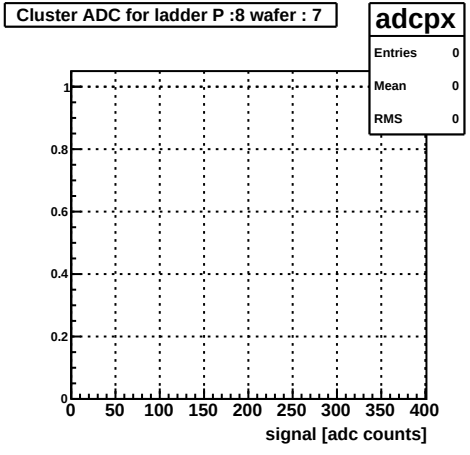

Cluster ADC for ladder P :8 wafer : 6
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

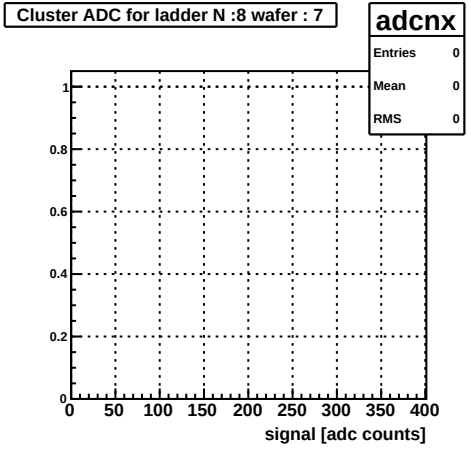

Cluster ADC for ladder N :8 wafer : 6
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

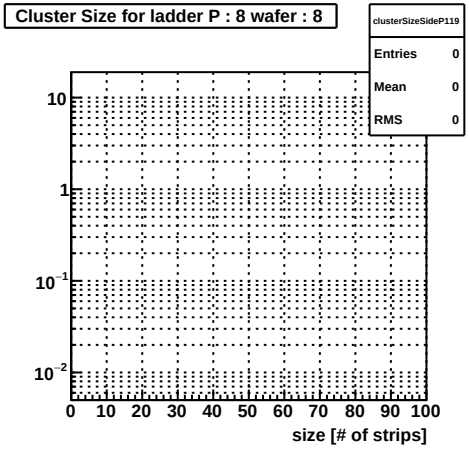

Cluster Size for ladder P : 8 wafer : 7
clusterSizeSideP118
Entries 0
Mean 0
RMS 0
size [# of strips]

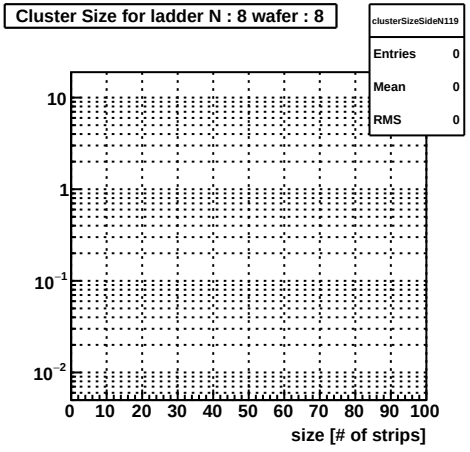

Cluster Size for ladder N : 8 wafer : 7
clusterSizeSideN118
Entries 0
Mean 0
RMS 0
size [# of strips]

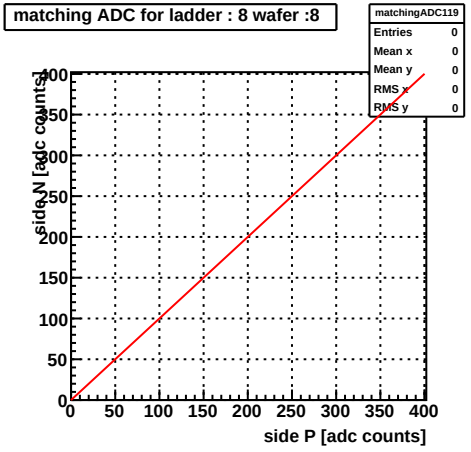

matching ADC for ladder : 8 wafer :7
matchingADC118
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

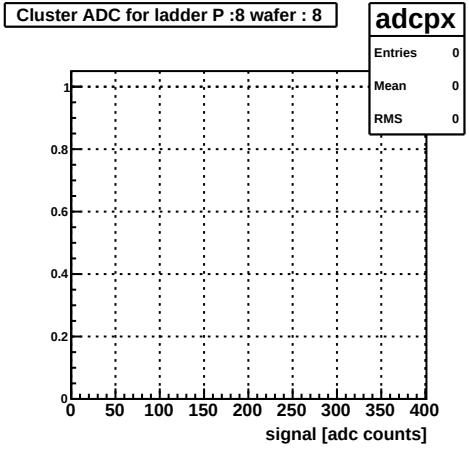

Cluster ADC for ladder P :8 wafer : 7
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

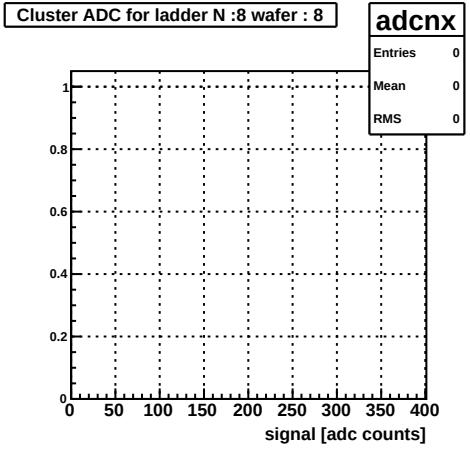

Cluster ADC for ladder N :8 wafer : 7
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

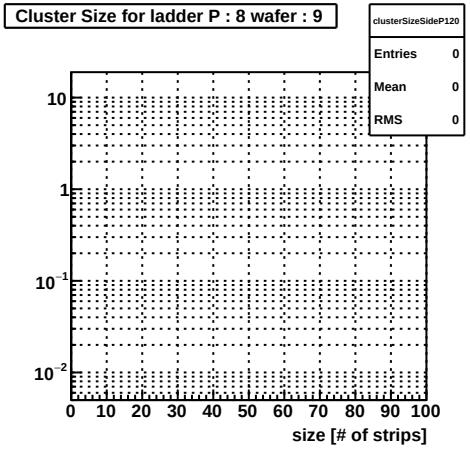

Cluster Size for ladder P : 8 wafer : 8
clusterSizeSideP119
Entries 0
Mean 0
RMS 0
size [# of strips]

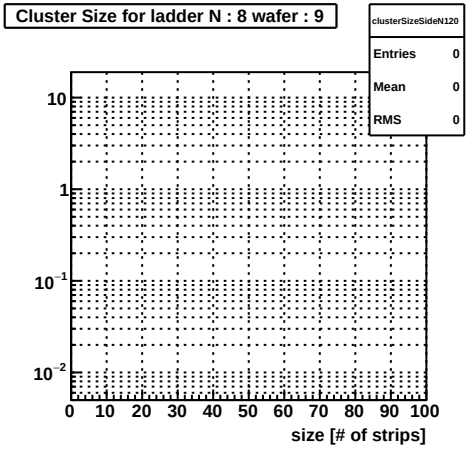

Cluster Size for ladder N : 8 wafer : 8
clusterSizeSideN119
Entries 0
Mean 0
RMS 0
size [# of strips]

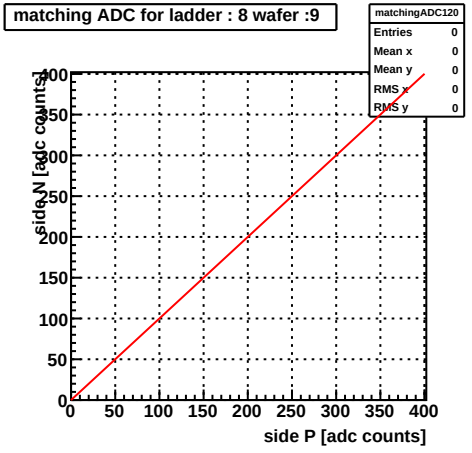

matching ADC for ladder : 8 wafer :8
matchingADC119
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

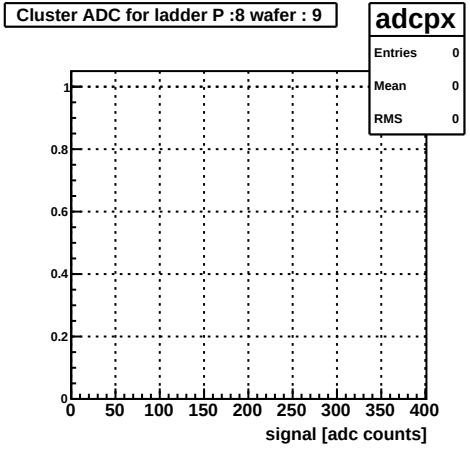

Cluster ADC for ladder P :8 wafer : 8
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

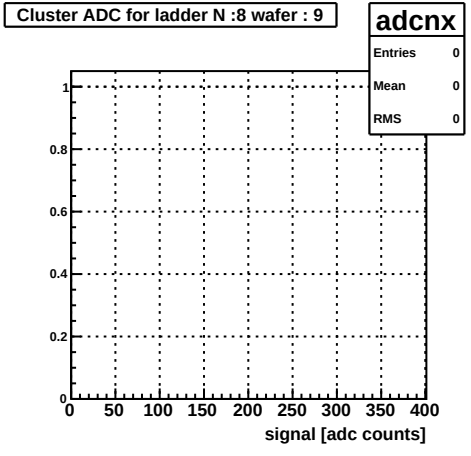

Cluster ADC for ladder N :8 wafer : 8
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

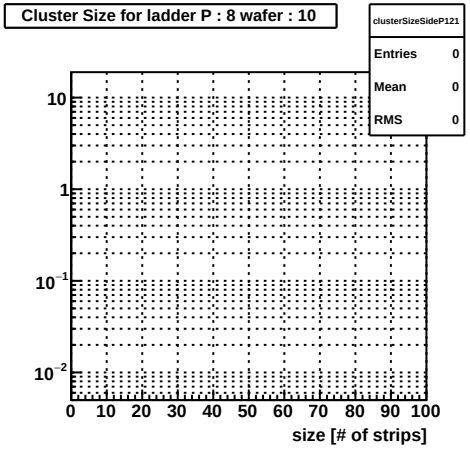

Cluster Size for ladder P : 8 wafer : 9
clusterSizeSideP120
Entries 0
Mean 0
RMS 0
size [# of strips]

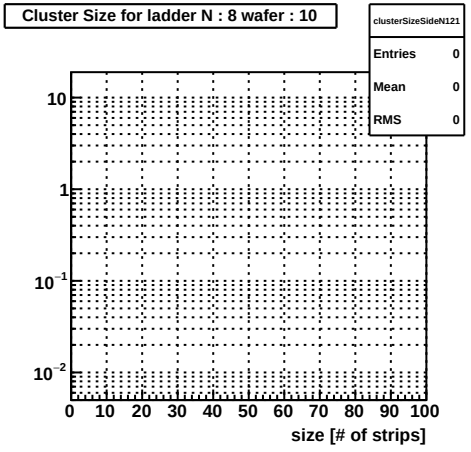

Cluster Size for ladder N : 8 wafer : 9
clusterSizeSideN120
Entries 0
Mean 0
RMS 0
size [# of strips]

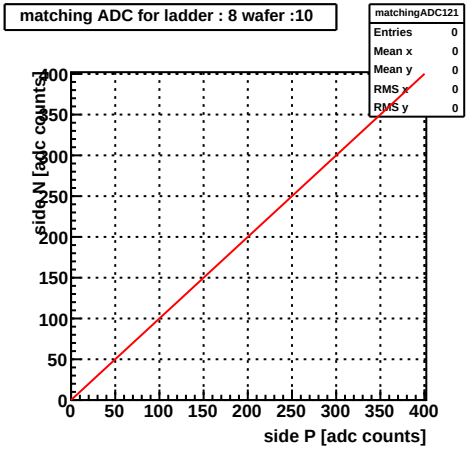

matching ADC for ladder : 8 wafer :9
matchingADC120
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

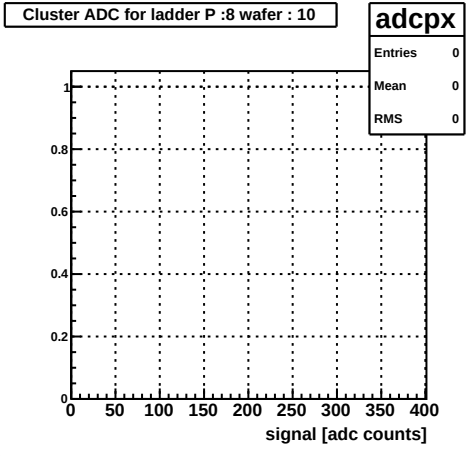

Cluster ADC for ladder P :8 wafer : 9
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

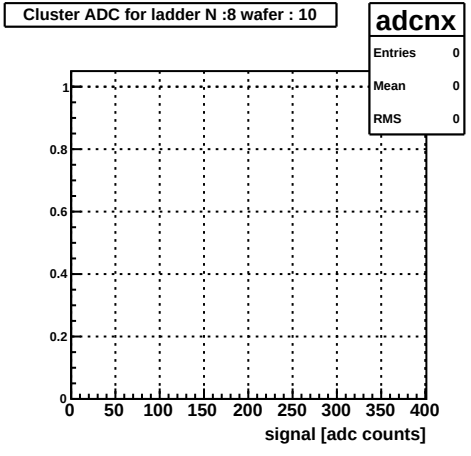

Cluster ADC for ladder N :8 wafer : 9
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

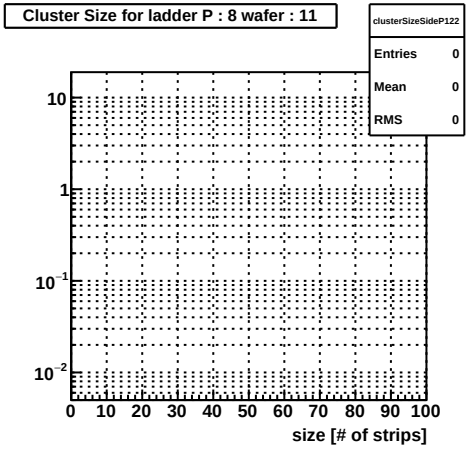

Cluster Size for ladder P : 8 wafer : 10
clusterSizeSideP121
Entries 0
Mean 0
RMS 0
size [# of strips]

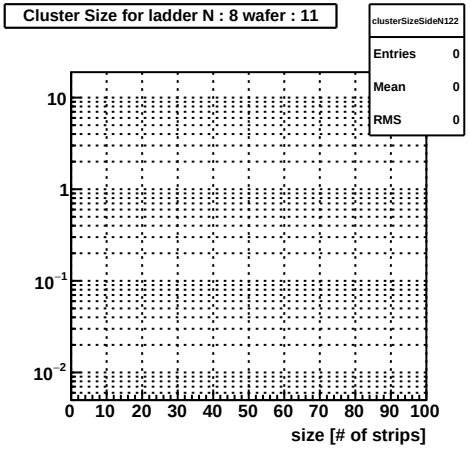

Cluster Size for ladder N : 8 wafer : 10
clusterSizeSideN121
Entries 0
Mean 0
RMS 0
size [# of strips]

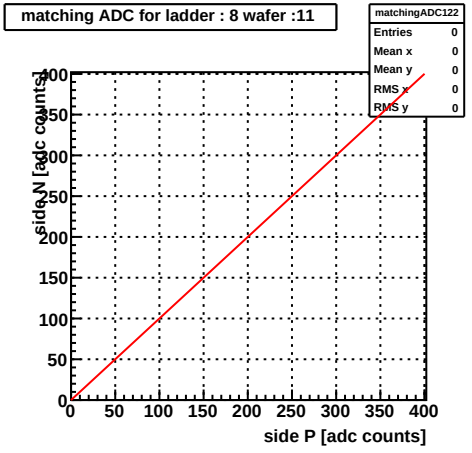

matching ADC for ladder : 8 wafer :10
matchingADC121
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

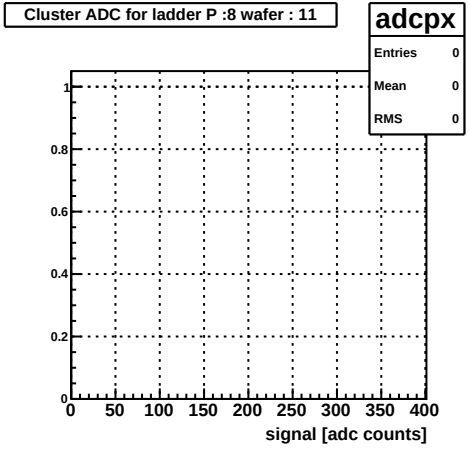

Cluster ADC for ladder P :8 wafer : 10
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

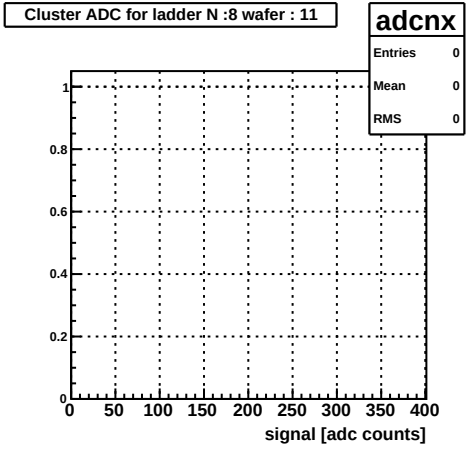

Cluster ADC for ladder N :8 wafer : 10
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

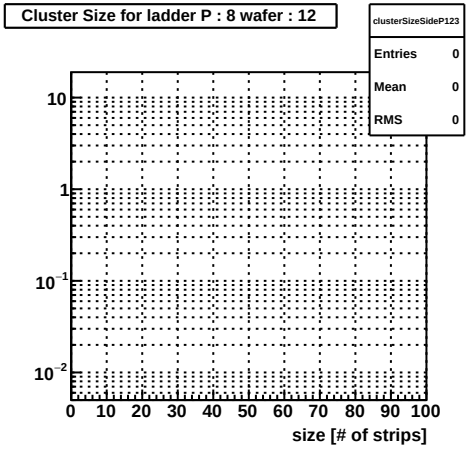

Cluster Size for ladder P : 8 wafer : 11
clusterSizeSideP122
Entries 0
Mean 0
RMS 0
size [# of strips]

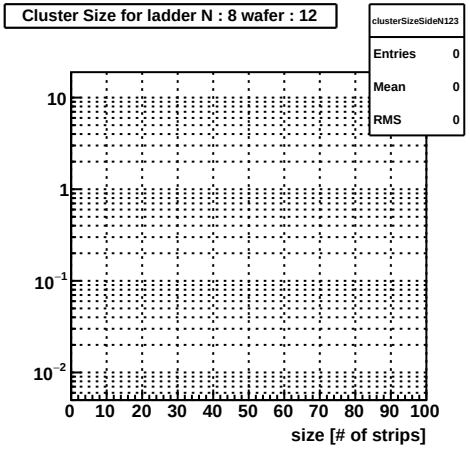

Cluster Size for ladder N : 8 wafer : 11
clusterSizeSideN122
Entries 0
Mean 0
RMS 0
size [# of strips]

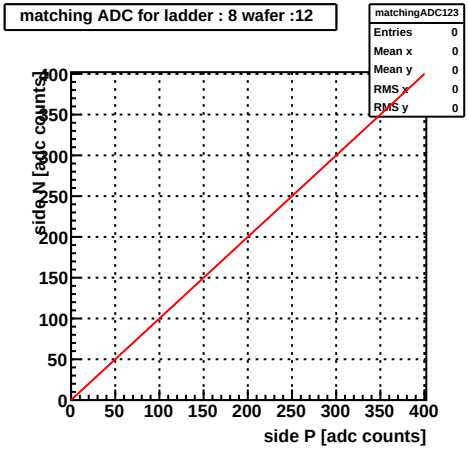

matching ADC for ladder : 8 wafer :11
matchingADC122
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

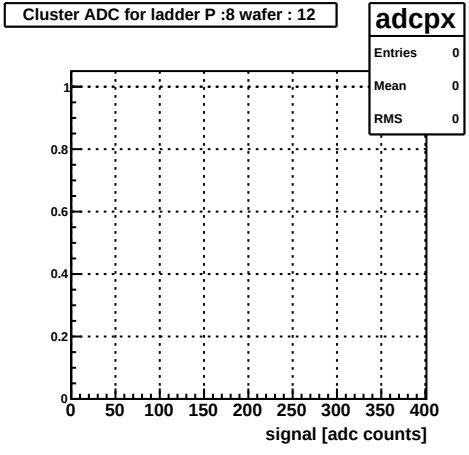

Cluster ADC for ladder P :8 wafer : 11
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

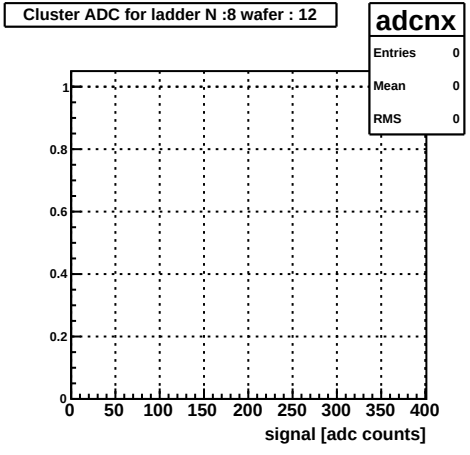

Cluster ADC for ladder N :8 wafer : 11
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

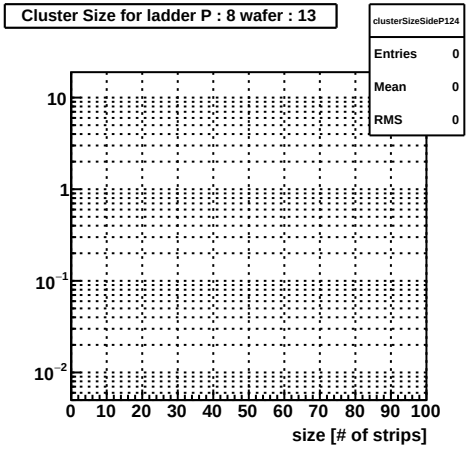

Cluster Size for ladder P : 8 wafer : 12
clusterSizeSideP123
Entries 0
Mean 0
RMS 0
size [# of strips]

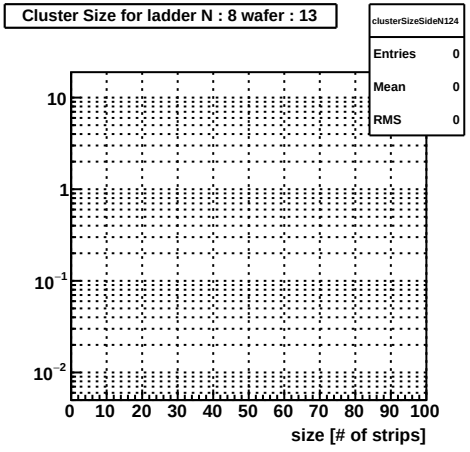

Cluster Size for ladder N : 8 wafer : 12
clusterSizeSideN123
Entries 0
Mean 0
RMS 0
size [# of strips]

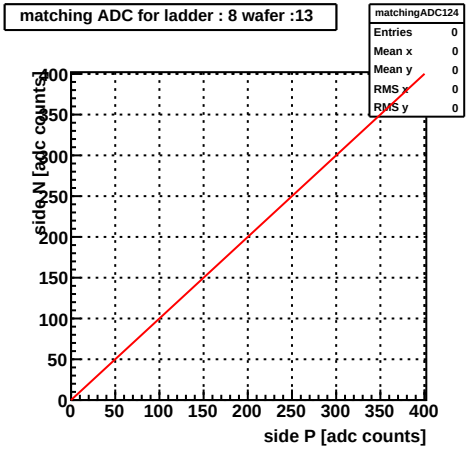

matching ADC for ladder : 8 wafer :12
matchingADC123
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

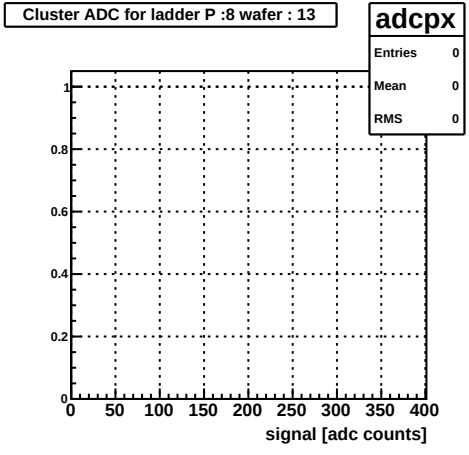

Cluster ADC for ladder P :8 wafer : 12
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

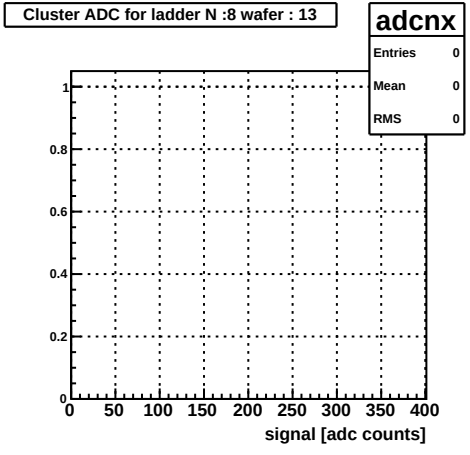

Cluster ADC for ladder N :8 wafer : 12
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

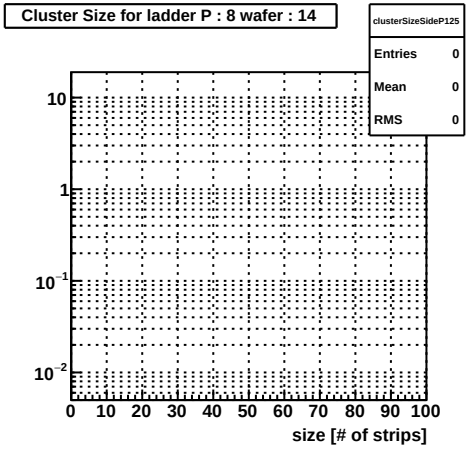

Cluster Size for ladder P : 8 wafer : 13
clusterSizeSideP124
Entries 0
Mean 0
RMS 0
size [# of strips]

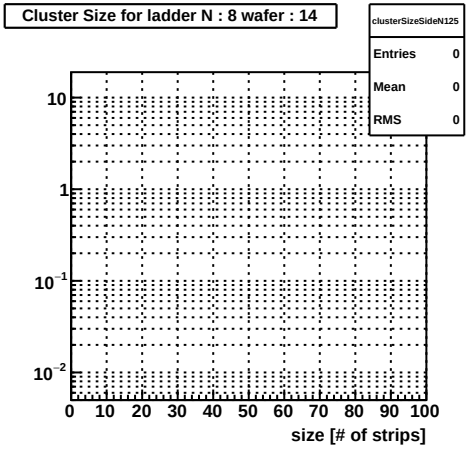

Cluster Size for ladder N : 8 wafer : 13
clusterSizeSideN124
Entries 0
Mean 0
RMS 0
size [# of strips]

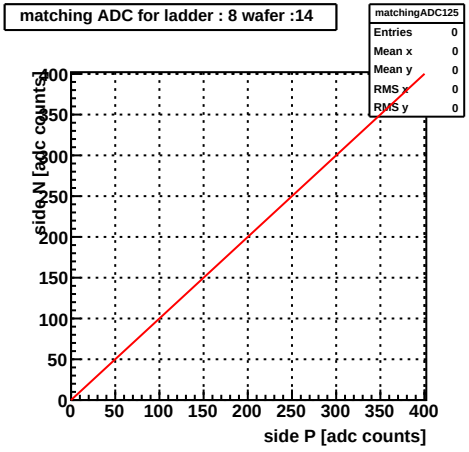

matching ADC for ladder : 8 wafer :13
matchingADC124
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

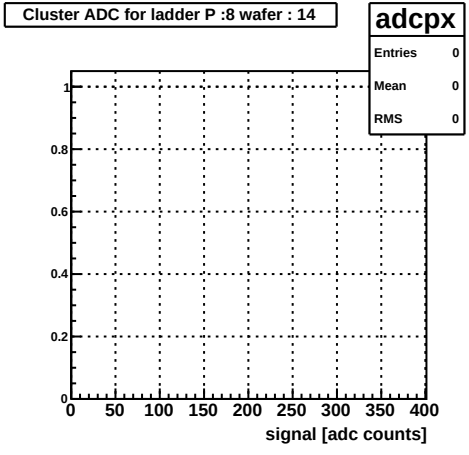

Cluster ADC for ladder P :8 wafer : 13
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

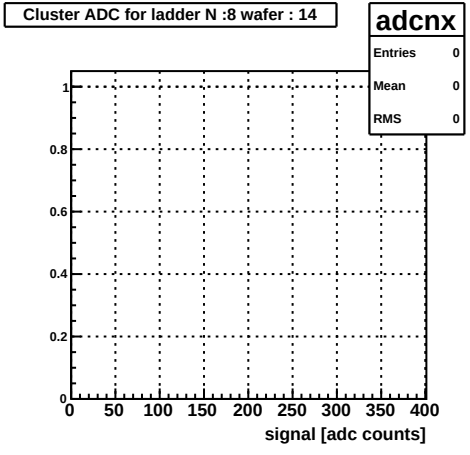

Cluster ADC for ladder N :8 wafer : 13
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

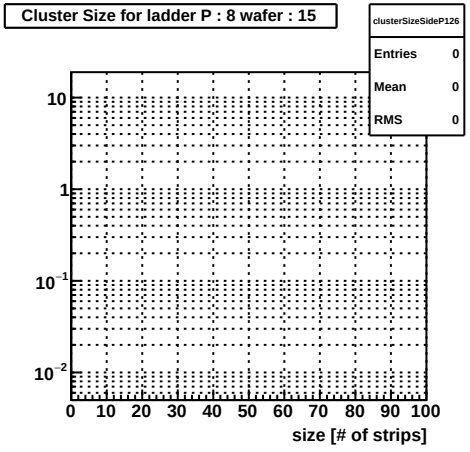

Cluster Size for ladder P : 8 wafer : 14
clusterSizeSideP125
Entries 0
Mean 0
RMS 0
size [# of strips]

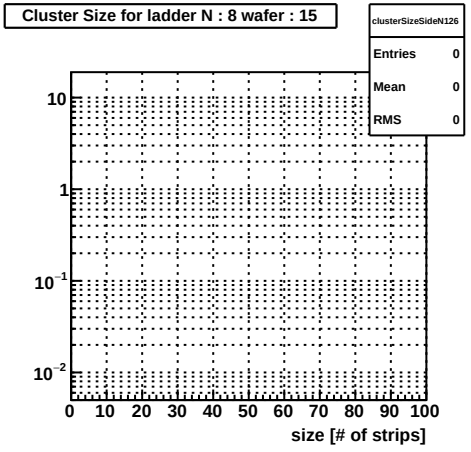

Cluster Size for ladder N : 8 wafer : 14
clusterSizeSideN125
Entries 0
Mean 0
RMS 0
size [# of strips]

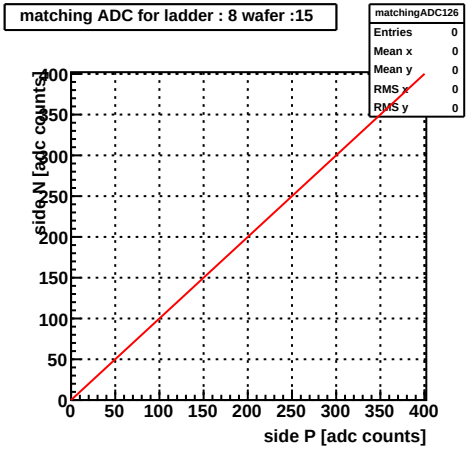

matching ADC for ladder : 8 wafer :14
matchingADC125
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

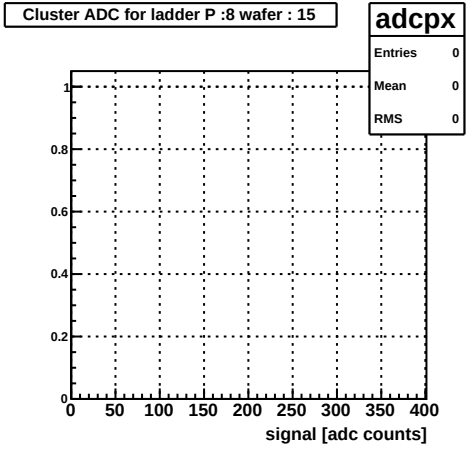

Cluster ADC for ladder P :8 wafer : 14
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

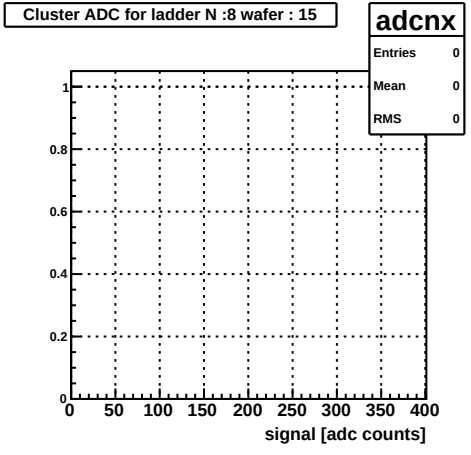

Cluster ADC for ladder N :8 wafer : 14
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

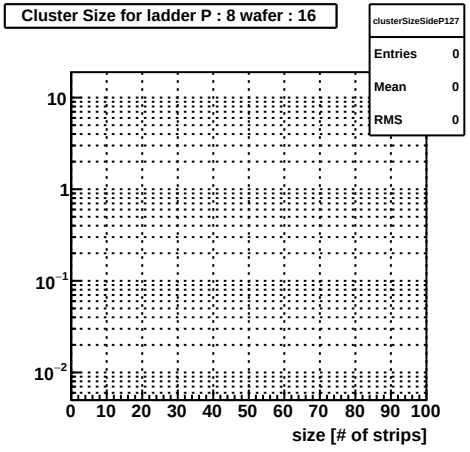

Cluster Size for ladder P : 8 wafer : 15
clusterSizeSideP126
Entries 0
Mean 0
RMS 0
size [# of strips]

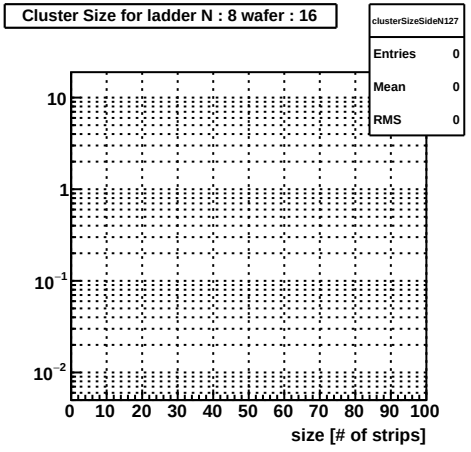

Cluster Size for ladder N : 8 wafer : 15
clusterSizeSideN126
Entries 0
Mean 0
RMS 0
size [# of strips]

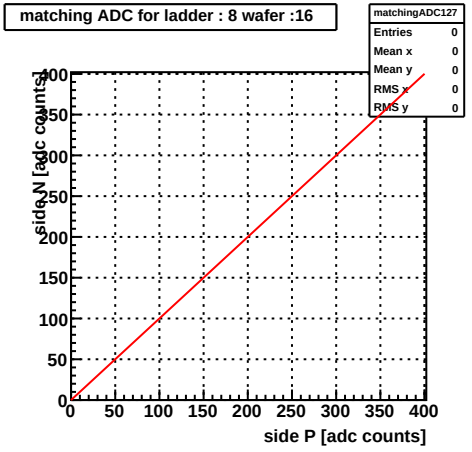

matching ADC for ladder : 8 wafer :15
matchingADC126
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

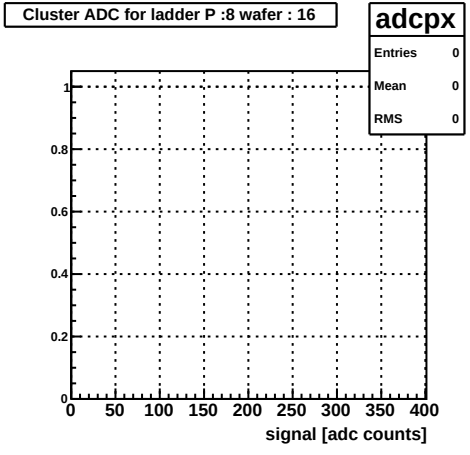

Cluster ADC for ladder P :8 wafer : 15
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

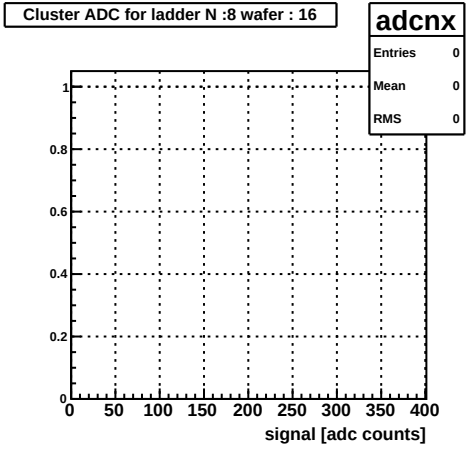

Cluster ADC for ladder N :8 wafer : 15
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

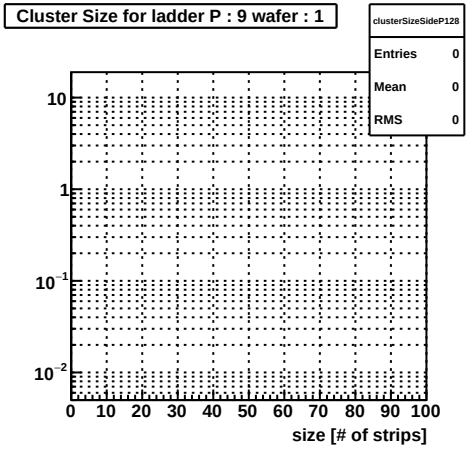

Cluster Size for ladder P : 8 wafer : 16
clusterSizeSideP127
Entries 0
Mean 0
RMS 0
size [# of strips]

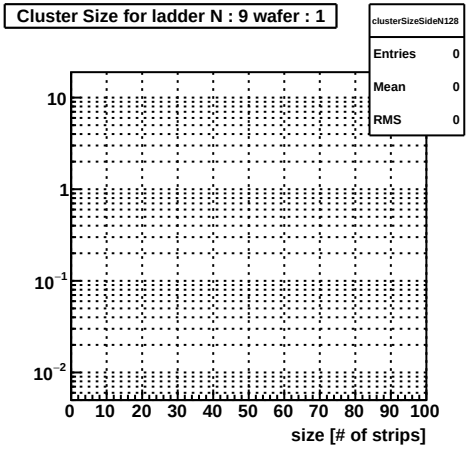

Cluster Size for ladder N : 8 wafer : 16
clusterSizeSideN127
Entries 0
Mean 0
RMS 0
size [# of strips]

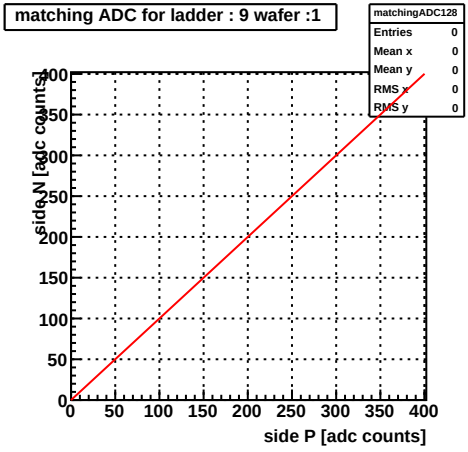

matching ADC for ladder : 8 wafer :16
matchingADC127
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

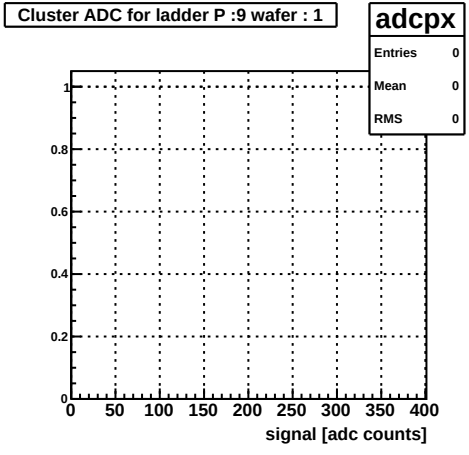

Cluster ADC for ladder P :8 wafer : 16
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

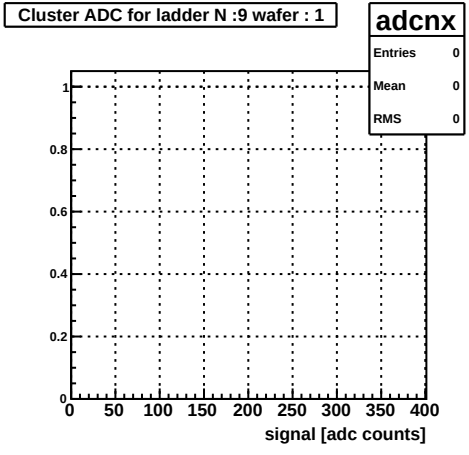

Cluster ADC for ladder N :8 wafer : 16
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

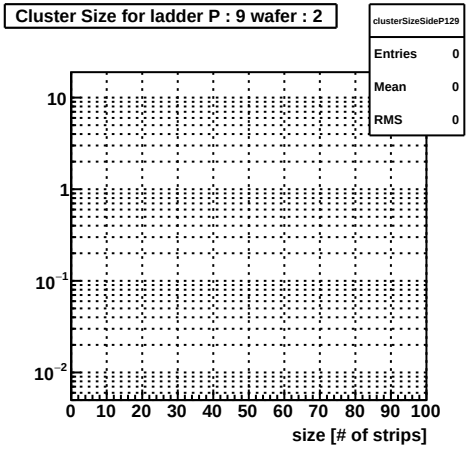

Cluster Size for ladder P : 9 wafer : 1
clusterSizeSideP128
Entries 0
Mean 0
RMS 0
size [# of strips]

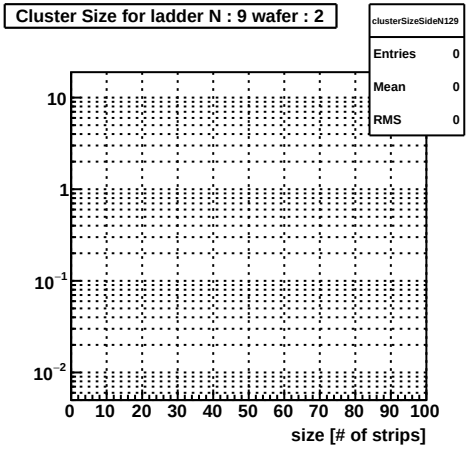

Cluster Size for ladder N : 9 wafer : 1
clusterSizeSideN128
Entries 0
Mean 0
RMS 0
size [# of strips]

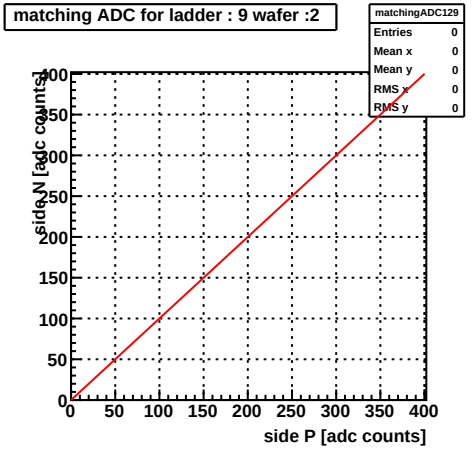

matching ADC for ladder : 9 wafer :1
matchingADC128
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

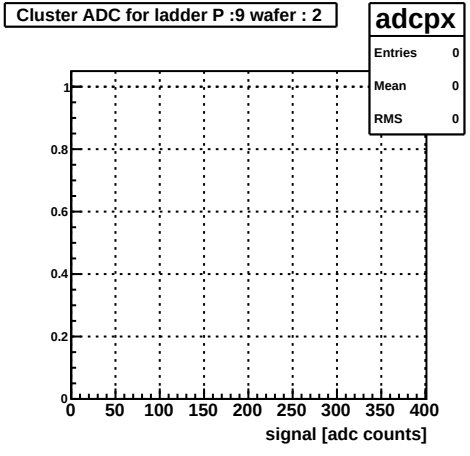

Cluster ADC for ladder P :9 wafer : 1
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

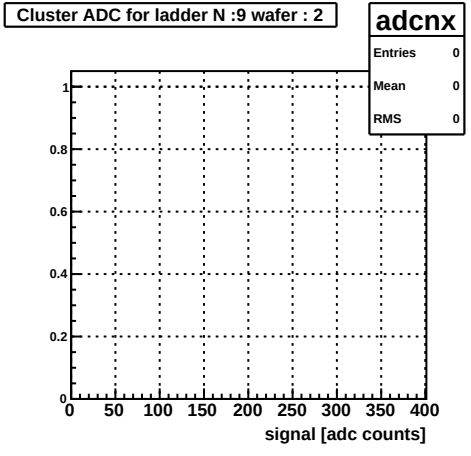

Cluster ADC for ladder N :9 wafer : 1
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

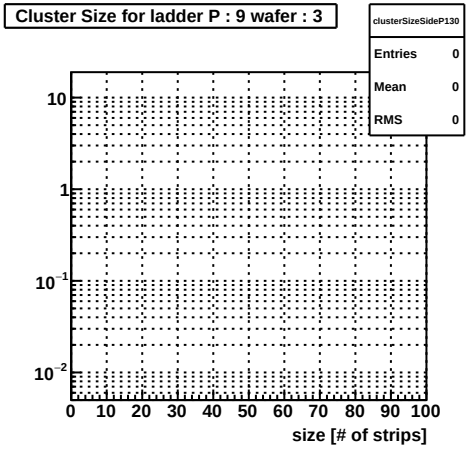

Cluster Size for ladder P : 9 wafer : 2
clusterSizeSideP129
Entries 0
Mean 0
RMS 0
size [# of strips]

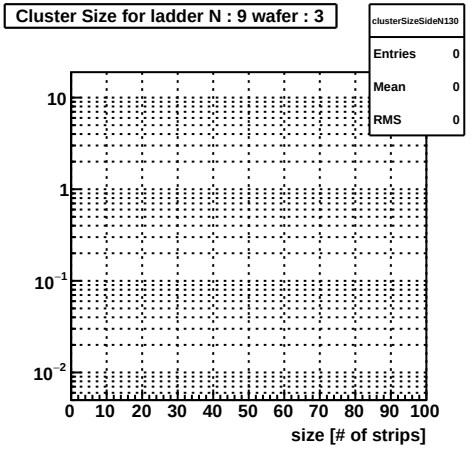

Cluster Size for ladder N : 9 wafer : 2
clusterSizeSideN129
Entries 0
Mean 0
RMS 0
size [# of strips]

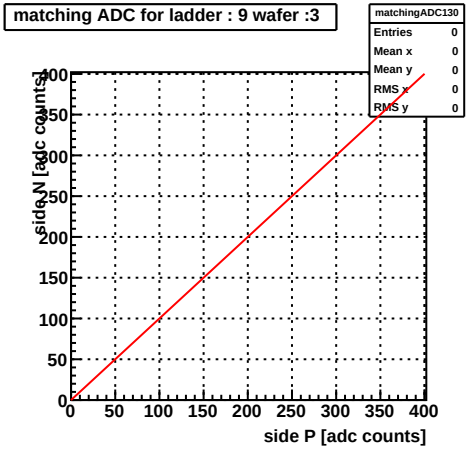

matching ADC for ladder : 9 wafer :2
matchingADC129
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

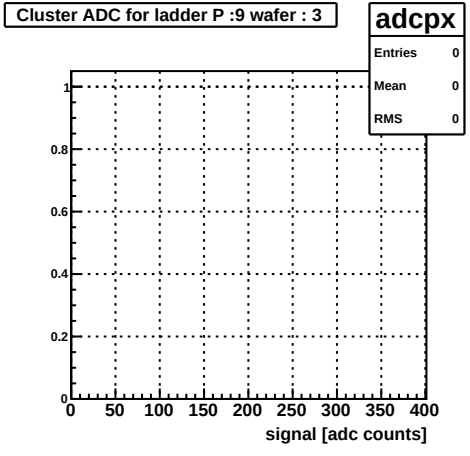

Cluster ADC for ladder P :9 wafer : 2
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

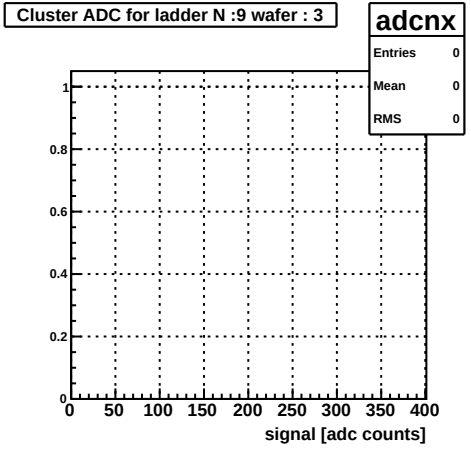

Cluster ADC for ladder N :9 wafer : 2
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

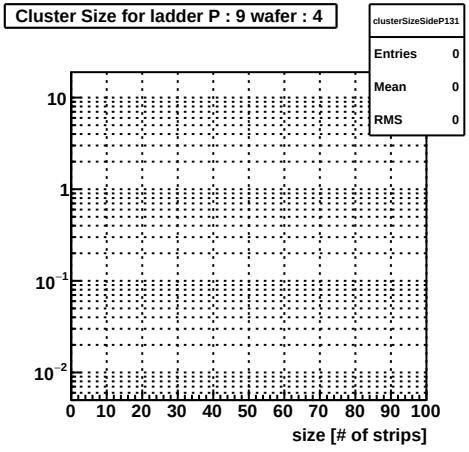

Cluster Size for ladder P : 9 wafer : 3
clusterSizeSideP130
Entries 0
Mean 0
RMS 0
size [# of strips]

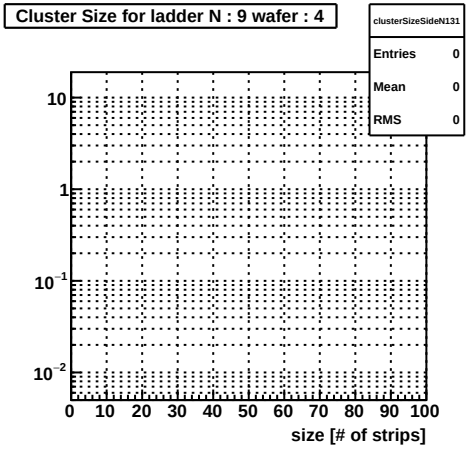

Cluster Size for ladder N : 9 wafer : 3
clusterSizeSideN130
Entries 0
Mean 0
RMS 0
size [# of strips]

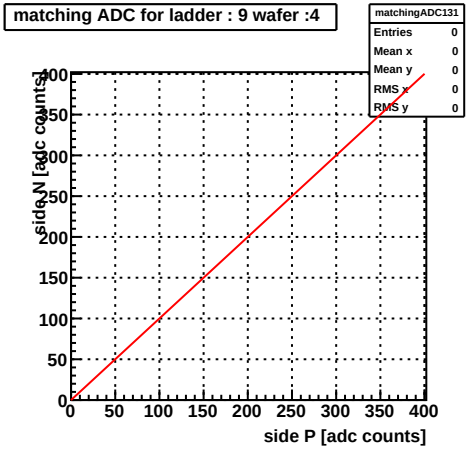

matching ADC for ladder : 9 wafer :3
matchingADC130
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

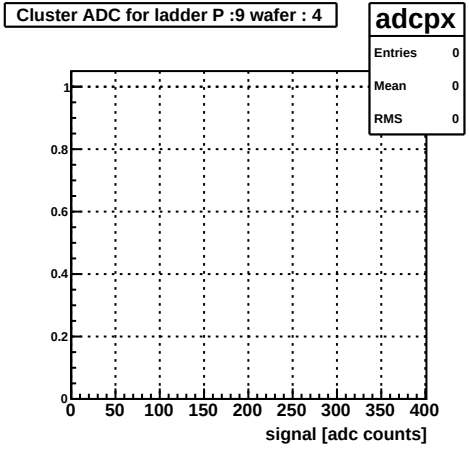

Cluster ADC for ladder P :9 wafer : 3
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

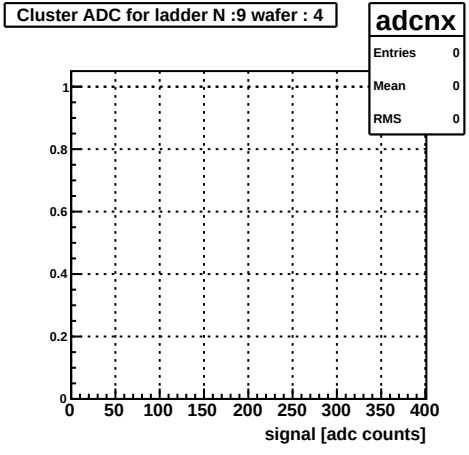

Cluster ADC for ladder N :9 wafer : 3
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

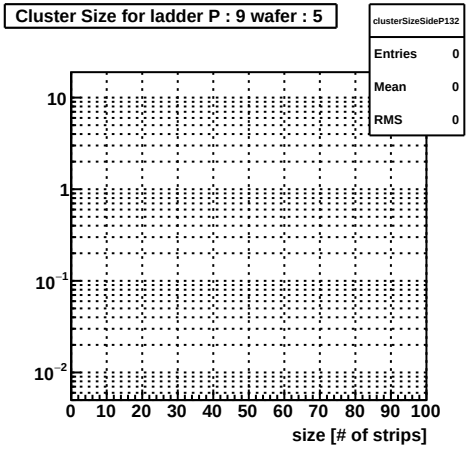

Cluster Size for ladder P : 9 wafer : 4
clusterSizeSideP131
Entries 0
Mean 0
RMS 0
size [# of strips]

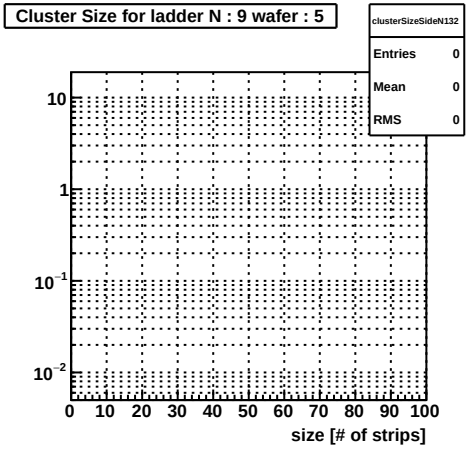

Cluster Size for ladder N : 9 wafer : 4
clusterSizeSideN131
Entries 0
Mean 0
RMS 0
size [# of strips]

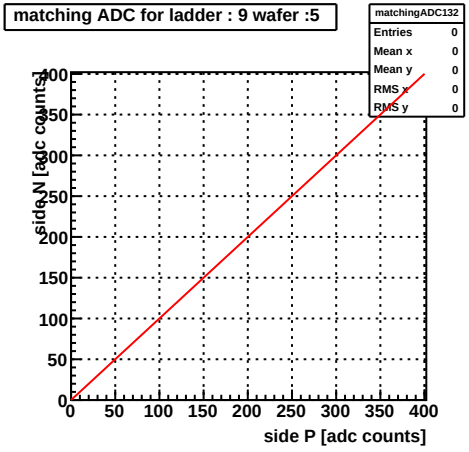

matching ADC for ladder : 9 wafer :4
matchingADC131
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

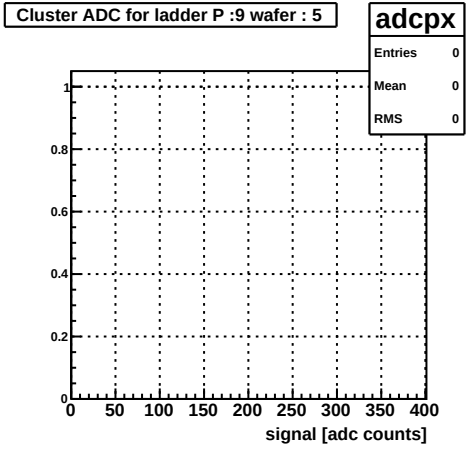

Cluster ADC for ladder P :9 wafer : 4
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

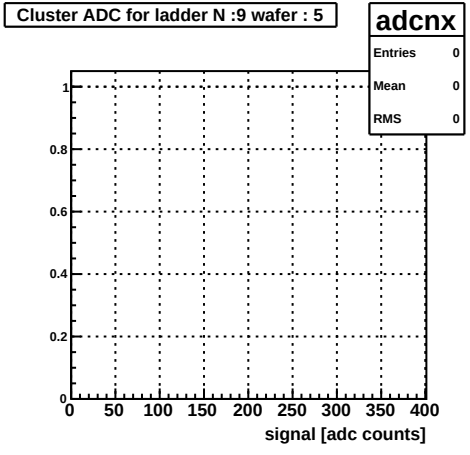

Cluster ADC for ladder N :9 wafer : 4
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

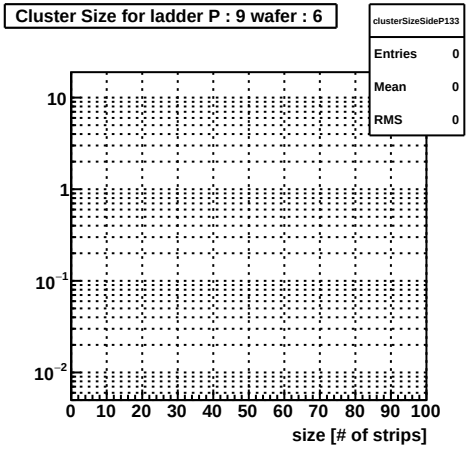

Cluster Size for ladder P : 9 wafer : 5
clusterSizeSideP132
Entries 0
Mean 0
RMS 0
size [# of strips]

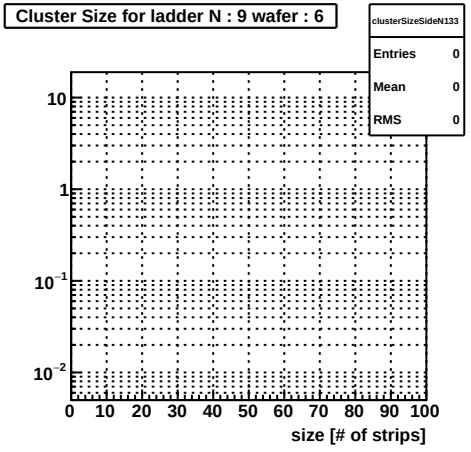

Cluster Size for ladder N : 9 wafer : 5
clusterSizeSideN132
Entries 0
Mean 0
RMS 0
size [# of strips]

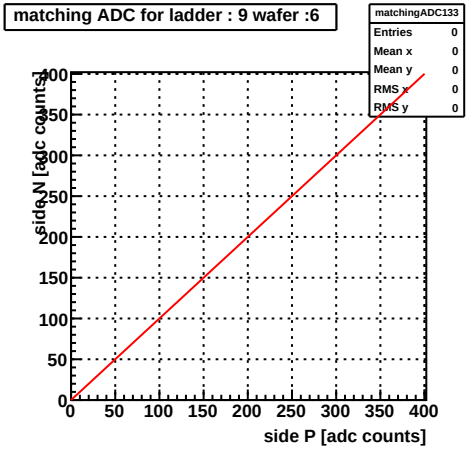

matching ADC for ladder : 9 wafer :5
matchingADC132
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

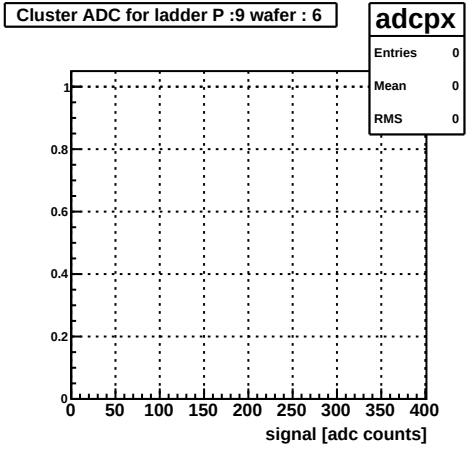

Cluster ADC for ladder P :9 wafer : 5
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

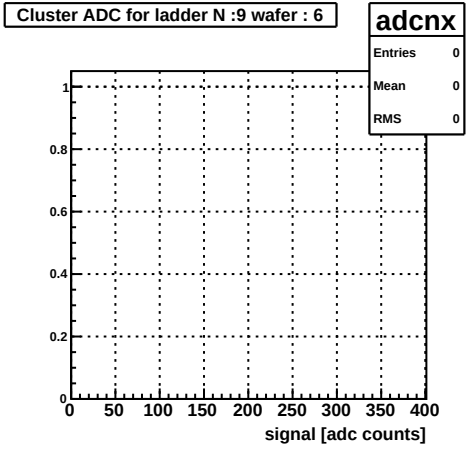

Cluster ADC for ladder N :9 wafer : 5
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

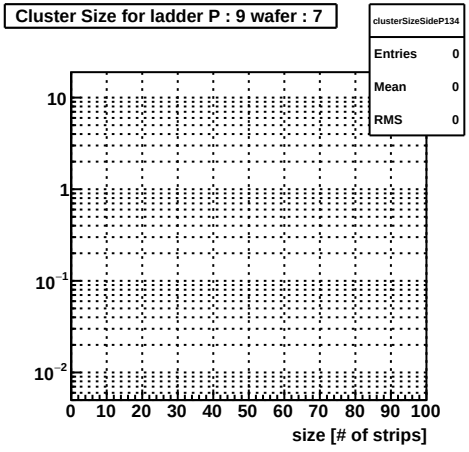

Cluster Size for ladder P : 9 wafer : 6
clusterSizeSideP133
Entries 0
Mean 0
RMS 0
size [# of strips]

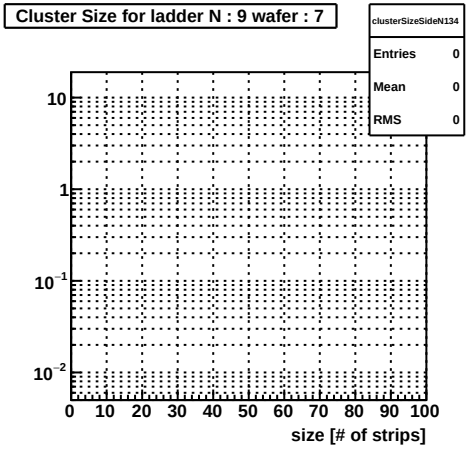

Cluster Size for ladder N : 9 wafer : 6
clusterSizeSideN133
Entries 0
Mean 0
RMS 0
size [# of strips]

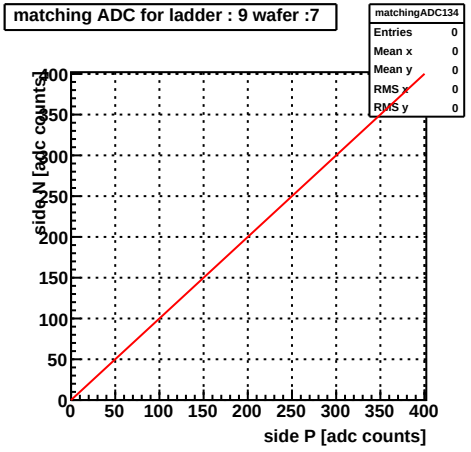

matching ADC for ladder : 9 wafer :6
matchingADC133
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

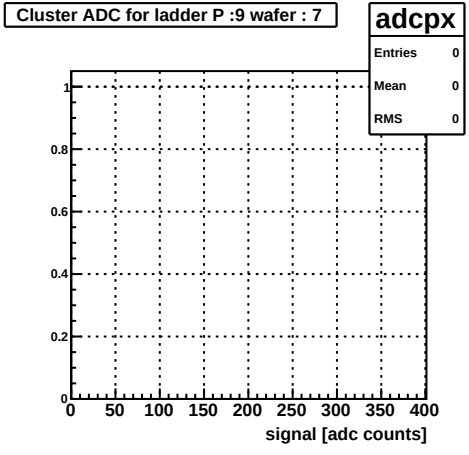

Cluster ADC for ladder P :9 wafer : 6
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

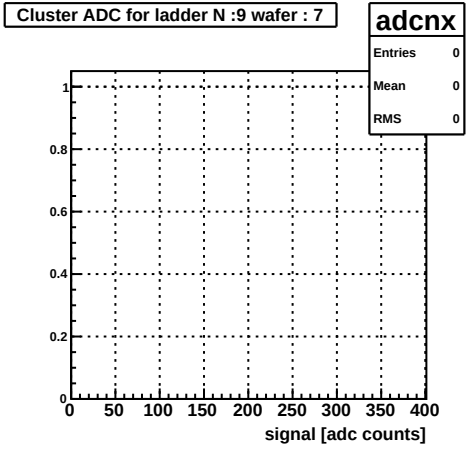

Cluster ADC for ladder N :9 wafer : 6
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

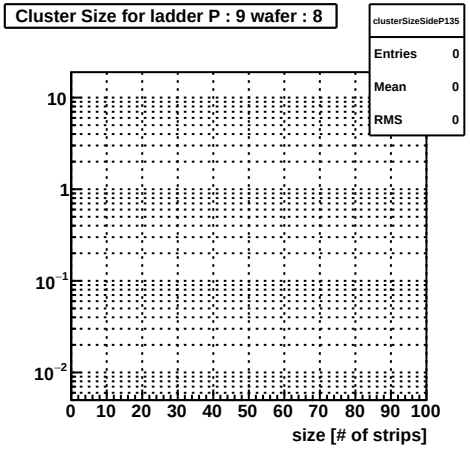

Cluster Size for ladder P : 9 wafer : 7
clusterSizeSideP134
Entries 0
Mean 0
RMS 0
size [# of strips]

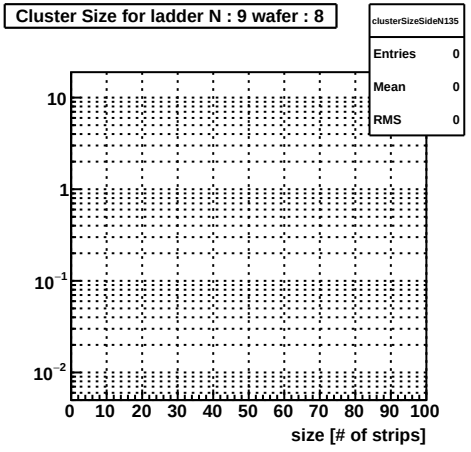

Cluster Size for ladder N : 9 wafer : 7
clusterSizeSideN134
Entries 0
Mean 0
RMS 0
size [# of strips]

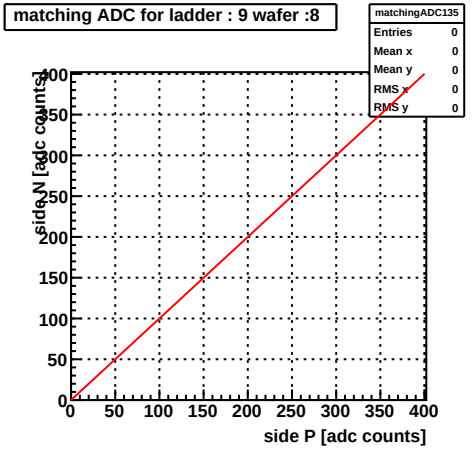

matching ADC for ladder : 9 wafer :7
matchingADC134
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

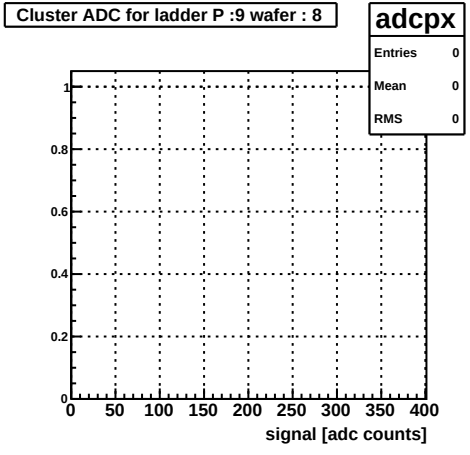

Cluster ADC for ladder P :9 wafer : 7
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

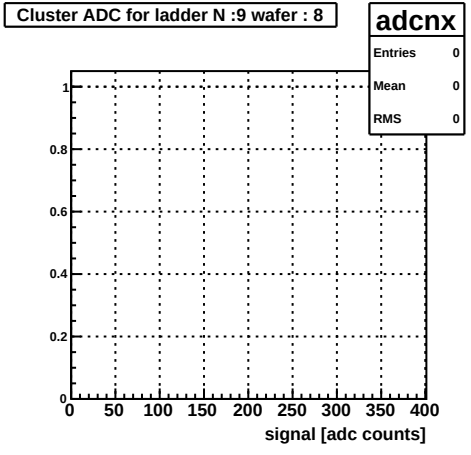

Cluster ADC for ladder N :9 wafer : 7
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

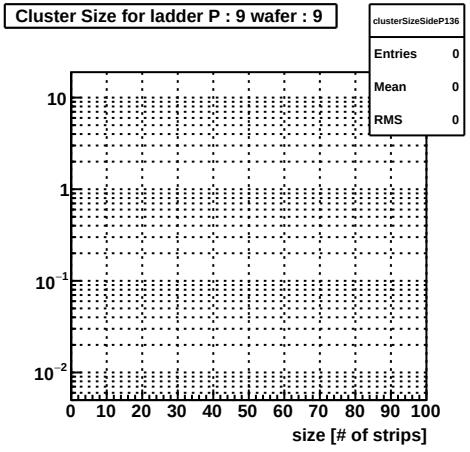

Cluster Size for ladder P : 9 wafer : 8
clusterSizeSideP135
Entries 0
Mean 0
RMS 0
size [# of strips]

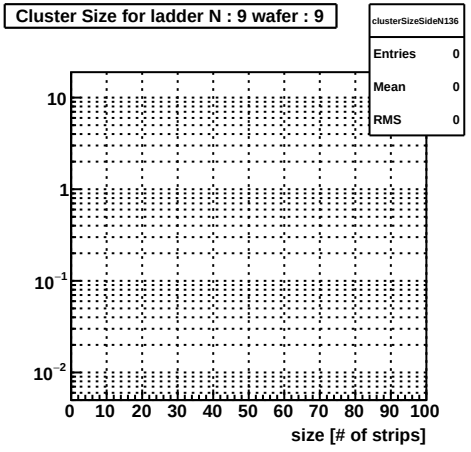

Cluster Size for ladder N : 9 wafer : 8
clusterSizeSideN135
Entries 0
Mean 0
RMS 0
size [# of strips]

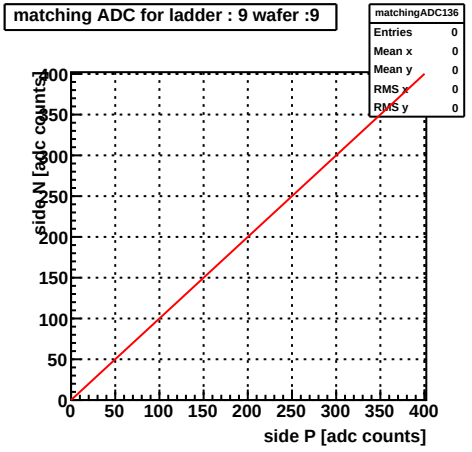

matching ADC for ladder : 9 wafer :8
matchingADC135
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

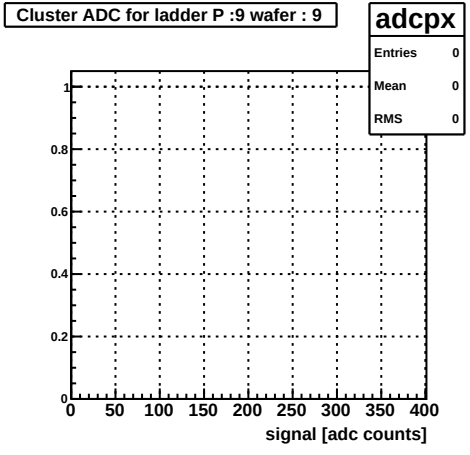

Cluster ADC for ladder P :9 wafer : 8
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

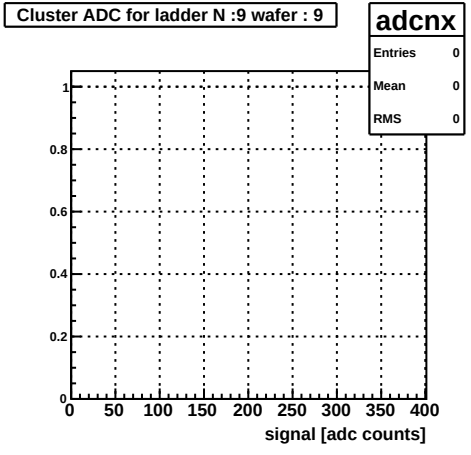

Cluster ADC for ladder N :9 wafer : 8
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

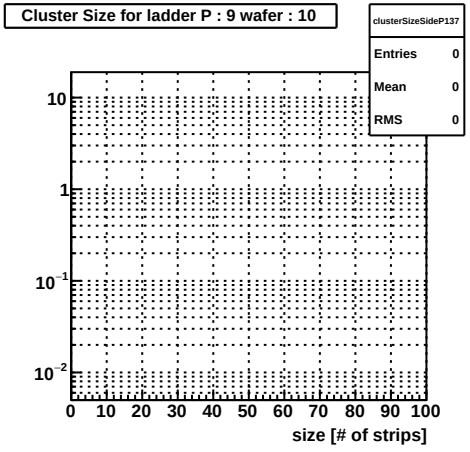

Cluster Size for ladder P : 9 wafer : 9
clusterSizeSideP136
Entries 0
Mean 0
RMS 0
size [# of strips]

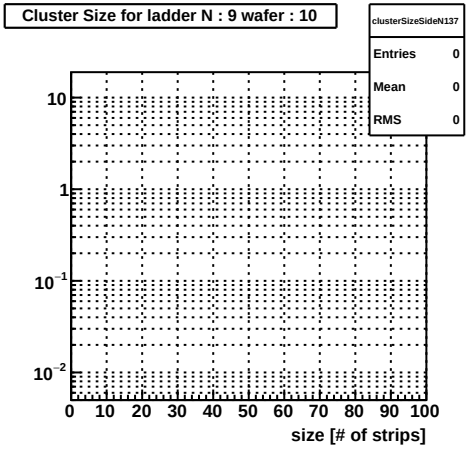

Cluster Size for ladder N : 9 wafer : 9
clusterSizeSideN136
Entries 0
Mean 0
RMS 0
size [# of strips]

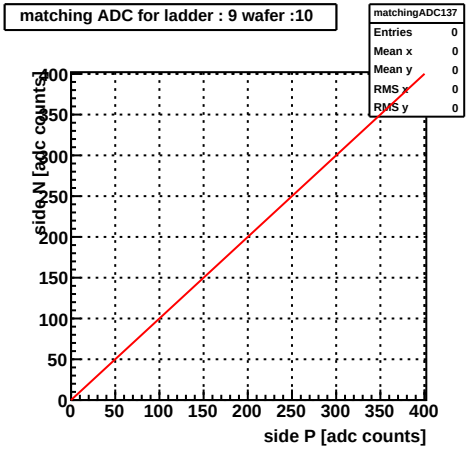

matching ADC for ladder : 9 wafer :9
matchingADC136
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

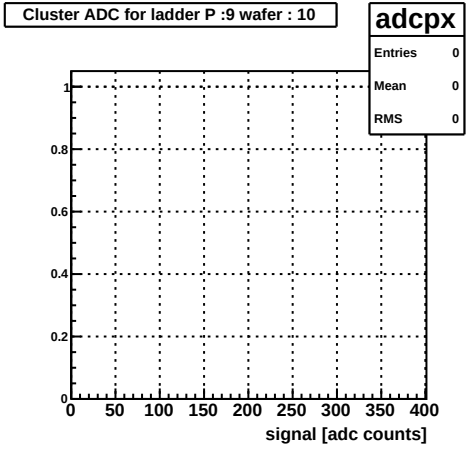

Cluster ADC for ladder P :9 wafer : 9
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

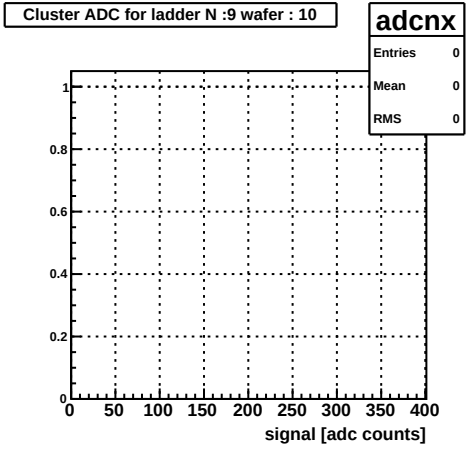

Cluster ADC for ladder N :9 wafer : 9
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

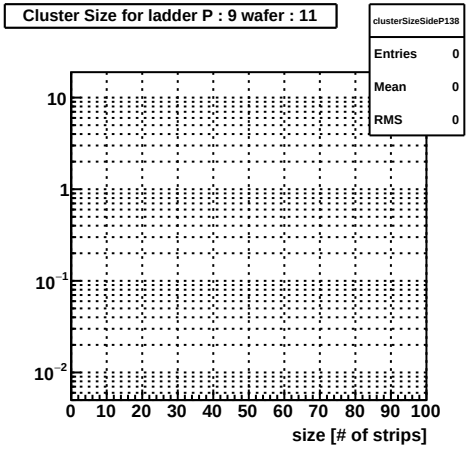

Cluster Size for ladder P : 9 wafer : 10
clusterSizeSideP137
Entries 0
Mean 0
RMS 0
size [# of strips]

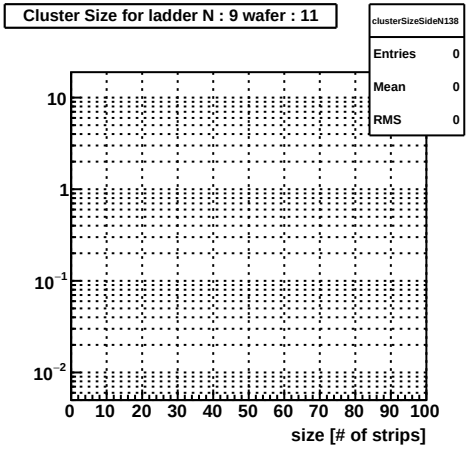

Cluster Size for ladder N : 9 wafer : 10
clusterSizeSideN137
Entries 0
Mean 0
RMS 0
size [# of strips]

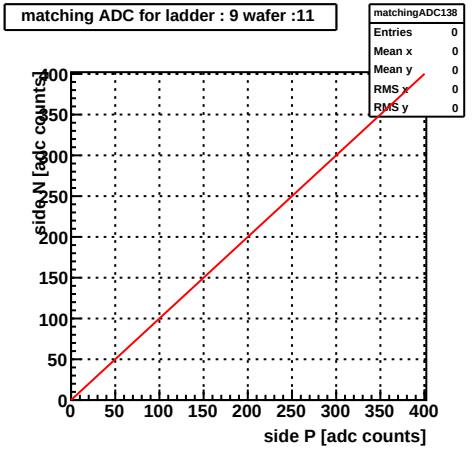

matching ADC for ladder : 9 wafer :10
matchingADC137
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

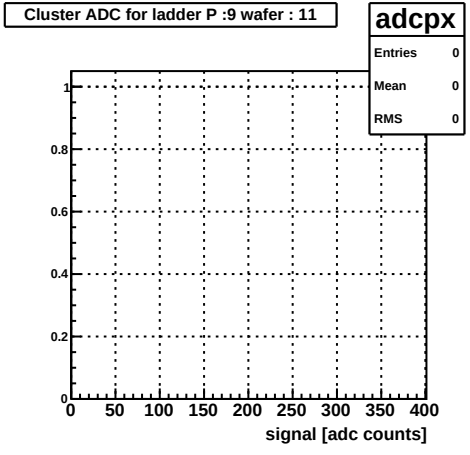

Cluster ADC for ladder P :9 wafer : 10
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

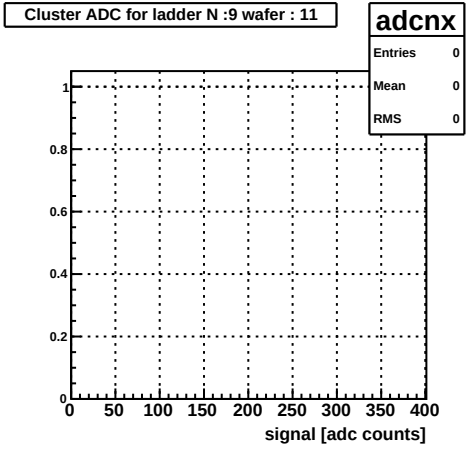

Cluster ADC for ladder N :9 wafer : 10
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

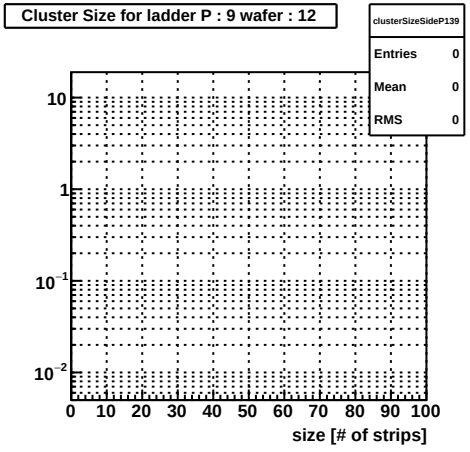

Cluster Size for ladder P : 9 wafer : 11
clusterSizeSideP138
Entries 0
Mean 0
RMS 0
size [# of strips]

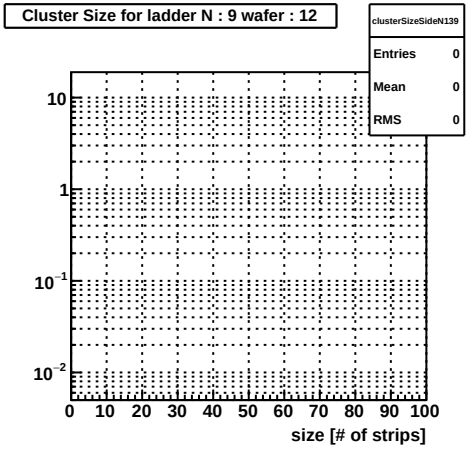

Cluster Size for ladder N : 9 wafer : 11
clusterSizeSideN138
Entries 0
Mean 0
RMS 0
size [# of strips]

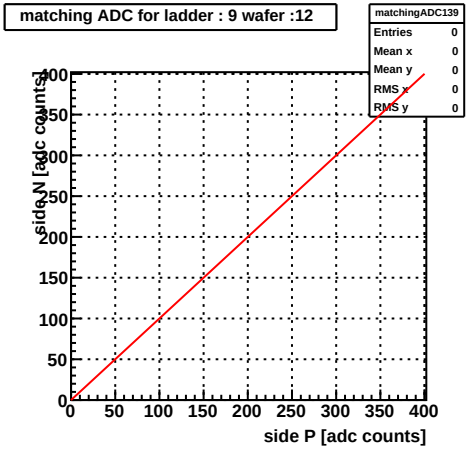

matching ADC for ladder : 9 wafer :11
matchingADC138
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

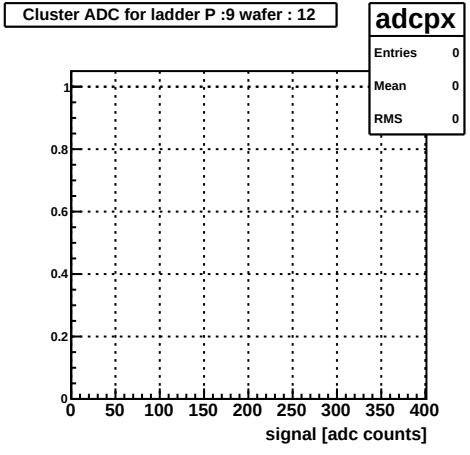

Cluster ADC for ladder P :9 wafer : 11
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

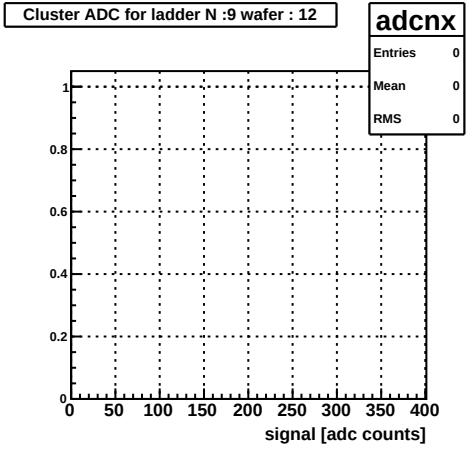

Cluster ADC for ladder N :9 wafer : 11
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

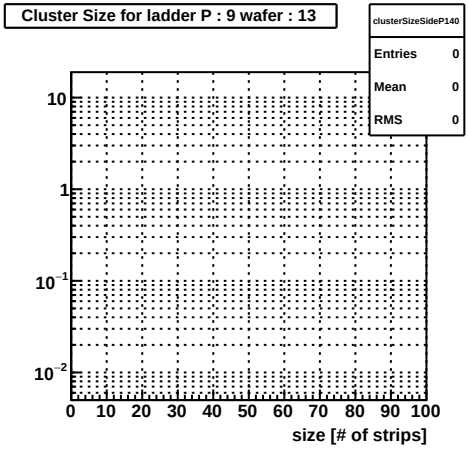

Cluster Size for ladder P : 9 wafer : 12
clusterSizeSideP139
Entries 0
Mean 0
RMS 0
size [# of strips]

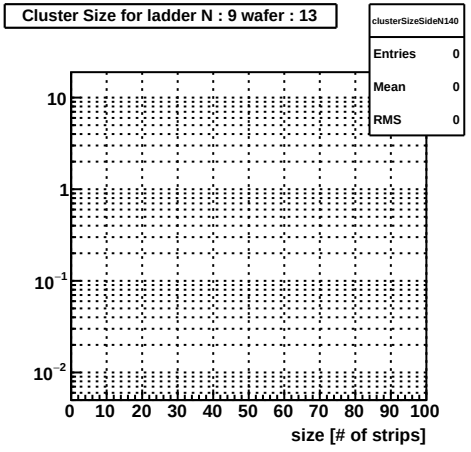

Cluster Size for ladder N : 9 wafer : 12
clusterSizeSideN139
Entries 0
Mean 0
RMS 0
size [# of strips]

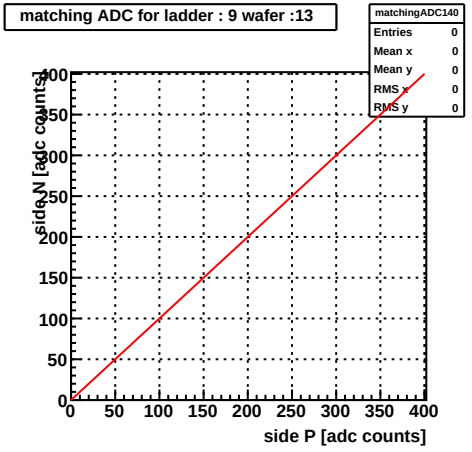

matching ADC for ladder : 9 wafer :12
matchingADC139
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

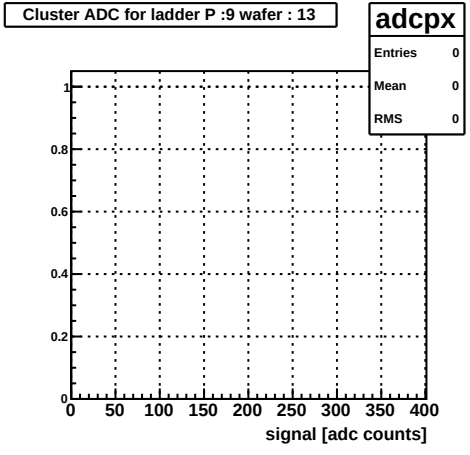

Cluster ADC for ladder P :9 wafer : 12
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

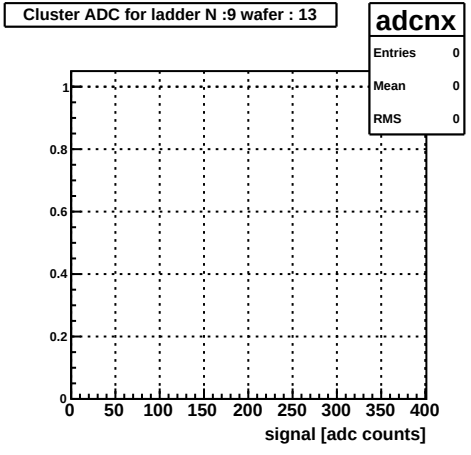

Cluster ADC for ladder N :9 wafer : 12
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

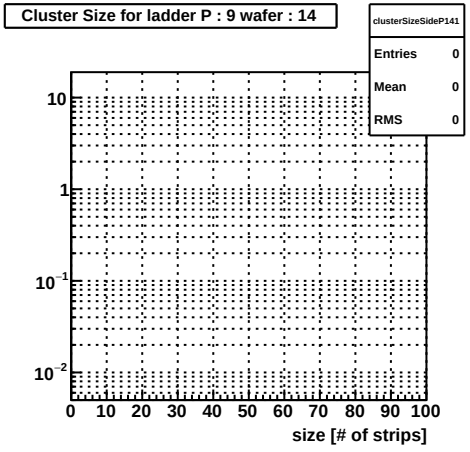

Cluster Size for ladder P : 9 wafer : 13
clusterSizeSideP140
Entries 0
Mean 0
RMS 0
size [# of strips]

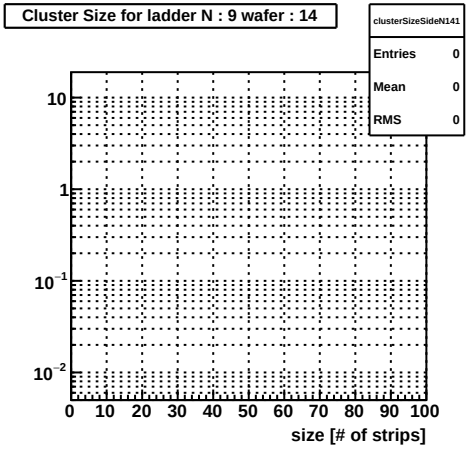

Cluster Size for ladder N : 9 wafer : 13
clusterSizeSideN140
Entries 0
Mean 0
RMS 0
size [# of strips]

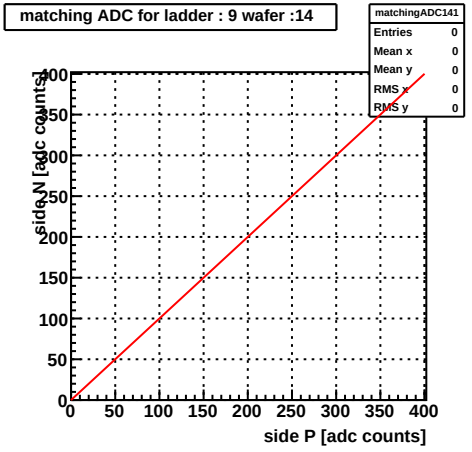

matching ADC for ladder : 9 wafer :13
matchingADC140
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

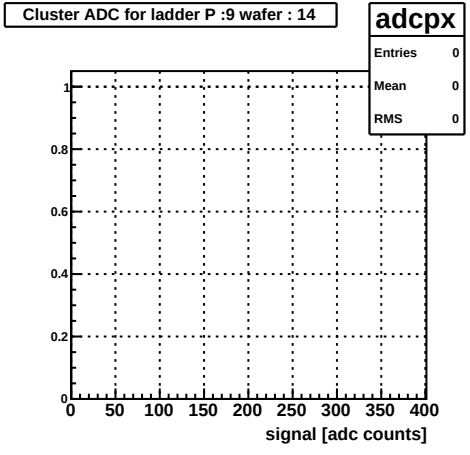

Cluster ADC for ladder P :9 wafer : 13
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

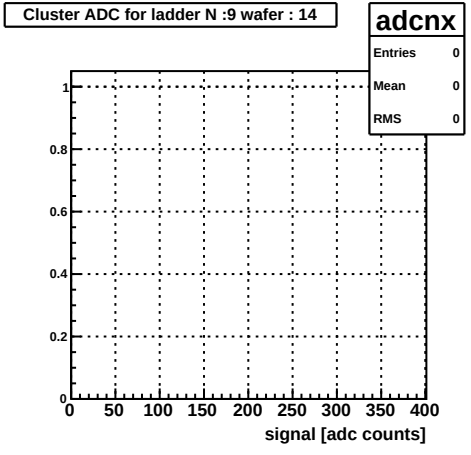

Cluster ADC for ladder N :9 wafer : 13
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

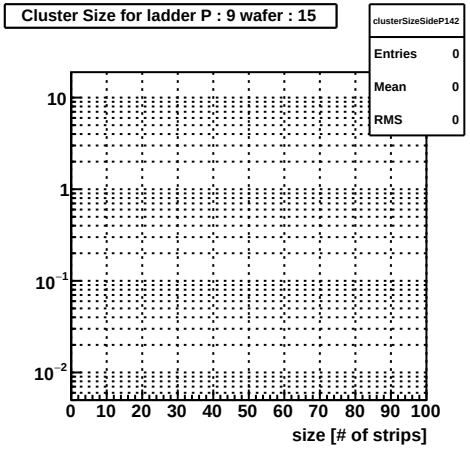

Cluster Size for ladder P : 9 wafer : 14
clusterSizeSideP141
Entries 0
Mean 0
RMS 0
size [# of strips]

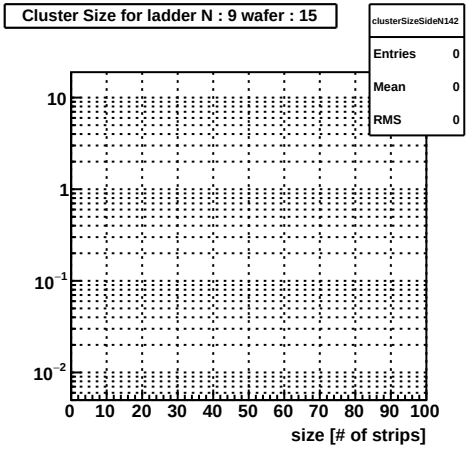

Cluster Size for ladder N : 9 wafer : 14
clusterSizeSideN141
Entries 0
Mean 0
RMS 0
size [# of strips]

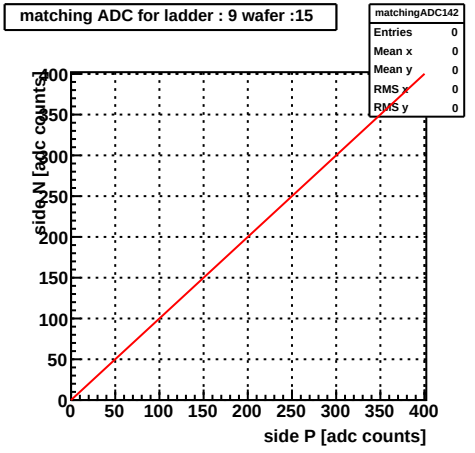

matching ADC for ladder : 9 wafer :14
matchingADC141
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

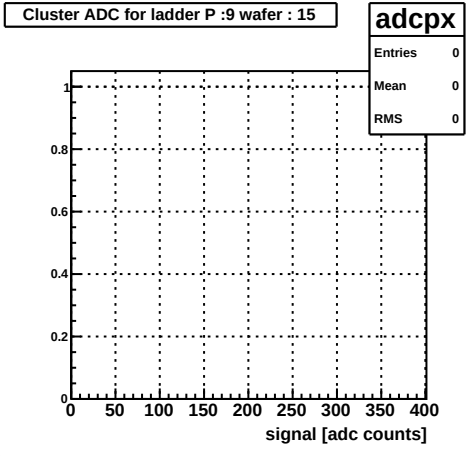

Cluster ADC for ladder P :9 wafer : 14
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

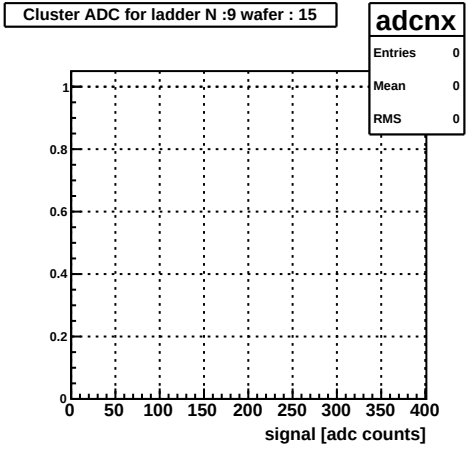

Cluster ADC for ladder N :9 wafer : 14
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

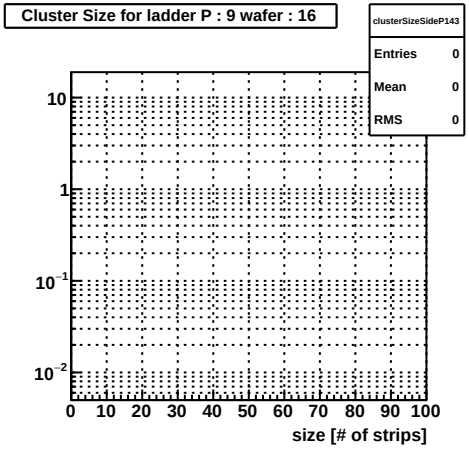

Cluster Size for ladder P : 9 wafer : 15
clusterSizeSideP142
Entries 0
Mean 0
RMS 0
size [# of strips]

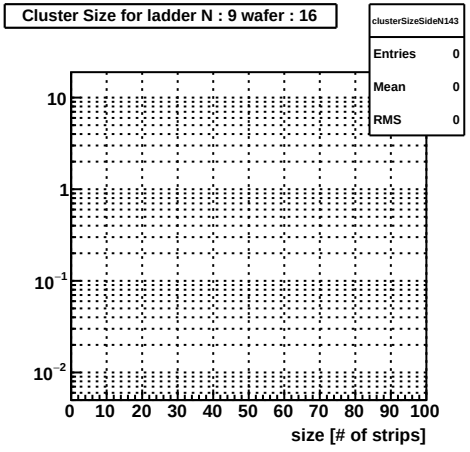

Cluster Size for ladder N : 9 wafer : 15
clusterSizeSideN142
Entries 0
Mean 0
RMS 0
size [# of strips]

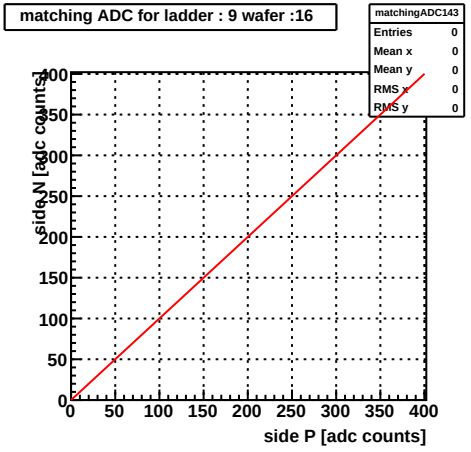

matching ADC for ladder : 9 wafer :15
matchingADC142
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

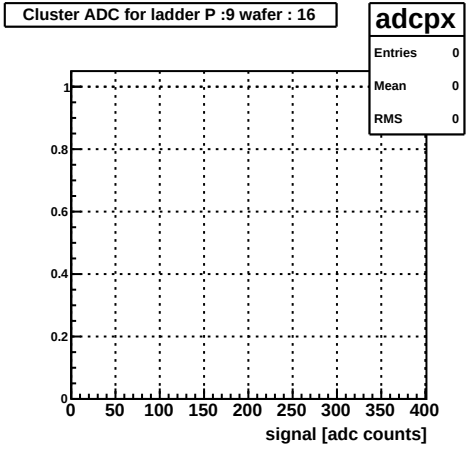

Cluster ADC for ladder P :9 wafer : 15
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

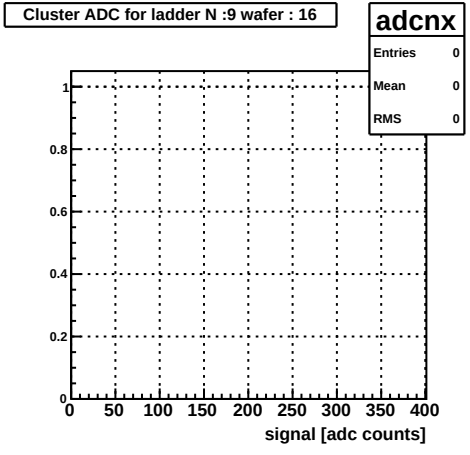

Cluster ADC for ladder N :9 wafer : 15
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

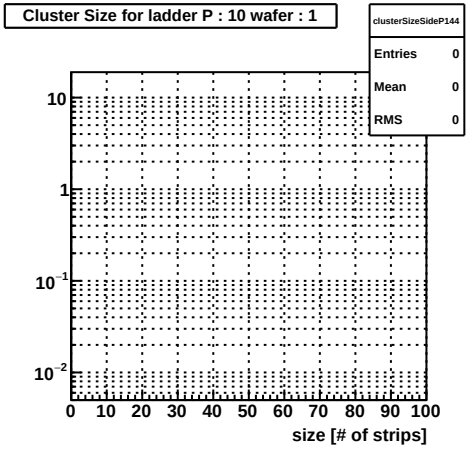

Cluster Size for ladder P : 9 wafer : 16
clusterSizeSideP143
Entries 0
Mean 0
RMS 0
size [# of strips]

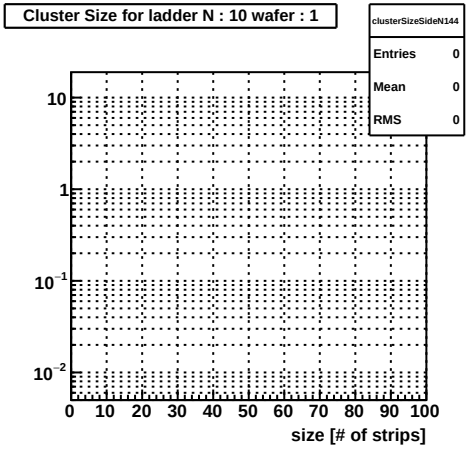

Cluster Size for ladder N : 9 wafer : 16
clusterSizeSideN143
Entries 0
Mean 0
RMS 0
size [# of strips]

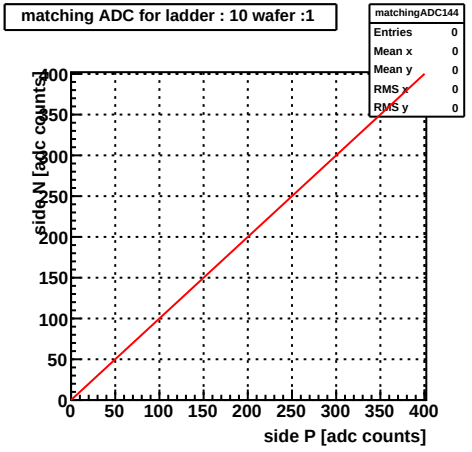

matching ADC for ladder : 9 wafer :16
matchingADC143
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

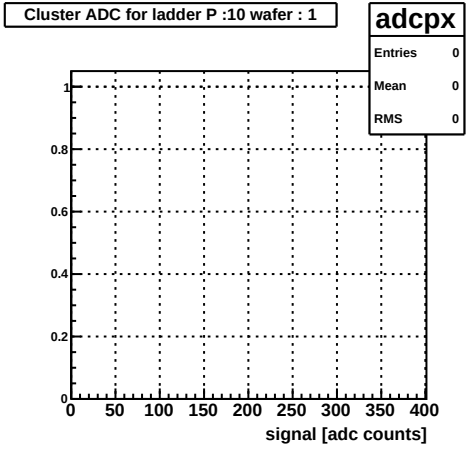

Cluster ADC for ladder P :9 wafer : 16
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

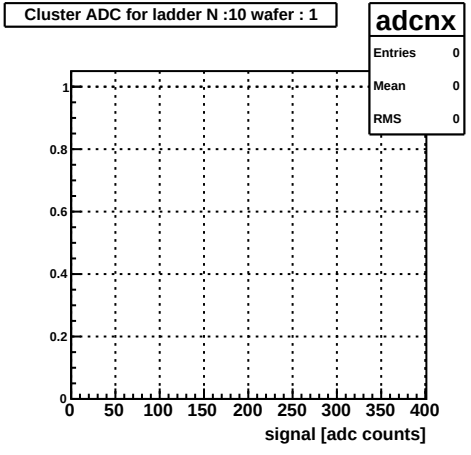

Cluster ADC for ladder N :9 wafer : 16
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

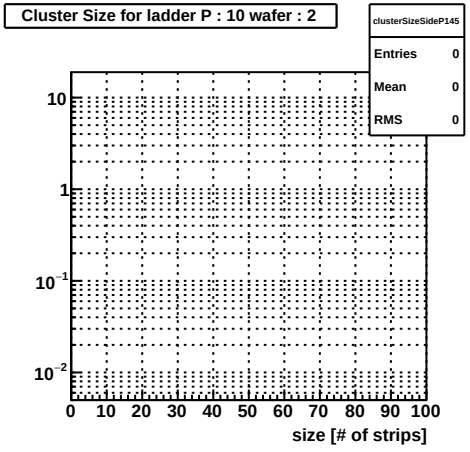

Cluster Size for ladder P : 10 wafer : 1
clusterSizeSideP144
Entries 0
Mean 0
RMS 0
size [# of strips]

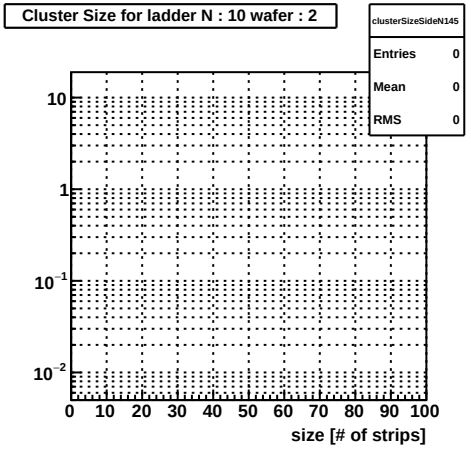

Cluster Size for ladder N : 10 wafer : 1
clusterSizeSideN144
Entries 0
Mean 0
RMS 0
size [# of strips]

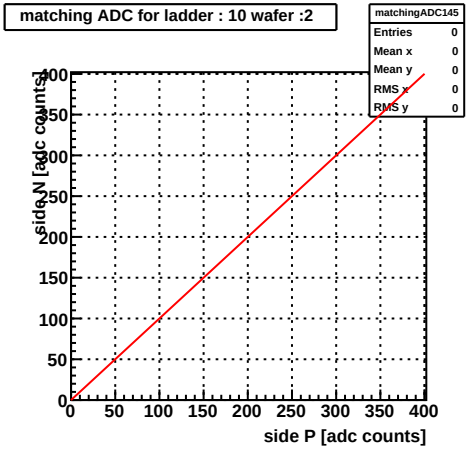

matching ADC for ladder : 10 wafer :1
matchingADC144
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

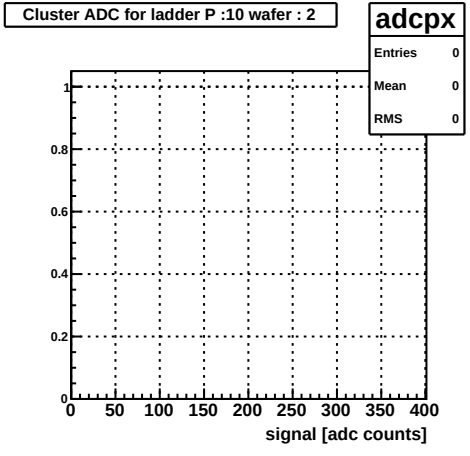

Cluster ADC for ladder P :10 wafer : 1
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

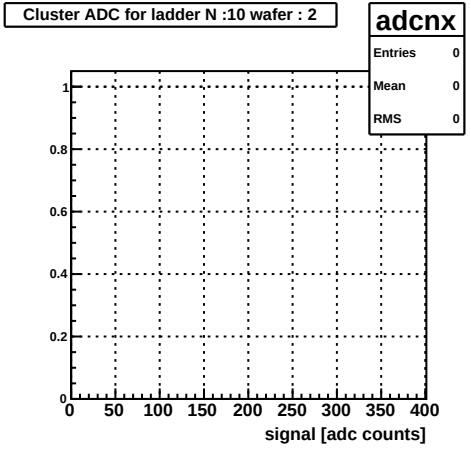

Cluster ADC for ladder N :10 wafer : 1
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

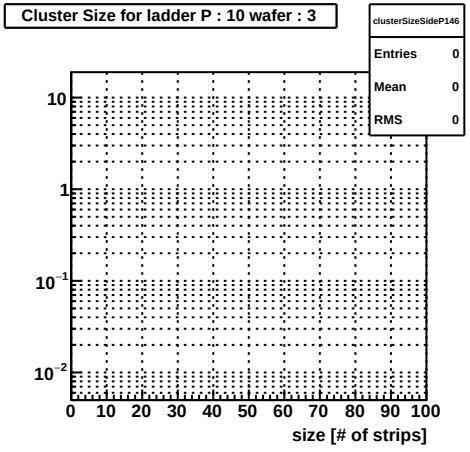

Cluster Size for ladder P : 10 wafer : 2
clusterSizeSideP145
Entries 0
Mean 0
RMS 0
size [# of strips]

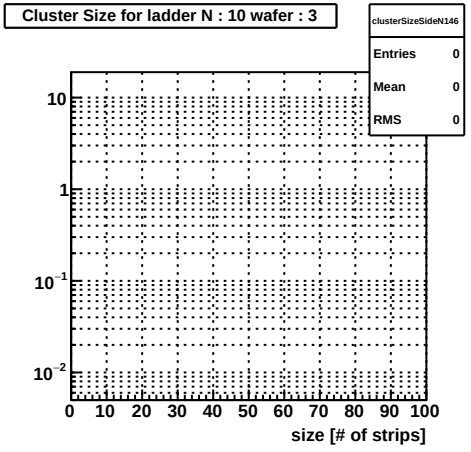

Cluster Size for ladder N : 10 wafer : 2
clusterSizeSideN145
Entries 0
Mean 0
RMS 0
size [# of strips]

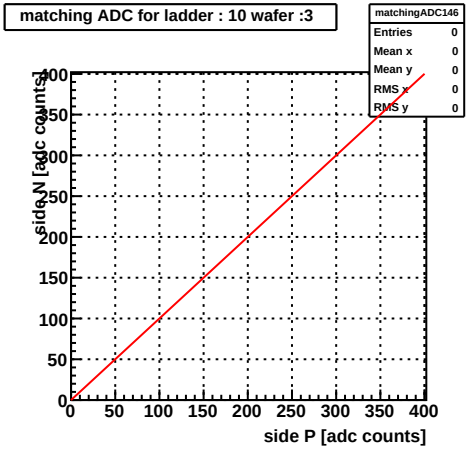

matching ADC for ladder : 10 wafer :2
matchingADC145
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

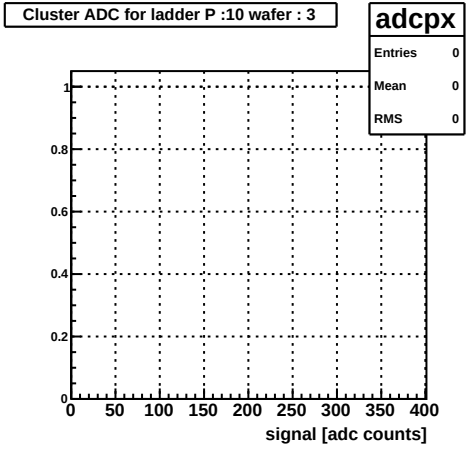

Cluster ADC for ladder P :10 wafer : 2
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

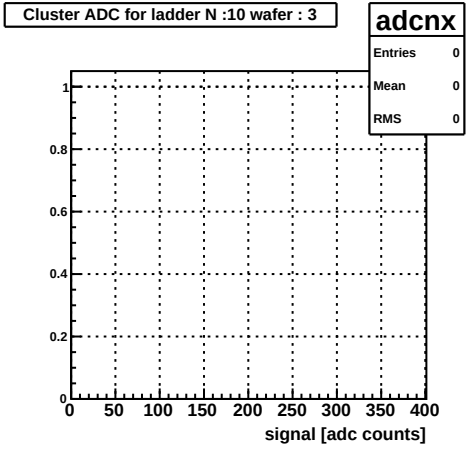

Cluster ADC for ladder N :10 wafer : 2
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

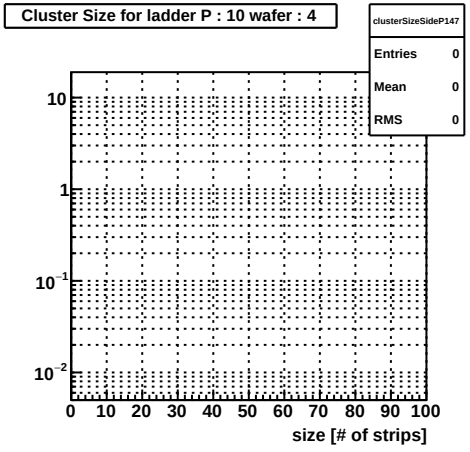

Cluster Size for ladder P : 10 wafer : 3
clusterSizeSideP146
Entries 0
Mean 0
RMS 0
size [# of strips]

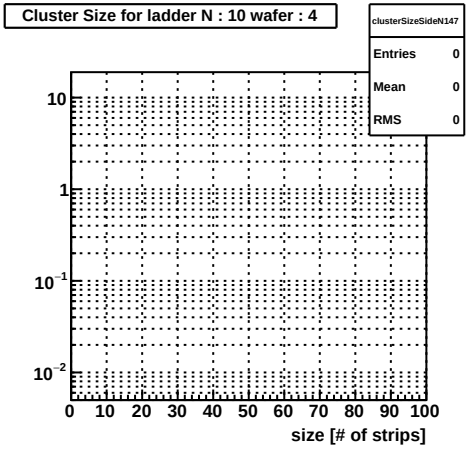

Cluster Size for ladder N : 10 wafer : 3
clusterSizeSideN146
Entries 0
Mean 0
RMS 0
size [# of strips]

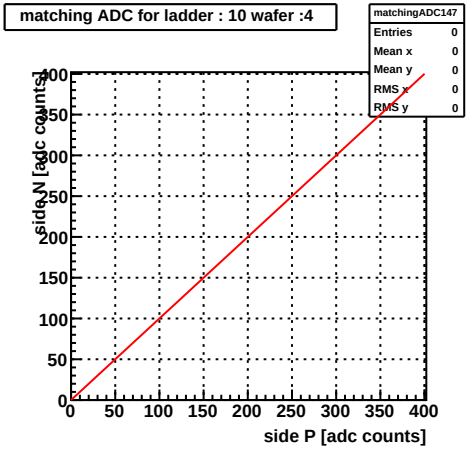

matching ADC for ladder : 10 wafer :3
matchingADC146
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

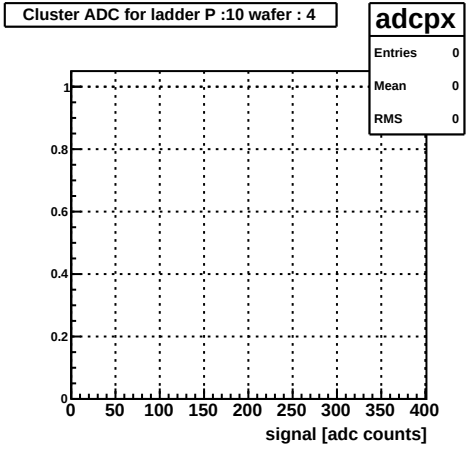

Cluster ADC for ladder P :10 wafer : 3
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

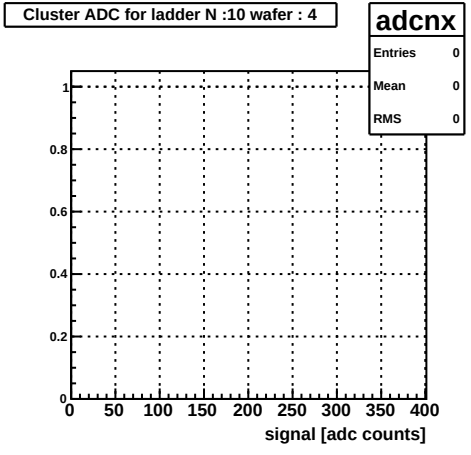

Cluster ADC for ladder N :10 wafer : 3
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

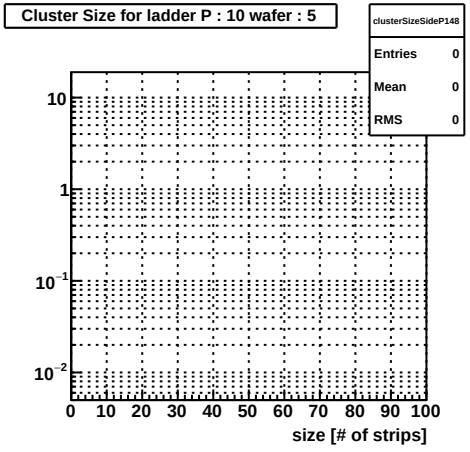

Cluster Size for ladder P : 10 wafer : 4
clusterSizeSideP147
Entries 0
Mean 0
RMS 0
size [# of strips]

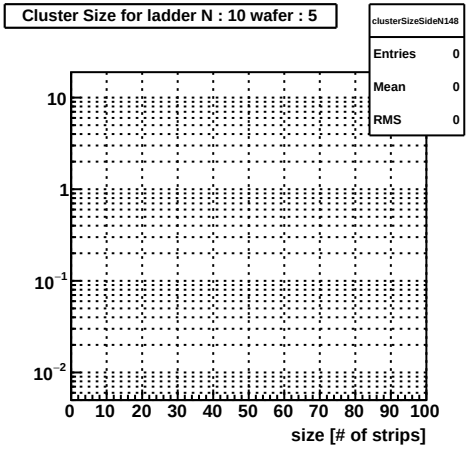

Cluster Size for ladder N : 10 wafer : 4
clusterSizeSideN147
Entries 0
Mean 0
RMS 0
size [# of strips]

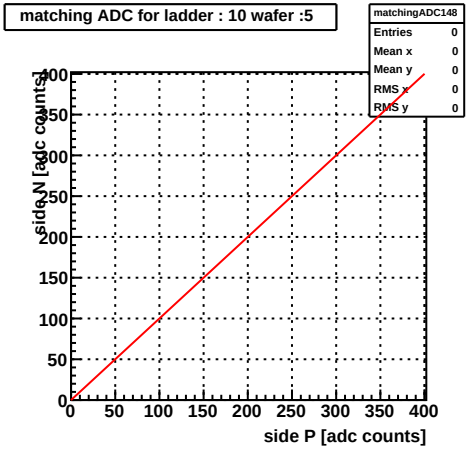

matching ADC for ladder : 10 wafer :4
matchingADC147
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

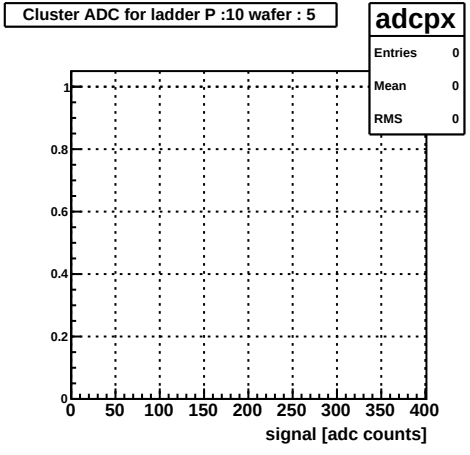

Cluster ADC for ladder P :10 wafer : 4
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

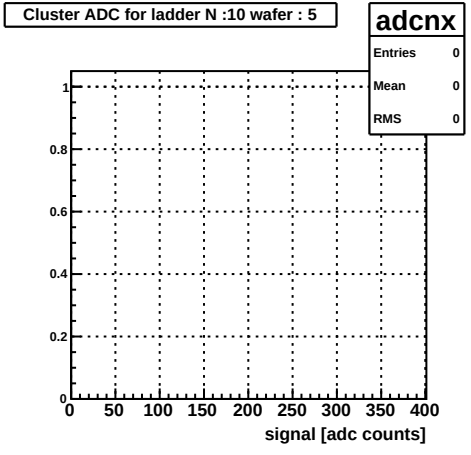

Cluster ADC for ladder N :10 wafer : 4
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

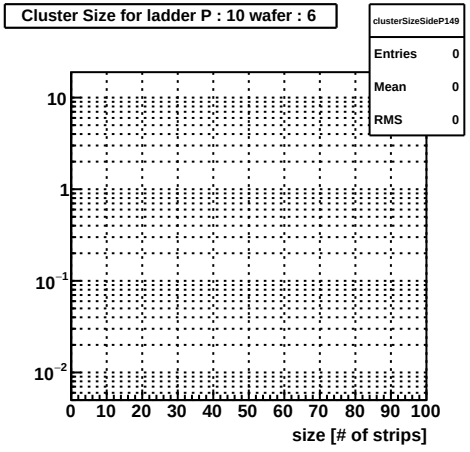

Cluster Size for ladder P : 10 wafer : 5
clusterSizeSideP148
Entries 0
Mean 0
RMS 0
size [# of strips]

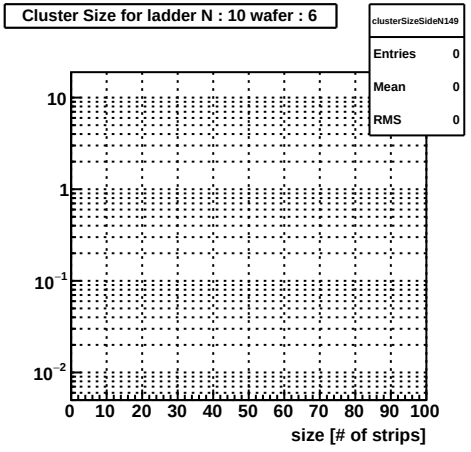

Cluster Size for ladder N : 10 wafer : 5
clusterSizeSideN148
Entries 0
Mean 0
RMS 0
size [# of strips]

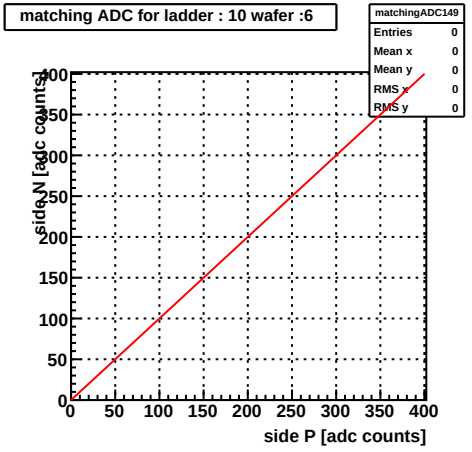

matching ADC for ladder : 10 wafer :5
matchingADC148
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

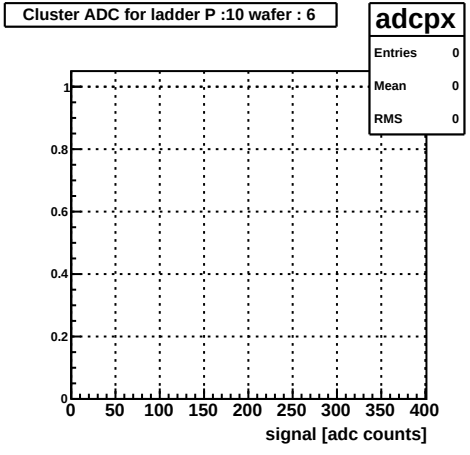

Cluster ADC for ladder P :10 wafer : 5
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

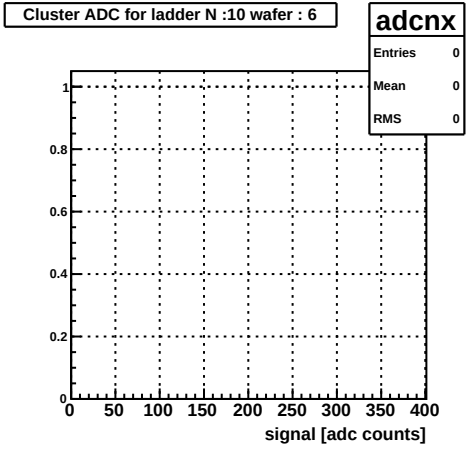

Cluster ADC for ladder N :10 wafer : 5
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

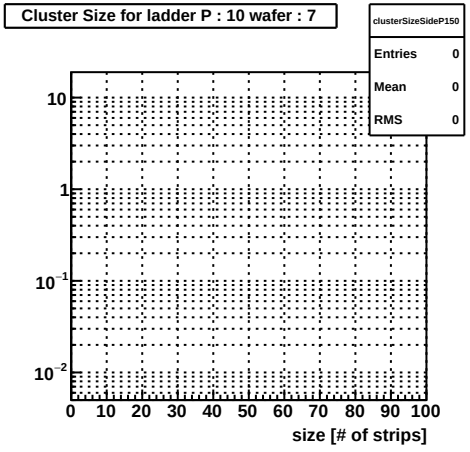

Cluster Size for ladder P : 10 wafer : 6
clusterSizeSideP149
Entries 0
Mean 0
RMS 0
size [# of strips]

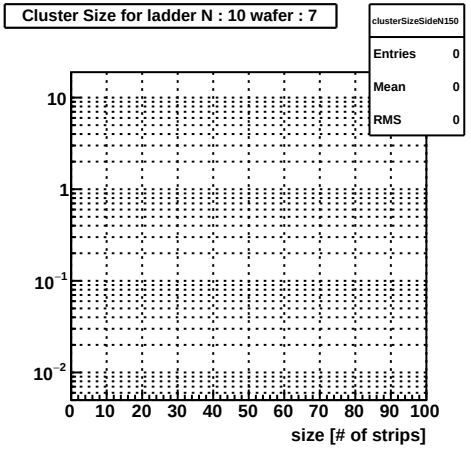

Cluster Size for ladder N : 10 wafer : 6
clusterSizeSideN149
Entries 0
Mean 0
RMS 0
size [# of strips]

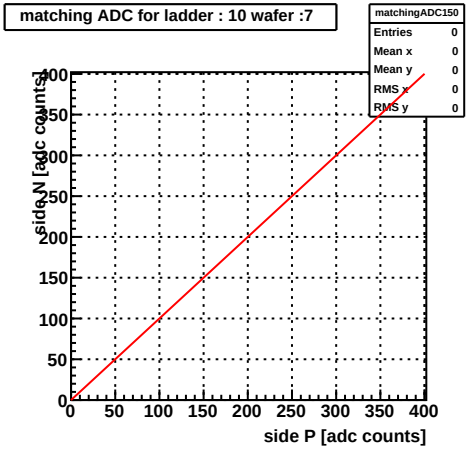

matching ADC for ladder : 10 wafer :6
matchingADC149
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

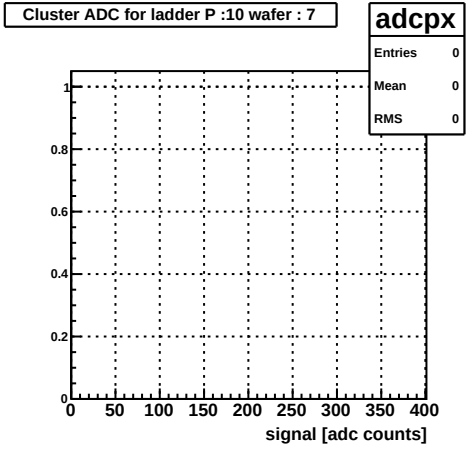

Cluster ADC for ladder P :10 wafer : 6
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

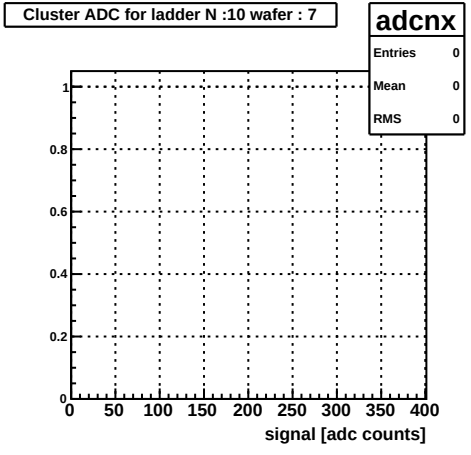

Cluster ADC for ladder N :10 wafer : 6
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

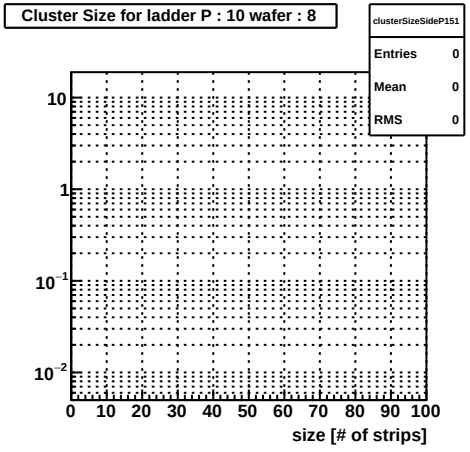

Cluster Size for ladder P : 10 wafer : 7
clusterSizeSideP150
Entries 0
Mean 0
RMS 0
size [# of strips]

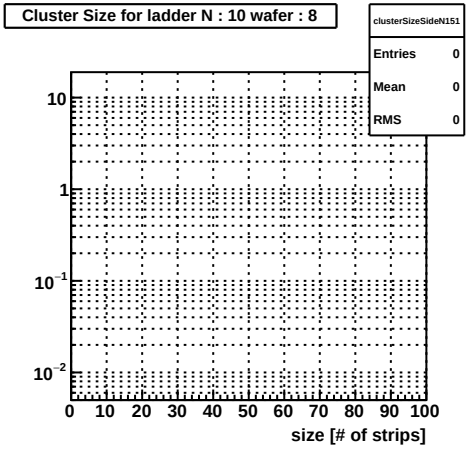

Cluster Size for ladder N : 10 wafer : 7
clusterSizeSideN150
Entries 0
Mean 0
RMS 0
size [# of strips]

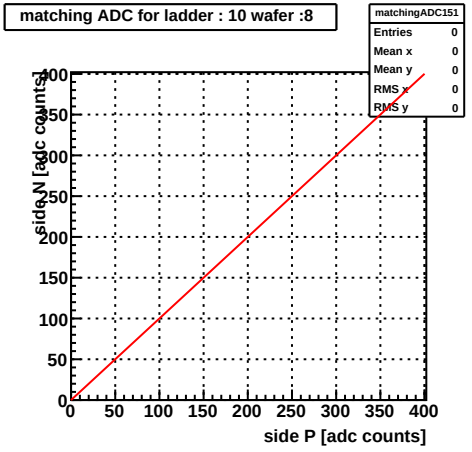

matching ADC for ladder : 10 wafer :7
matchingADC150
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

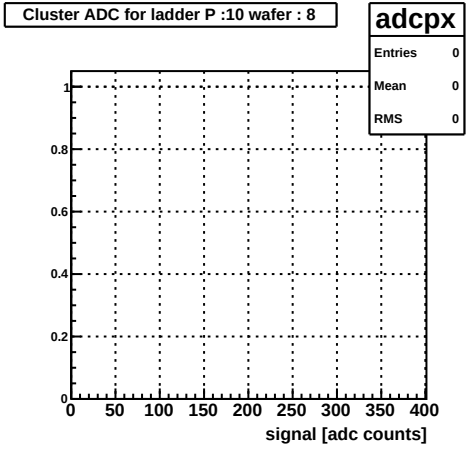

Cluster ADC for ladder P :10 wafer : 7
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

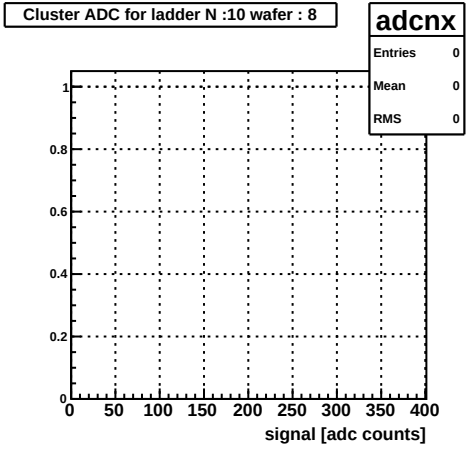

Cluster ADC for ladder N :10 wafer : 7
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

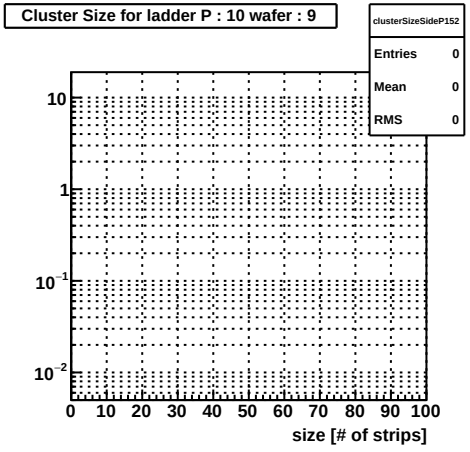

Cluster Size for ladder P : 10 wafer : 8
clusterSizeSideP151
Entries 0
Mean 0
RMS 0
size [# of strips]

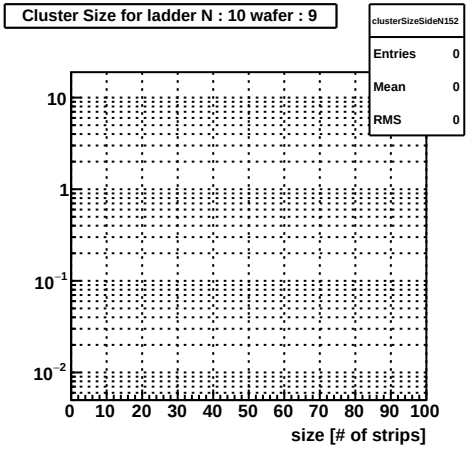

Cluster Size for ladder N : 10 wafer : 8
clusterSizeSideN151
Entries 0
Mean 0
RMS 0
size [# of strips]

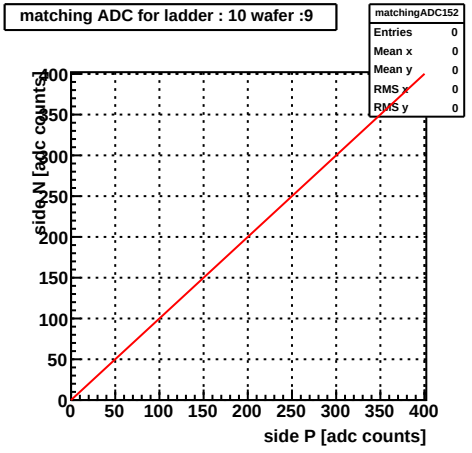

matching ADC for ladder : 10 wafer :8
matchingADC151
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

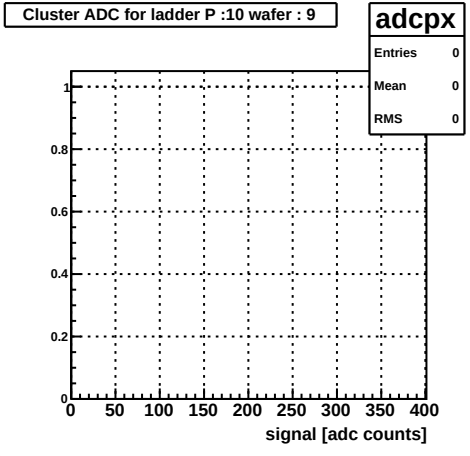

Cluster ADC for ladder P :10 wafer : 8
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

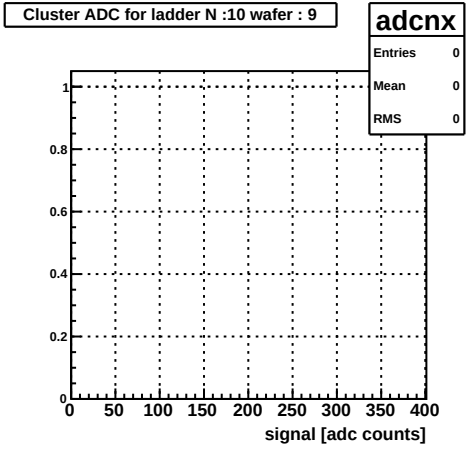

Cluster ADC for ladder N :10 wafer : 8
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

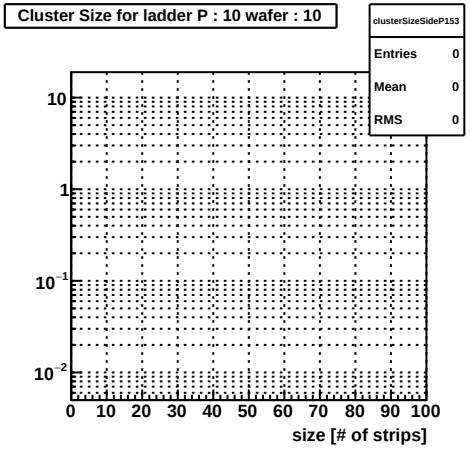

Cluster Size for ladder P : 10 wafer : 9
clusterSizeSideP152
Entries 0
Mean 0
RMS 0
size [# of strips]

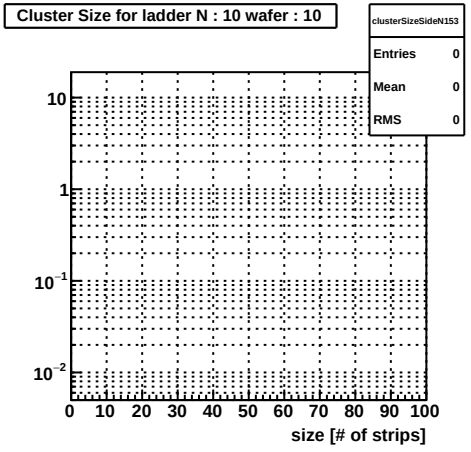

Cluster Size for ladder N : 10 wafer : 9
clusterSizeSideN152
Entries 0
Mean 0
RMS 0
size [# of strips]

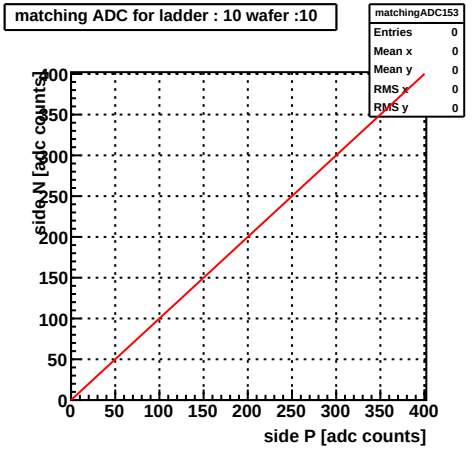

matching ADC for ladder : 10 wafer :9
matchingADC152
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

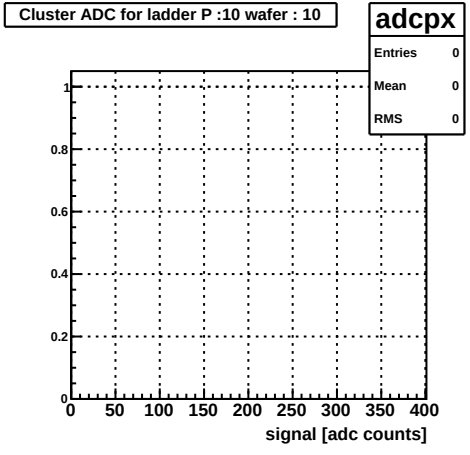

Cluster ADC for ladder P :10 wafer : 9
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

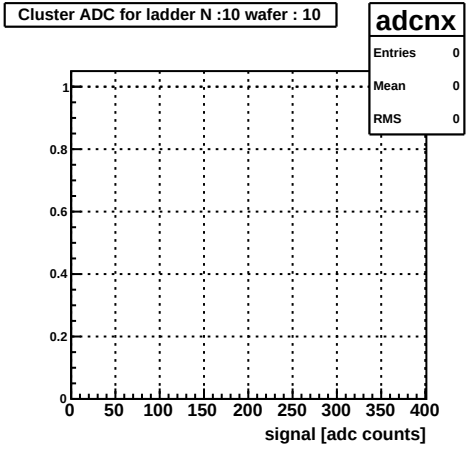

Cluster ADC for ladder N :10 wafer : 9
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

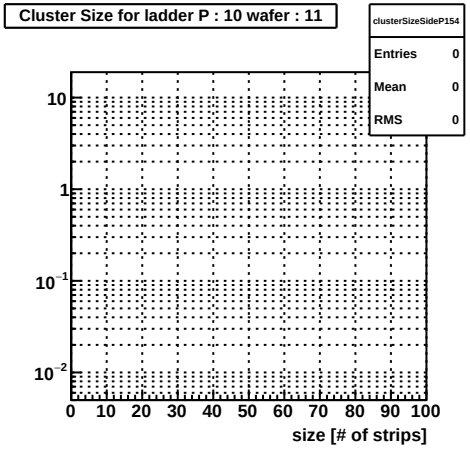

Cluster Size for ladder P : 10 wafer : 10
clusterSizeSideP153
Entries 0
Mean 0
RMS 0
size [# of strips]

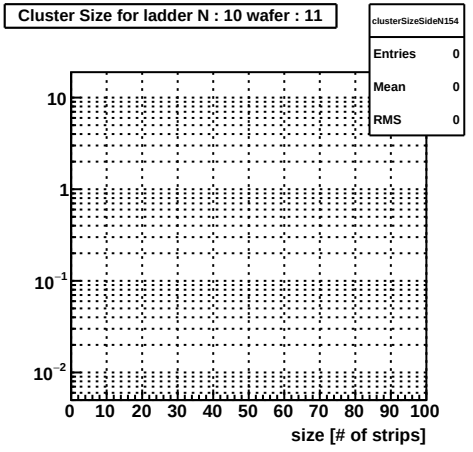

Cluster Size for ladder N : 10 wafer : 10
clusterSizeSideN153
Entries 0
Mean 0
RMS 0
size [# of strips]

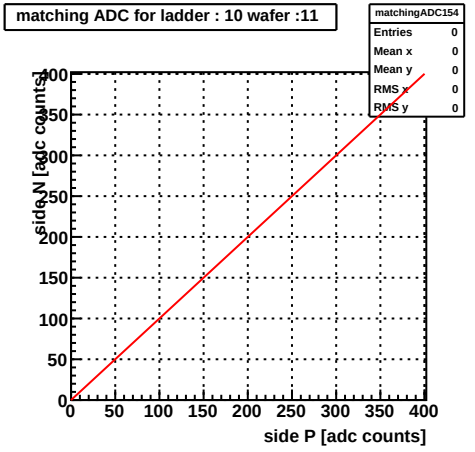

matching ADC for ladder : 10 wafer :10
matchingADC153
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

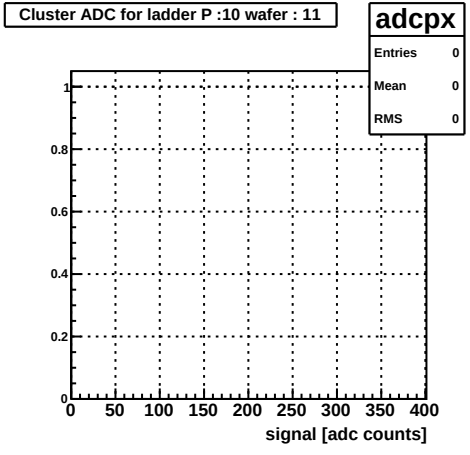

Cluster ADC for ladder P :10 wafer : 10
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

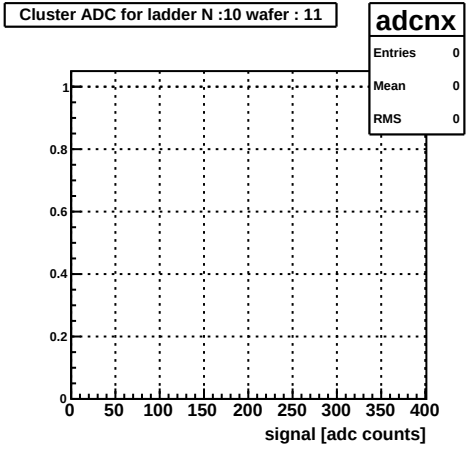

Cluster ADC for ladder N :10 wafer : 10
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

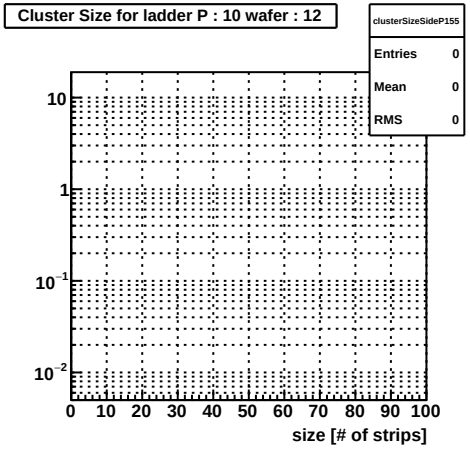

Cluster Size for ladder P : 10 wafer : 11
clusterSizeSideP154
Entries 0
Mean 0
RMS 0
size [# of strips]

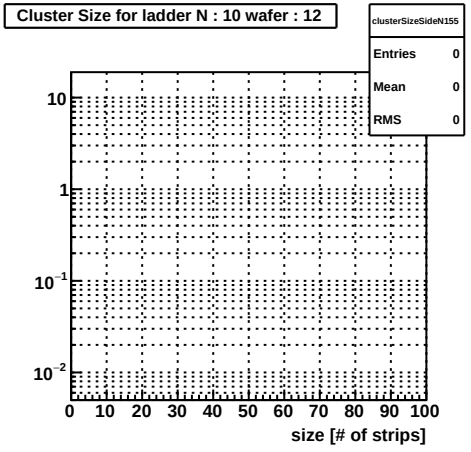

Cluster Size for ladder N : 10 wafer : 11
clusterSizeSideN154
Entries 0
Mean 0
RMS 0
size [# of strips]

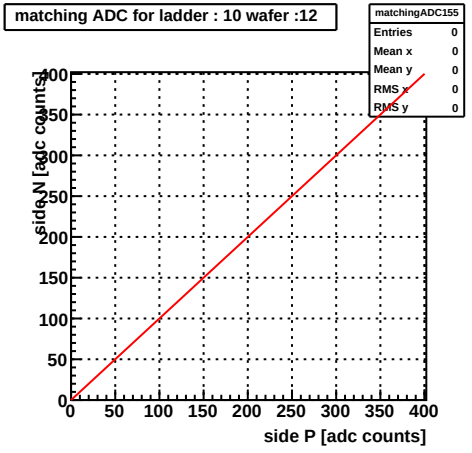

matching ADC for ladder : 10 wafer :11
matchingADC154
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

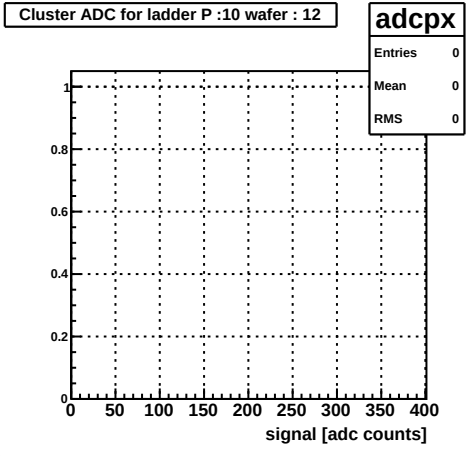

Cluster ADC for ladder P :10 wafer : 11
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

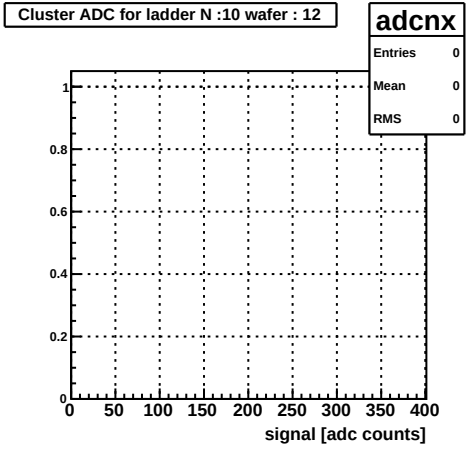

Cluster ADC for ladder N :10 wafer : 11
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

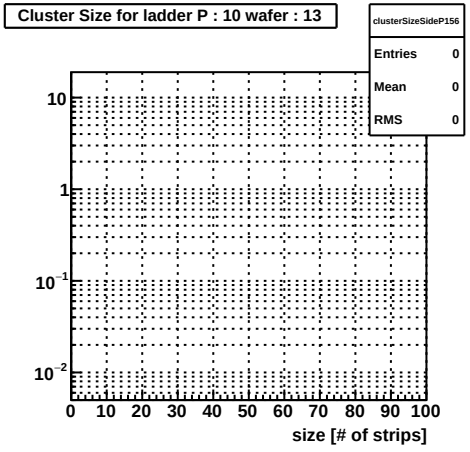

Cluster Size for ladder P : 10 wafer : 12
clusterSizeSideP155
Entries 0
Mean 0
RMS 0
size [# of strips]

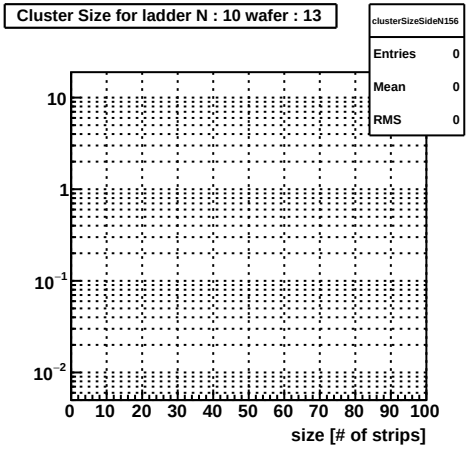

Cluster Size for ladder N : 10 wafer : 12
clusterSizeSideN155
Entries 0
Mean 0
RMS 0
size [# of strips]

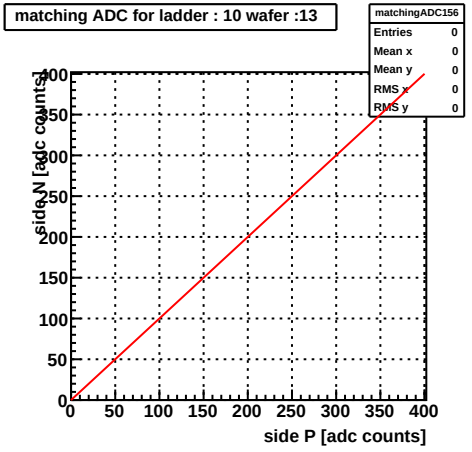

matching ADC for ladder : 10 wafer :12
matchingADC155
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

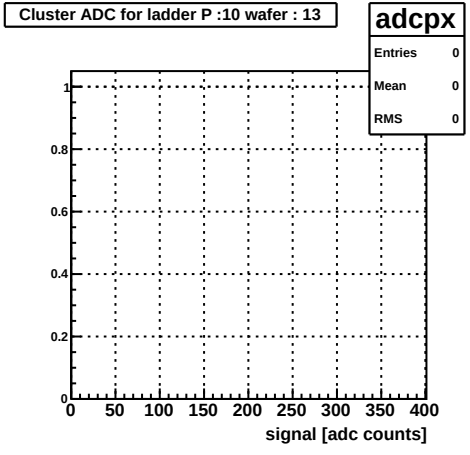

Cluster ADC for ladder P :10 wafer : 12
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

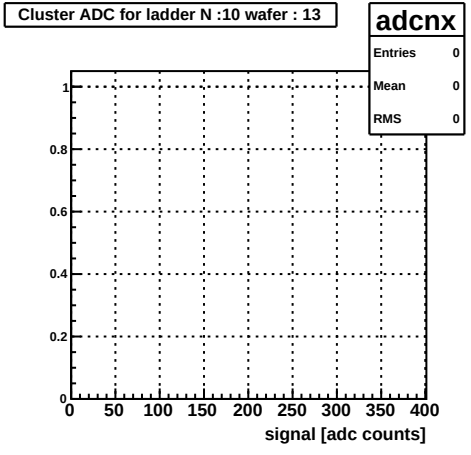

Cluster ADC for ladder N :10 wafer : 12
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

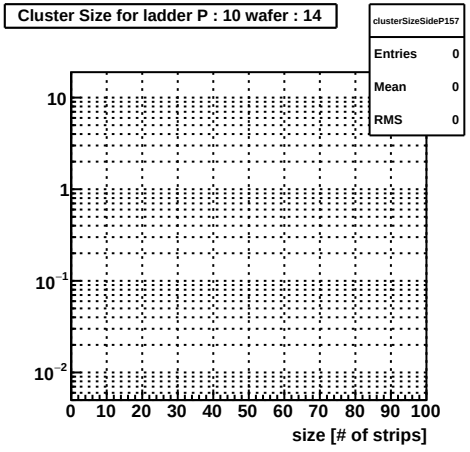

Cluster Size for ladder P : 10 wafer : 13
clusterSizeSideP156
Entries 0
Mean 0
RMS 0
size [# of strips]

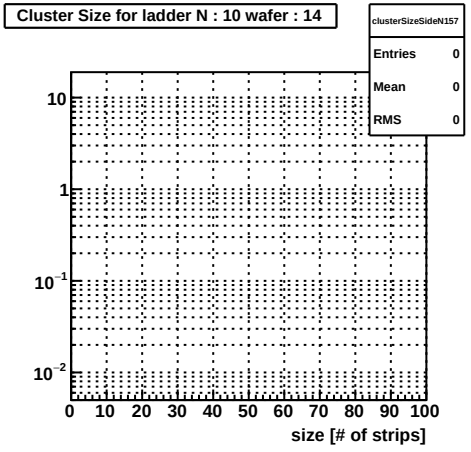

Cluster Size for ladder N : 10 wafer : 13
clusterSizeSideN156
Entries 0
Mean 0
RMS 0
size [# of strips]

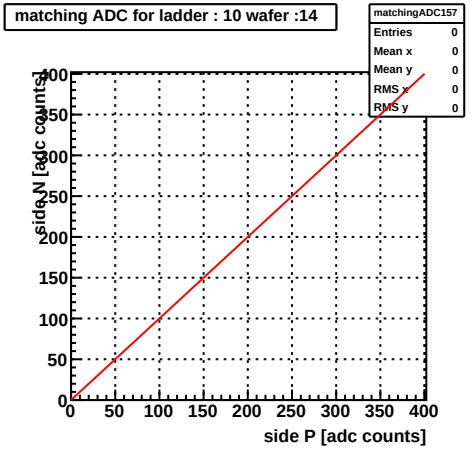

matching ADC for ladder : 10 wafer :13
matchingADC156
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

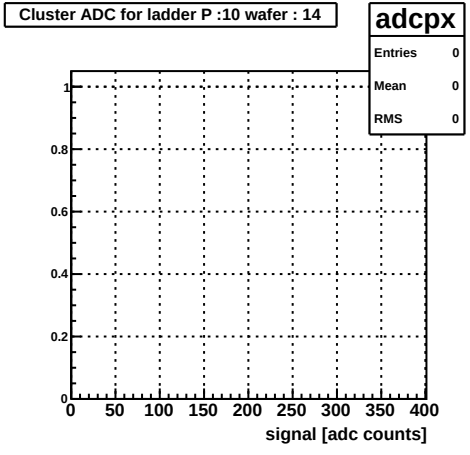

Cluster ADC for ladder P :10 wafer : 13
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

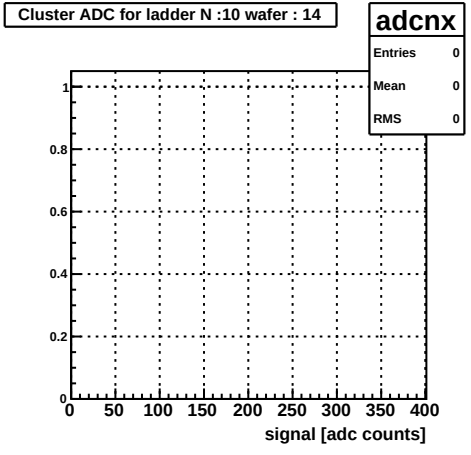

Cluster ADC for ladder N :10 wafer : 13
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

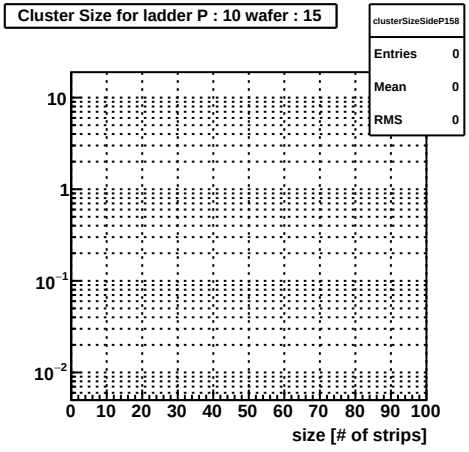

Cluster Size for ladder P : 10 wafer : 14
clusterSizeSideP157
Entries 0
Mean 0
RMS 0
size [# of strips]

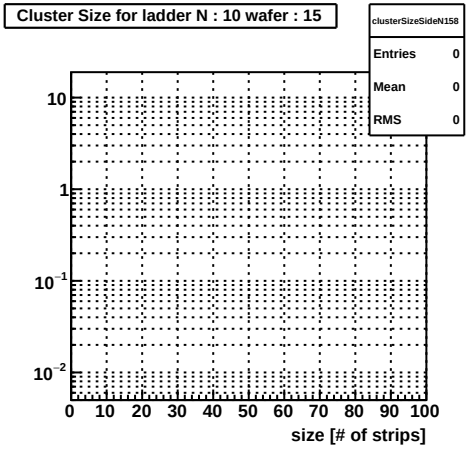

Cluster Size for ladder N : 10 wafer : 14
clusterSizeSideN157
Entries 0
Mean 0
RMS 0
size [# of strips]

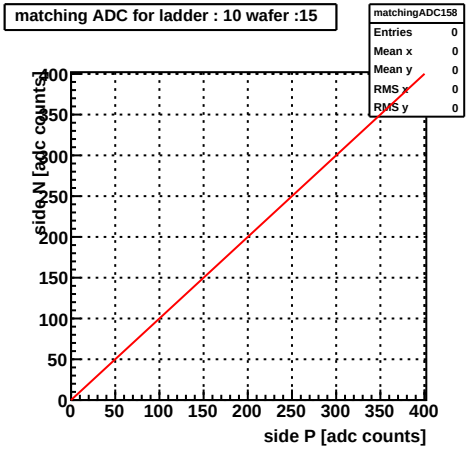

matching ADC for ladder : 10 wafer :14
matchingADC157
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

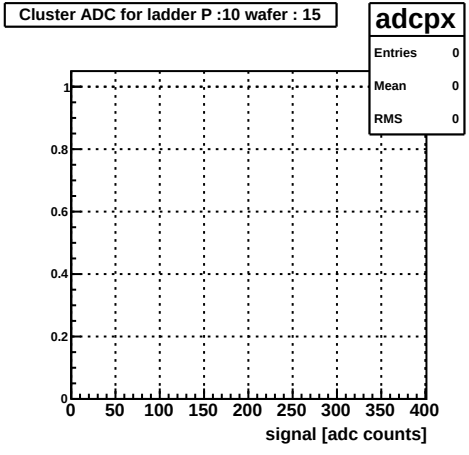

Cluster ADC for ladder P :10 wafer : 14
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

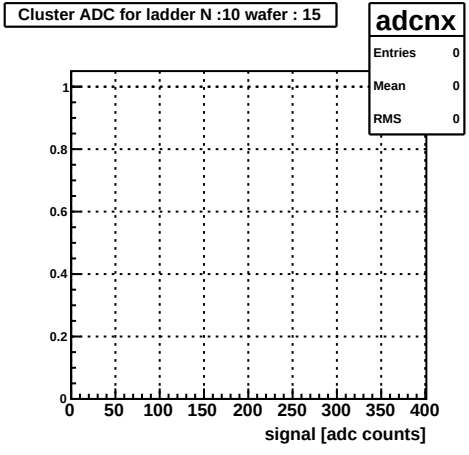

Cluster ADC for ladder N :10 wafer : 14
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

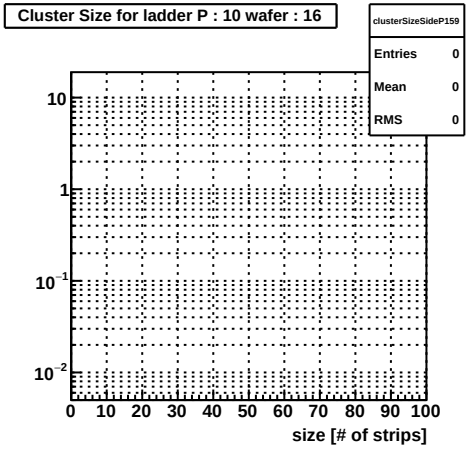

Cluster Size for ladder P : 10 wafer : 15
clusterSizeSideP158
Entries 0
Mean 0
RMS 0
size [# of strips]

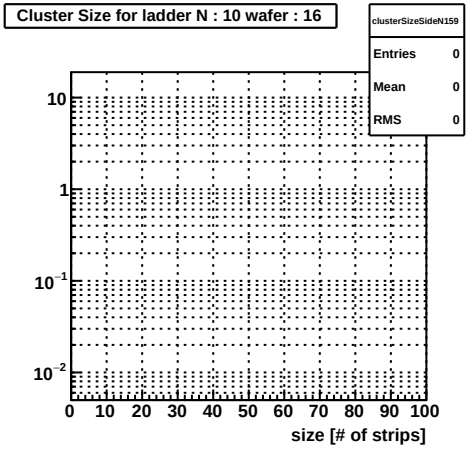

Cluster Size for ladder N : 10 wafer : 15
clusterSizeSideN158
Entries 0
Mean 0
RMS 0
size [# of strips]

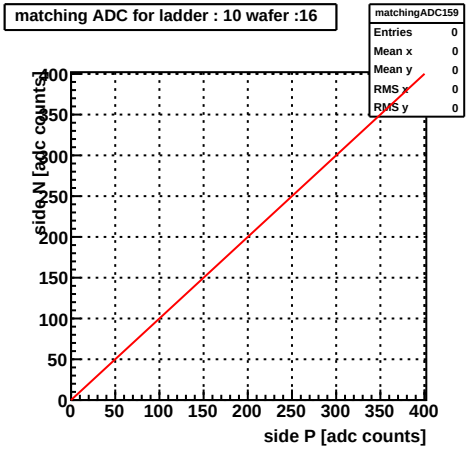

matching ADC for ladder : 10 wafer :15
matchingADC158
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

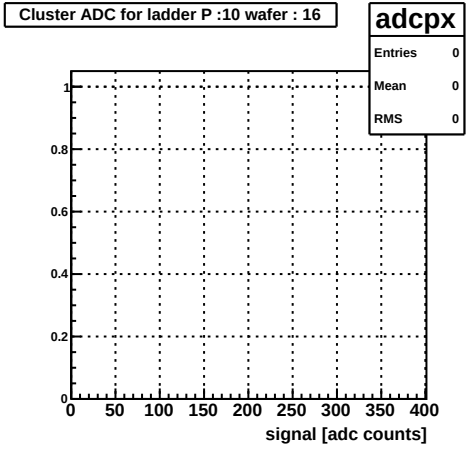

Cluster ADC for ladder P :10 wafer : 15
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

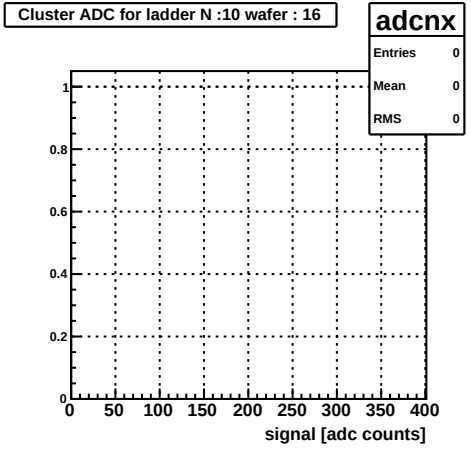

Cluster ADC for ladder N :10 wafer : 15
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

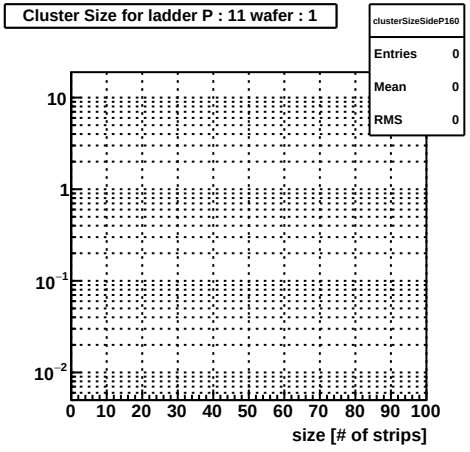

Cluster Size for ladder P : 10 wafer : 16
clusterSizeSideP159
Entries 0
Mean 0
RMS 0
size [# of strips]

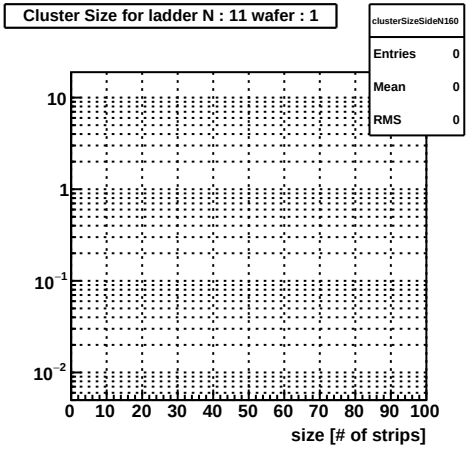

Cluster Size for ladder N : 10 wafer : 16
clusterSizeSideN159
Entries 0
Mean 0
RMS 0
size [# of strips]

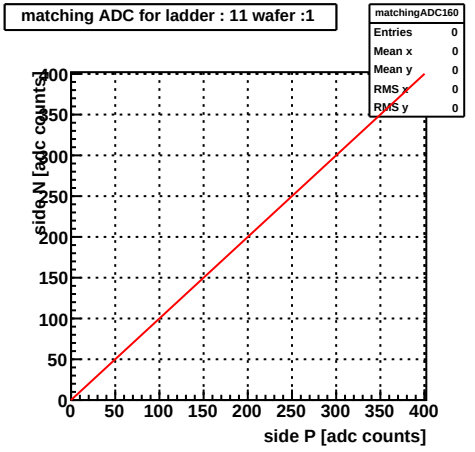

matching ADC for ladder : 10 wafer :16
matchingADC159
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

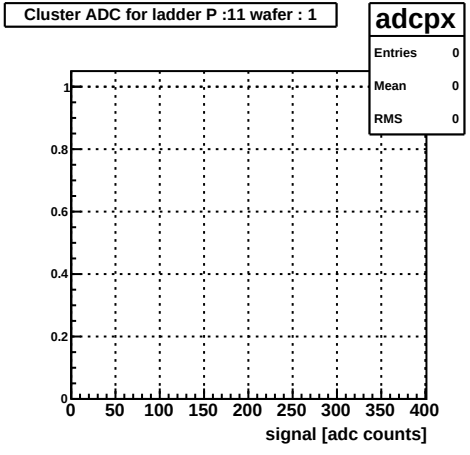

Cluster ADC for ladder P :10 wafer : 16
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

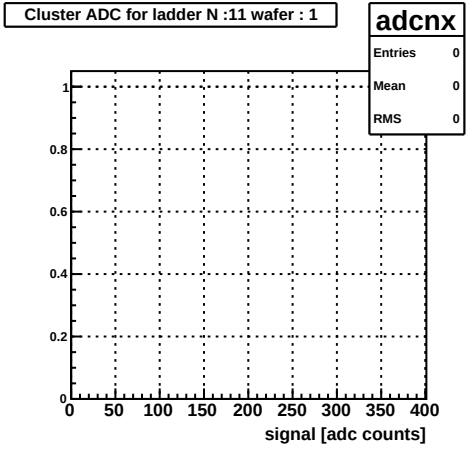

Cluster ADC for ladder N :10 wafer : 16
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

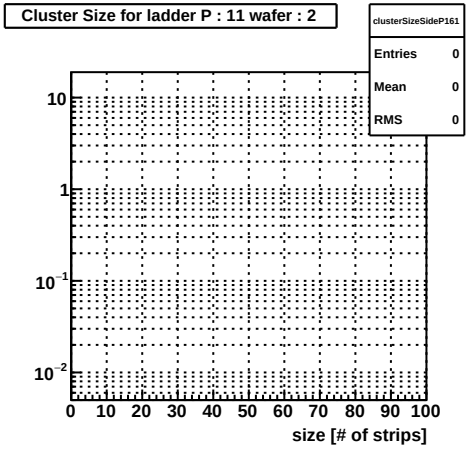

Cluster Size for ladder P : 11 wafer : 1
clusterSizeSideP160
Entries 0
Mean 0
RMS 0
size [# of strips]

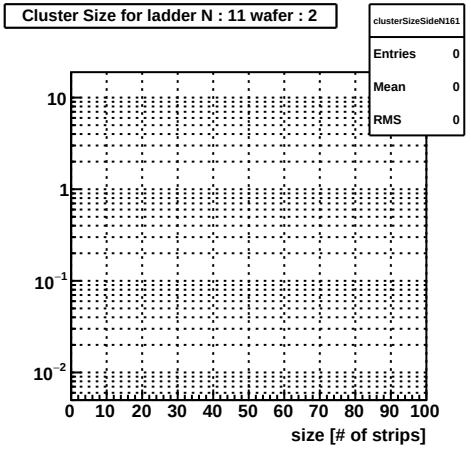

Cluster Size for ladder N : 11 wafer : 1
clusterSizeSideN160
Entries 0
Mean 0
RMS 0
size [# of strips]

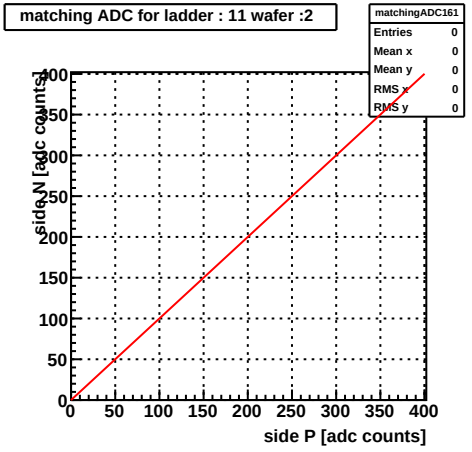

matching ADC for ladder : 11 wafer :1
matchingADC160
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

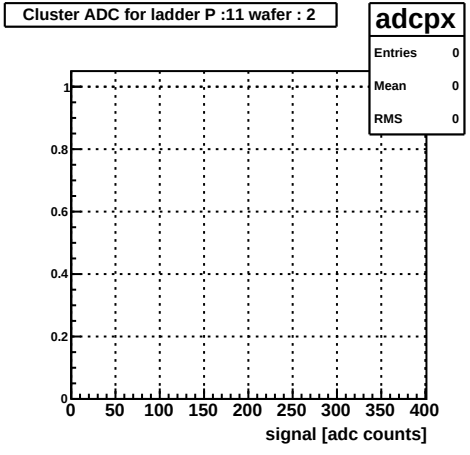

Cluster ADC for ladder P :11 wafer : 1
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

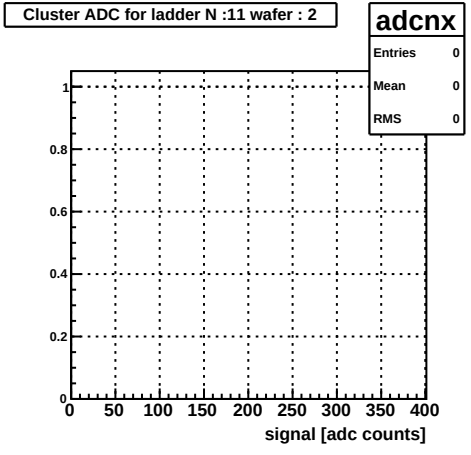

Cluster ADC for ladder N :11 wafer : 1
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

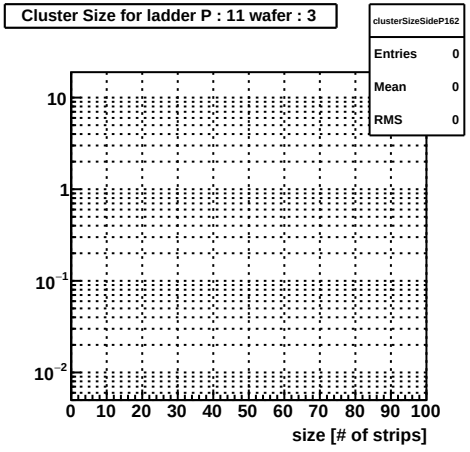

Cluster Size for ladder P : 11 wafer : 2
clusterSizeSideP161
Entries 0
Mean 0
RMS 0
size [# of strips]

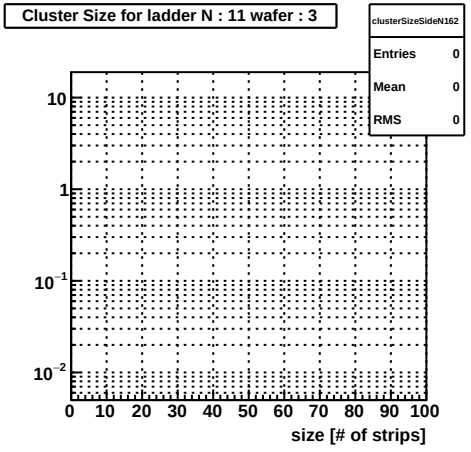

Cluster Size for ladder N : 11 wafer : 2
clusterSizeSideN161
Entries 0
Mean 0
RMS 0
size [# of strips]

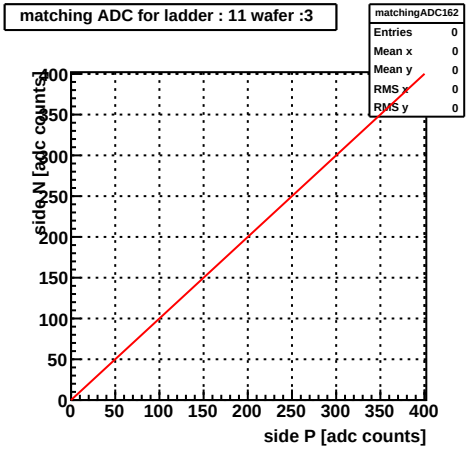

matching ADC for ladder : 11 wafer :2
matchingADC161
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

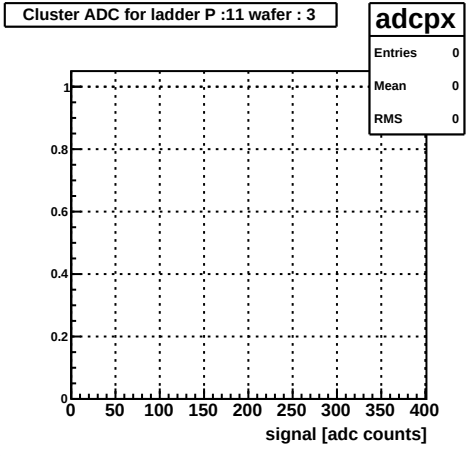

Cluster ADC for ladder P :11 wafer : 2
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

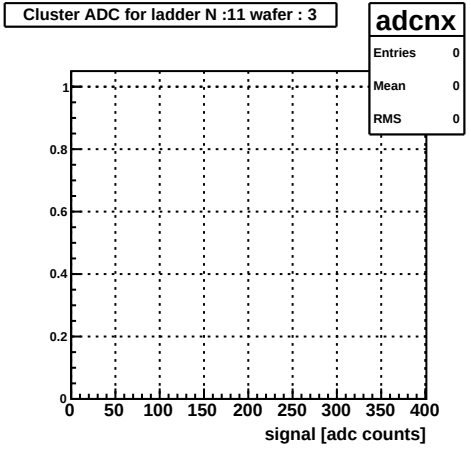

Cluster ADC for ladder N :11 wafer : 2
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

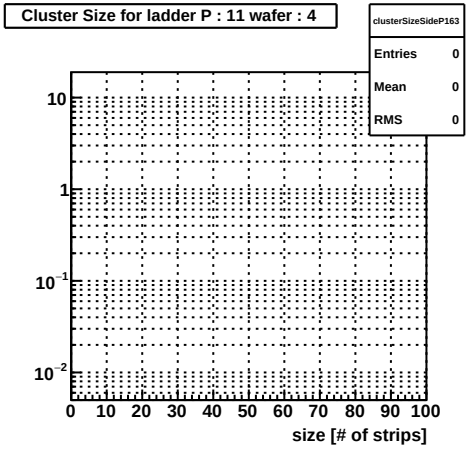

Cluster Size for ladder P : 11 wafer : 3
clusterSizeSideP162
Entries 0
Mean 0
RMS 0
size [# of strips]

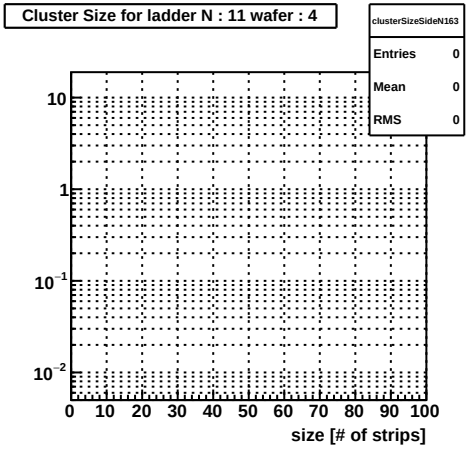

Cluster Size for ladder N : 11 wafer : 3
clusterSizeSideN162
Entries 0
Mean 0
RMS 0
size [# of strips]

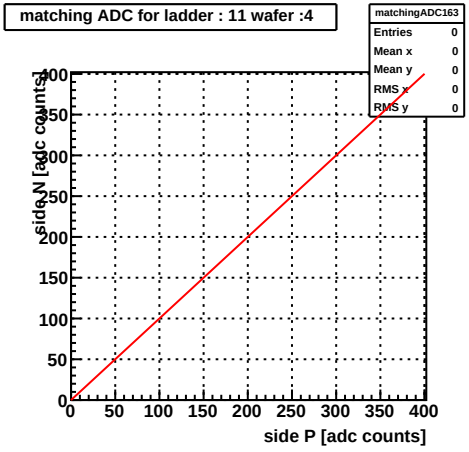

matching ADC for ladder : 11 wafer :3
matchingADC162
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

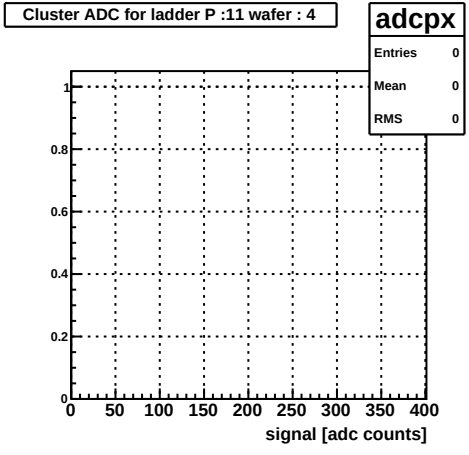

Cluster ADC for ladder P :11 wafer : 3
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

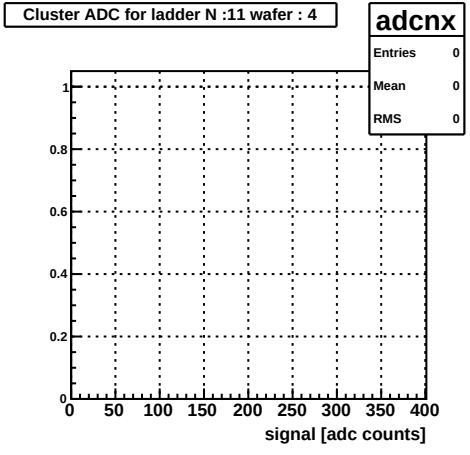

Cluster ADC for ladder N :11 wafer : 3
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

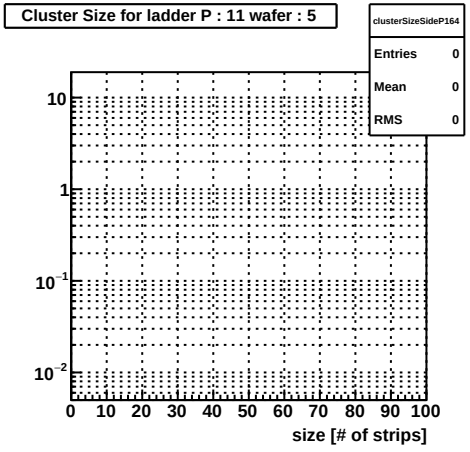

Cluster Size for ladder P : 11 wafer : 4
clusterSizeSideP163
Entries 0
Mean 0
RMS 0
size [# of strips]

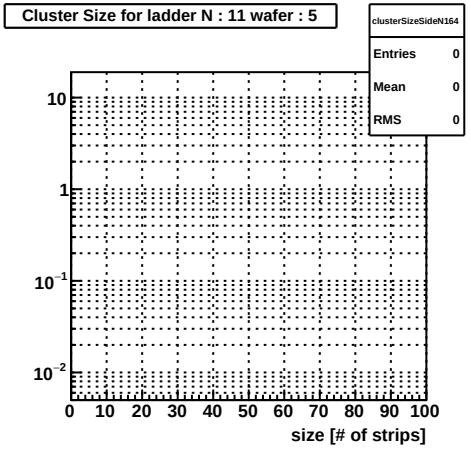

Cluster Size for ladder N : 11 wafer : 4
clusterSizeSideN163
Entries 0
Mean 0
RMS 0
size [# of strips]

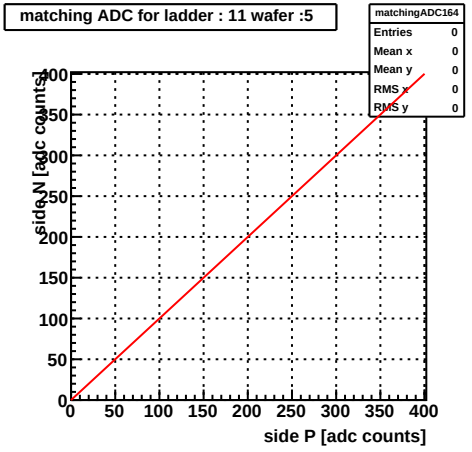

matching ADC for ladder : 11 wafer :4
matchingADC163
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

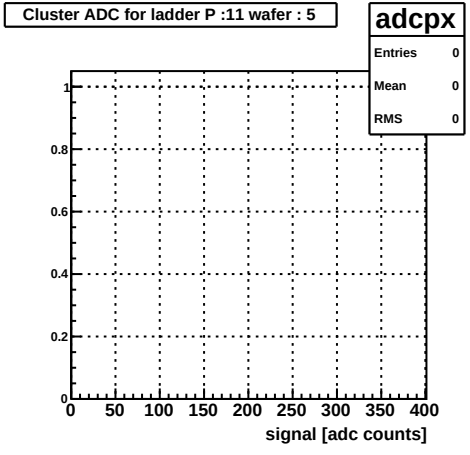

Cluster ADC for ladder P :11 wafer : 4
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

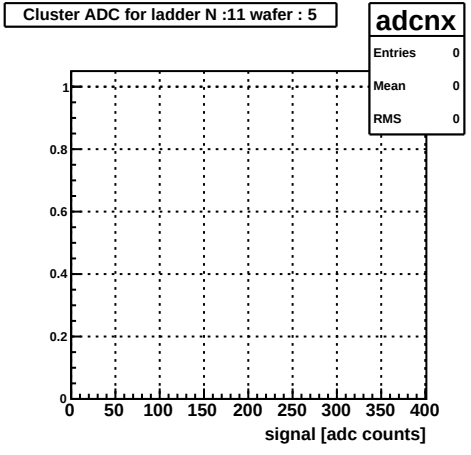

Cluster ADC for ladder N :11 wafer : 4
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

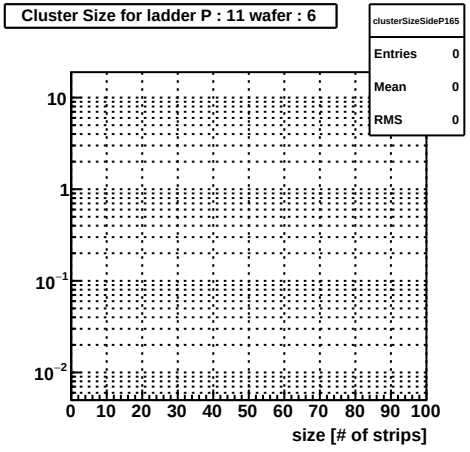

Cluster Size for ladder P : 11 wafer : 5
clusterSizeSideP164
Entries 0
Mean 0
RMS 0
size [# of strips]

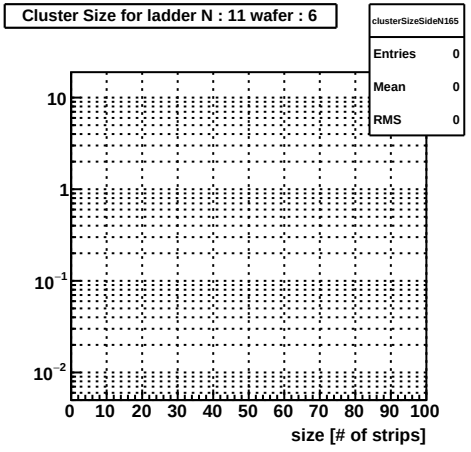

Cluster Size for ladder N : 11 wafer : 5
clusterSizeSideN164
Entries 0
Mean 0
RMS 0
size [# of strips]

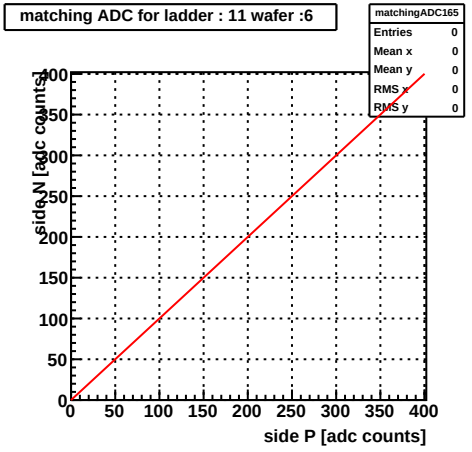

matching ADC for ladder : 11 wafer :5
matchingADC164
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

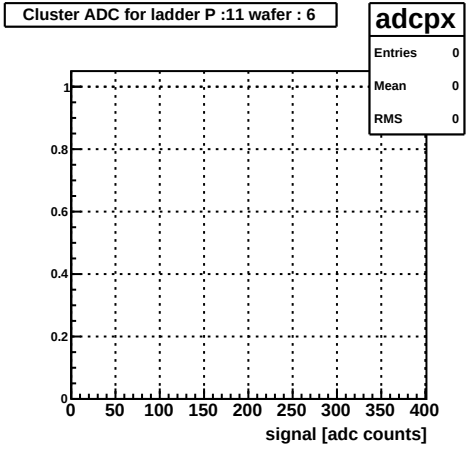

Cluster ADC for ladder P :11 wafer : 5
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

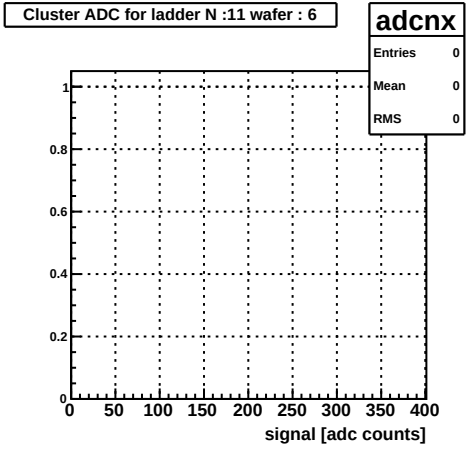

Cluster ADC for ladder N :11 wafer : 5
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

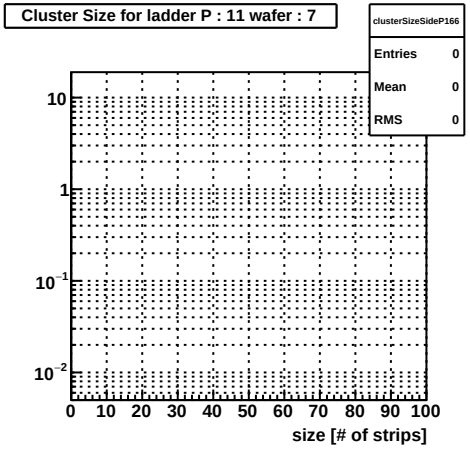

Cluster Size for ladder P : 11 wafer : 6
clusterSizeSideP165
Entries 0
Mean 0
RMS 0
size [# of strips]

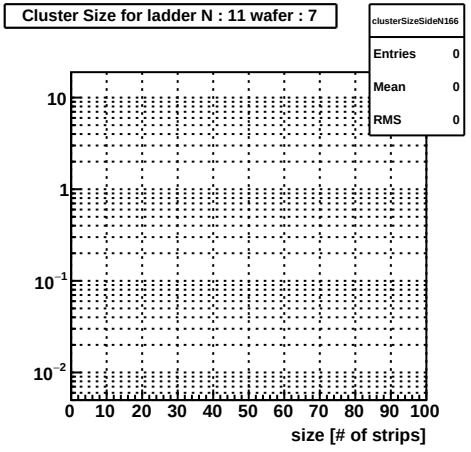

Cluster Size for ladder N : 11 wafer : 6
clusterSizeSideN165
Entries 0
Mean 0
RMS 0
size [# of strips]

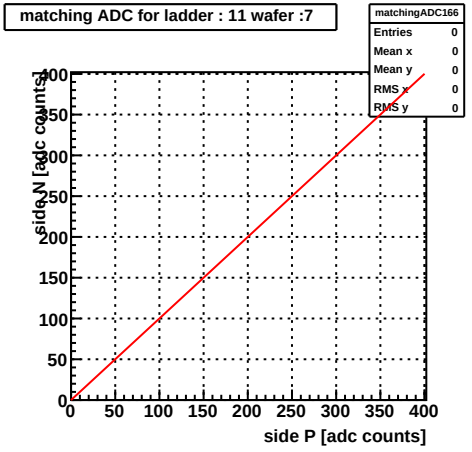

matching ADC for ladder : 11 wafer :6
matchingADC165
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

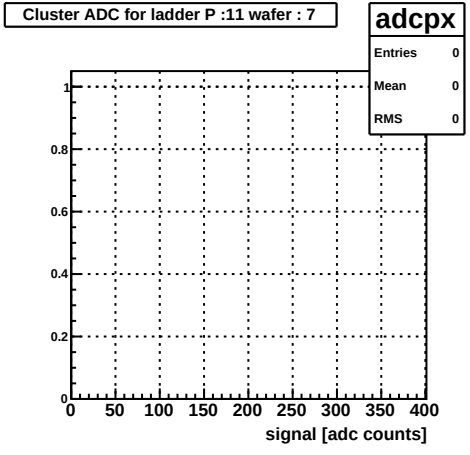

Cluster ADC for ladder P :11 wafer : 6
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

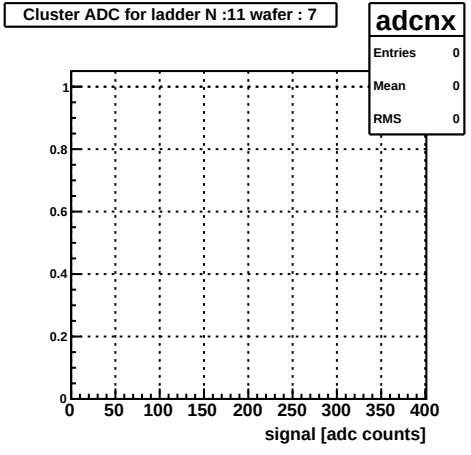

Cluster ADC for ladder N :11 wafer : 6
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

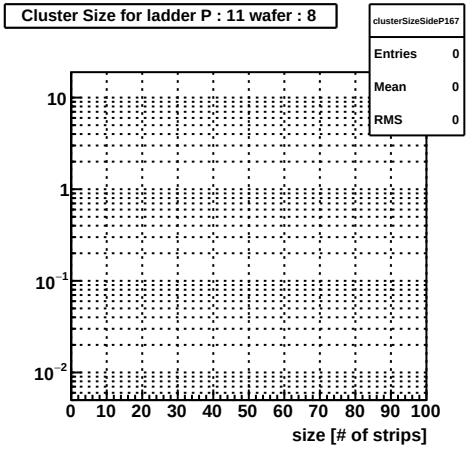

Cluster Size for ladder P : 11 wafer : 7
clusterSizeSideP166
Entries 0
Mean 0
RMS 0
size [# of strips]

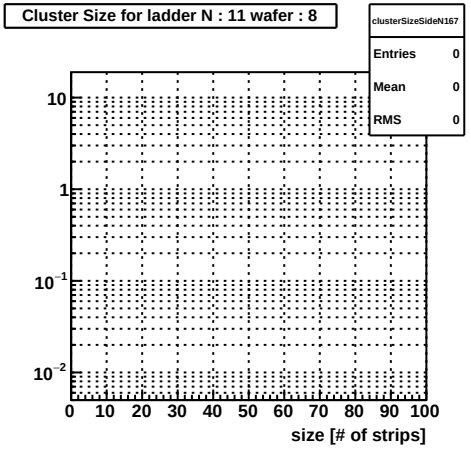

Cluster Size for ladder N : 11 wafer : 7
clusterSizeSideN166
Entries 0
Mean 0
RMS 0
size [# of strips]

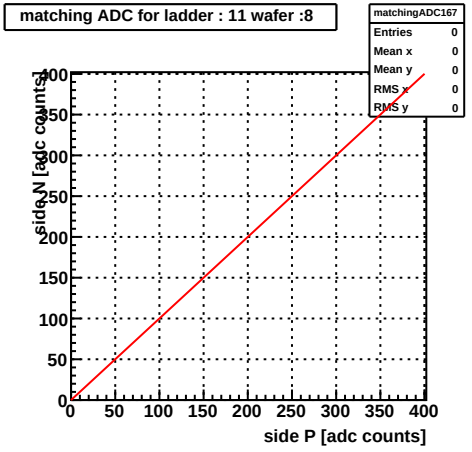

matching ADC for ladder : 11 wafer :7
matchingADC166
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

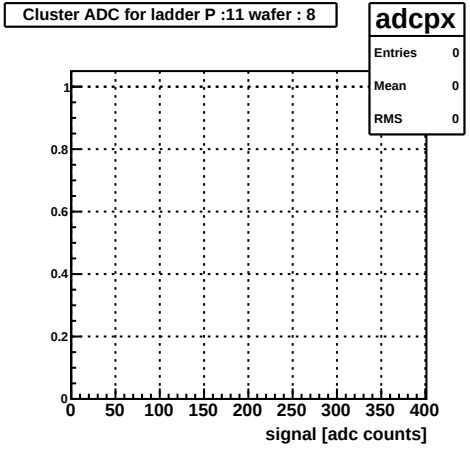

Cluster ADC for ladder P :11 wafer : 7
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

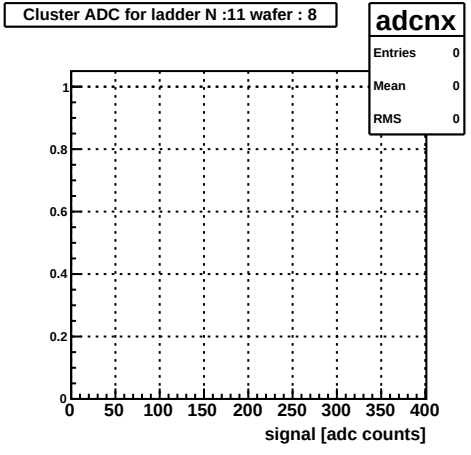

Cluster ADC for ladder N :11 wafer : 7
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

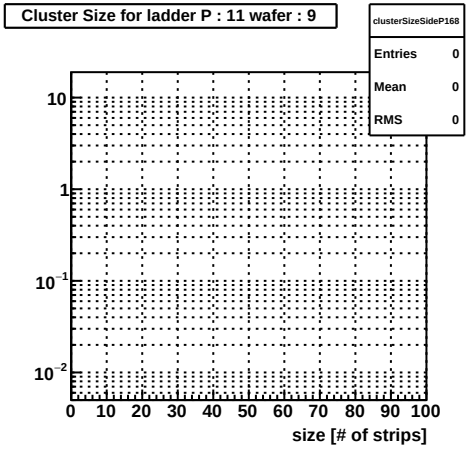

Cluster Size for ladder P : 11 wafer : 8
clusterSizeSideP167
Entries 0
Mean 0
RMS 0
size [# of strips]

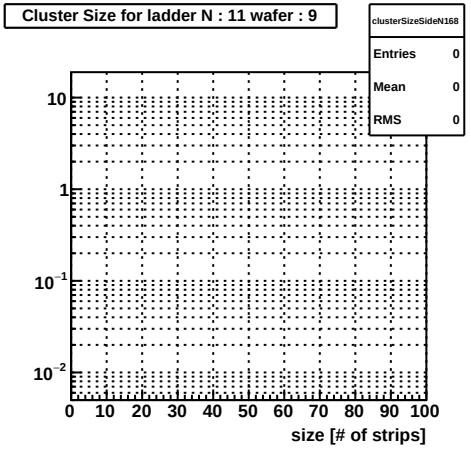

Cluster Size for ladder N : 11 wafer : 8
clusterSizeSideN167
Entries 0
Mean 0
RMS 0
size [# of strips]

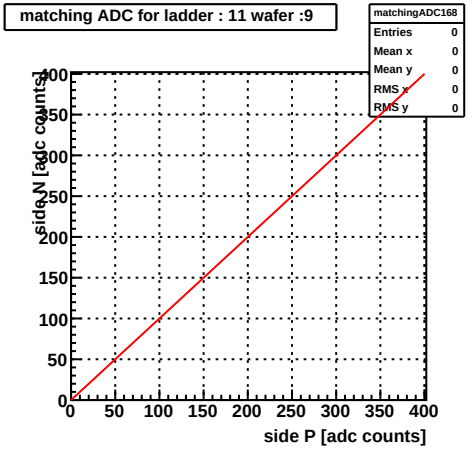

matching ADC for ladder : 11 wafer :8
matchingADC167
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

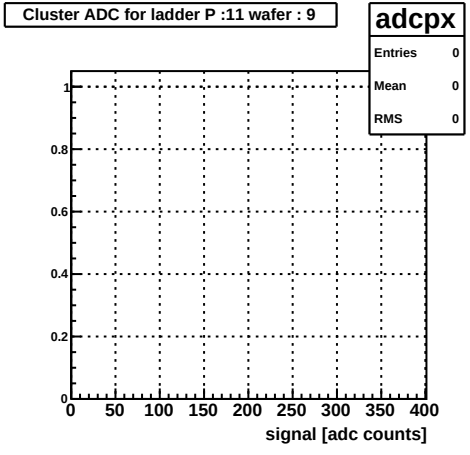

Cluster ADC for ladder P :11 wafer : 8
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

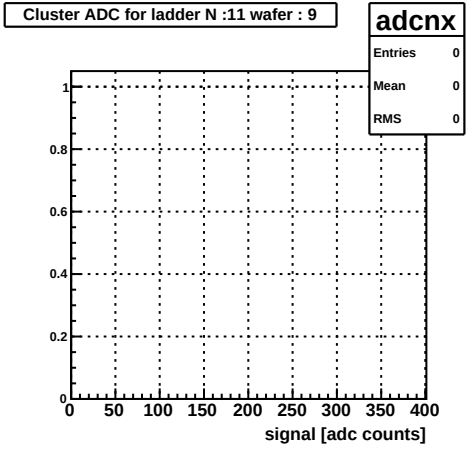

Cluster ADC for ladder N :11 wafer : 8
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

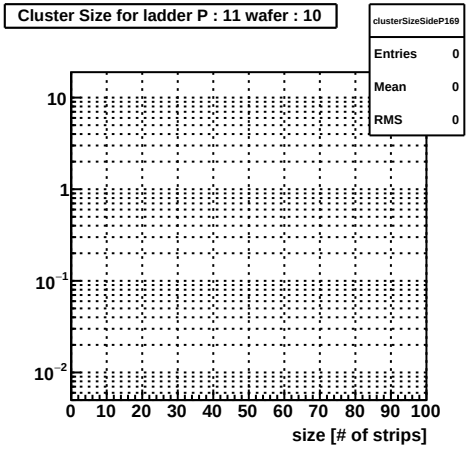

Cluster Size for ladder P : 11 wafer : 9
clusterSizeSideP168
Entries 0
Mean 0
RMS 0
size [# of strips]

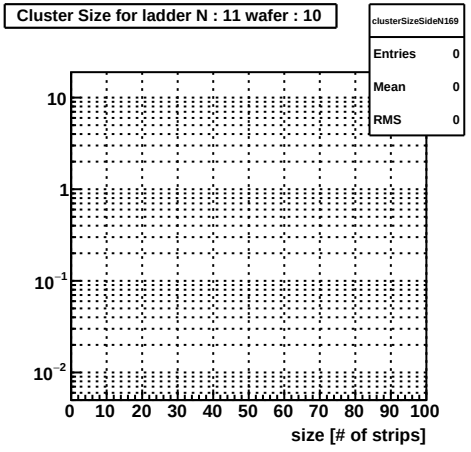

Cluster Size for ladder N : 11 wafer : 9
clusterSizeSideN168
Entries 0
Mean 0
RMS 0
size [# of strips]

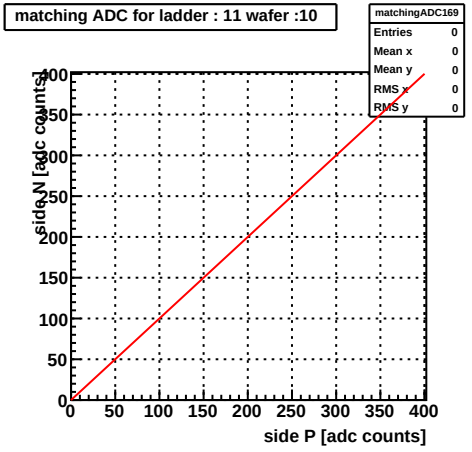

matching ADC for ladder : 11 wafer :9
matchingADC168
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

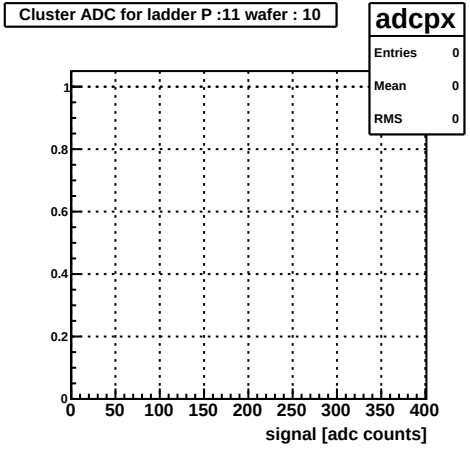

Cluster ADC for ladder P :11 wafer : 9
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

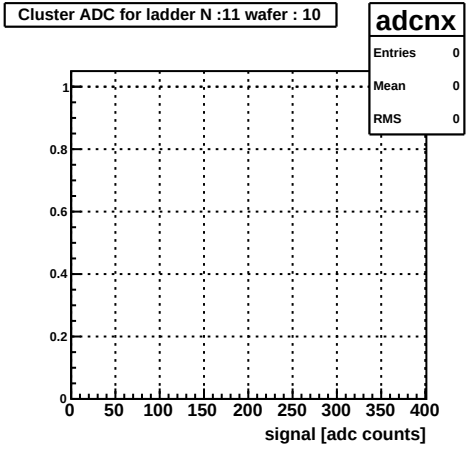

Cluster ADC for ladder N :11 wafer : 9
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

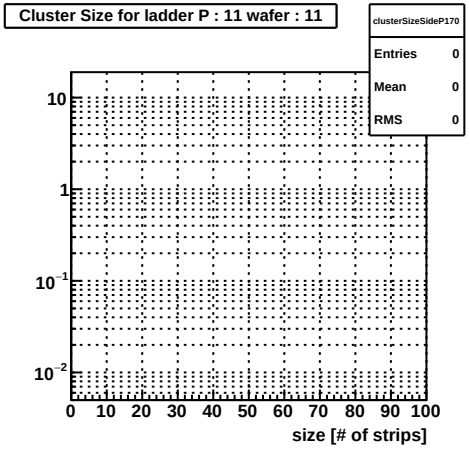

Cluster Size for ladder P : 11 wafer : 10
clusterSizeSideP169
Entries 0
Mean 0
RMS 0
size [# of strips]

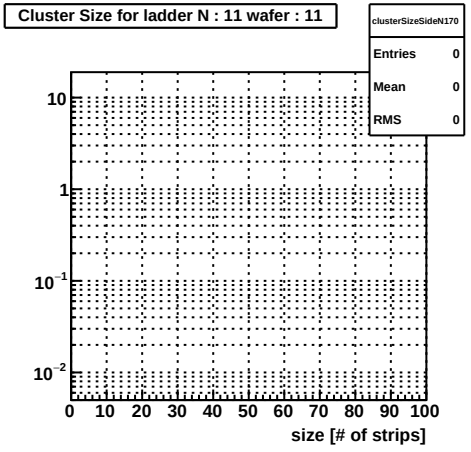

Cluster Size for ladder N : 11 wafer : 10
clusterSizeSideN169
Entries 0
Mean 0
RMS 0
size [# of strips]

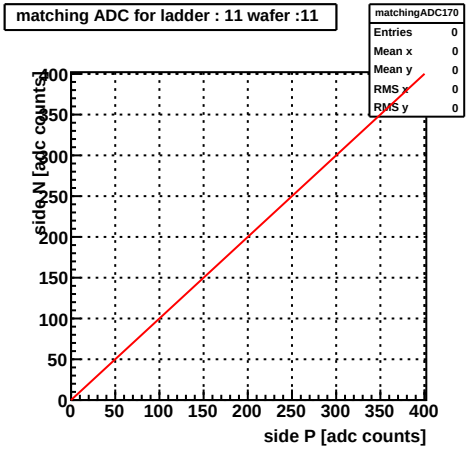

matching ADC for ladder : 11 wafer :10
matchingADC169
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

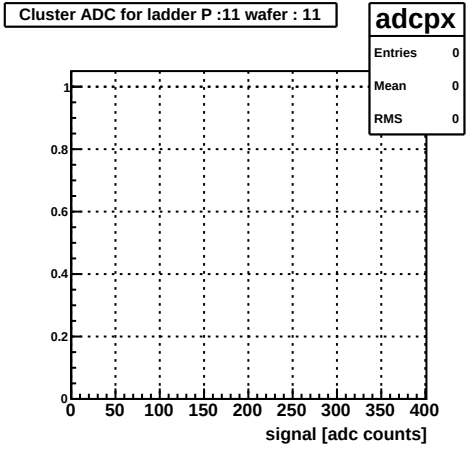

Cluster ADC for ladder P :11 wafer : 10
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

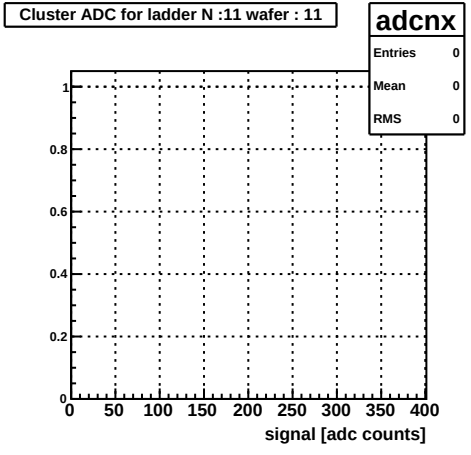

Cluster ADC for ladder N :11 wafer : 10
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

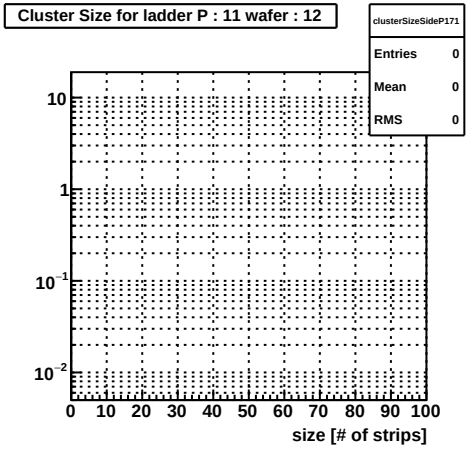

Cluster Size for ladder P : 11 wafer : 11
clusterSizeSideP170
Entries 0
Mean 0
RMS 0
size [# of strips]

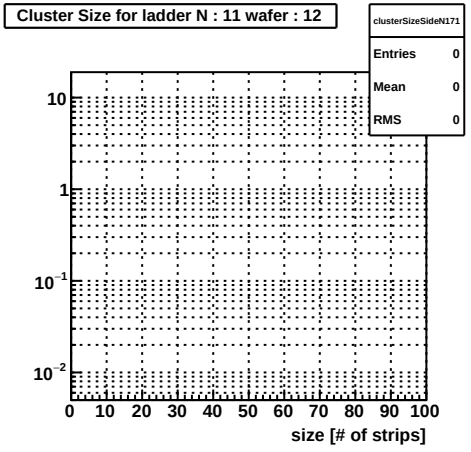

Cluster Size for ladder N : 11 wafer : 11
clusterSizeSideN170
Entries 0
Mean 0
RMS 0
size [# of strips]

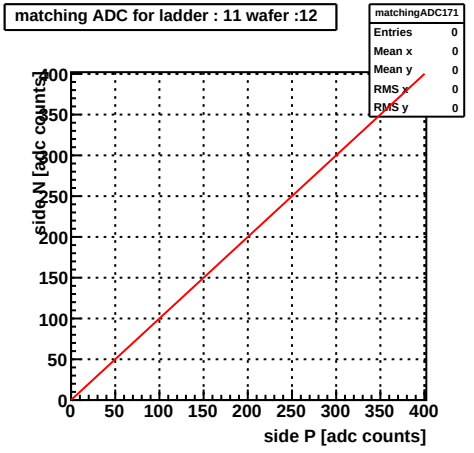

matching ADC for ladder : 11 wafer :11
matchingADC170
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

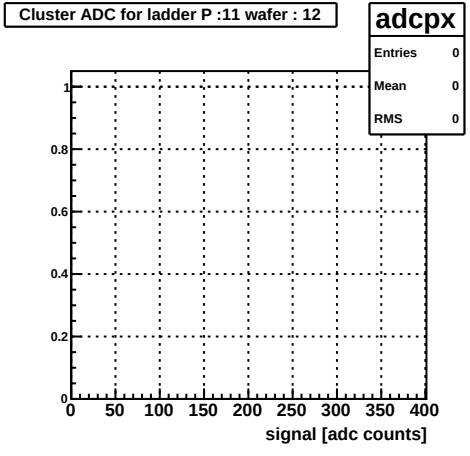

Cluster ADC for ladder P :11 wafer : 11
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

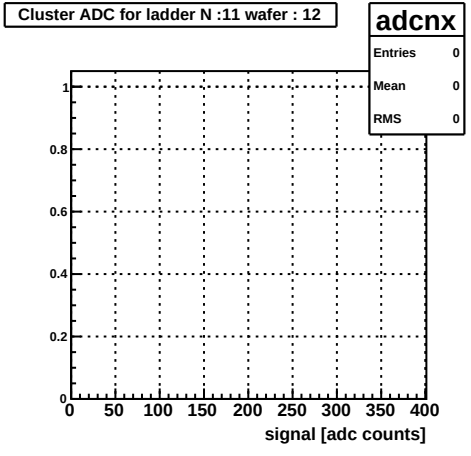

Cluster ADC for ladder N :11 wafer : 11
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

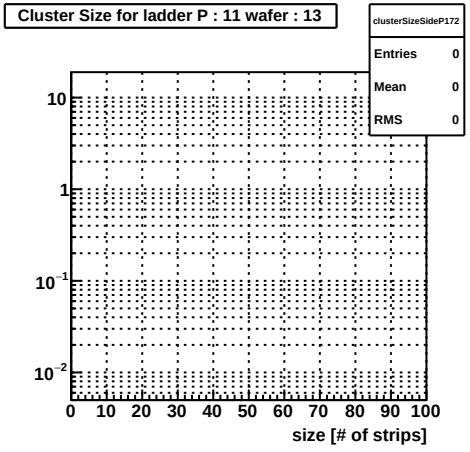

Cluster Size for ladder P : 11 wafer : 12
clusterSizeSideP171
Entries 0
Mean 0
RMS 0
size [# of strips]

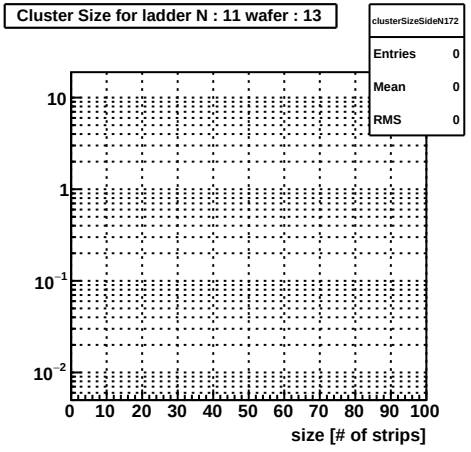

Cluster Size for ladder N : 11 wafer : 12
clusterSizeSideN171
Entries 0
Mean 0
RMS 0
size [# of strips]

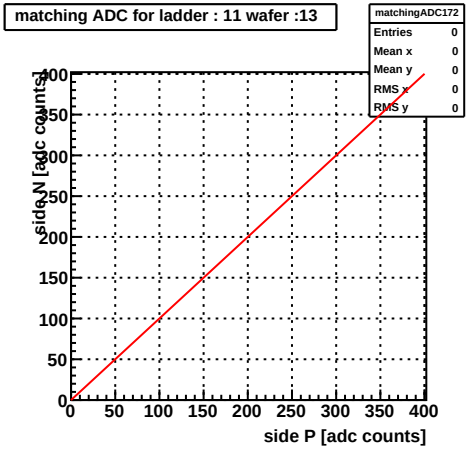

matching ADC for ladder : 11 wafer :12
matchingADC171
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

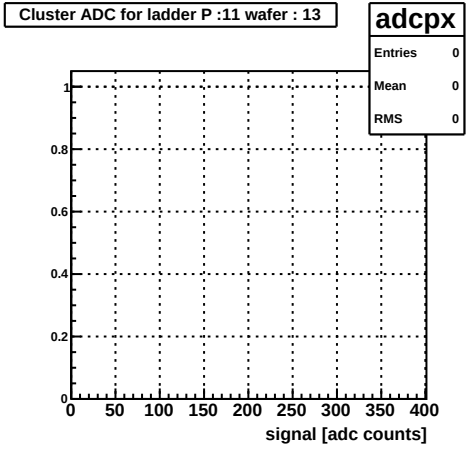

Cluster ADC for ladder P :11 wafer : 12
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

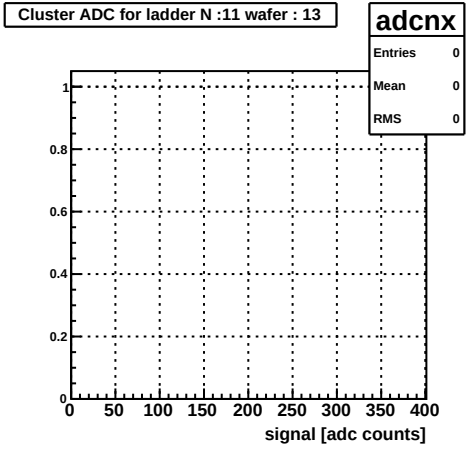

Cluster ADC for ladder N :11 wafer : 12
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

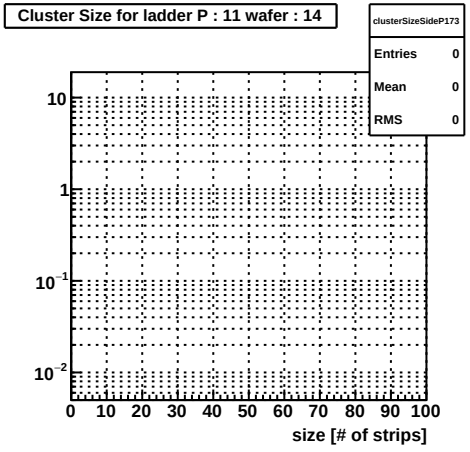

Cluster Size for ladder P : 11 wafer : 13
clusterSizeSideP172
Entries 0
Mean 0
RMS 0
size [# of strips]

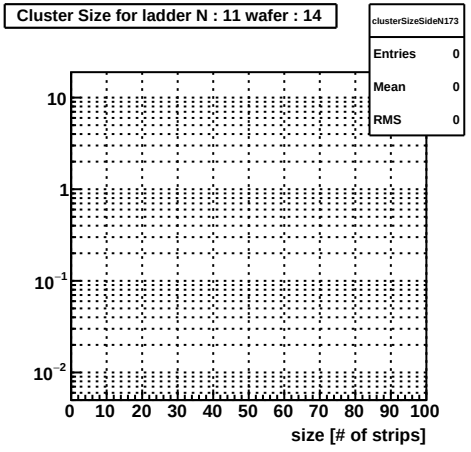

Cluster Size for ladder N : 11 wafer : 13
clusterSizeSideN172
Entries 0
Mean 0
RMS 0
size [# of strips]

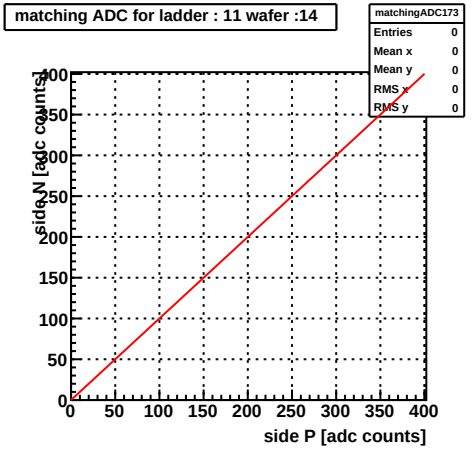

matching ADC for ladder : 11 wafer :13
matchingADC172
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

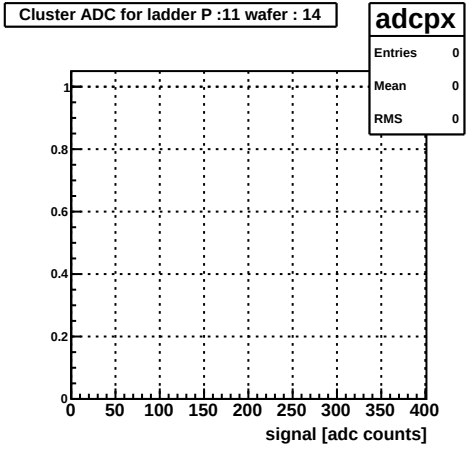

Cluster ADC for ladder P :11 wafer : 13
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

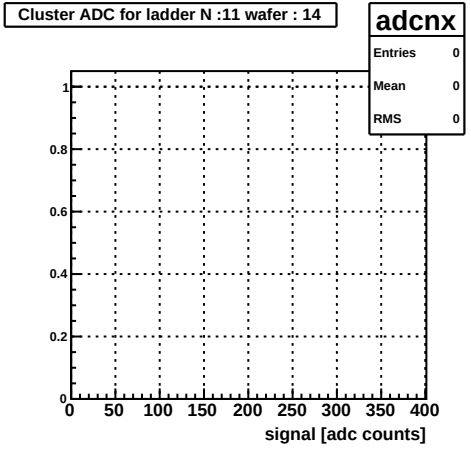

Cluster ADC for ladder N :11 wafer : 13
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

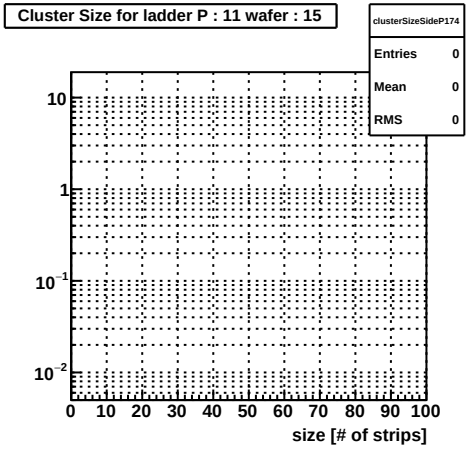

Cluster Size for ladder P : 11 wafer : 14
clusterSizeSideP173
Entries 0
Mean 0
RMS 0
size [# of strips]

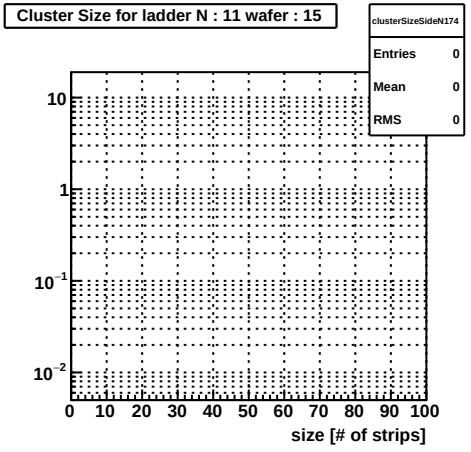

Cluster Size for ladder N : 11 wafer : 14
clusterSizeSideN173
Entries 0
Mean 0
RMS 0
size [# of strips]

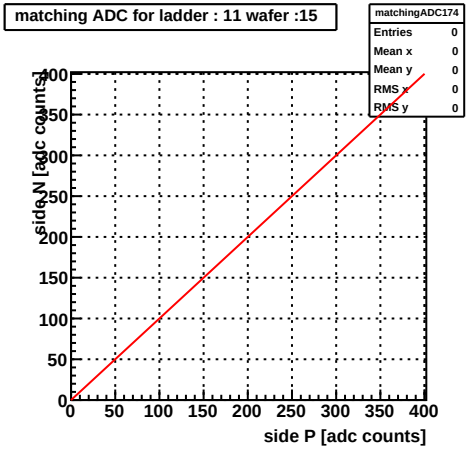

matching ADC for ladder : 11 wafer :14
matchingADC173
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

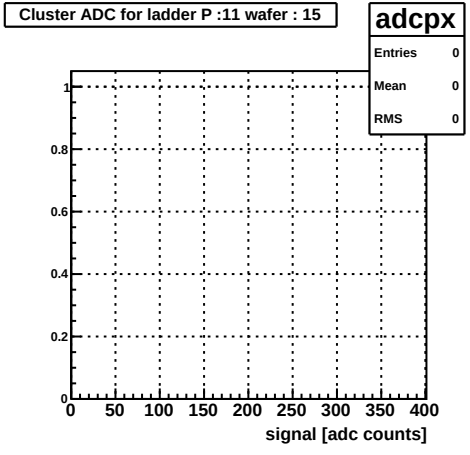

Cluster ADC for ladder P :11 wafer : 14
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

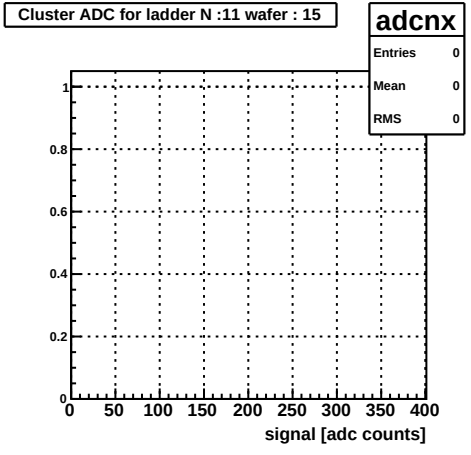

Cluster ADC for ladder N :11 wafer : 14
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

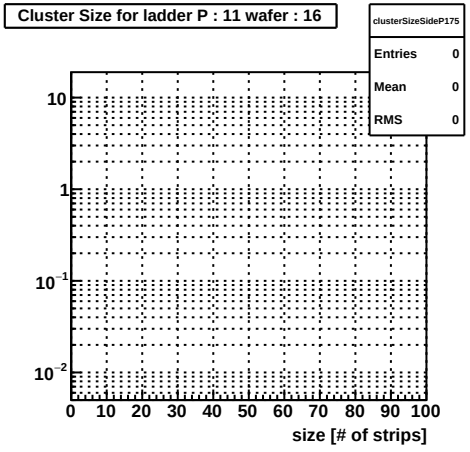

Cluster Size for ladder P : 11 wafer : 15
clusterSizeSideP174
Entries 0
Mean 0
RMS 0
size [# of strips]

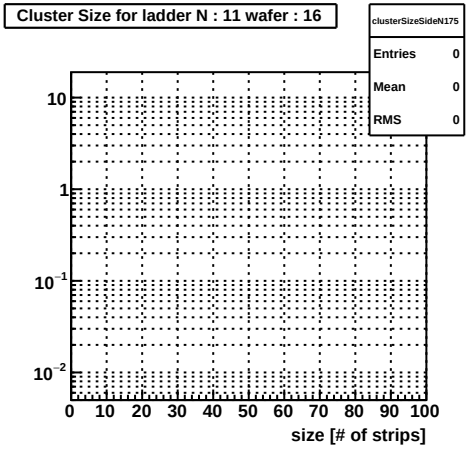

Cluster Size for ladder N : 11 wafer : 15
clusterSizeSideN174
Entries 0
Mean 0
RMS 0
size [# of strips]

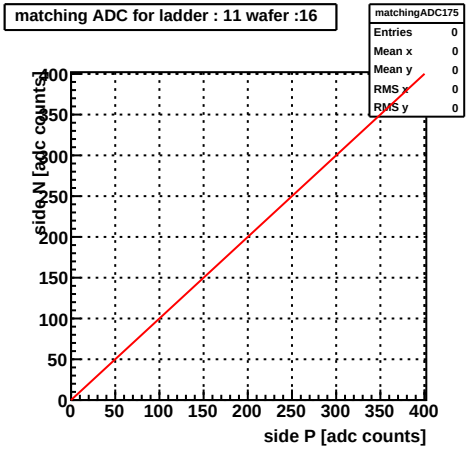

matching ADC for ladder : 11 wafer :15
matchingADC174
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

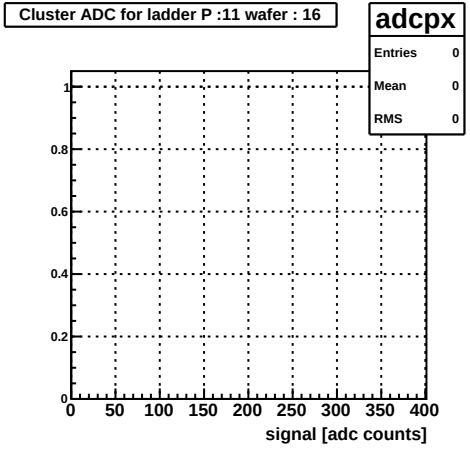

Cluster ADC for ladder P :11 wafer : 15
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

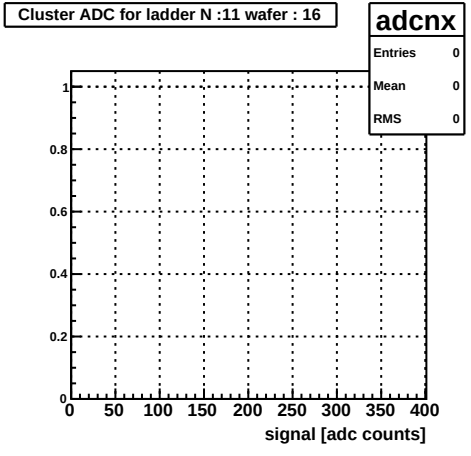

Cluster ADC for ladder N :11 wafer : 15
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

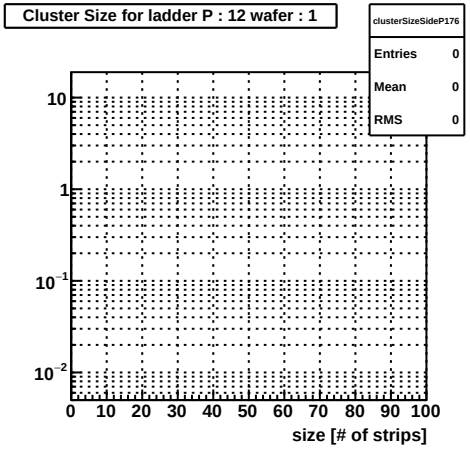

Cluster Size for ladder P : 11 wafer : 16
clusterSizeSideP175
Entries 0
Mean 0
RMS 0
size [# of strips]

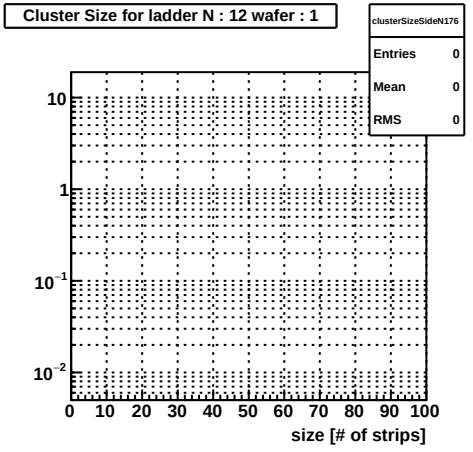

Cluster Size for ladder N : 11 wafer : 16
clusterSizeSideN175
Entries 0
Mean 0
RMS 0
size [# of strips]

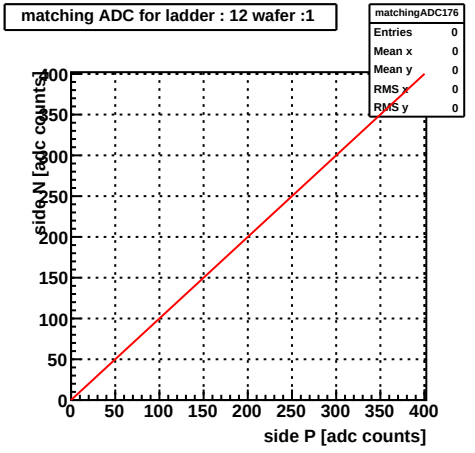

matching ADC for ladder : 11 wafer :16
matchingADC175
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

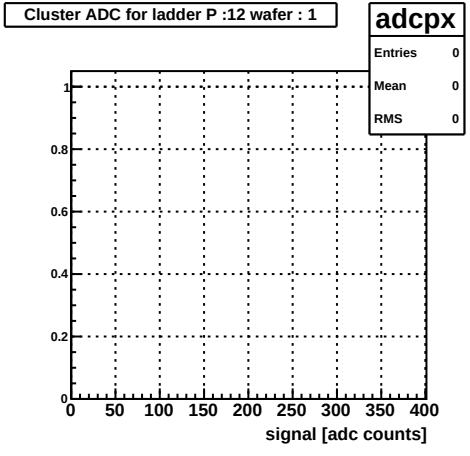

Cluster ADC for ladder P :11 wafer : 16
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

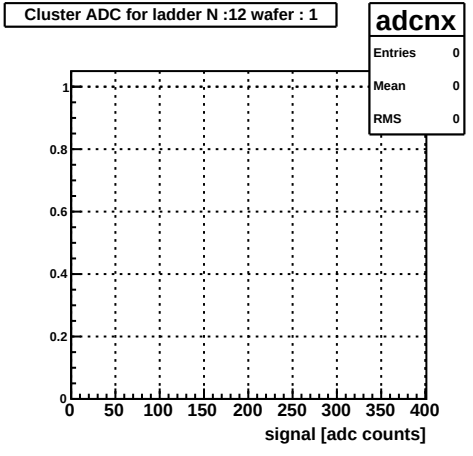

Cluster ADC for ladder N :11 wafer : 16
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

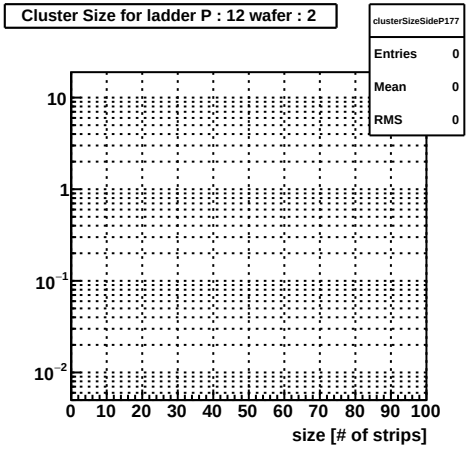

Cluster Size for ladder P : 12 wafer : 1
clusterSizeSideP176
Entries 0
Mean 0
RMS 0
size [# of strips]

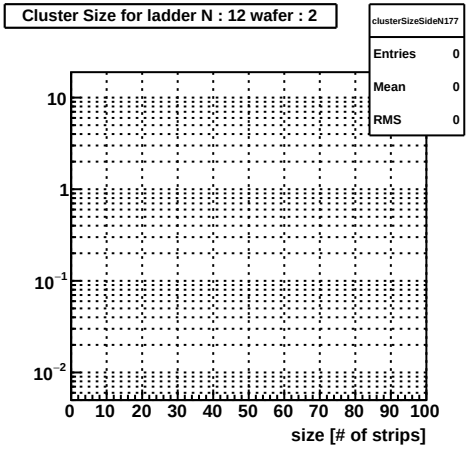

Cluster Size for ladder N : 12 wafer : 1
clusterSizeSideN176
Entries 0
Mean 0
RMS 0
size [# of strips]

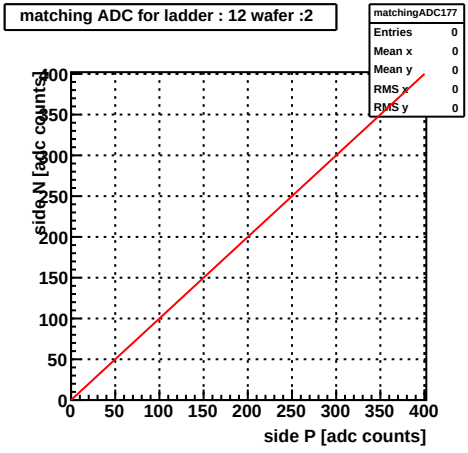

matching ADC for ladder : 12 wafer :1
matchingADC176
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

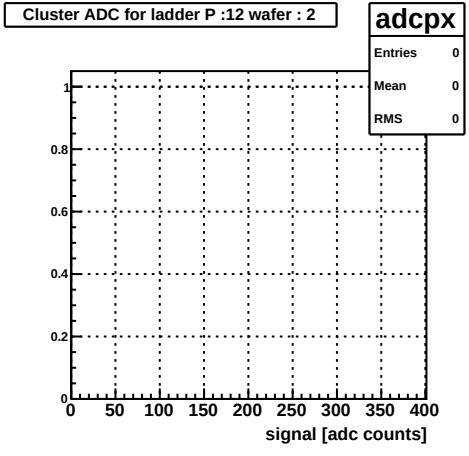

Cluster ADC for ladder P :12 wafer : 1
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

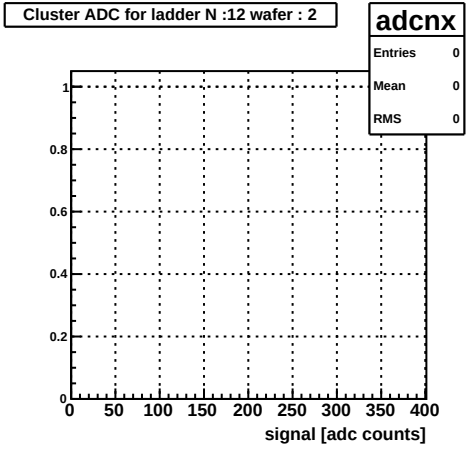

Cluster ADC for ladder N :12 wafer : 1
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

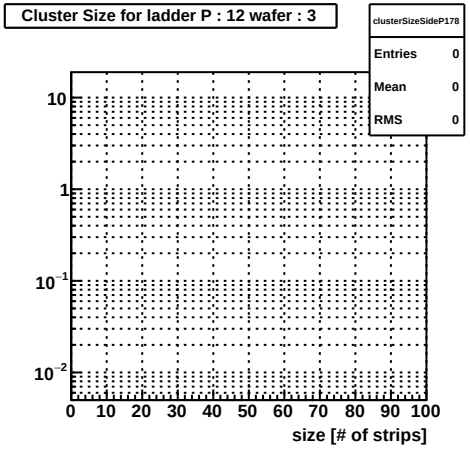

Cluster Size for ladder P : 12 wafer : 2
clusterSizeSideP177
Entries 0
Mean 0
RMS 0
size [# of strips]

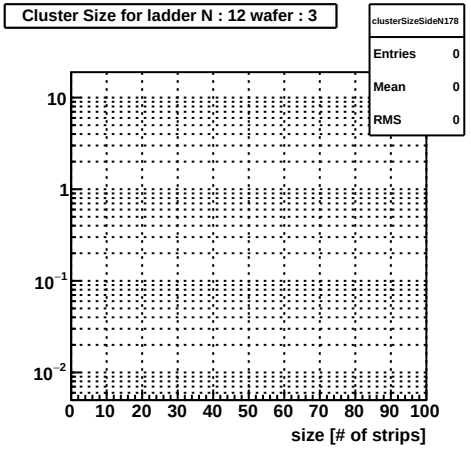

Cluster Size for ladder N : 12 wafer : 2
clusterSizeSideN177
Entries 0
Mean 0
RMS 0
size [# of strips]

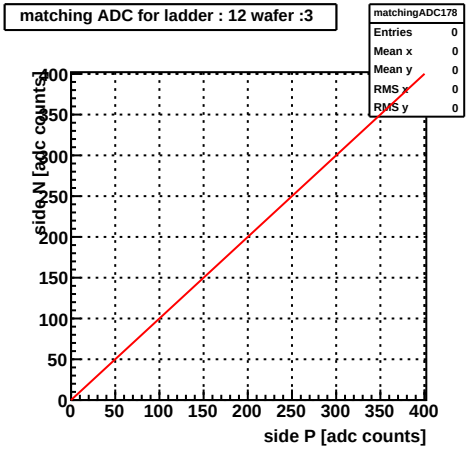

matching ADC for ladder : 12 wafer :2
matchingADC177
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

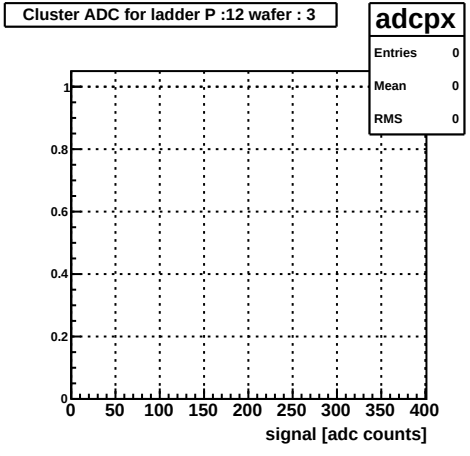

Cluster ADC for ladder P :12 wafer : 2
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

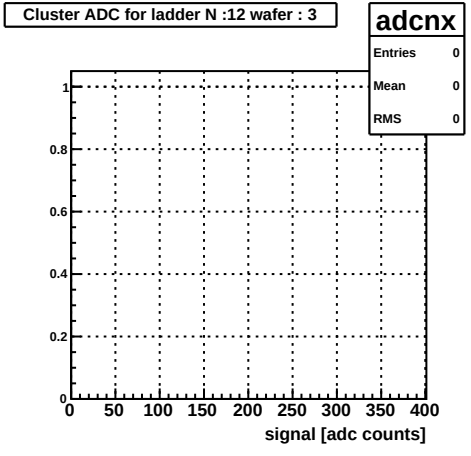

Cluster ADC for ladder N :12 wafer : 2
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

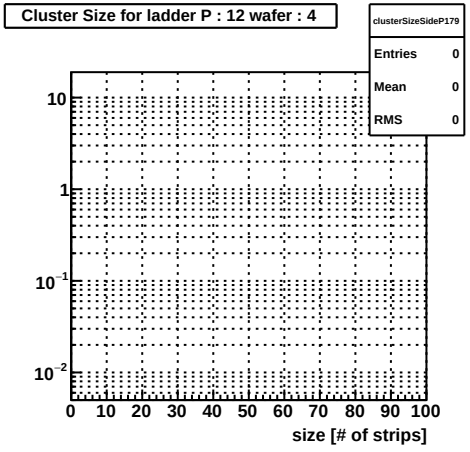

Cluster Size for ladder P : 12 wafer : 3
clusterSizeSideP178
Entries 0
Mean 0
RMS 0
size [# of strips]

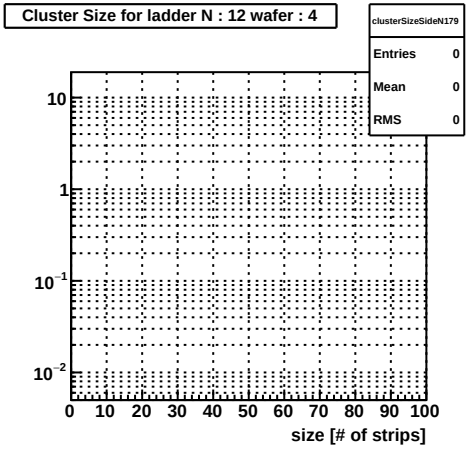

Cluster Size for ladder N : 12 wafer : 3
clusterSizeSideN178
Entries 0
Mean 0
RMS 0
size [# of strips]

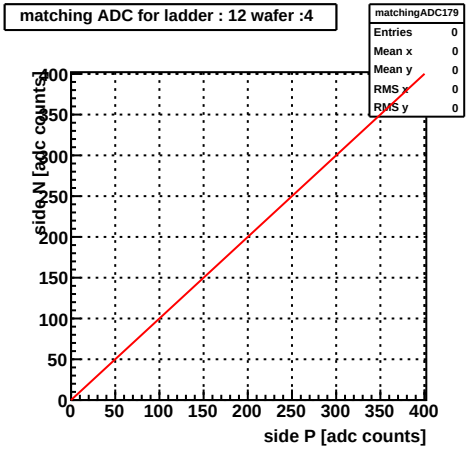

matching ADC for ladder : 12 wafer :3
matchingADC178
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

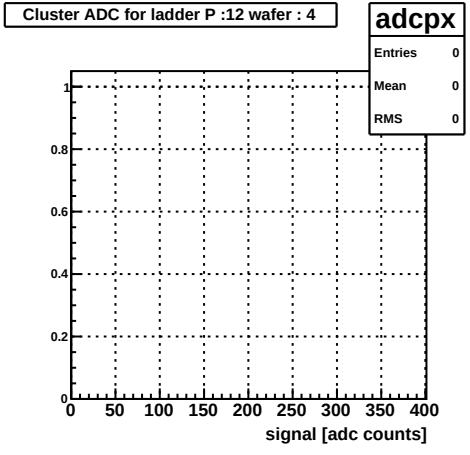

Cluster ADC for ladder P :12 wafer : 3
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

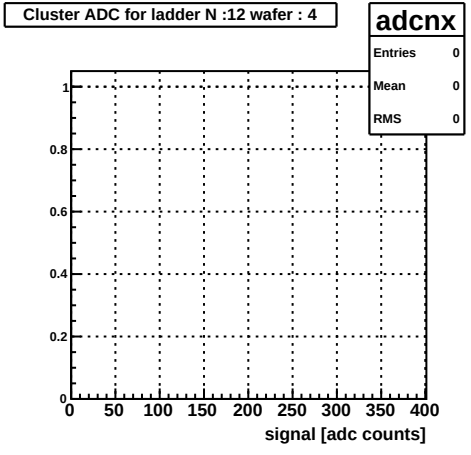

Cluster ADC for ladder N :12 wafer : 3
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

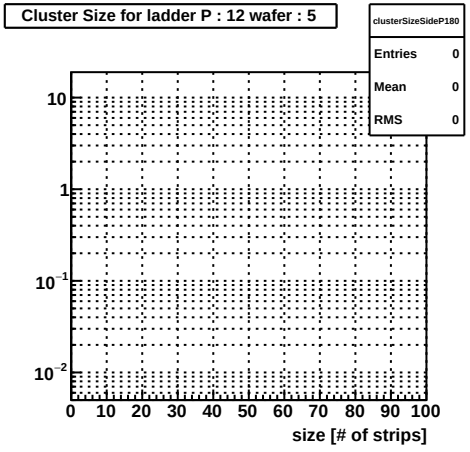

Cluster Size for ladder P : 12 wafer : 4
clusterSizeSideP179
Entries 0
Mean 0
RMS 0
size [# of strips]

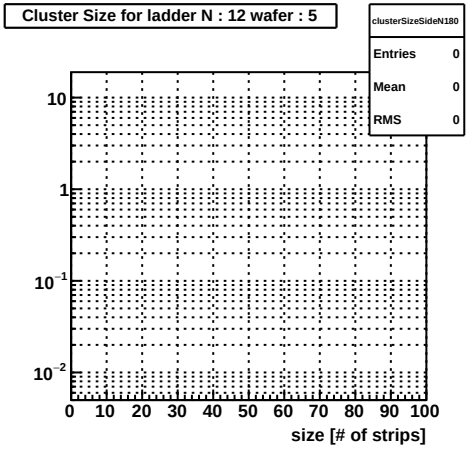

Cluster Size for ladder N : 12 wafer : 4
clusterSizeSideN179
Entries 0
Mean 0
RMS 0
size [# of strips]

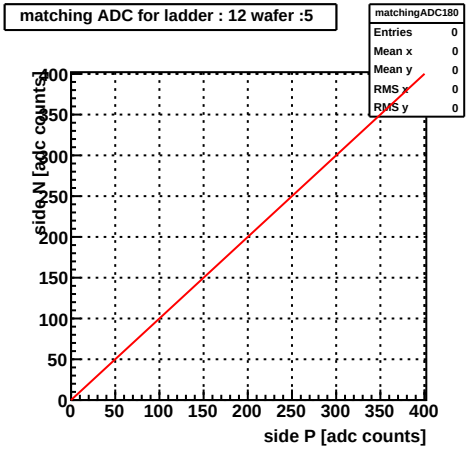

matching ADC for ladder : 12 wafer :4
matchingADC179
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

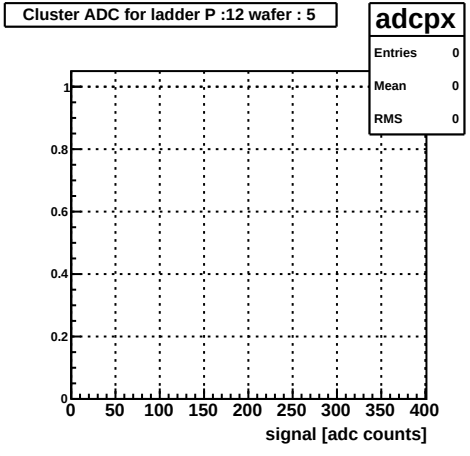

Cluster ADC for ladder P :12 wafer : 4
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

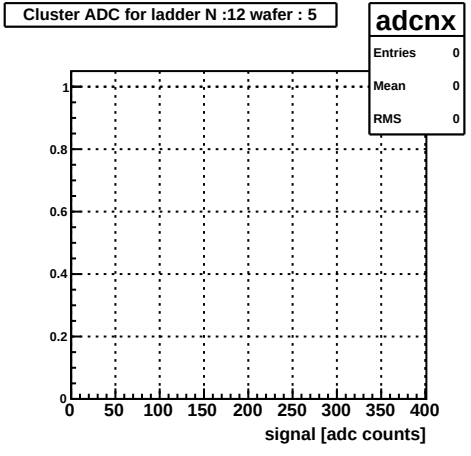

Cluster ADC for ladder N :12 wafer : 4
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

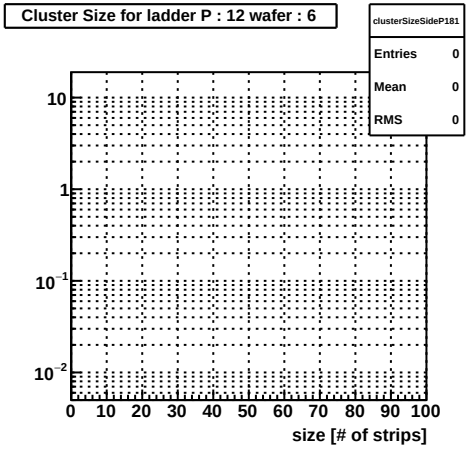

Cluster Size for ladder P : 12 wafer : 5
clusterSizeSideP180
Entries 0
Mean 0
RMS 0
size [# of strips]

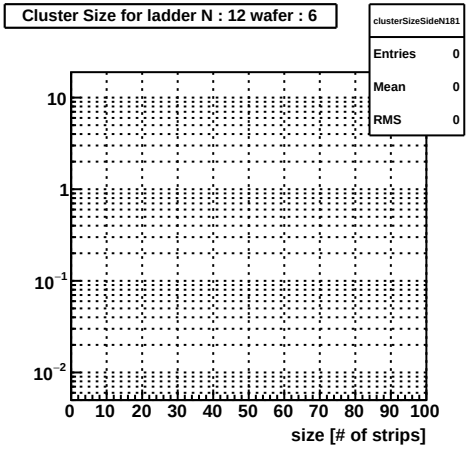

Cluster Size for ladder N : 12 wafer : 5
clusterSizeSideN180
Entries 0
Mean 0
RMS 0
size [# of strips]

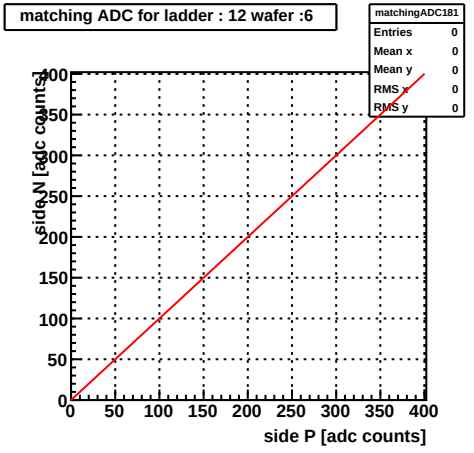

matching ADC for ladder : 12 wafer :5
matchingADC180
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

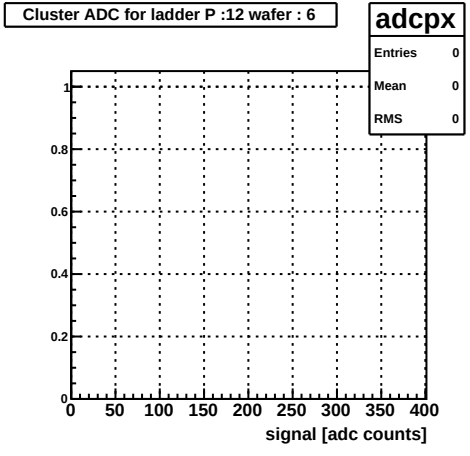

Cluster ADC for ladder P :12 wafer : 5
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

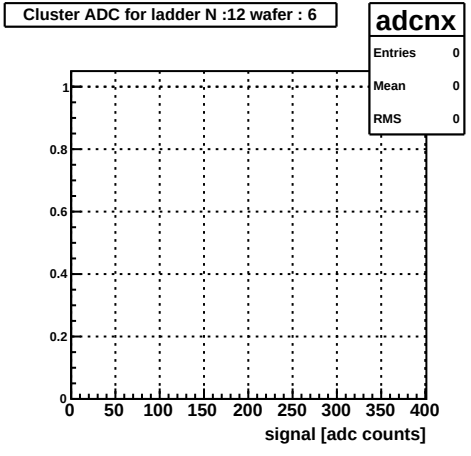

Cluster ADC for ladder N :12 wafer : 5
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

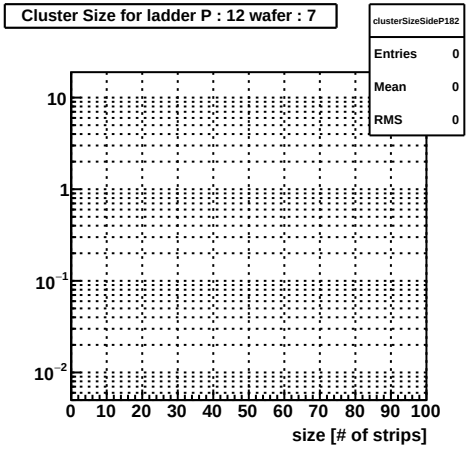

Cluster Size for ladder P : 12 wafer : 6
clusterSizeSideP181
Entries 0
Mean 0
RMS 0
size [# of strips]

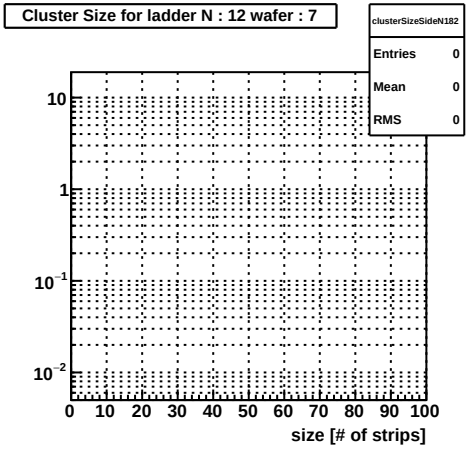

Cluster Size for ladder N : 12 wafer : 6
clusterSizeSideN181
Entries 0
Mean 0
RMS 0
size [# of strips]

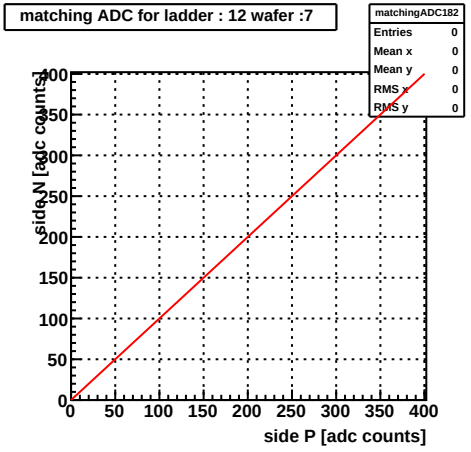

matching ADC for ladder : 12 wafer :6
matchingADC181
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

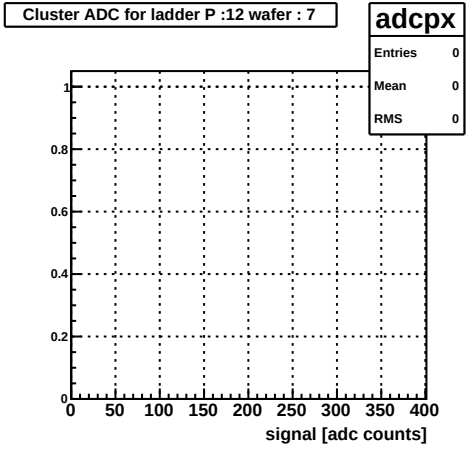

Cluster ADC for ladder P :12 wafer : 6
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

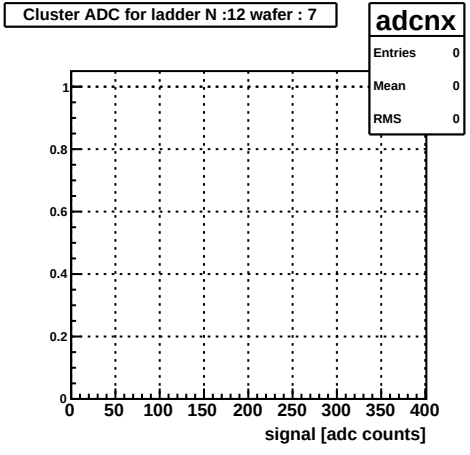

Cluster ADC for ladder N :12 wafer : 6
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

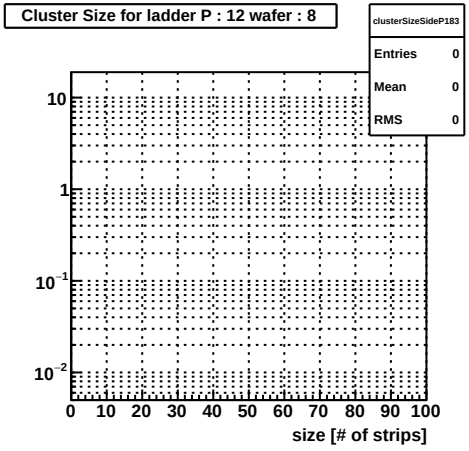

Cluster Size for ladder P : 12 wafer : 7
clusterSizeSideP182
Entries 0
Mean 0
RMS 0
size [# of strips]

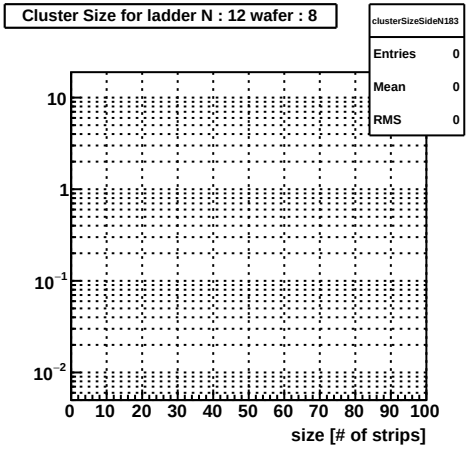

Cluster Size for ladder N : 12 wafer : 7
clusterSizeSideN182
Entries 0
Mean 0
RMS 0
size [# of strips]

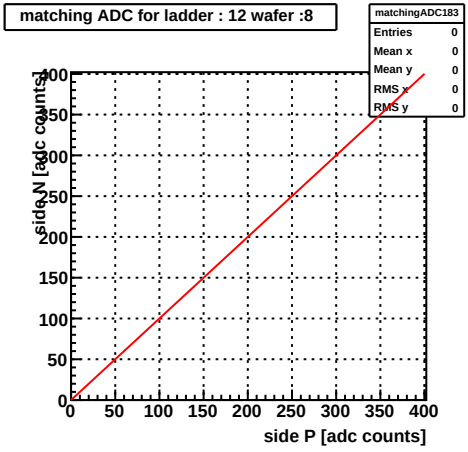

matching ADC for ladder : 12 wafer :7
matchingADC182
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

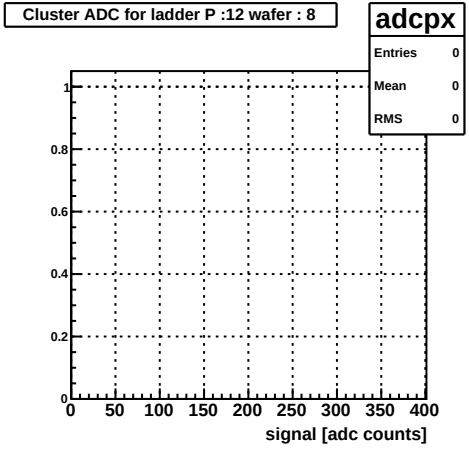

Cluster ADC for ladder P :12 wafer : 7
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

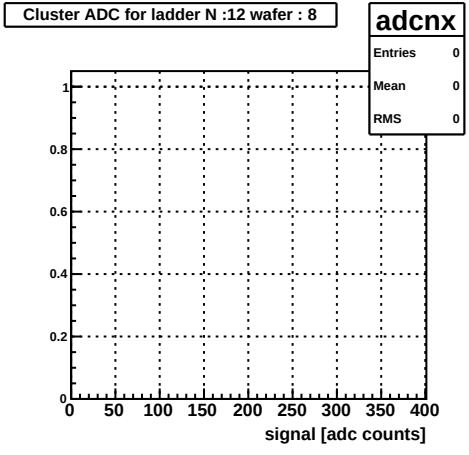

Cluster ADC for ladder N :12 wafer : 7
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

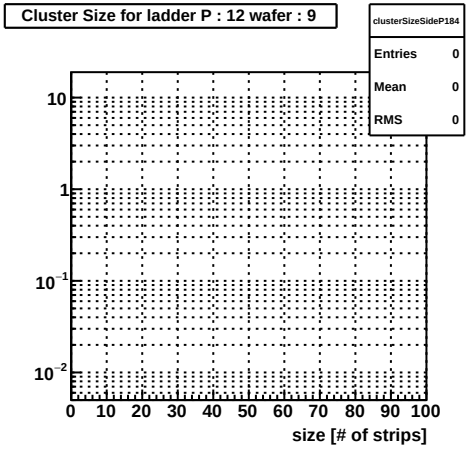

Cluster Size for ladder P : 12 wafer : 8
clusterSizeSideP183
Entries 0
Mean 0
RMS 0
size [# of strips]

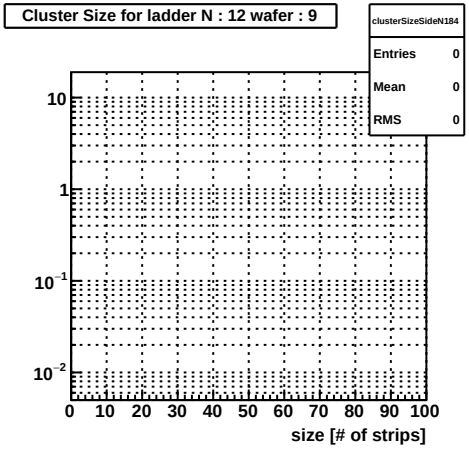

Cluster Size for ladder N : 12 wafer : 8
clusterSizeSideN183
Entries 0
Mean 0
RMS 0
size [# of strips]

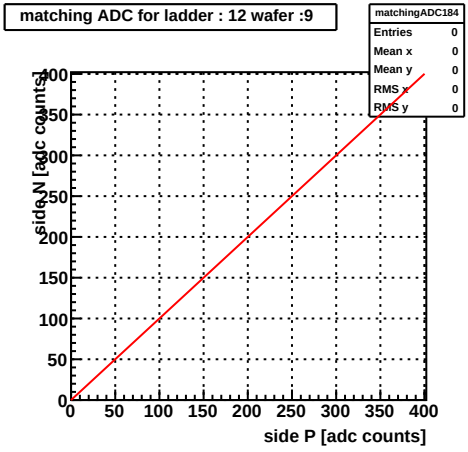

matching ADC for ladder : 12 wafer :8
matchingADC183
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

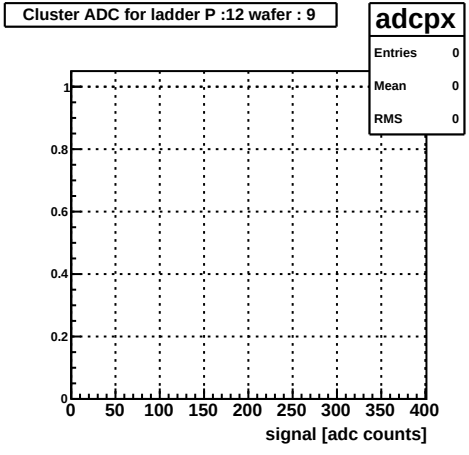

Cluster ADC for ladder P :12 wafer : 8
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

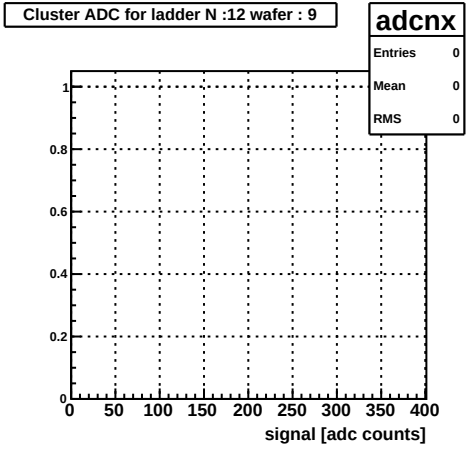

Cluster ADC for ladder N :12 wafer : 8
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

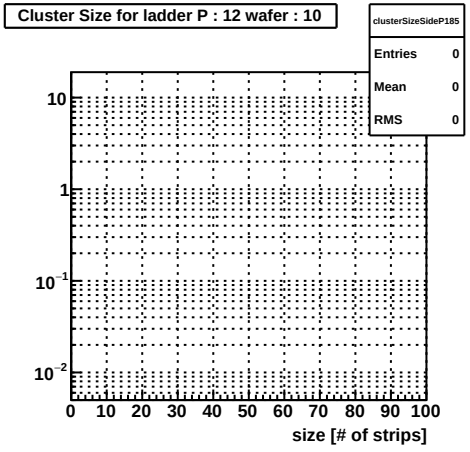

Cluster Size for ladder P : 12 wafer : 9
clusterSizeSideP184
Entries 0
Mean 0
RMS 0
size [# of strips]

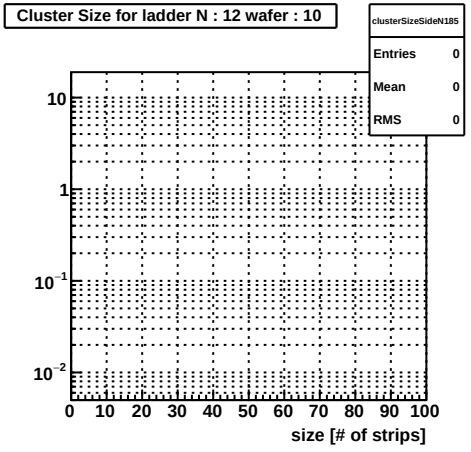

Cluster Size for ladder N : 12 wafer : 9
clusterSizeSideN184
Entries 0
Mean 0
RMS 0
size [# of strips]

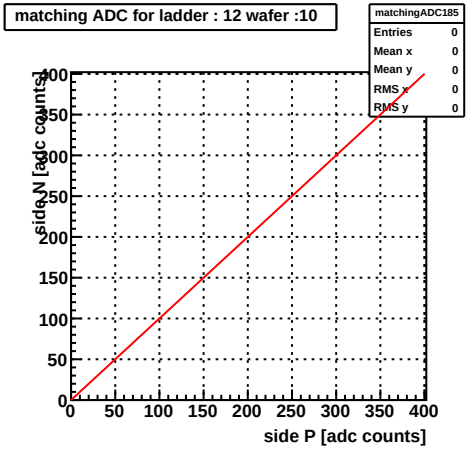

matching ADC for ladder : 12 wafer :9
matchingADC184
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

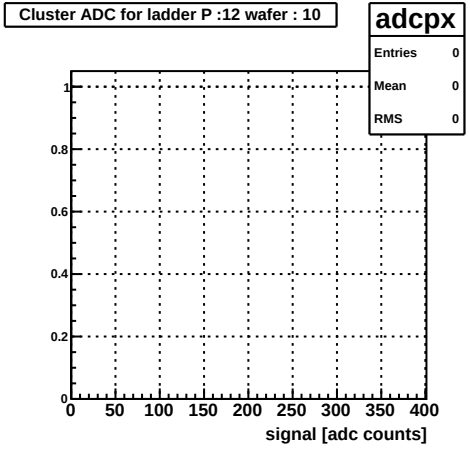

Cluster ADC for ladder P :12 wafer : 9
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

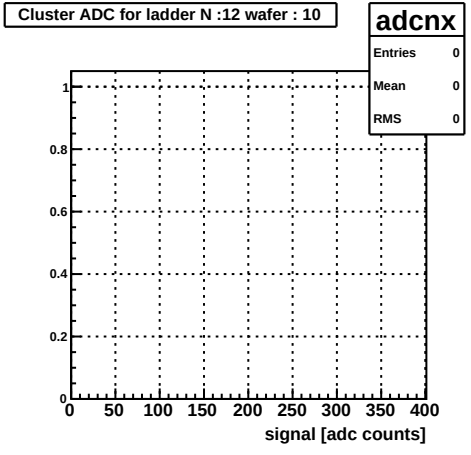

Cluster ADC for ladder N :12 wafer : 9
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

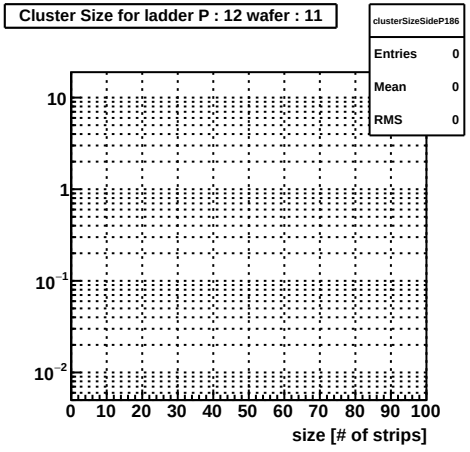

Cluster Size for ladder P : 12 wafer : 10
clusterSizeSideP185
Entries 0
Mean 0
RMS 0
size [# of strips]

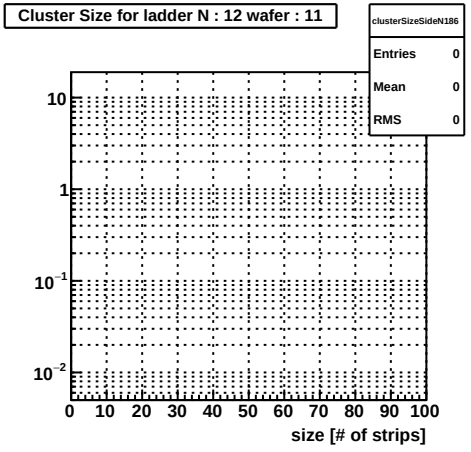

Cluster Size for ladder N : 12 wafer : 10
clusterSizeSideN185
Entries 0
Mean 0
RMS 0
size [# of strips]

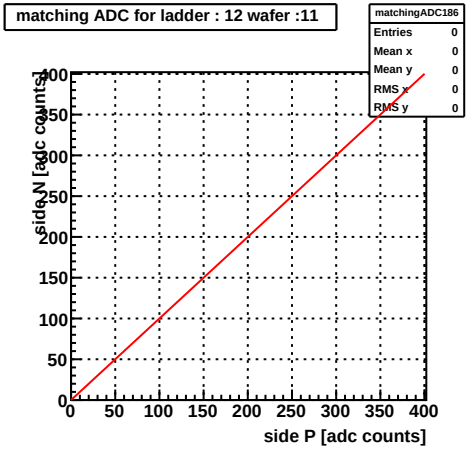

matching ADC for ladder : 12 wafer :10
matchingADC185
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

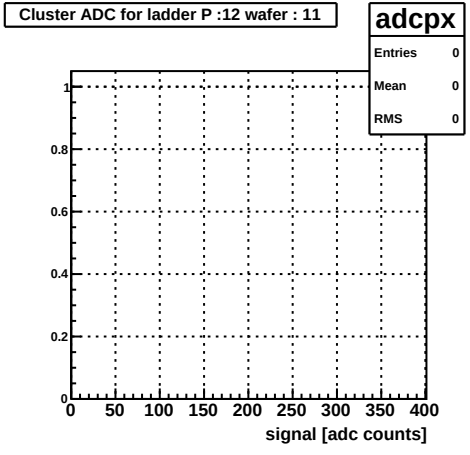

Cluster ADC for ladder P :12 wafer : 10
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

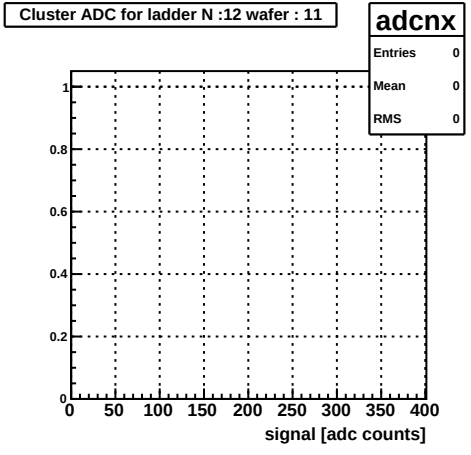

Cluster ADC for ladder N :12 wafer : 10
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

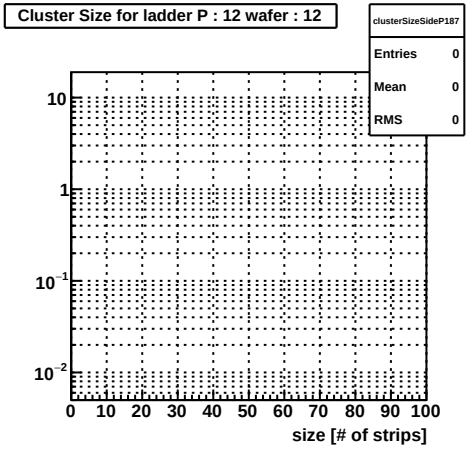

Cluster Size for ladder P : 12 wafer : 11
clusterSizeSideP186
Entries 0
Mean 0
RMS 0
size [# of strips]

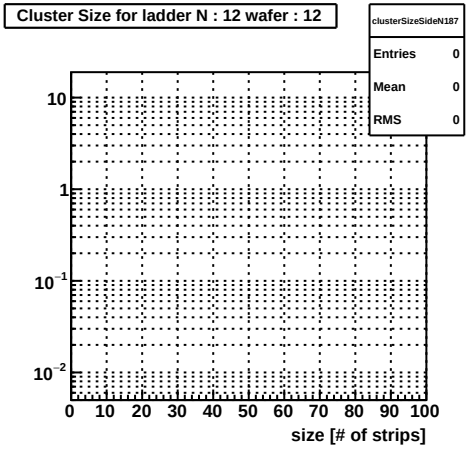

Cluster Size for ladder N : 12 wafer : 11
clusterSizeSideN186
Entries 0
Mean 0
RMS 0
size [# of strips]

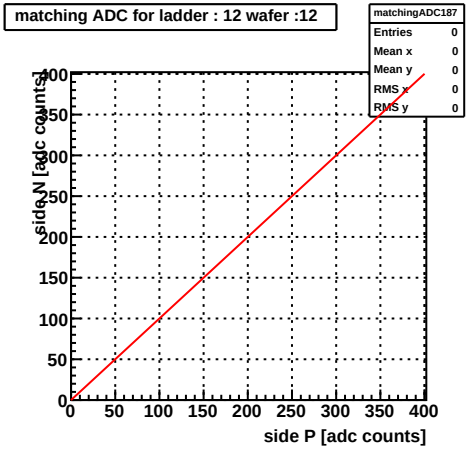

matching ADC for ladder : 12 wafer :11
matchingADC186
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

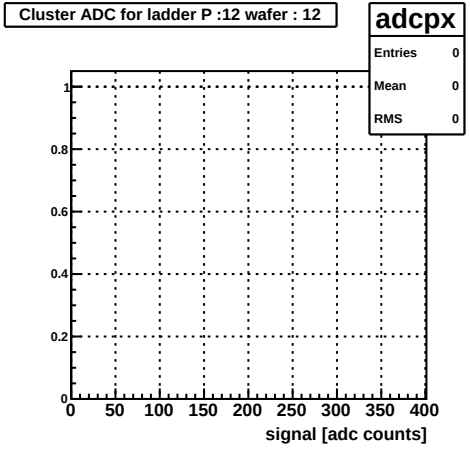

Cluster ADC for ladder P :12 wafer : 11
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

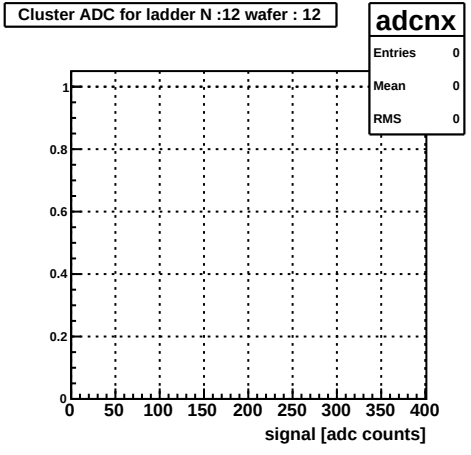

Cluster ADC for ladder N :12 wafer : 11
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

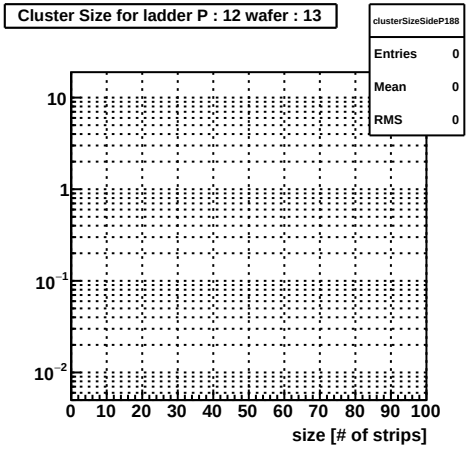

Cluster Size for ladder P : 12 wafer : 12
clusterSizeSideP187
Entries 0
Mean 0
RMS 0
size [# of strips]

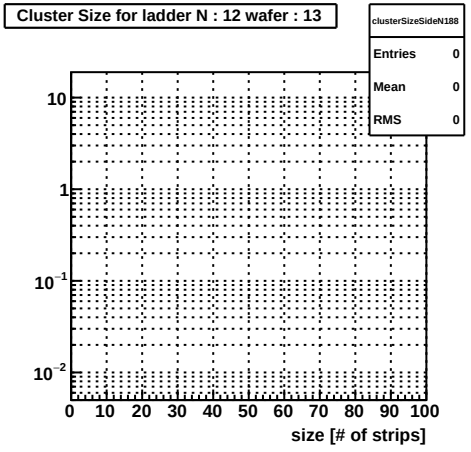

Cluster Size for ladder N : 12 wafer : 12
clusterSizeSideN187
Entries 0
Mean 0
RMS 0
size [# of strips]

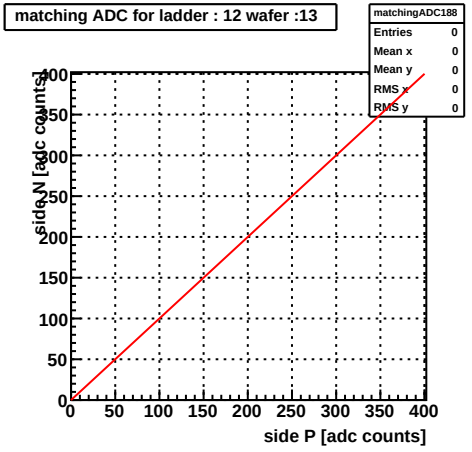

matching ADC for ladder : 12 wafer :12
matchingADC187
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

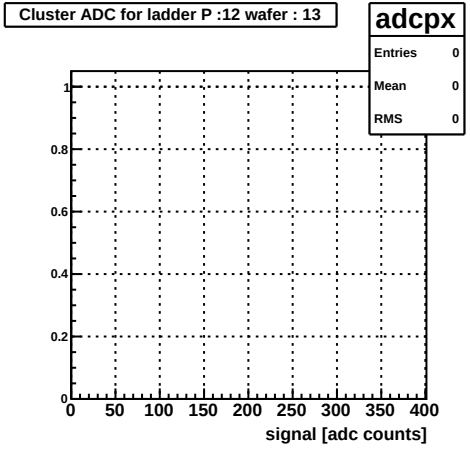

Cluster ADC for ladder P :12 wafer : 12
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

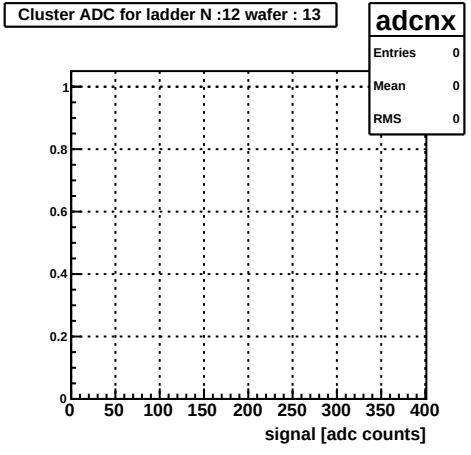

Cluster ADC for ladder N :12 wafer : 12
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

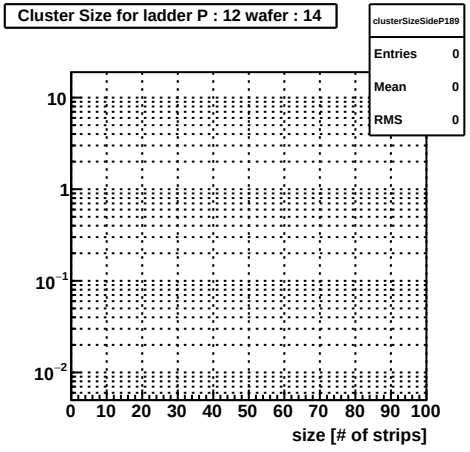

Cluster Size for ladder P : 12 wafer : 13
clusterSizeSideP188
Entries 0
Mean 0
RMS 0
size [# of strips]

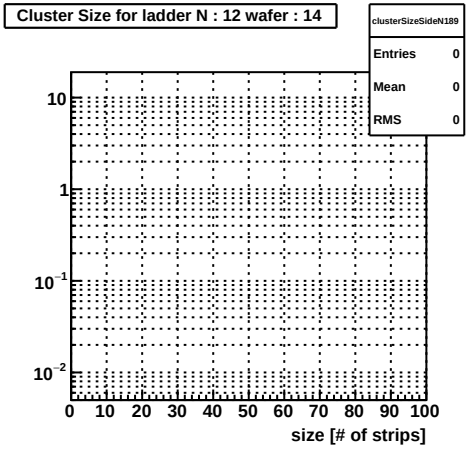

Cluster Size for ladder N : 12 wafer : 13
clusterSizeSideN188
Entries 0
Mean 0
RMS 0
size [# of strips]

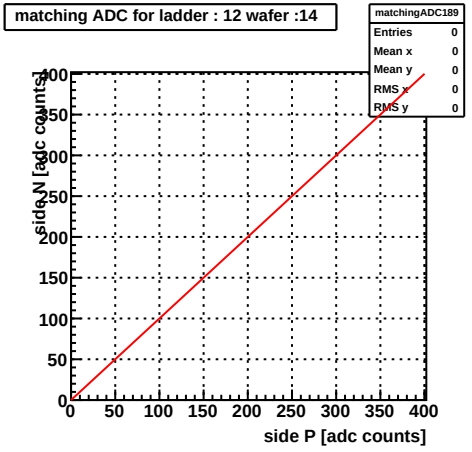

matching ADC for ladder : 12 wafer :13
matchingADC188
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

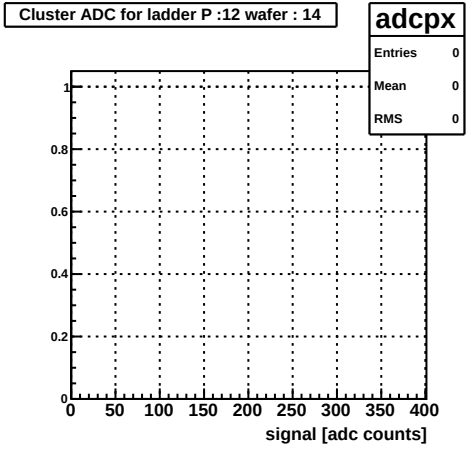

Cluster ADC for ladder P :12 wafer : 13
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

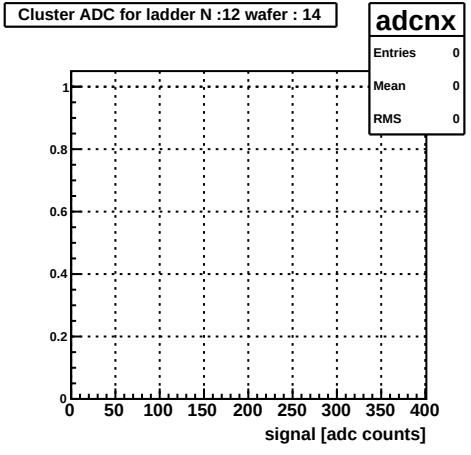

Cluster ADC for ladder N :12 wafer : 13
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

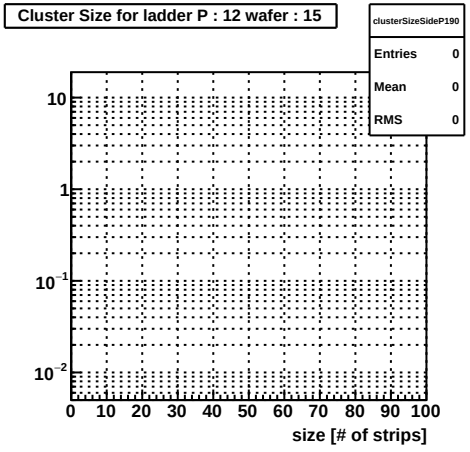

Cluster Size for ladder P : 12 wafer : 14
clusterSizeSideP189
Entries 0
Mean 0
RMS 0
size [# of strips]

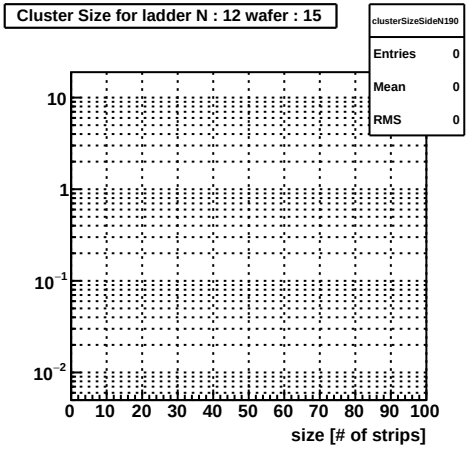

Cluster Size for ladder N : 12 wafer : 14
clusterSizeSideN189
Entries 0
Mean 0
RMS 0
size [# of strips]

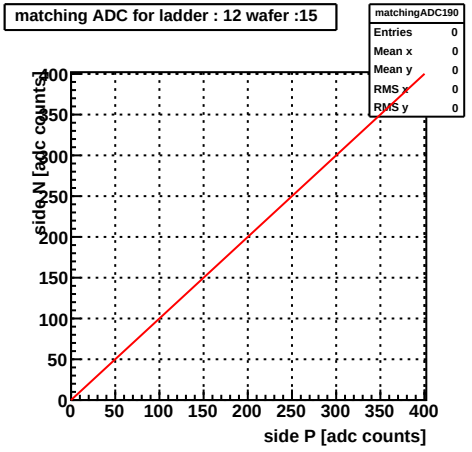

matching ADC for ladder : 12 wafer :14
matchingADC189
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

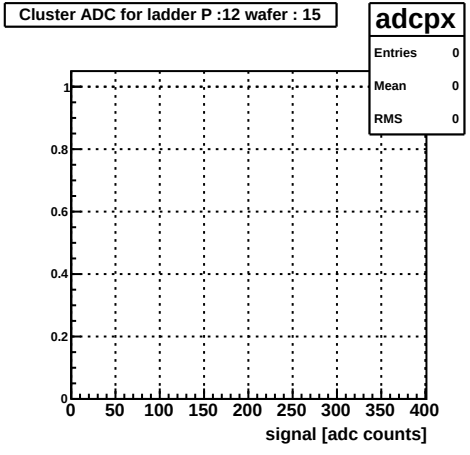

Cluster ADC for ladder P :12 wafer : 14
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

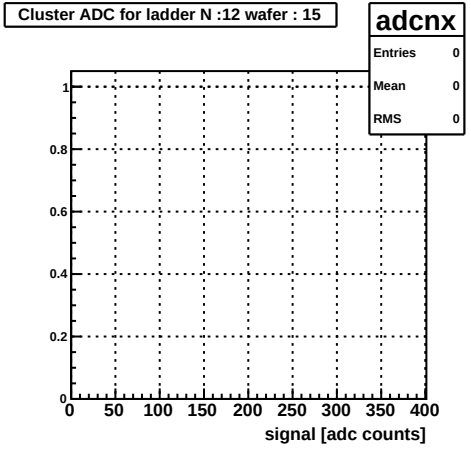

Cluster ADC for ladder N :12 wafer : 14
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

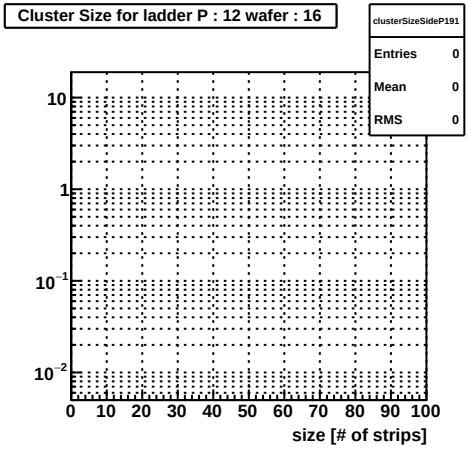

Cluster Size for ladder P : 12 wafer : 15
clusterSizeSideP190
Entries 0
Mean 0
RMS 0
size [# of strips]

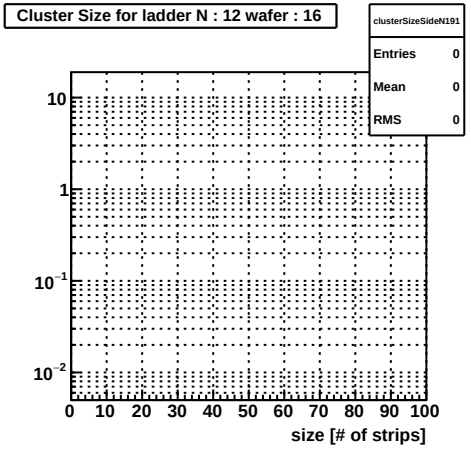

Cluster Size for ladder N : 12 wafer : 15
clusterSizeSideN190
Entries 0
Mean 0
RMS 0
size [# of strips]

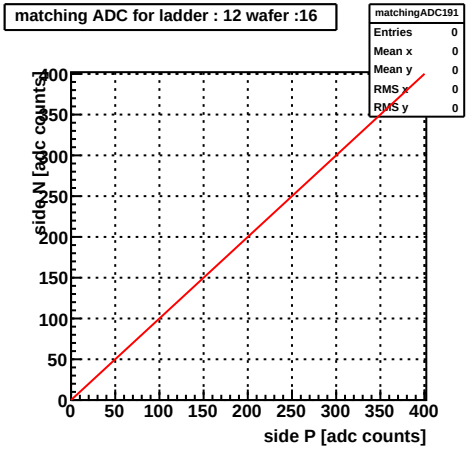

matching ADC for ladder : 12 wafer :15
matchingADC190
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

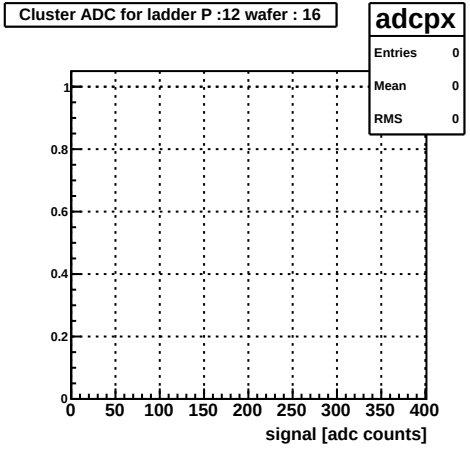

Cluster ADC for ladder P :12 wafer : 15
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

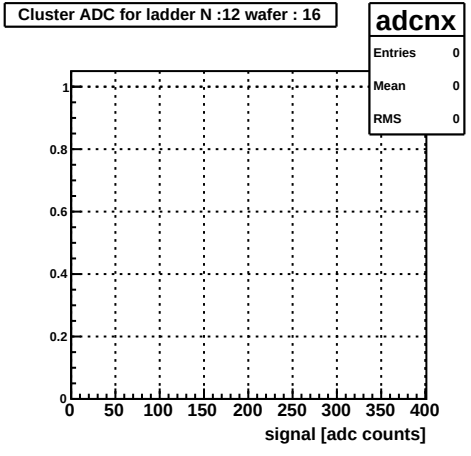

Cluster ADC for ladder N :12 wafer : 15
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

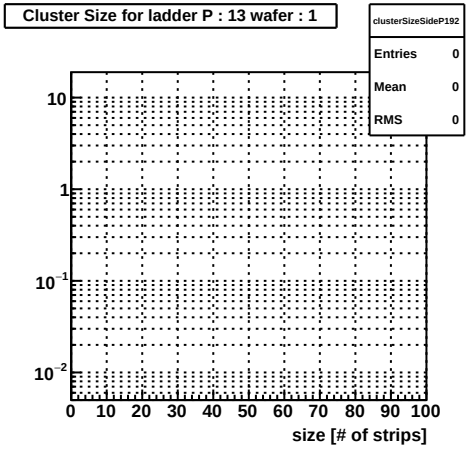

Cluster Size for ladder P : 12 wafer : 16
clusterSizeSideP191
Entries 0
Mean 0
RMS 0
size [# of strips]

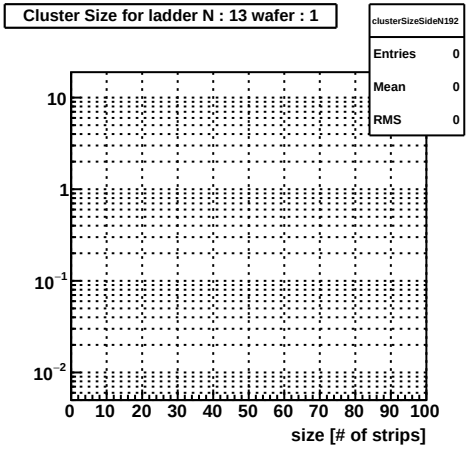

Cluster Size for ladder N : 12 wafer : 16
clusterSizeSideN191
Entries 0
Mean 0
RMS 0
size [# of strips]

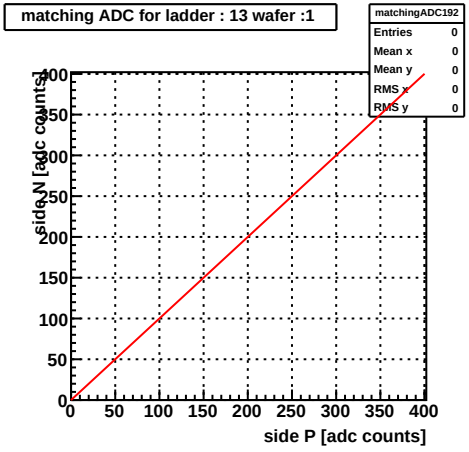

matching ADC for ladder : 12 wafer :16
matchingADC191
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

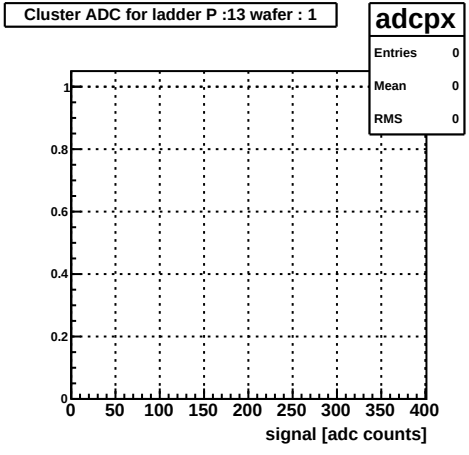

Cluster ADC for ladder P :12 wafer : 16
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

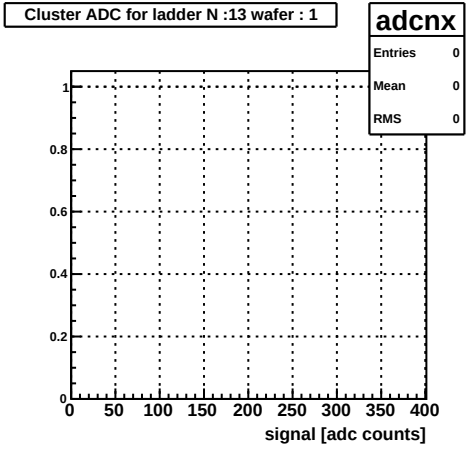

Cluster ADC for ladder N :12 wafer : 16
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

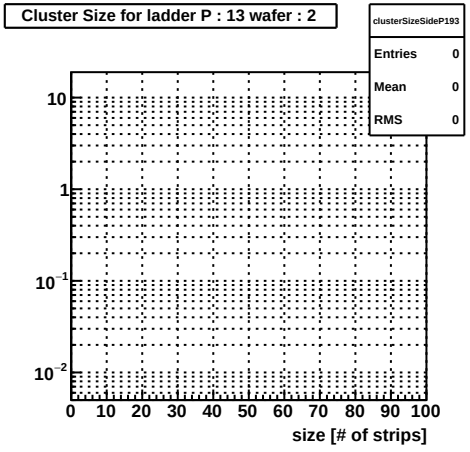

Cluster Size for ladder P : 13 wafer : 1
clusterSizeSideP192
Entries 0
Mean 0
RMS 0
size [# of strips]

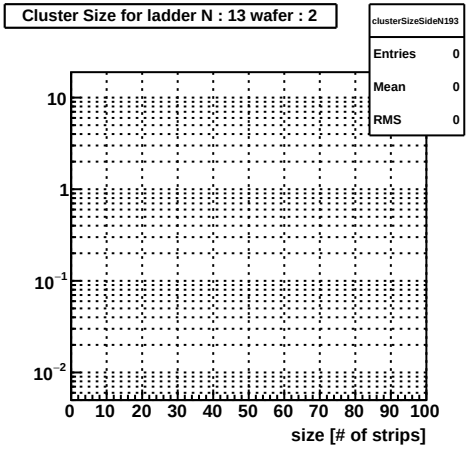

Cluster Size for ladder N : 13 wafer : 1
clusterSizeSideN192
Entries 0
Mean 0
RMS 0
size [# of strips]

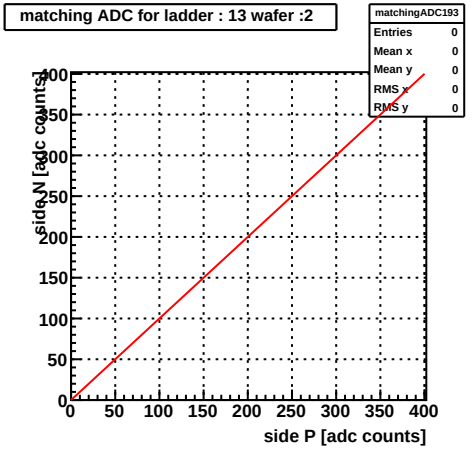

matching ADC for ladder : 13 wafer :1
matchingADC192
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

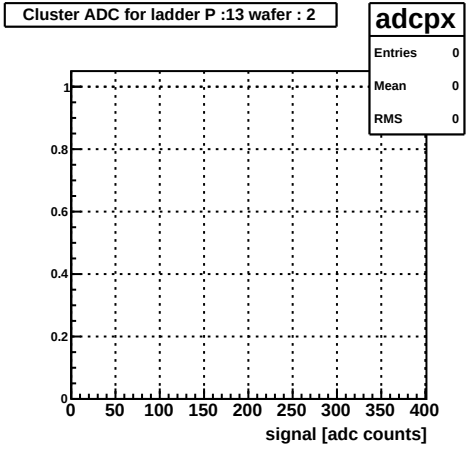

Cluster ADC for ladder P :13 wafer : 1
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

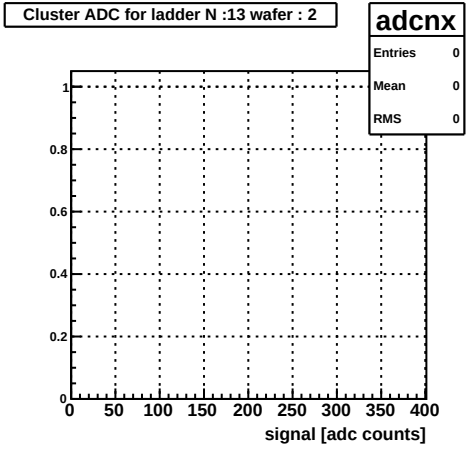

Cluster ADC for ladder N :13 wafer : 1
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

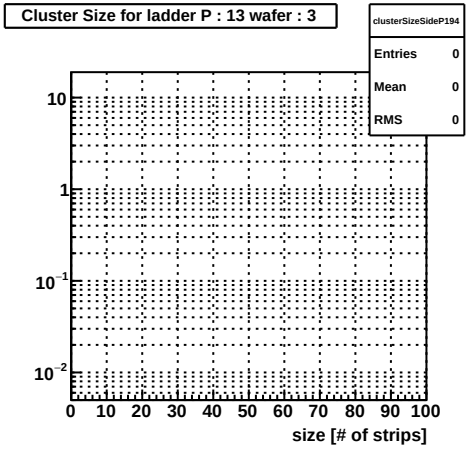

Cluster Size for ladder P : 13 wafer : 2
clusterSizeSideP193
Entries 0
Mean 0
RMS 0
size [# of strips]

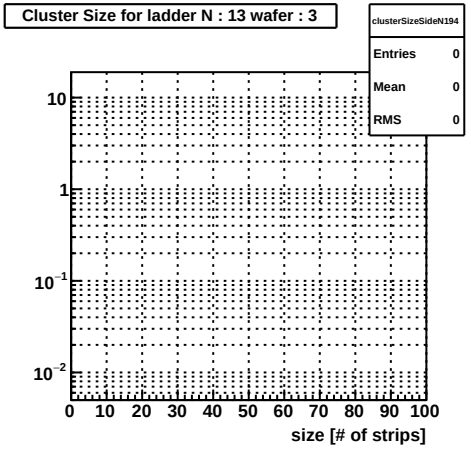

Cluster Size for ladder N : 13 wafer : 2
clusterSizeSideN193
Entries 0
Mean 0
RMS 0
size [# of strips]

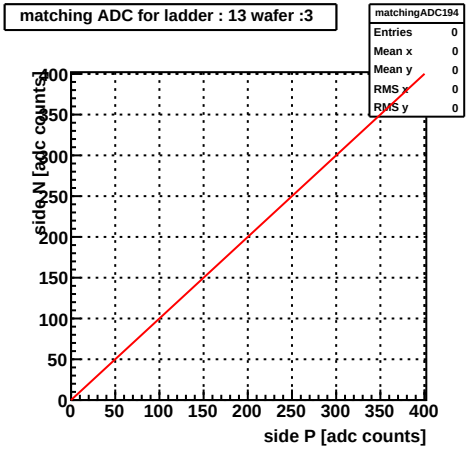

matching ADC for ladder : 13 wafer :2
matchingADC193
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

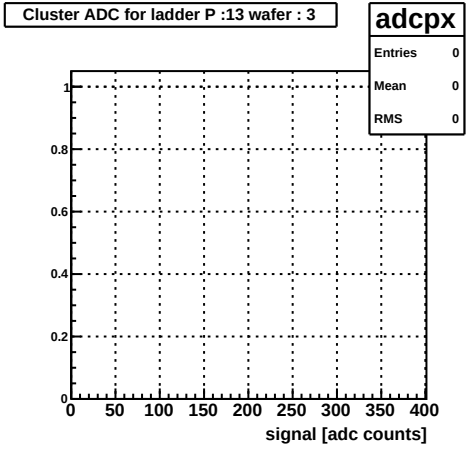

Cluster ADC for ladder P :13 wafer : 2
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

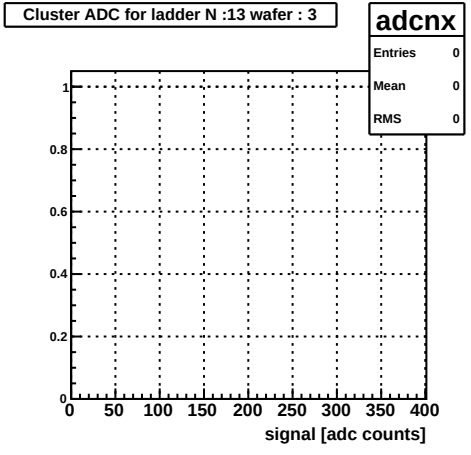

Cluster ADC for ladder N :13 wafer : 2
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

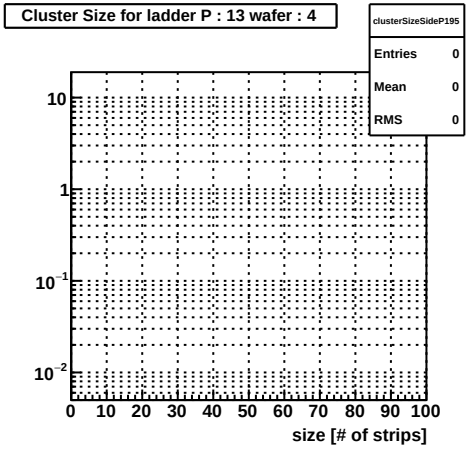

Cluster Size for ladder P : 13 wafer : 3
clusterSizeSideP194
Entries 0
Mean 0
RMS 0
size [# of strips]

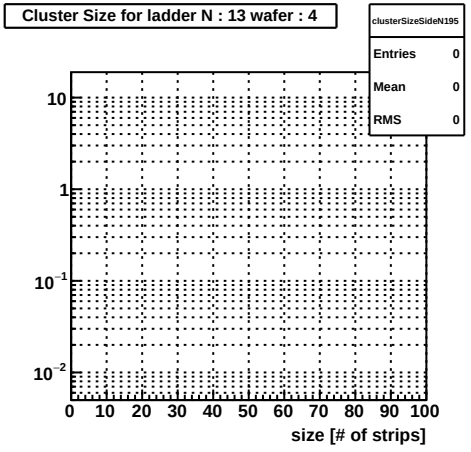

Cluster Size for ladder N : 13 wafer : 3
clusterSizeSideN194
Entries 0
Mean 0
RMS 0
size [# of strips]

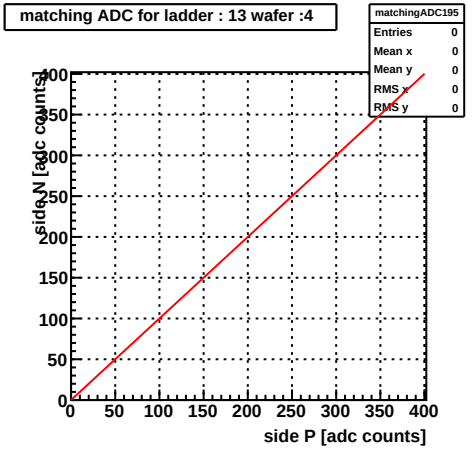

matching ADC for ladder : 13 wafer :3
matchingADC194
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

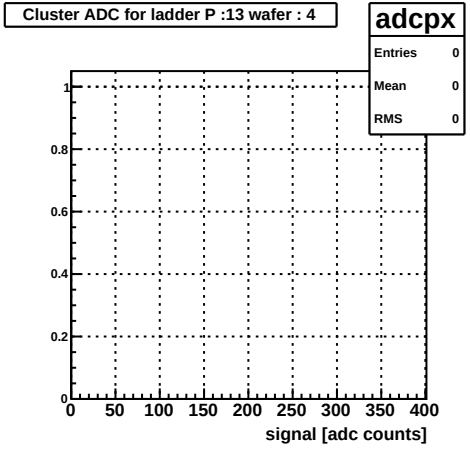

Cluster ADC for ladder P :13 wafer : 3
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

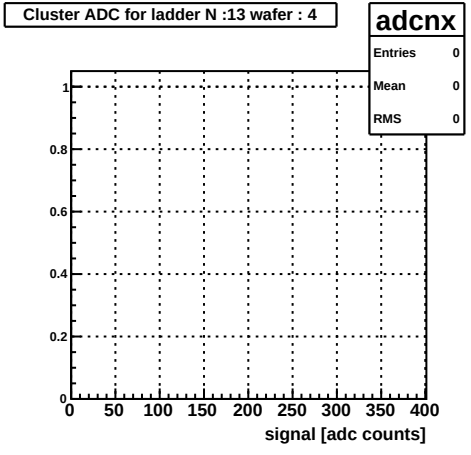

Cluster ADC for ladder N :13 wafer : 3
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

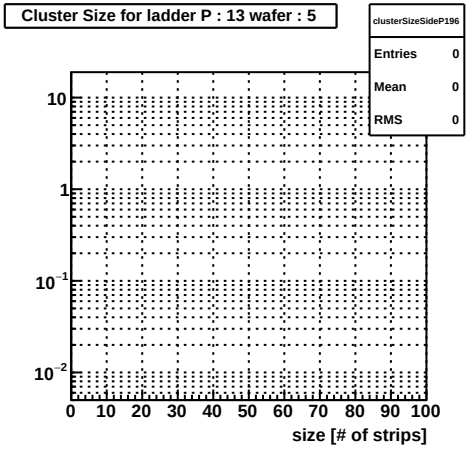

Cluster Size for ladder P : 13 wafer : 4
clusterSizeSideP195
Entries 0
Mean 0
RMS 0
size [# of strips]

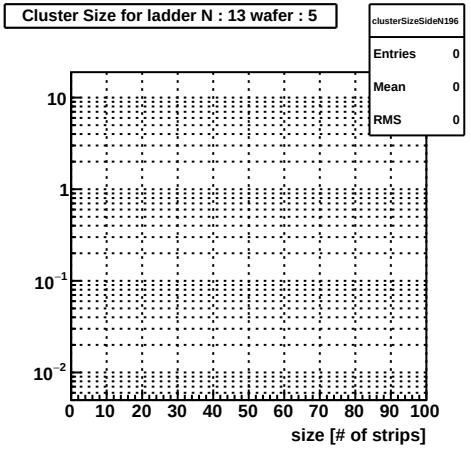

Cluster Size for ladder N : 13 wafer : 4
clusterSizeSideN195
Entries 0
Mean 0
RMS 0
size [# of strips]

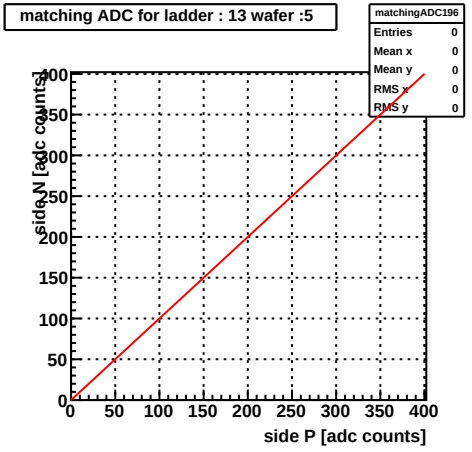

matching ADC for ladder : 13 wafer :4
matchingADC195
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

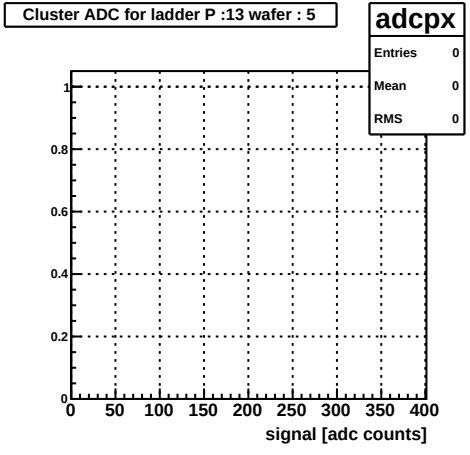

Cluster ADC for ladder P :13 wafer : 4
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

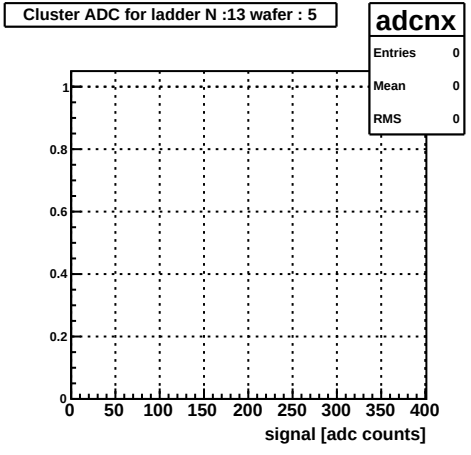

Cluster ADC for ladder N :13 wafer : 4
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

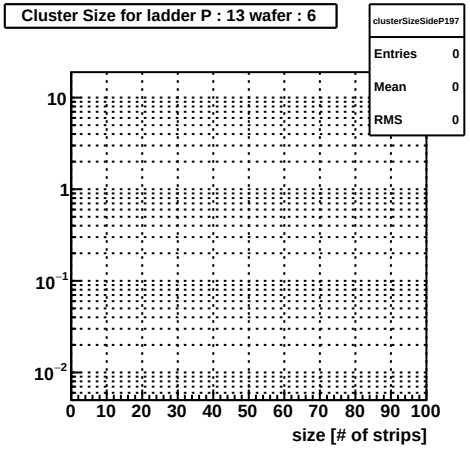

Cluster Size for ladder P : 13 wafer : 5
clusterSizeSideP196
Entries 0
Mean 0
RMS 0
size [# of strips]

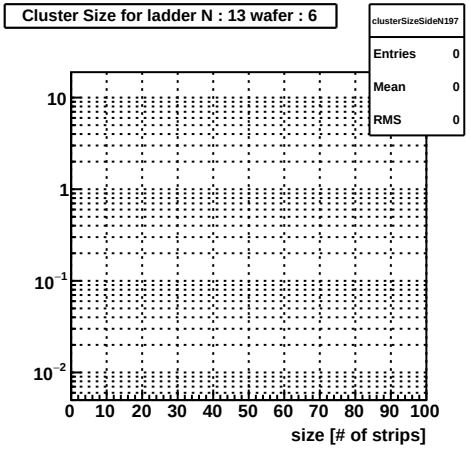

Cluster Size for ladder N : 13 wafer : 5
clusterSizeSideN196
Entries 0
Mean 0
RMS 0
size [# of strips]

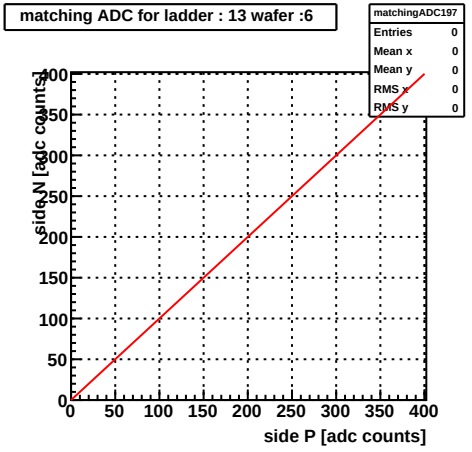

matching ADC for ladder : 13 wafer :5
matchingADC196
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

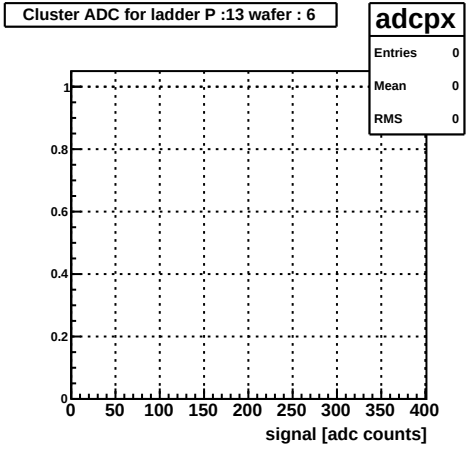

Cluster ADC for ladder P :13 wafer : 5
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

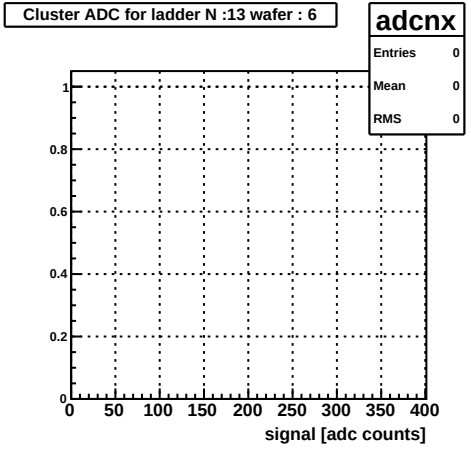

Cluster ADC for ladder N :13 wafer : 5
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

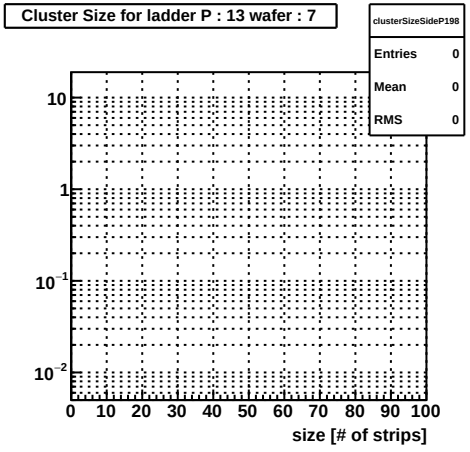

Cluster Size for ladder P : 13 wafer : 6
clusterSizeSideP197
Entries 0
Mean 0
RMS 0
size [# of strips]

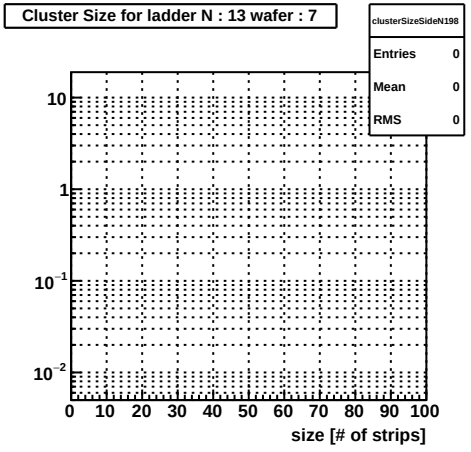

Cluster Size for ladder N : 13 wafer : 6
clusterSizeSideN197
Entries 0
Mean 0
RMS 0
size [# of strips]

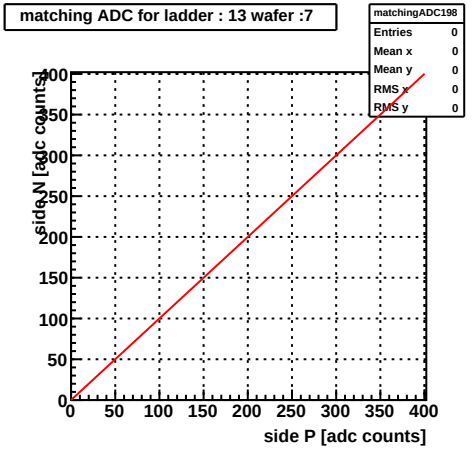

matching ADC for ladder : 13 wafer :6
matchingADC197
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

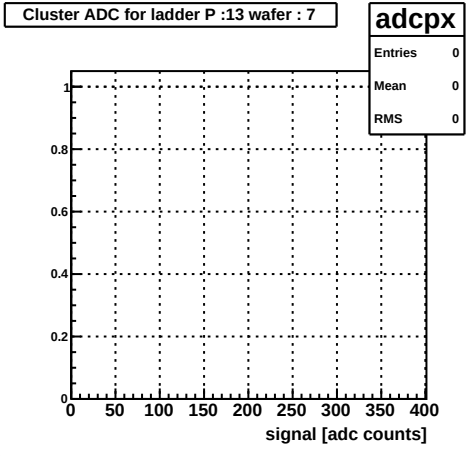

Cluster ADC for ladder P :13 wafer : 6
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

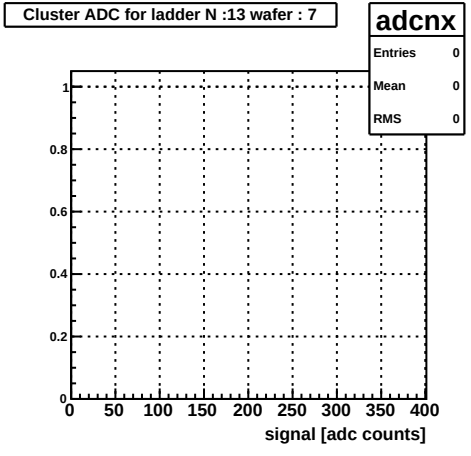

Cluster ADC for ladder N :13 wafer : 6
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

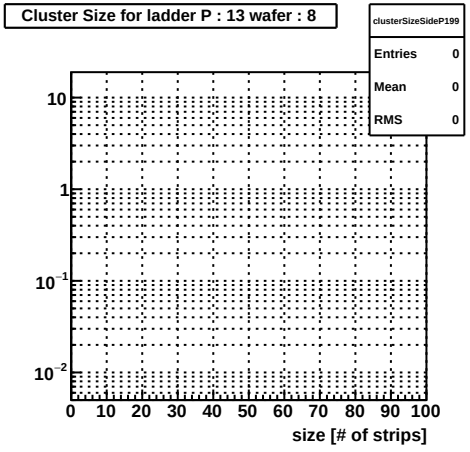

Cluster Size for ladder P : 13 wafer : 7
clusterSizeSideP198
Entries 0
Mean 0
RMS 0
size [# of strips]

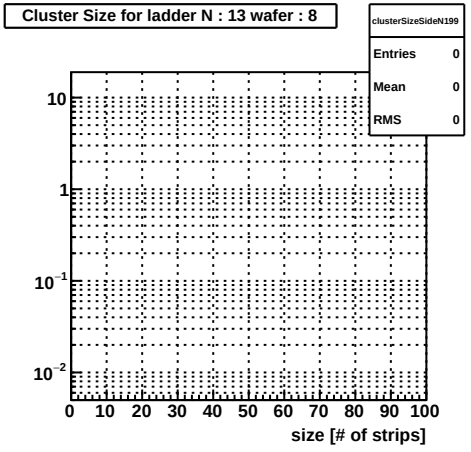

Cluster Size for ladder N : 13 wafer : 7
clusterSizeSideN198
Entries 0
Mean 0
RMS 0
size [# of strips]

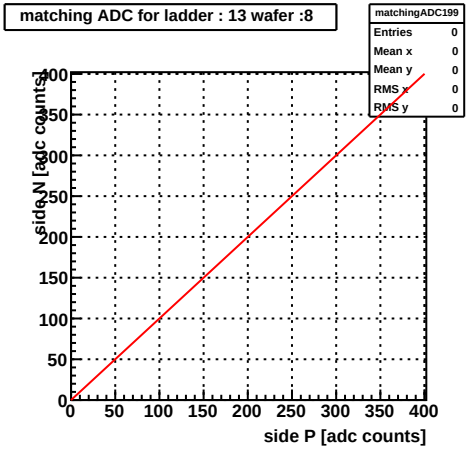

matching ADC for ladder : 13 wafer :7
matchingADC198
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

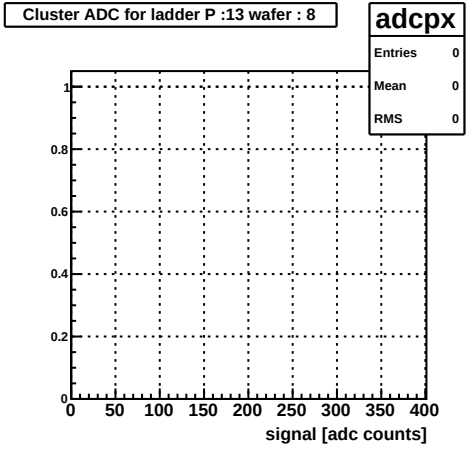

Cluster ADC for ladder P :13 wafer : 7
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

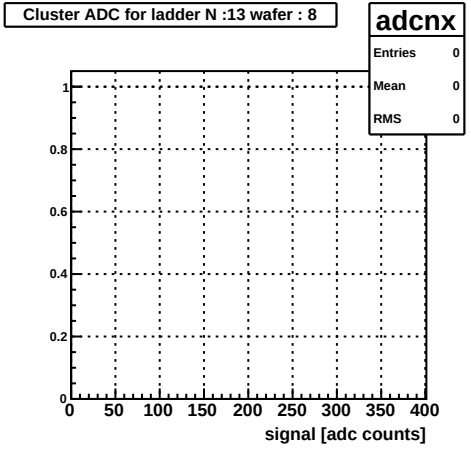

Cluster ADC for ladder N :13 wafer : 7
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

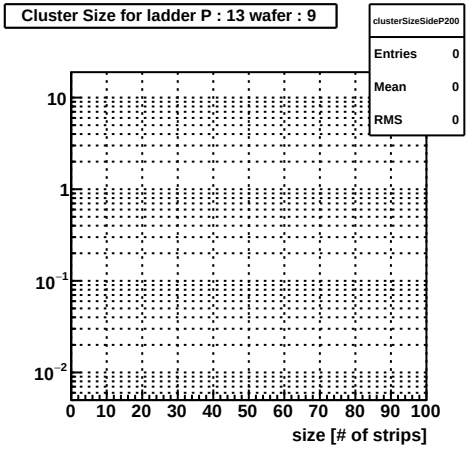

Cluster Size for ladder P : 13 wafer : 8
clusterSizeSideP199
Entries 0
Mean 0
RMS 0
size [# of strips]

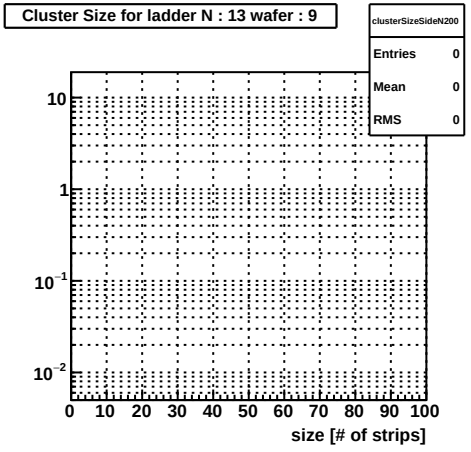

Cluster Size for ladder N : 13 wafer : 8
clusterSizeSideN199
Entries 0
Mean 0
RMS 0
size [# of strips]

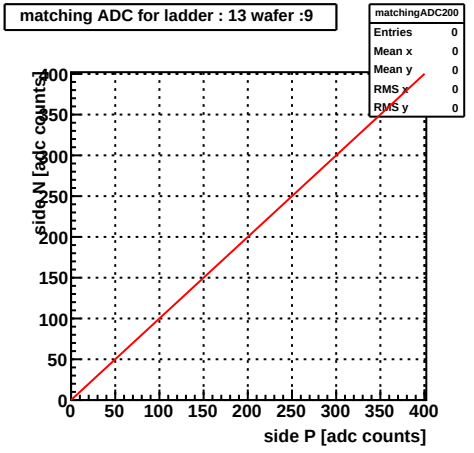

matching ADC for ladder : 13 wafer :8
matchingADC199
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

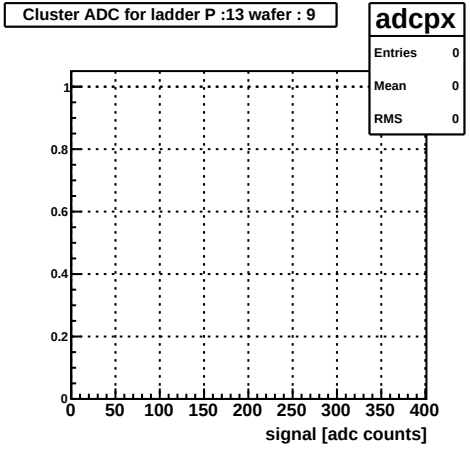

Cluster ADC for ladder P :13 wafer : 8
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

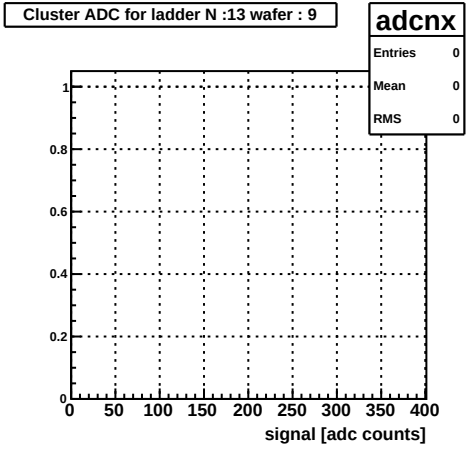

Cluster ADC for ladder N :13 wafer : 8
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

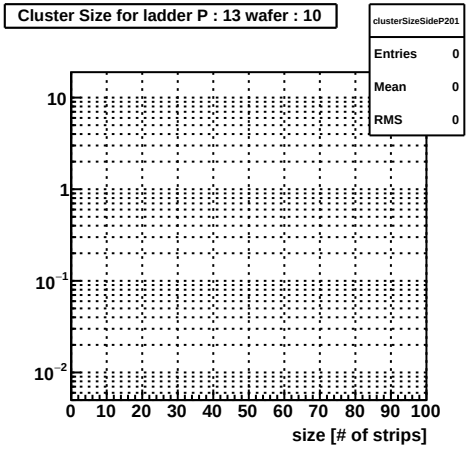

Cluster Size for ladder P : 13 wafer : 9
clusterSizeSideP200
Entries 0
Mean 0
RMS 0
size [# of strips]

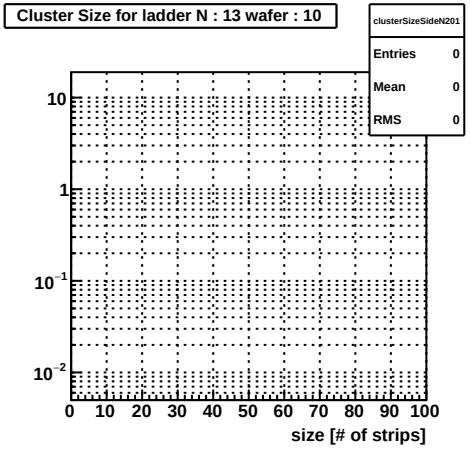

Cluster Size for ladder N : 13 wafer : 9
clusterSizeSideN200
Entries 0
Mean 0
RMS 0
size [# of strips]

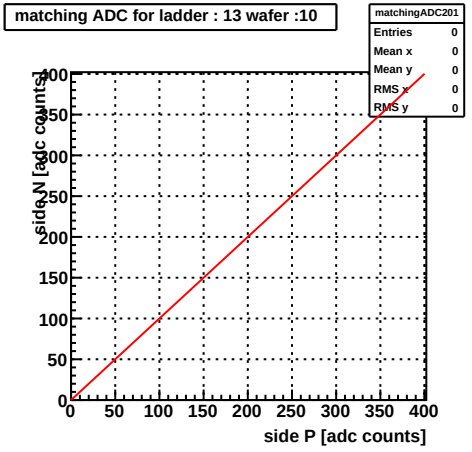

matching ADC for ladder : 13 wafer :9
matchingADC200
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

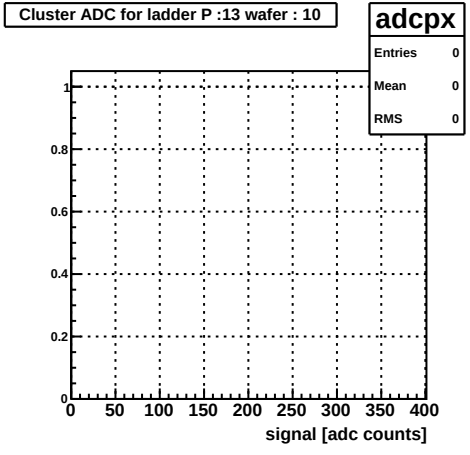

Cluster ADC for ladder P :13 wafer : 9
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

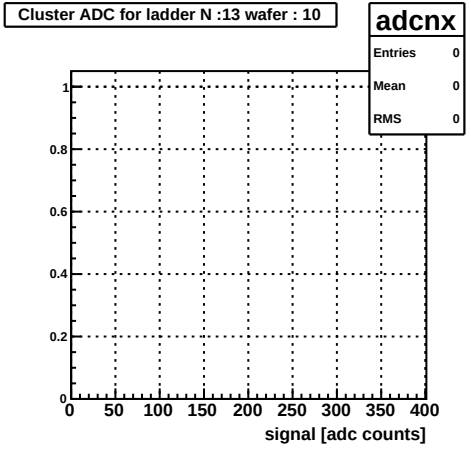

Cluster ADC for ladder N :13 wafer : 9
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

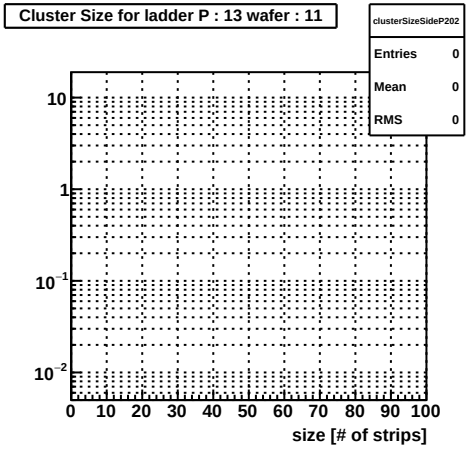

Cluster Size for ladder P : 13 wafer : 10
clusterSizeSideP201
Entries 0
Mean 0
RMS 0
size [# of strips]

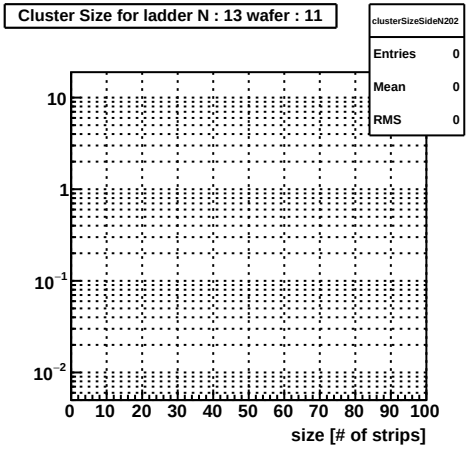

Cluster Size for ladder N : 13 wafer : 10
clusterSizeSideN201
Entries 0
Mean 0
RMS 0
size [# of strips]

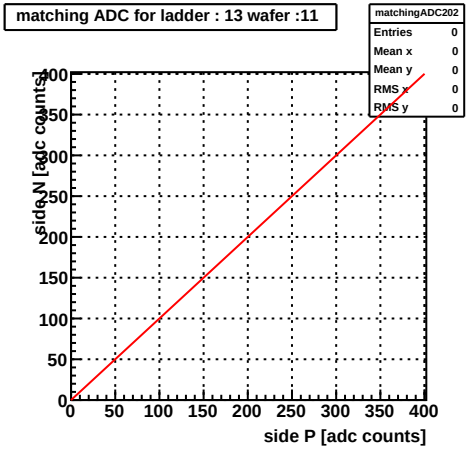

matching ADC for ladder : 13 wafer :10
matchingADC201
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

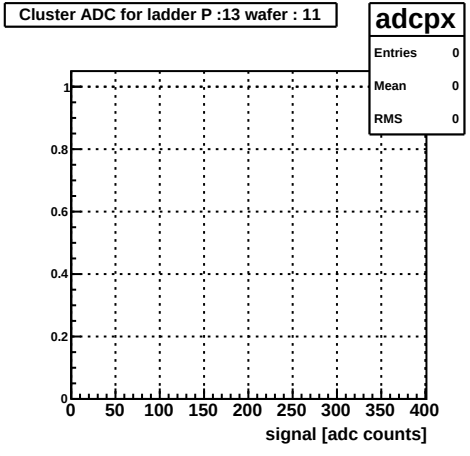

Cluster ADC for ladder P :13 wafer : 10
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

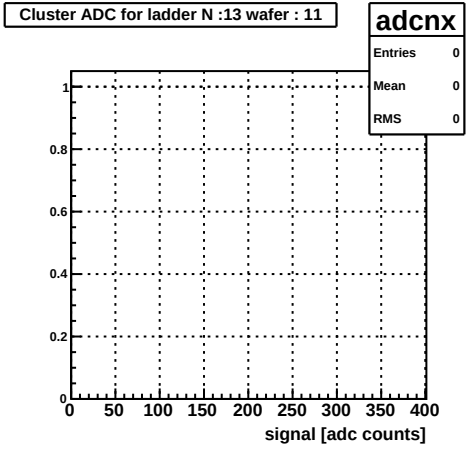

Cluster ADC for ladder N :13 wafer : 10
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

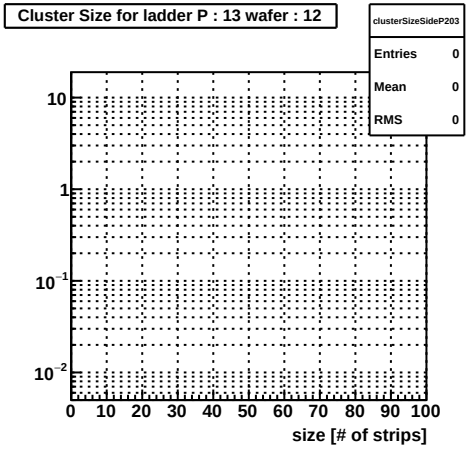

Cluster Size for ladder P : 13 wafer : 11
clusterSizeSideP202
Entries 0
Mean 0
RMS 0
size [# of strips]

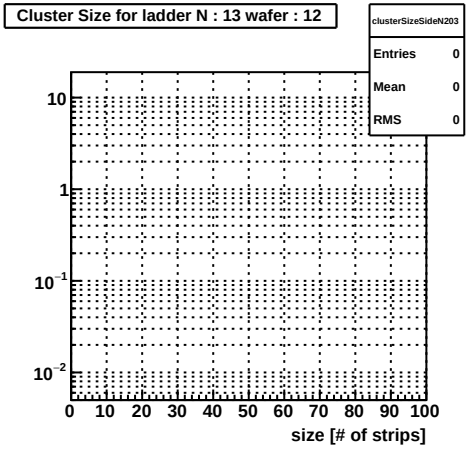

Cluster Size for ladder N : 13 wafer : 11
clusterSizeSideN202
Entries 0
Mean 0
RMS 0
size [# of strips]

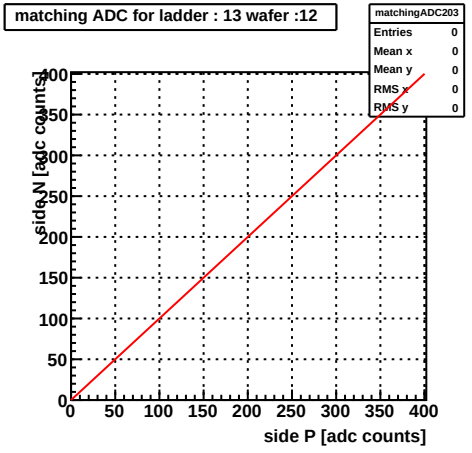

matching ADC for ladder : 13 wafer :11
matchingADC202
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

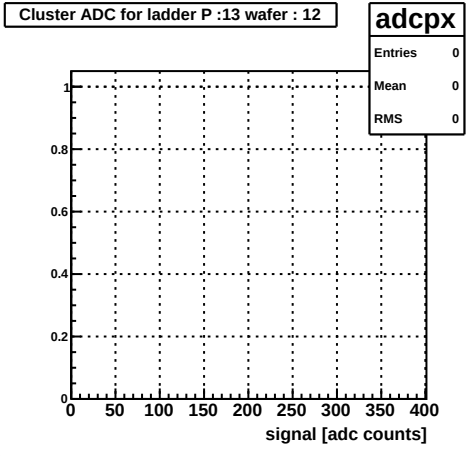

Cluster ADC for ladder P :13 wafer : 11
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

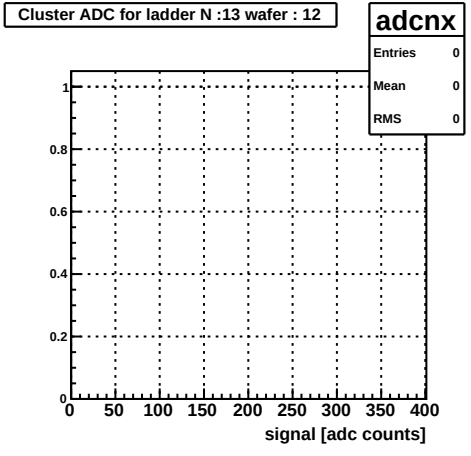

Cluster ADC for ladder N :13 wafer : 11
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

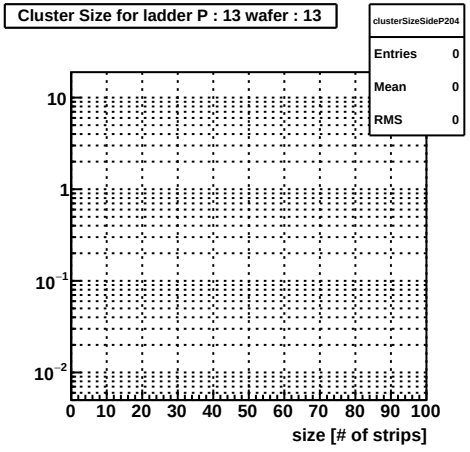

Cluster Size for ladder P : 13 wafer : 12
clusterSizeSideP203
Entries 0
Mean 0
RMS 0
size [# of strips]

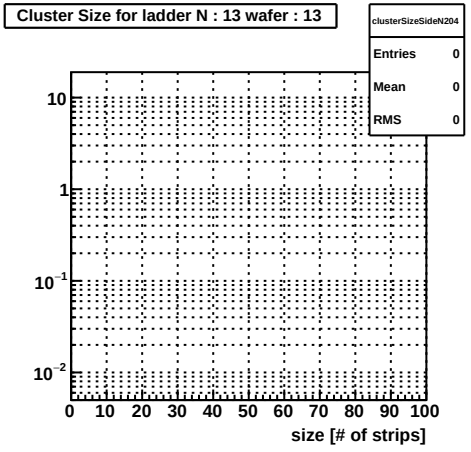

Cluster Size for ladder N : 13 wafer : 12
clusterSizeSideN203
Entries 0
Mean 0
RMS 0
size [# of strips]

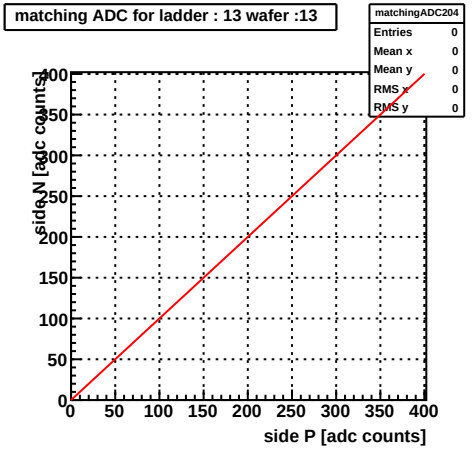

matching ADC for ladder : 13 wafer :12
matchingADC203
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

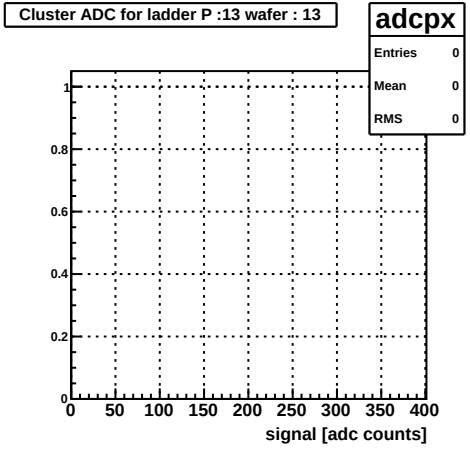

Cluster ADC for ladder P :13 wafer : 12
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

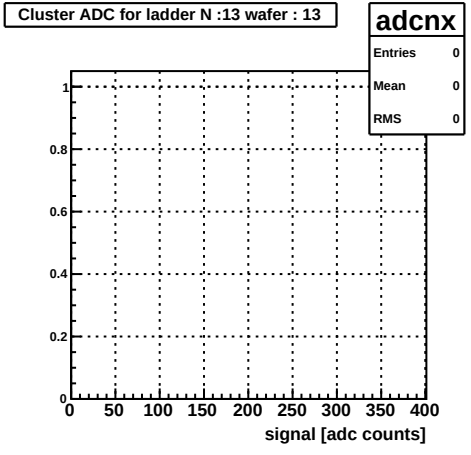

Cluster ADC for ladder N :13 wafer : 12
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

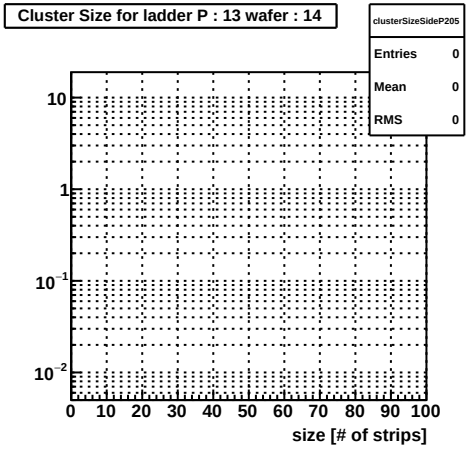

Cluster Size for ladder P : 13 wafer : 13
clusterSizeSideP204
Entries 0
Mean 0
RMS 0
size [# of strips]

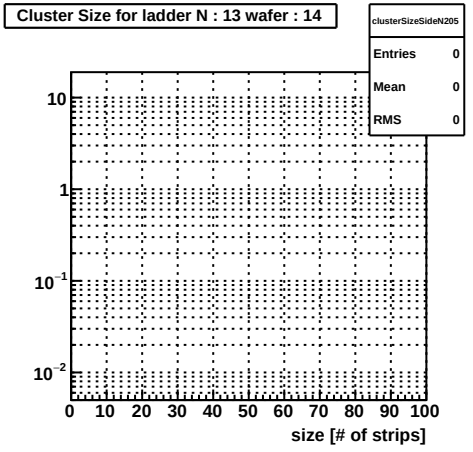

Cluster Size for ladder N : 13 wafer : 13
clusterSizeSideN204
Entries 0
Mean 0
RMS 0
size [# of strips]

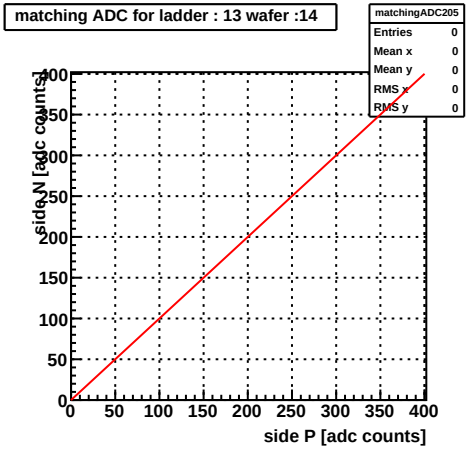

matching ADC for ladder : 13 wafer :13
matchingADC204
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

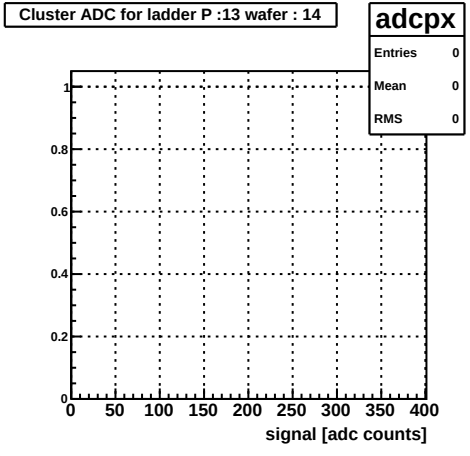

Cluster ADC for ladder P :13 wafer : 13
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

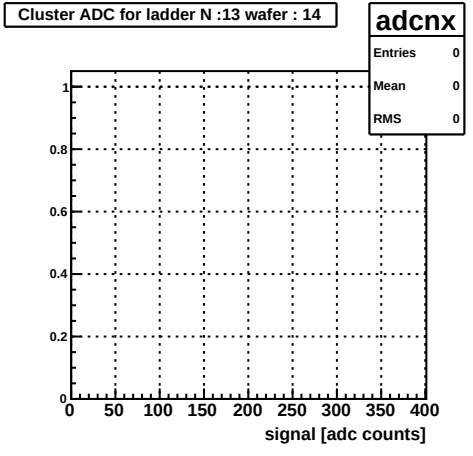

Cluster ADC for ladder N :13 wafer : 13
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

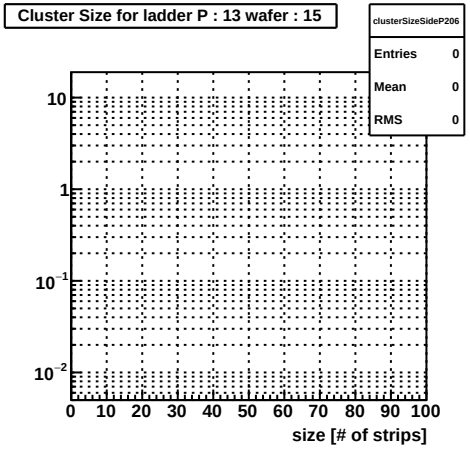

Cluster Size for ladder P : 13 wafer : 14
clusterSizeSideP205
Entries 0
Mean 0
RMS 0
size [# of strips]

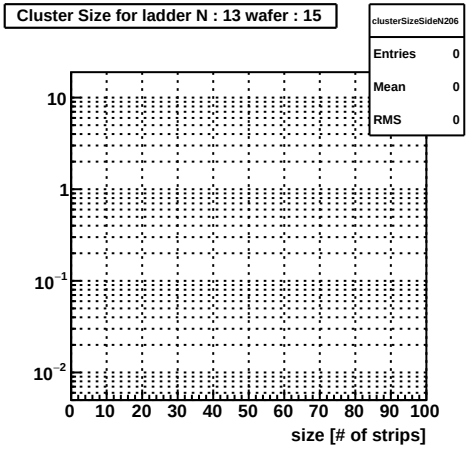

Cluster Size for ladder N : 13 wafer : 14
clusterSizeSideN205
Entries 0
Mean 0
RMS 0
size [# of strips]

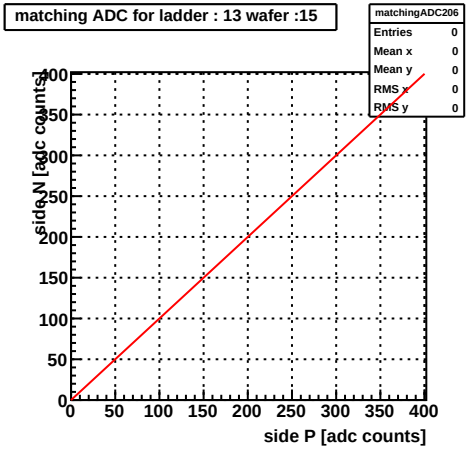

matching ADC for ladder : 13 wafer :14
matchingADC205
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

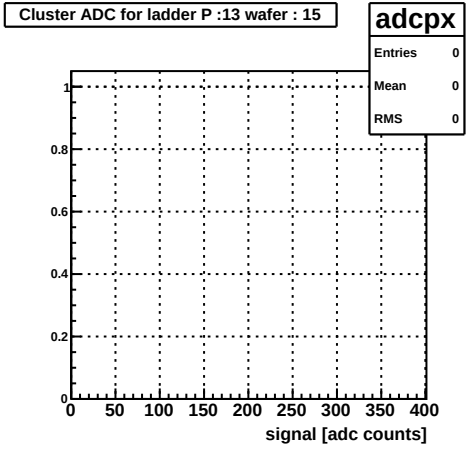

Cluster ADC for ladder P :13 wafer : 14
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

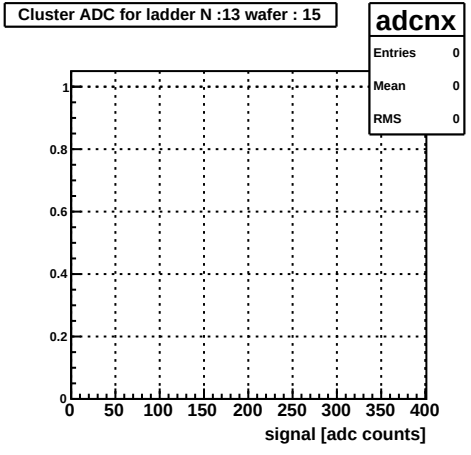

Cluster ADC for ladder N :13 wafer : 14
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

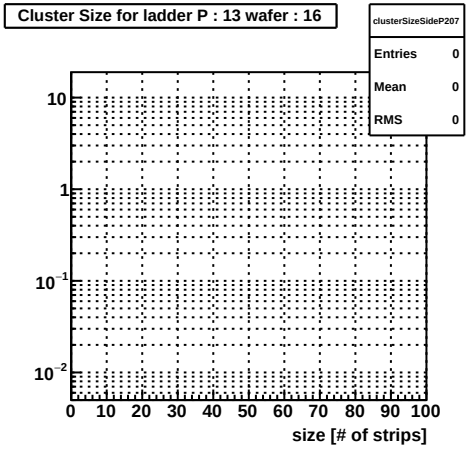

Cluster Size for ladder P : 13 wafer : 15
clusterSizeSideP206
Entries 0
Mean 0
RMS 0
size [# of strips]

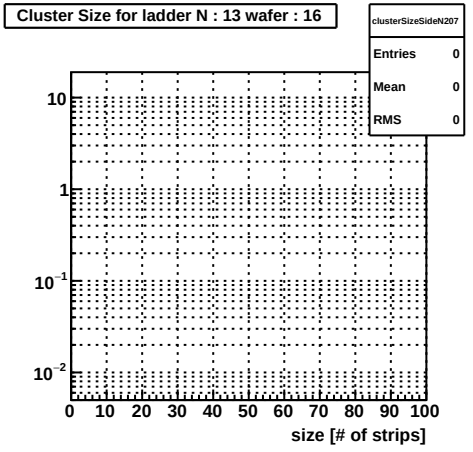

Cluster Size for ladder N : 13 wafer : 15
clusterSizeSideN206
Entries 0
Mean 0
RMS 0
size [# of strips]

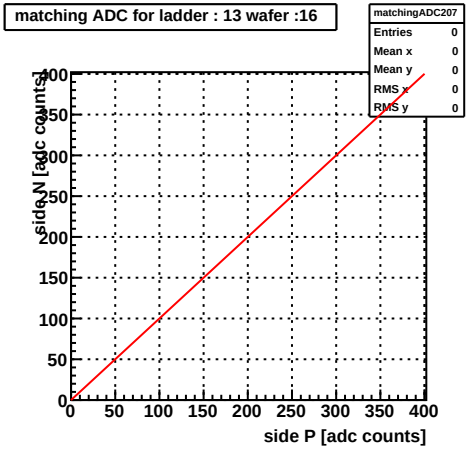

matching ADC for ladder : 13 wafer :15
matchingADC206
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

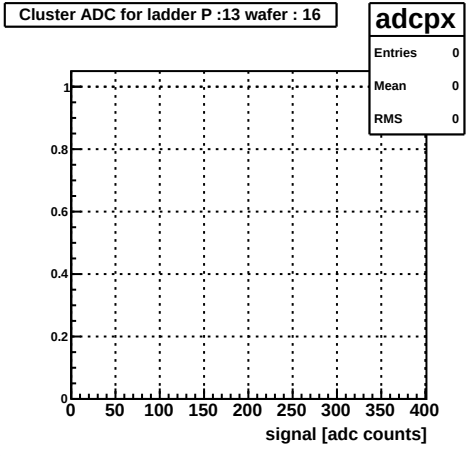

Cluster ADC for ladder P :13 wafer : 15
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

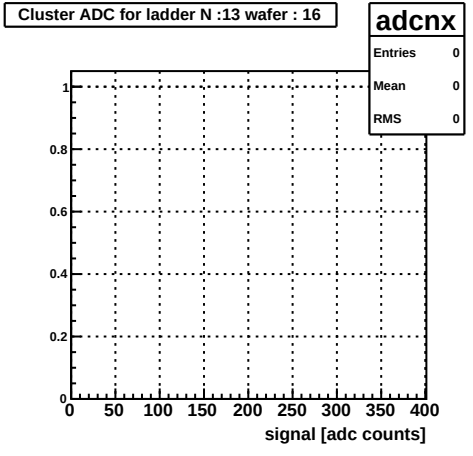

Cluster ADC for ladder N :13 wafer : 15
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

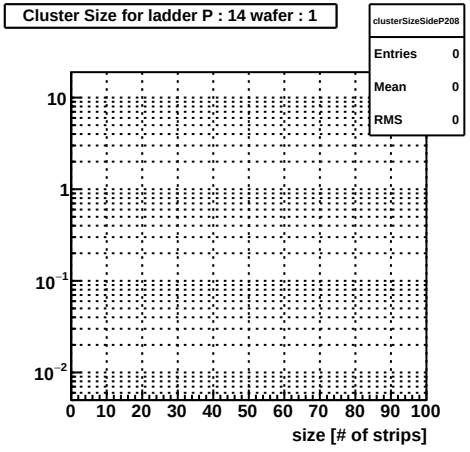

Cluster Size for ladder P : 13 wafer : 16
clusterSizeSideP207
Entries 0
Mean 0
RMS 0
size [# of strips]

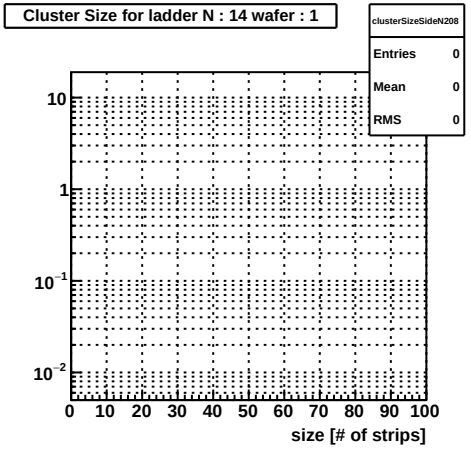

Cluster Size for ladder N : 13 wafer : 16
clusterSizeSideN207
Entries 0
Mean 0
RMS 0
size [# of strips]

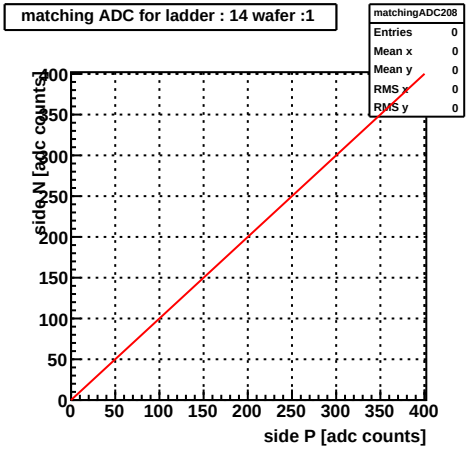

matching ADC for ladder : 13 wafer :16
matchingADC207
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

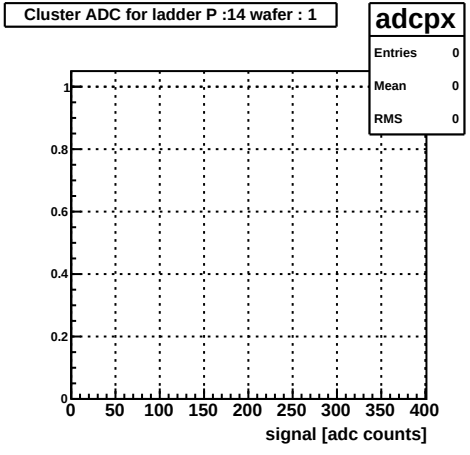

Cluster ADC for ladder P :13 wafer : 16
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

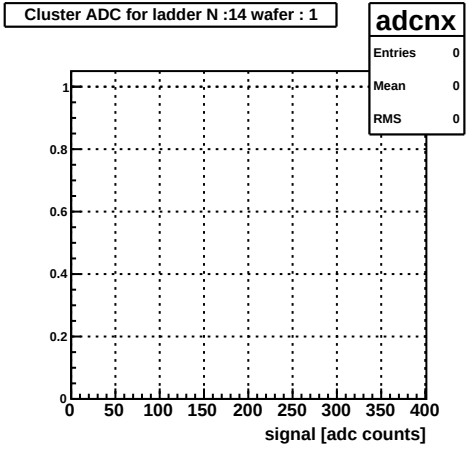

Cluster ADC for ladder N :13 wafer : 16
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

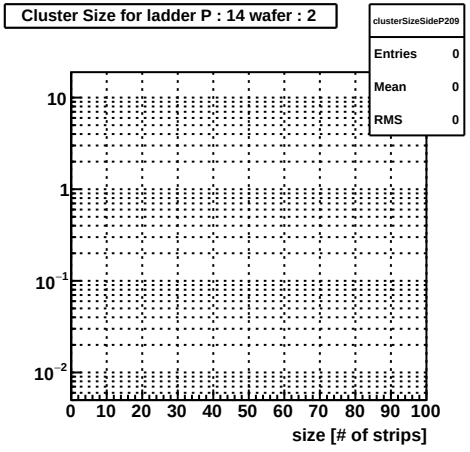

Cluster Size for ladder P : 14 wafer : 1
clusterSizeSideP208
Entries 0
Mean 0
RMS 0
size [# of strips]

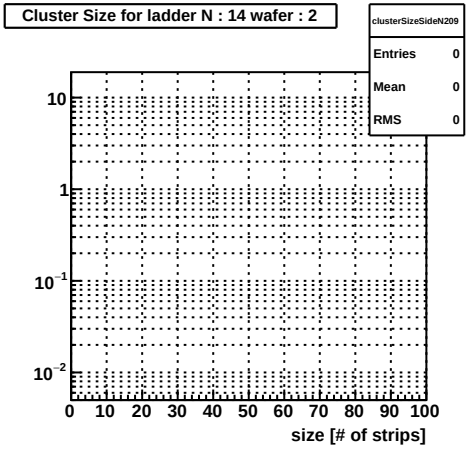

Cluster Size for ladder N : 14 wafer : 1
clusterSizeSideN208
Entries 0
Mean 0
RMS 0
size [# of strips]

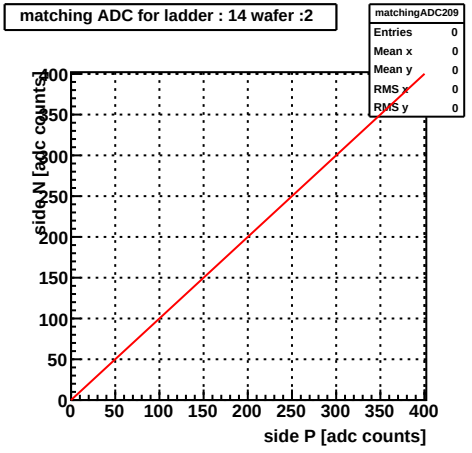

matching ADC for ladder : 14 wafer :1
matchingADC208
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

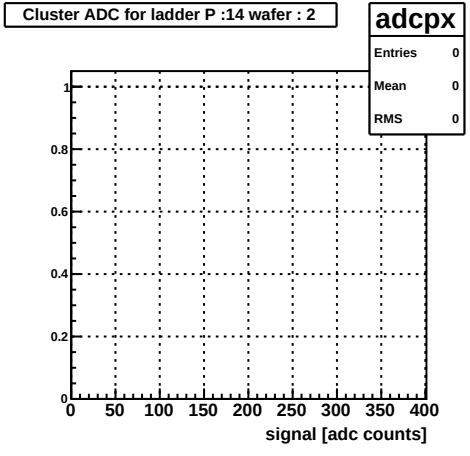

Cluster ADC for ladder P :14 wafer : 1
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

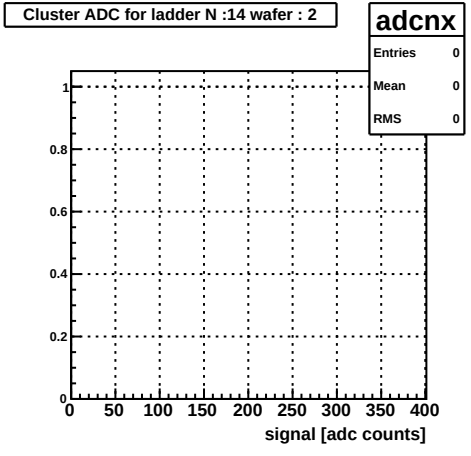

Cluster ADC for ladder N :14 wafer : 1
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

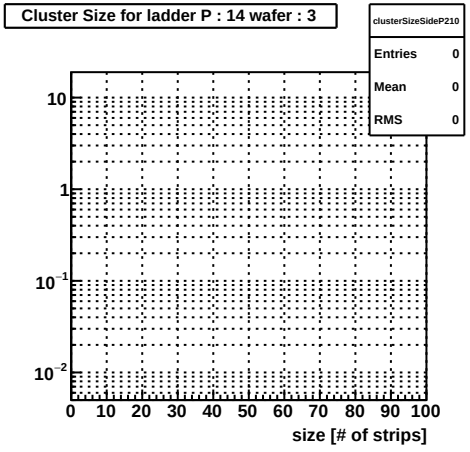

Cluster Size for ladder P : 14 wafer : 2
clusterSizeSideP209
Entries 0
Mean 0
RMS 0
size [# of strips]

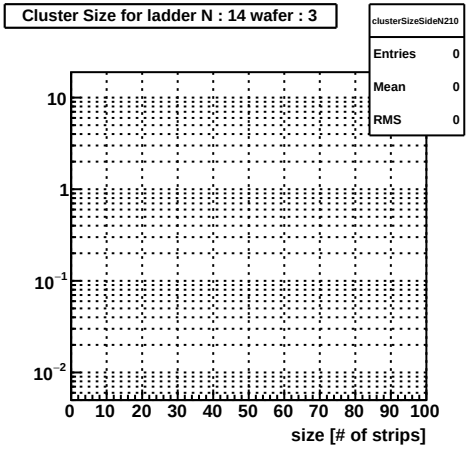

Cluster Size for ladder N : 14 wafer : 2
clusterSizeSideN209
Entries 0
Mean 0
RMS 0
size [# of strips]

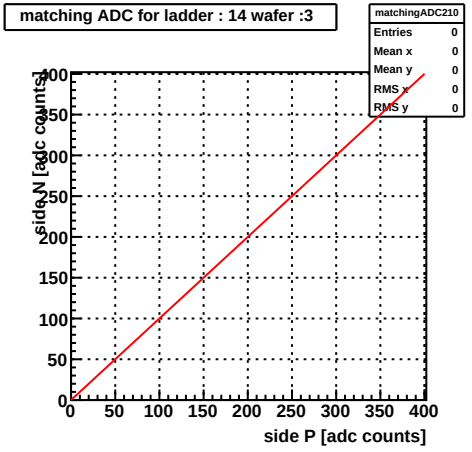

matching ADC for ladder : 14 wafer :2
matchingADC209
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

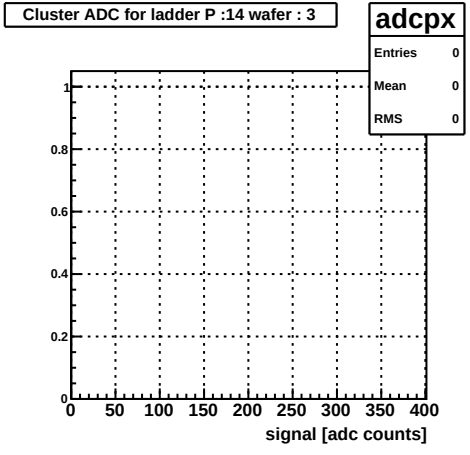

Cluster ADC for ladder P :14 wafer : 2
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

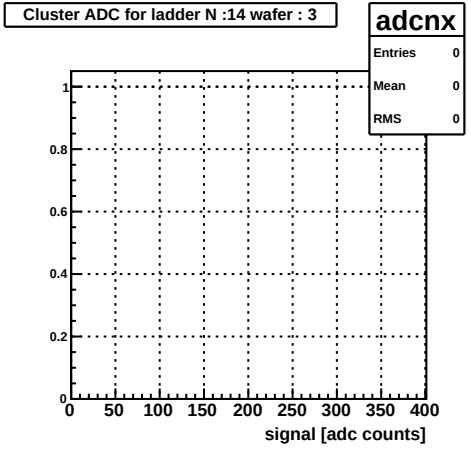

Cluster ADC for ladder N :14 wafer : 2
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

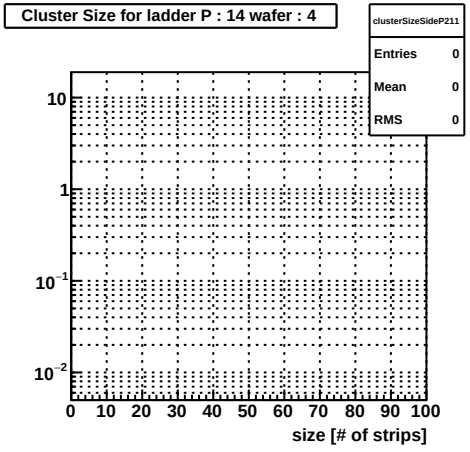

Cluster Size for ladder P : 14 wafer : 3
clusterSizeSideP210
Entries 0
Mean 0
RMS 0
size [# of strips]

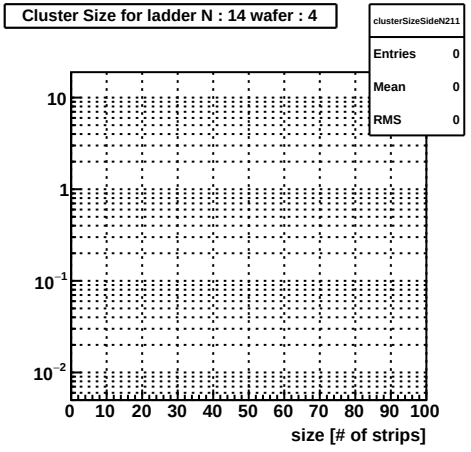

Cluster Size for ladder N : 14 wafer : 3
clusterSizeSideN210
Entries 0
Mean 0
RMS 0
size [# of strips]

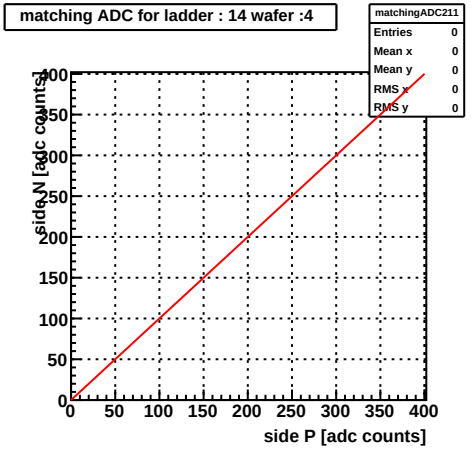

matching ADC for ladder : 14 wafer :3
matchingADC210
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

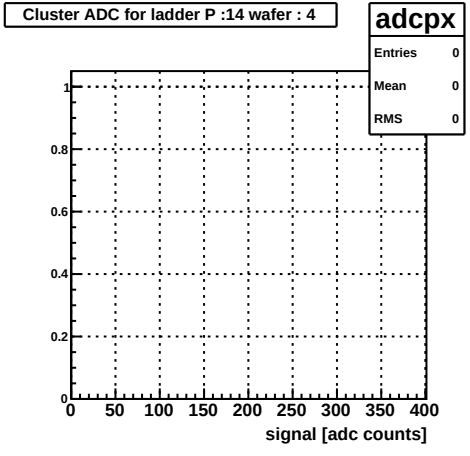

Cluster ADC for ladder P :14 wafer : 3
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

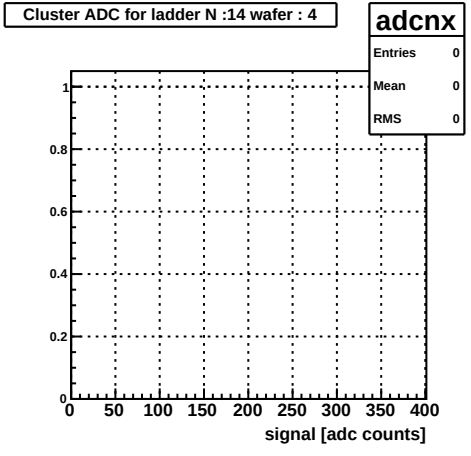

Cluster ADC for ladder N :14 wafer : 3
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

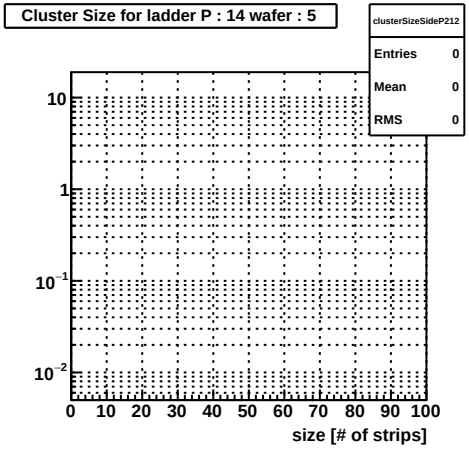

Cluster Size for ladder P : 14 wafer : 4
clusterSizeSideP211
Entries 0
Mean 0
RMS 0
size [# of strips]

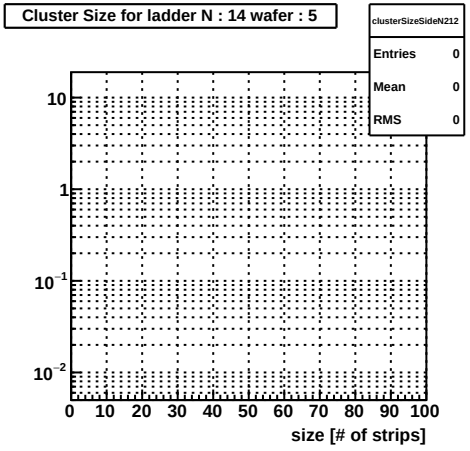

Cluster Size for ladder N : 14 wafer : 4
clusterSizeSideN211
Entries 0
Mean 0
RMS 0
size [# of strips]

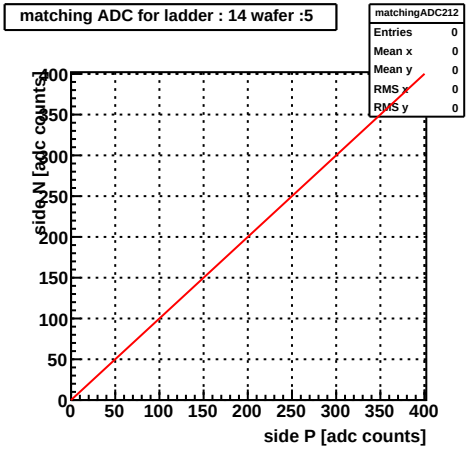

matching ADC for ladder : 14 wafer :4
matchingADC211
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

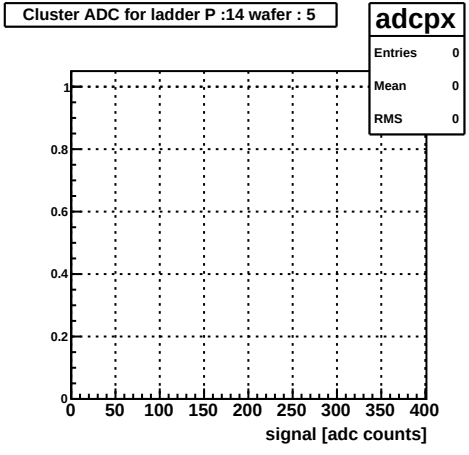

Cluster ADC for ladder P :14 wafer : 4
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

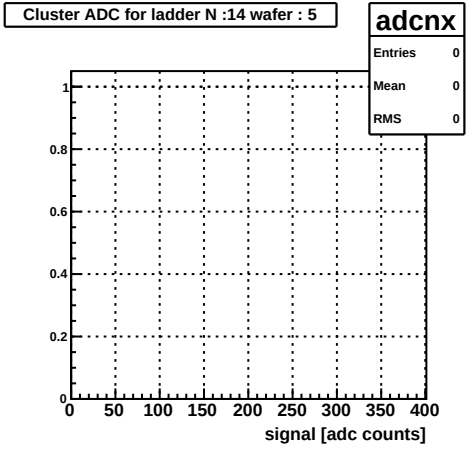

Cluster ADC for ladder N :14 wafer : 4
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

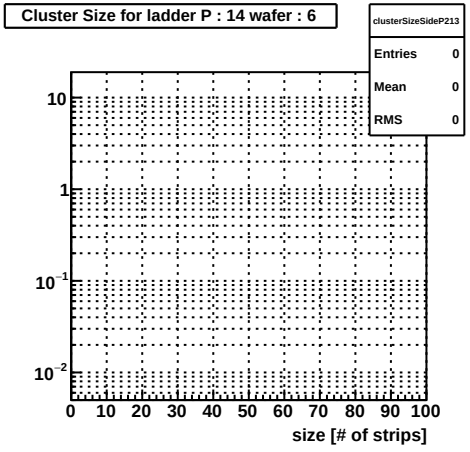

Cluster Size for ladder P : 14 wafer : 5
clusterSizeSideP212
Entries 0
Mean 0
RMS 0
size [# of strips]

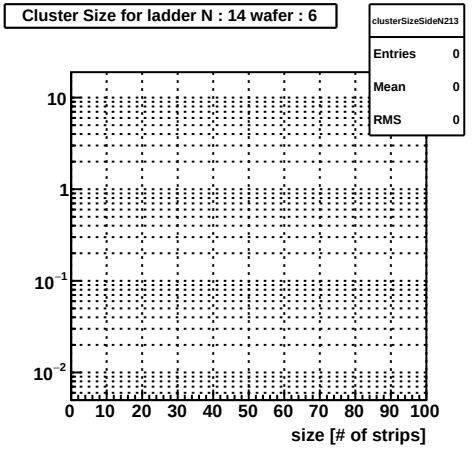

Cluster Size for ladder N : 14 wafer : 5
clusterSizeSideN212
Entries 0
Mean 0
RMS 0
size [# of strips]

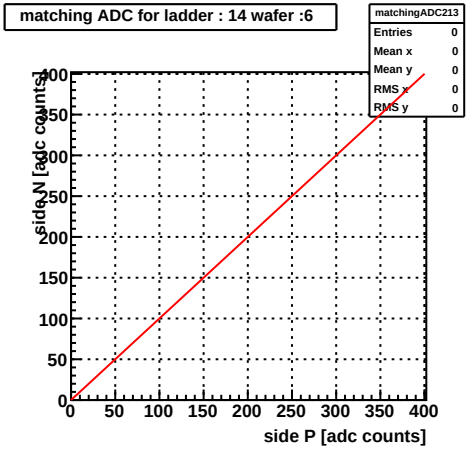

matching ADC for ladder : 14 wafer :5
matchingADC212
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

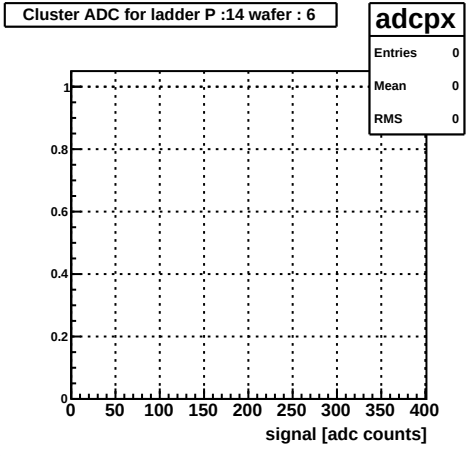

Cluster ADC for ladder P :14 wafer : 5
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

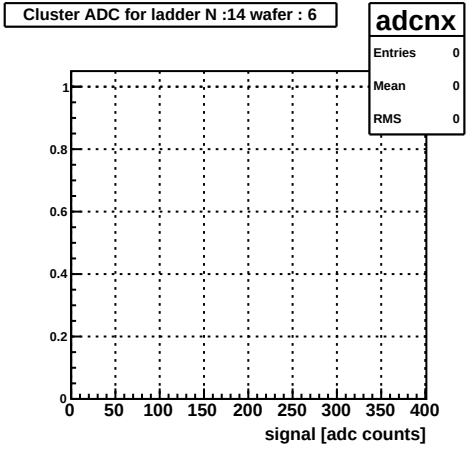

Cluster ADC for ladder N :14 wafer : 5
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

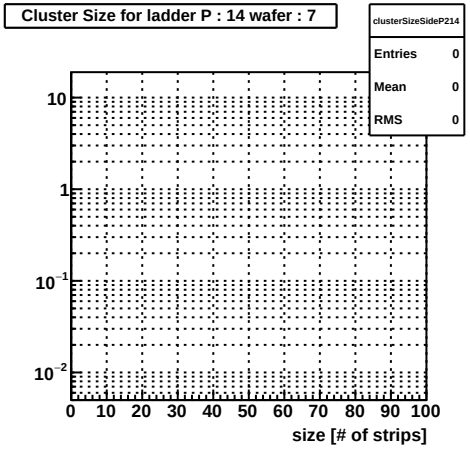

Cluster Size for ladder P : 14 wafer : 6
clusterSizeSideP213
Entries 0
Mean 0
RMS 0
size [# of strips]

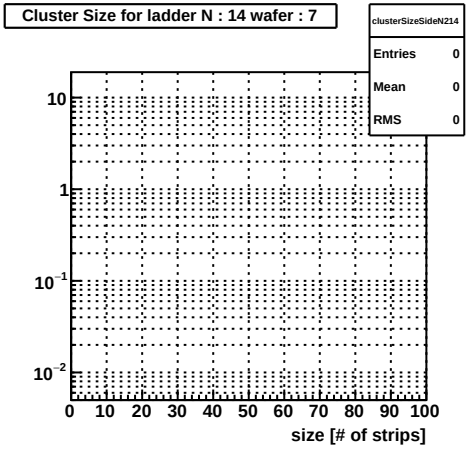

Cluster Size for ladder N : 14 wafer : 6
clusterSizeSideN213
Entries 0
Mean 0
RMS 0
size [# of strips]

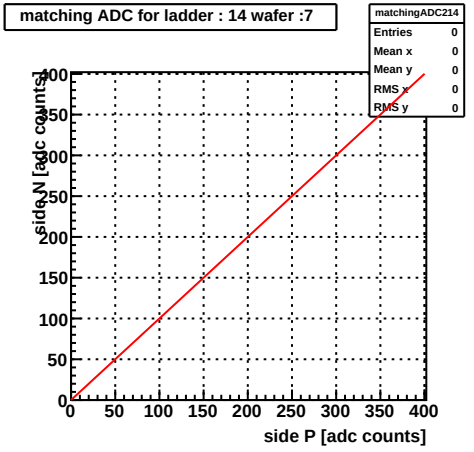

matching ADC for ladder : 14 wafer :6
matchingADC213
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

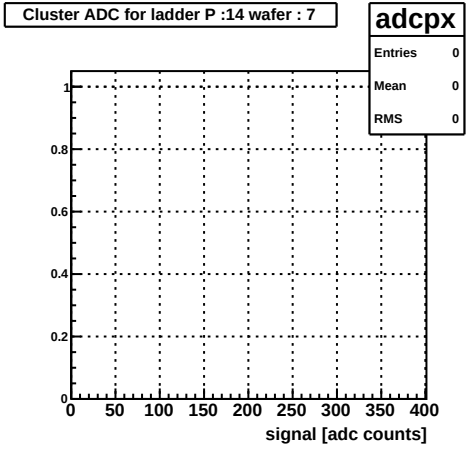

Cluster ADC for ladder P :14 wafer : 6
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

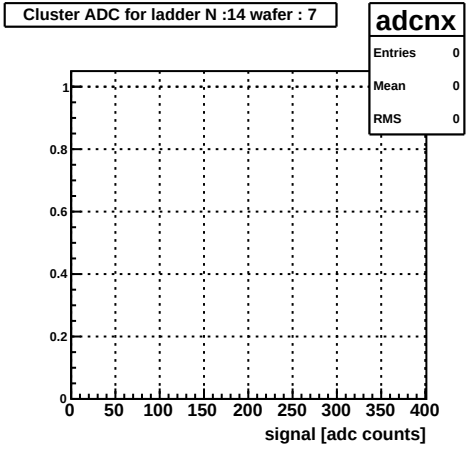

Cluster ADC for ladder N :14 wafer : 6
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

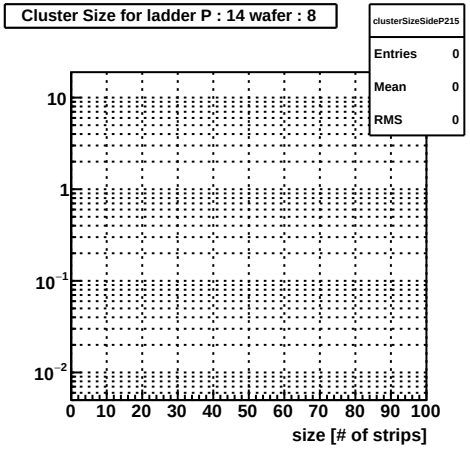

Cluster Size for ladder P : 14 wafer : 7
clusterSizeSideP214
Entries 0
Mean 0
RMS 0
size [# of strips]

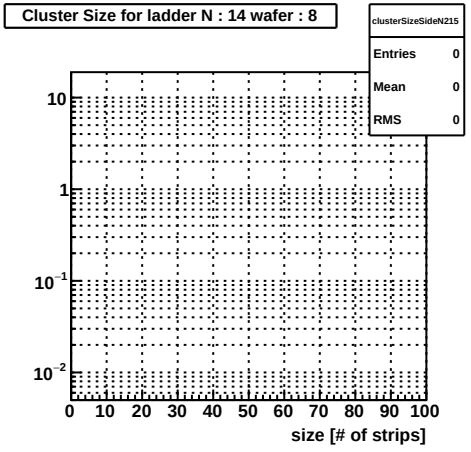

Cluster Size for ladder N : 14 wafer : 7
clusterSizeSideN214
Entries 0
Mean 0
RMS 0
size [# of strips]

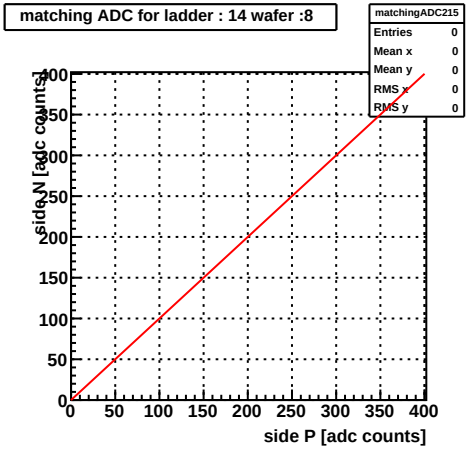

matching ADC for ladder : 14 wafer :7
matchingADC214
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

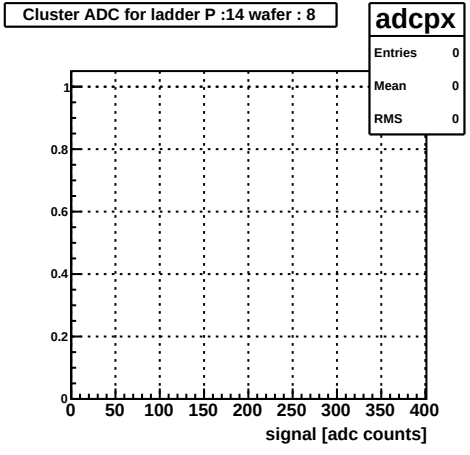

Cluster ADC for ladder P :14 wafer : 7
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

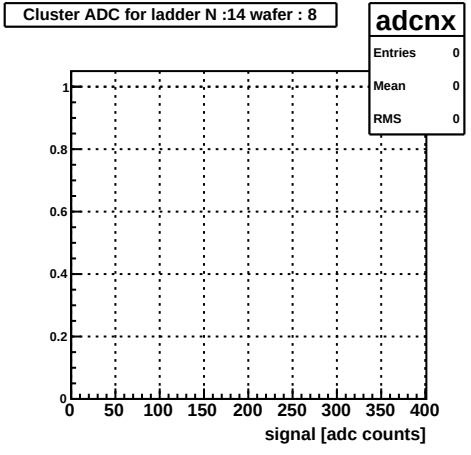

Cluster ADC for ladder N :14 wafer : 7
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

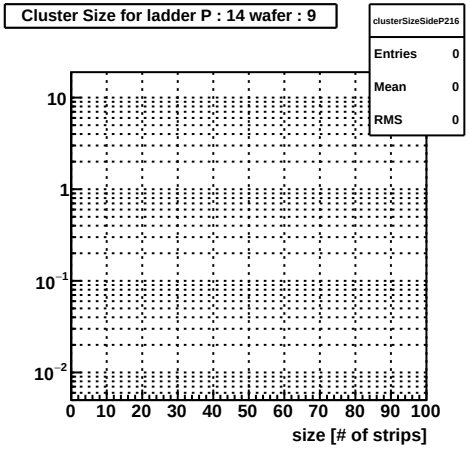

Cluster Size for ladder P : 14 wafer : 8
clusterSizeSideP215
Entries 0
Mean 0
RMS 0
size [# of strips]

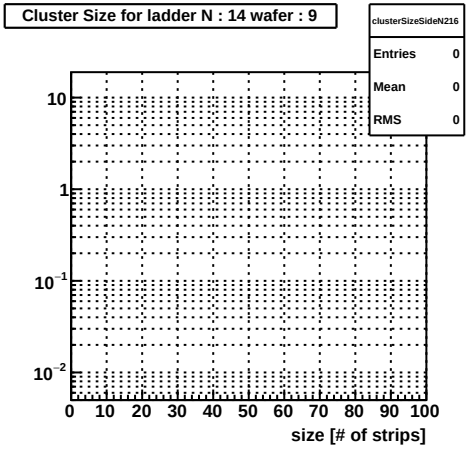

Cluster Size for ladder N : 14 wafer : 8
clusterSizeSideN215
Entries 0
Mean 0
RMS 0
size [# of strips]

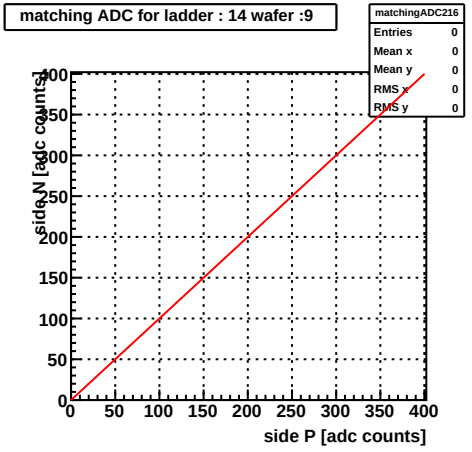

matching ADC for ladder : 14 wafer :8
matchingADC215
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

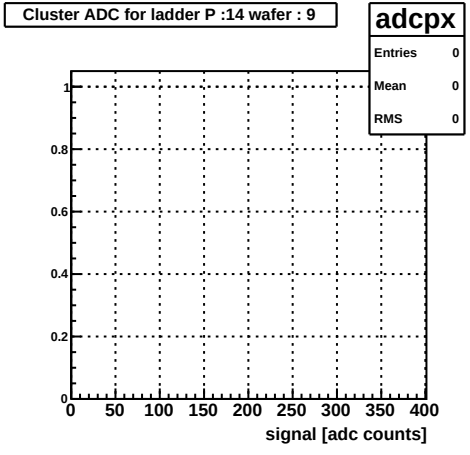

Cluster ADC for ladder P :14 wafer : 8
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

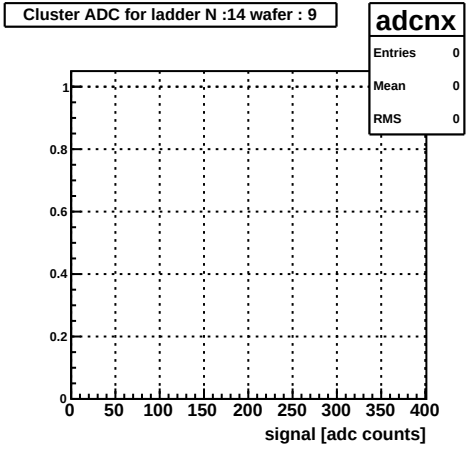

Cluster ADC for ladder N :14 wafer : 8
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

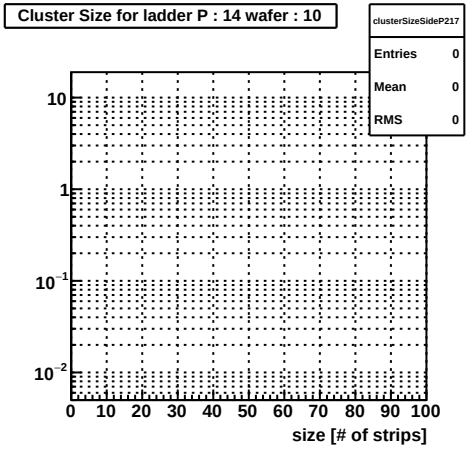

Cluster Size for ladder P : 14 wafer : 9
clusterSizeSideP216
Entries 0
Mean 0
RMS 0
size [# of strips]

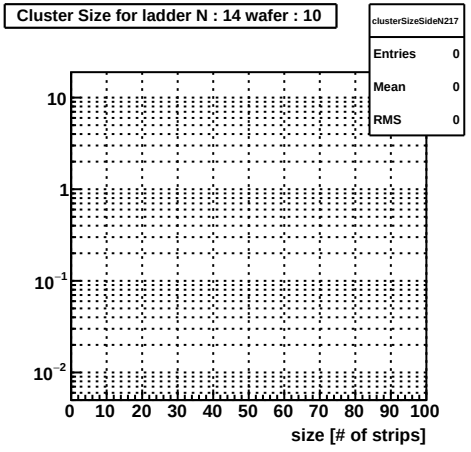

Cluster Size for ladder N : 14 wafer : 9
clusterSizeSideN216
Entries 0
Mean 0
RMS 0
size [# of strips]

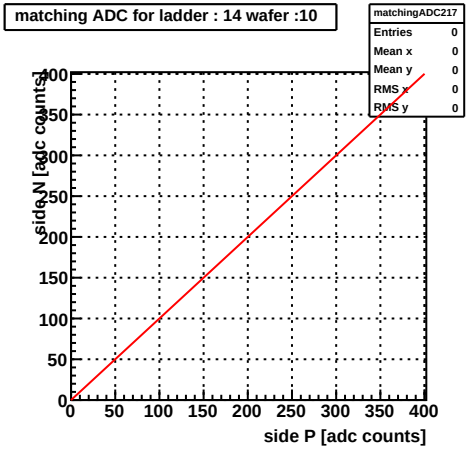

matching ADC for ladder : 14 wafer :9
matchingADC216
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

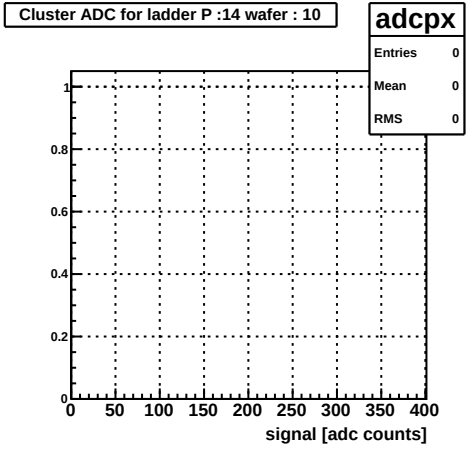

Cluster ADC for ladder P :14 wafer : 9
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

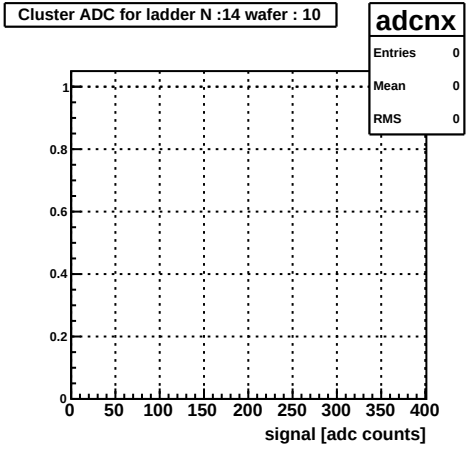

Cluster ADC for ladder N :14 wafer : 9
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

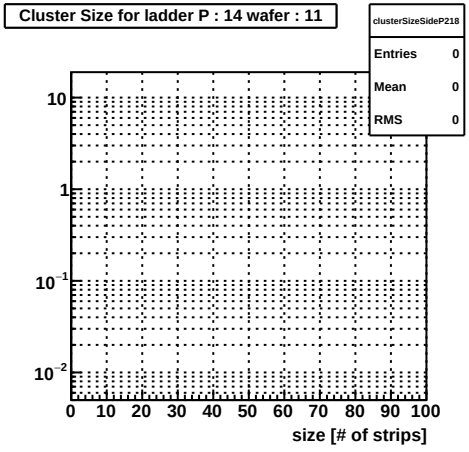

Cluster Size for ladder P : 14 wafer : 10
clusterSizeSideP217
Entries 0
Mean 0
RMS 0
size [# of strips]

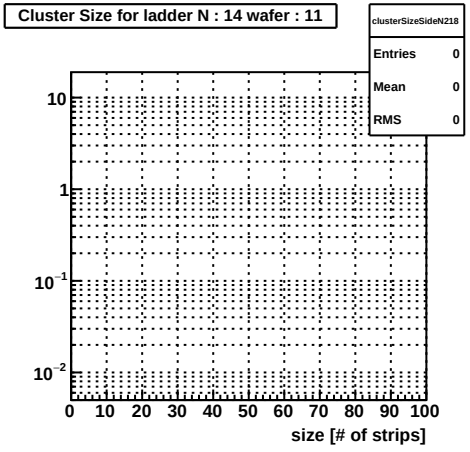

Cluster Size for ladder N : 14 wafer : 10
clusterSizeSideN217
Entries 0
Mean 0
RMS 0
size [# of strips]

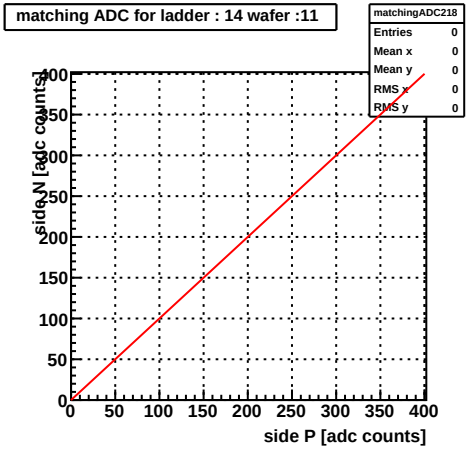

matching ADC for ladder : 14 wafer :10
matchingADC217
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

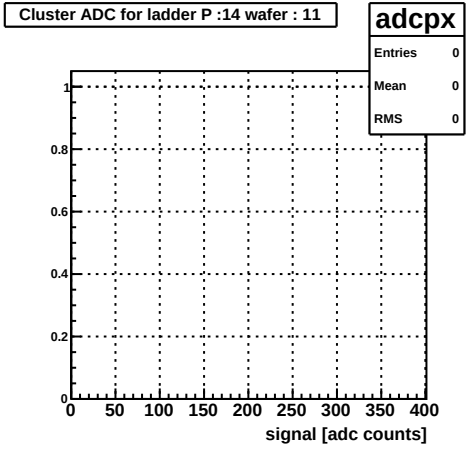

Cluster ADC for ladder P :14 wafer : 10
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

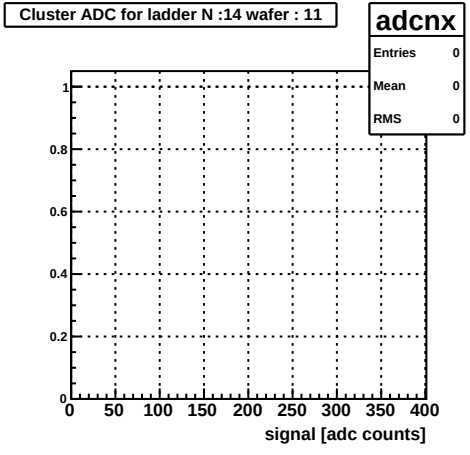

Cluster ADC for ladder N :14 wafer : 10
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

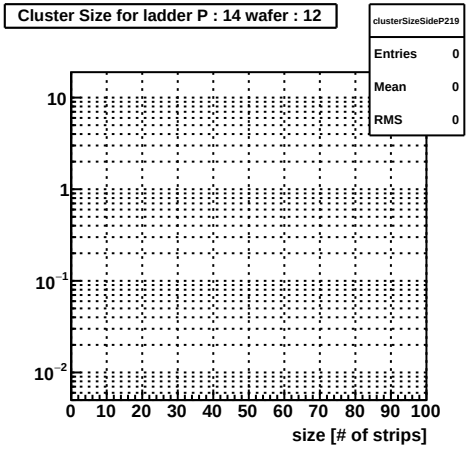

Cluster Size for ladder P : 14 wafer : 11
clusterSizeSideP218
Entries 0
Mean 0
RMS 0
size [# of strips]

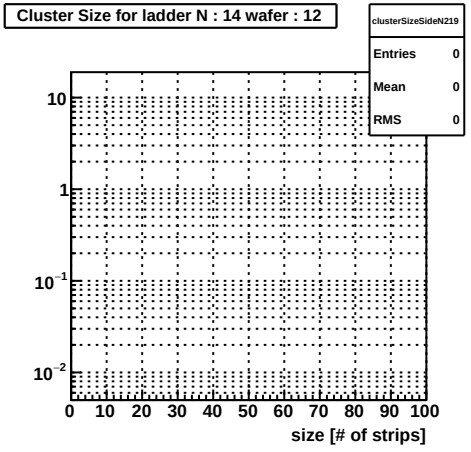

Cluster Size for ladder N : 14 wafer : 11
clusterSizeSideN218
Entries 0
Mean 0
RMS 0
size [# of strips]

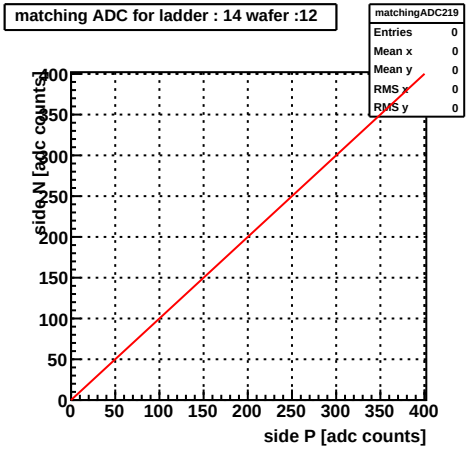

matching ADC for ladder : 14 wafer :11
matchingADC218
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

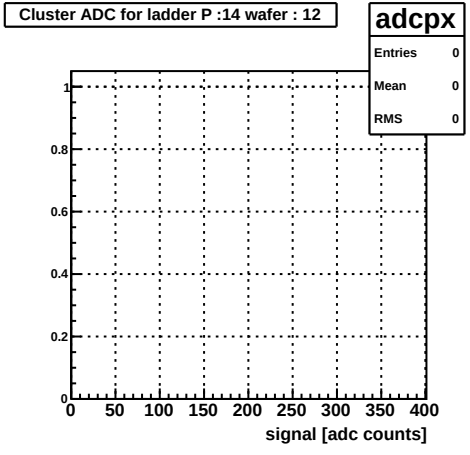

Cluster ADC for ladder P :14 wafer : 11
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

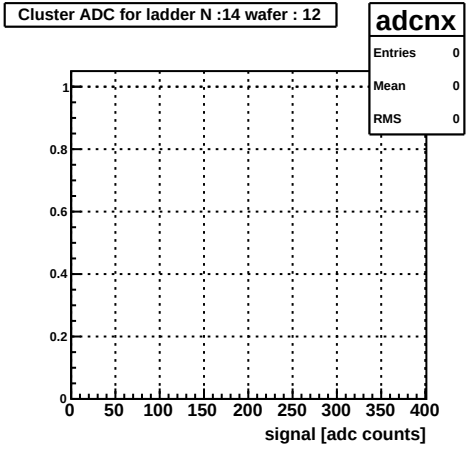

Cluster ADC for ladder N :14 wafer : 11
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

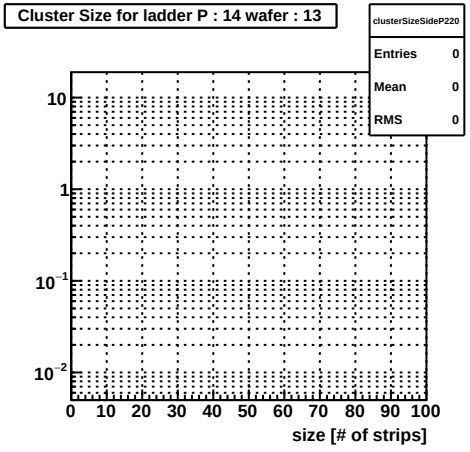

Cluster Size for ladder P : 14 wafer : 12
clusterSizeSideP219
Entries 0
Mean 0
RMS 0
size [# of strips]

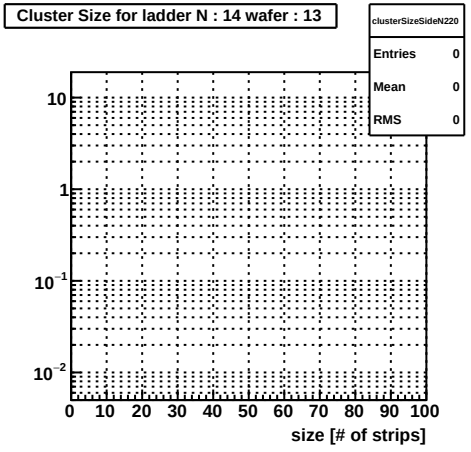

Cluster Size for ladder N : 14 wafer : 12
clusterSizeSideN219
Entries 0
Mean 0
RMS 0
size [# of strips]

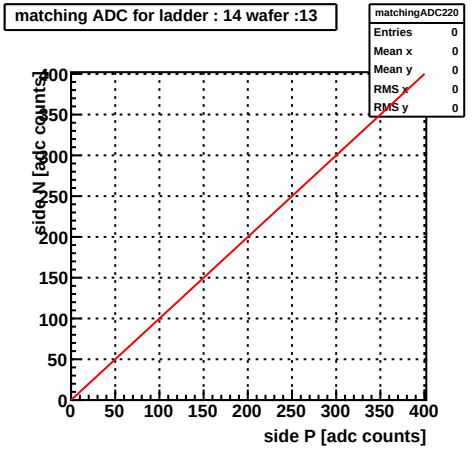

matching ADC for ladder : 14 wafer :12
matchingADC219
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

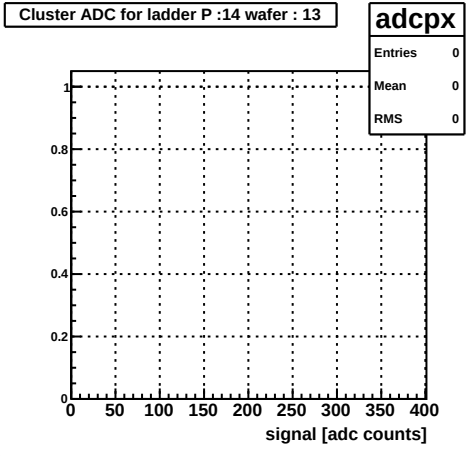

Cluster ADC for ladder P :14 wafer : 12
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

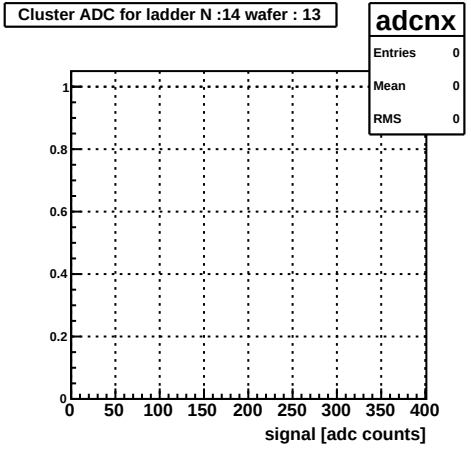

Cluster ADC for ladder N :14 wafer : 12
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

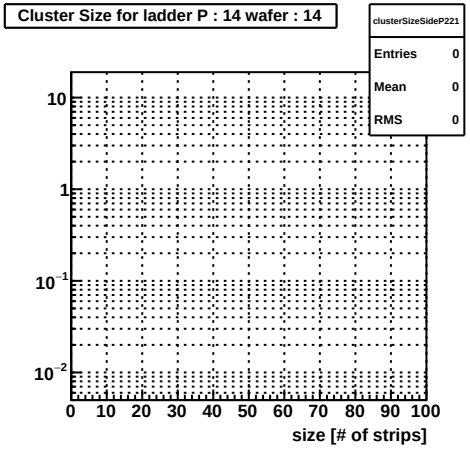

Cluster Size for ladder P : 14 wafer : 13
clusterSizeSideP220
Entries 0
Mean 0
RMS 0
size [# of strips]

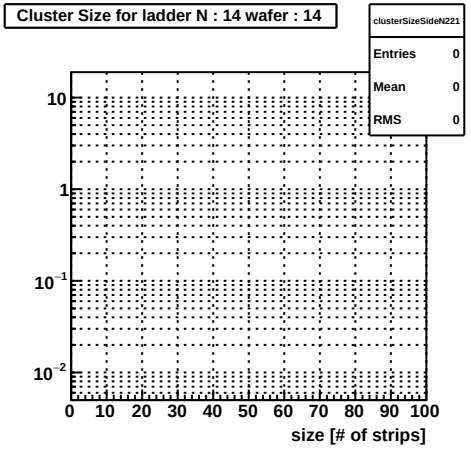

Cluster Size for ladder N : 14 wafer : 13
clusterSizeSideN220
Entries 0
Mean 0
RMS 0
size [# of strips]

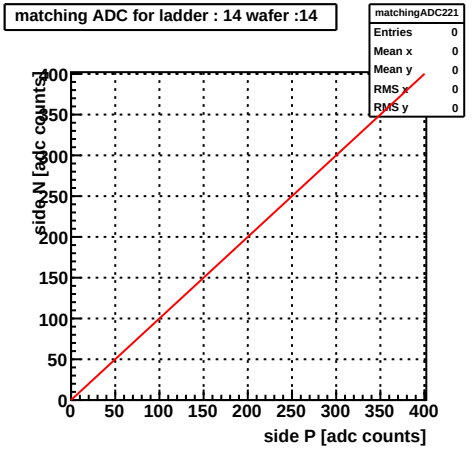

matching ADC for ladder : 14 wafer :13
matchingADC220
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

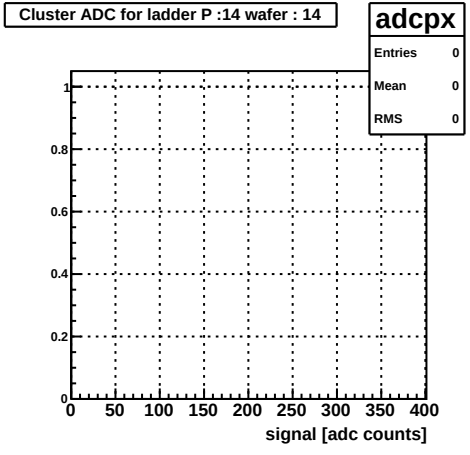

Cluster ADC for ladder P :14 wafer : 13
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

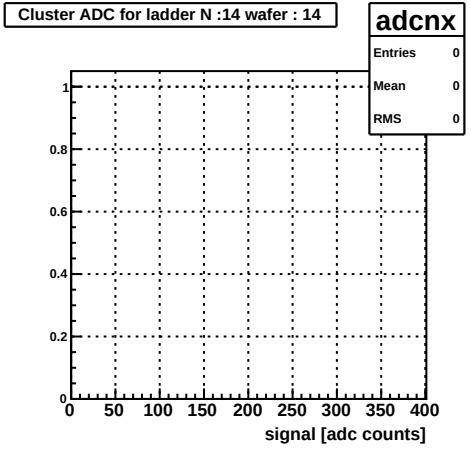

Cluster ADC for ladder N :14 wafer : 13
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

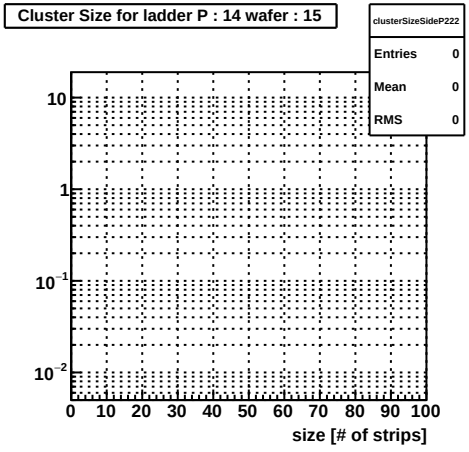

Cluster Size for ladder P : 14 wafer : 14
clusterSizeSideP221
Entries 0
Mean 0
RMS 0
size [# of strips]

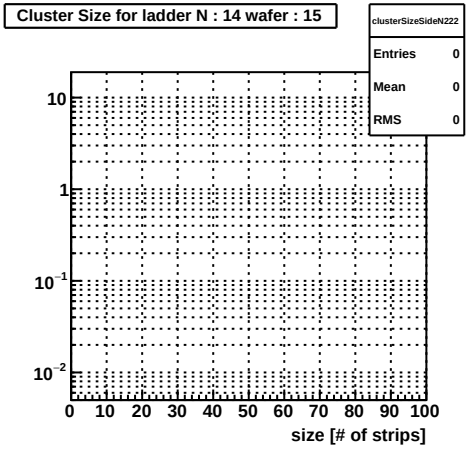

Cluster Size for ladder N : 14 wafer : 14
clusterSizeSideN221
Entries 0
Mean 0
RMS 0
size [# of strips]

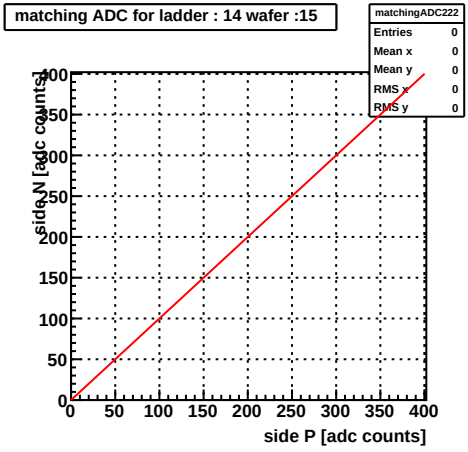

matching ADC for ladder : 14 wafer :14
matchingADC221
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

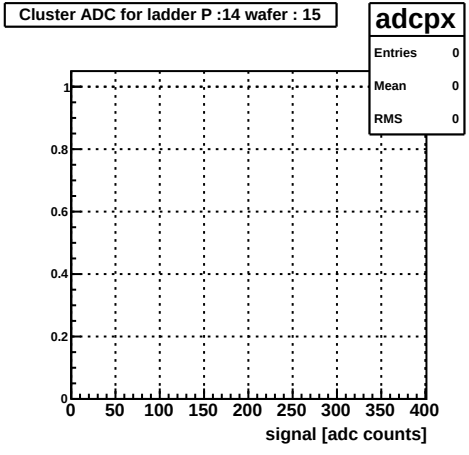

Cluster ADC for ladder P :14 wafer : 14
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

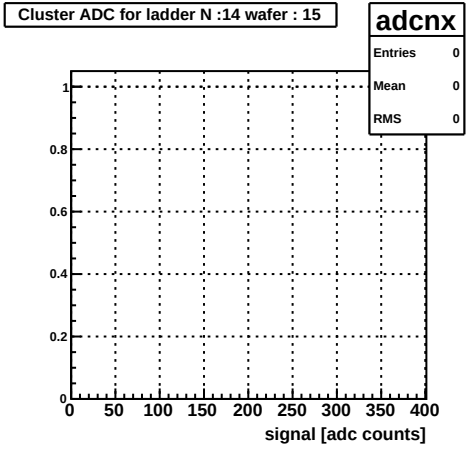

Cluster ADC for ladder N :14 wafer : 14
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

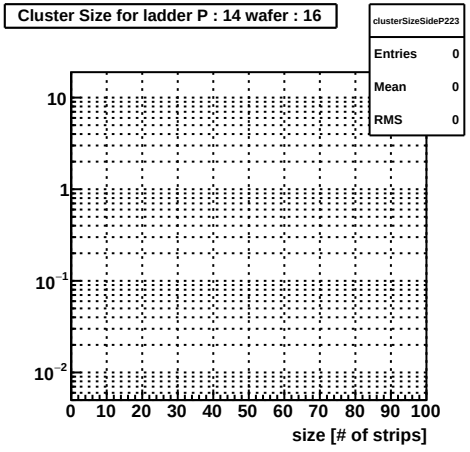

Cluster Size for ladder P : 14 wafer : 15
clusterSizeSideP222
Entries 0
Mean 0
RMS 0
size [# of strips]

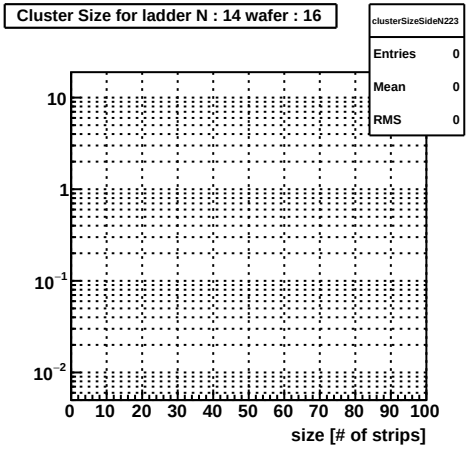

Cluster Size for ladder N : 14 wafer : 15
clusterSizeSideN222
Entries 0
Mean 0
RMS 0
size [# of strips]

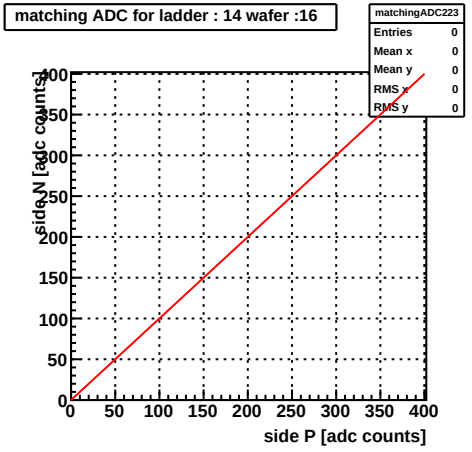

matching ADC for ladder : 14 wafer :15
matchingADC222
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

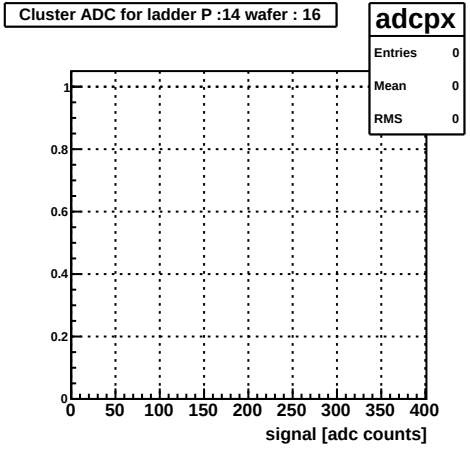

Cluster ADC for ladder P :14 wafer : 15
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

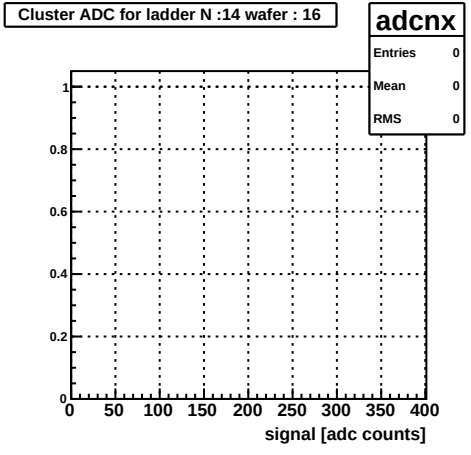

Cluster ADC for ladder N :14 wafer : 15
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

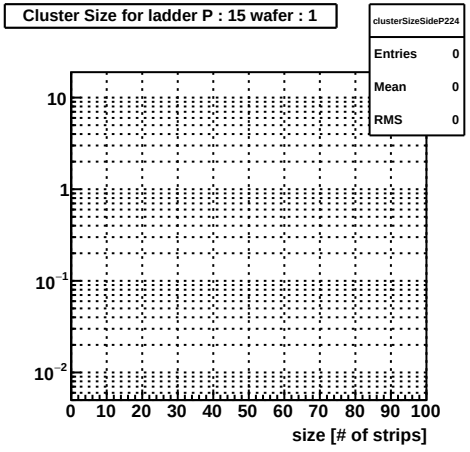

Cluster Size for ladder P : 14 wafer : 16
clusterSizeSideP223
Entries 0
Mean 0
RMS 0
size [# of strips]

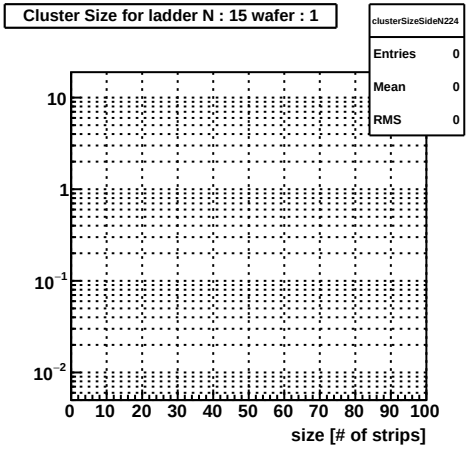

Cluster Size for ladder N : 14 wafer : 16
clusterSizeSideN223
Entries 0
Mean 0
RMS 0
size [# of strips]

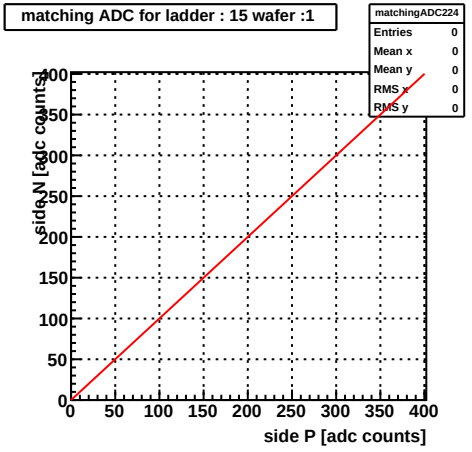

matching ADC for ladder : 14 wafer :16
matchingADC223
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

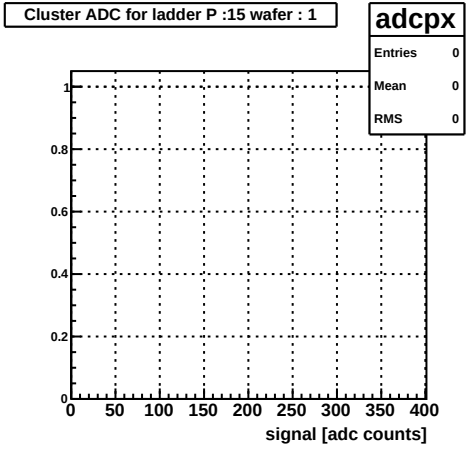

Cluster ADC for ladder P :14 wafer : 16
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

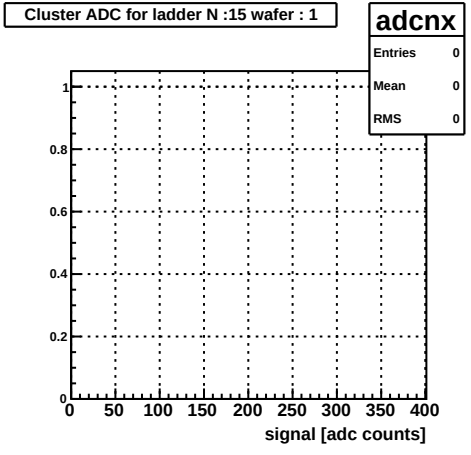

Cluster ADC for ladder N :14 wafer : 16
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

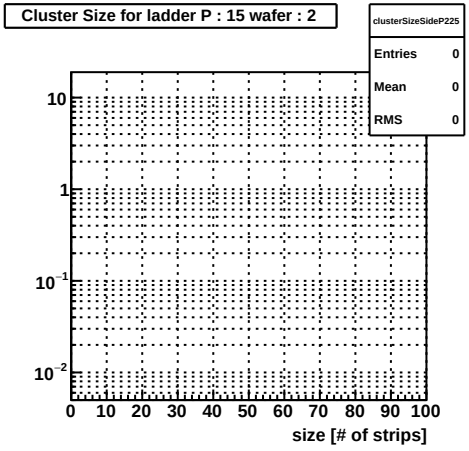

Cluster Size for ladder P : 15 wafer : 1
clusterSizeSideP224
Entries 0
Mean 0
RMS 0
size [# of strips]

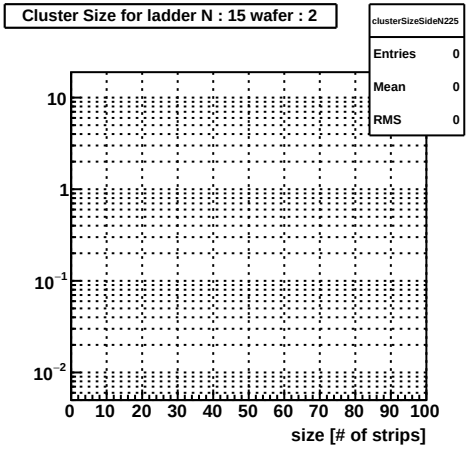

Cluster Size for ladder N : 15 wafer : 1
clusterSizeSideN224
Entries 0
Mean 0
RMS 0
size [# of strips]

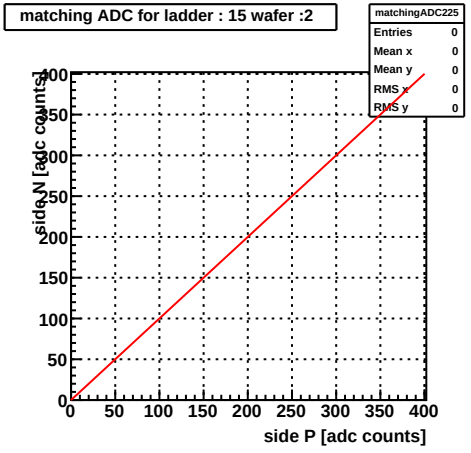

matching ADC for ladder : 15 wafer :1
matchingADC224
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

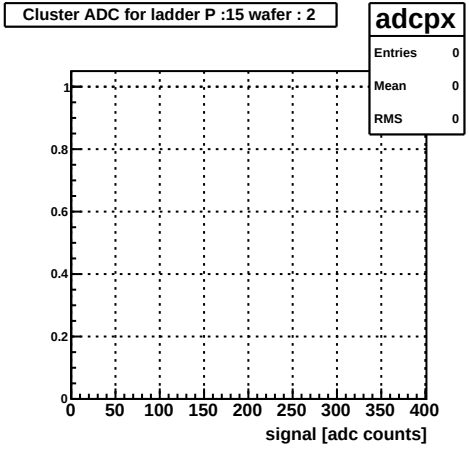

Cluster ADC for ladder P :15 wafer : 1
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

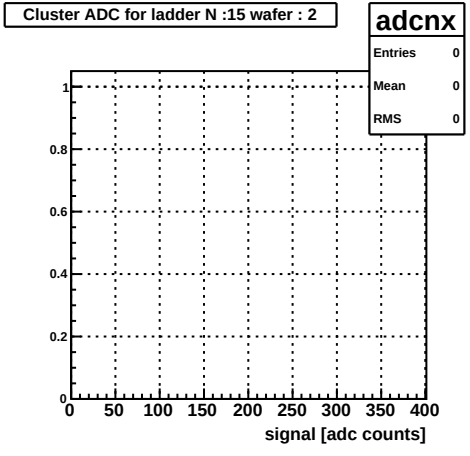

Cluster ADC for ladder N :15 wafer : 1
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

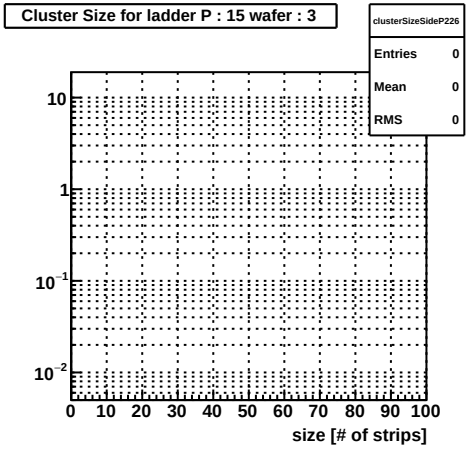

Cluster Size for ladder P : 15 wafer : 2
clusterSizeSideP225
Entries 0
Mean 0
RMS 0
size [# of strips]

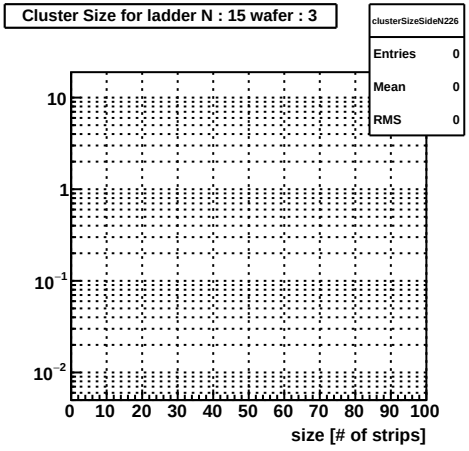

Cluster Size for ladder N : 15 wafer : 2
clusterSizeSideN225
Entries 0
Mean 0
RMS 0
size [# of strips]

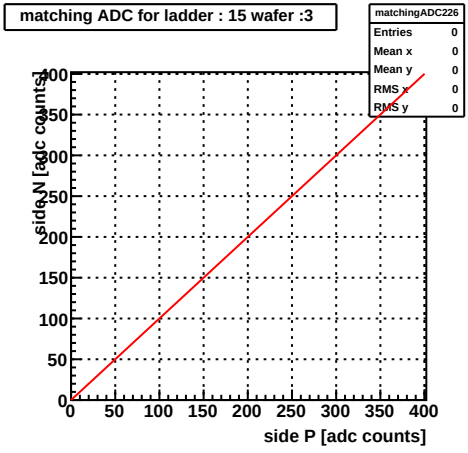

matching ADC for ladder : 15 wafer :2
matchingADC225
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

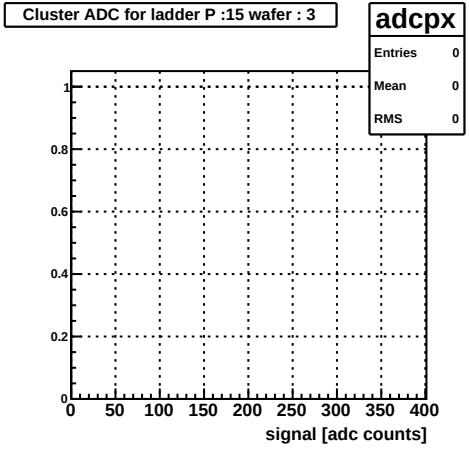

Cluster ADC for ladder P :15 wafer : 2
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

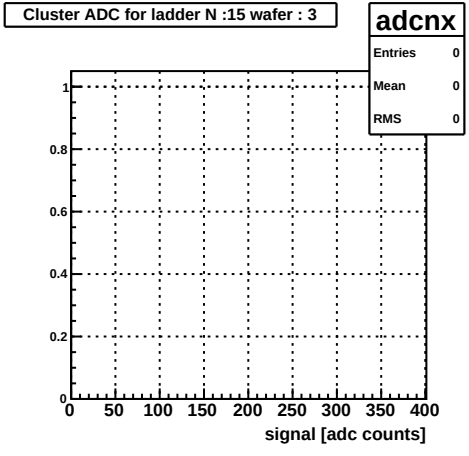

Cluster ADC for ladder N :15 wafer : 2
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

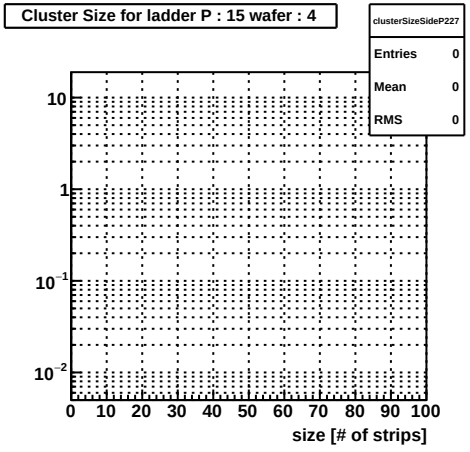

Cluster Size for ladder P : 15 wafer : 3
clusterSizeSideP226
Entries 0
Mean 0
RMS 0
size [# of strips]

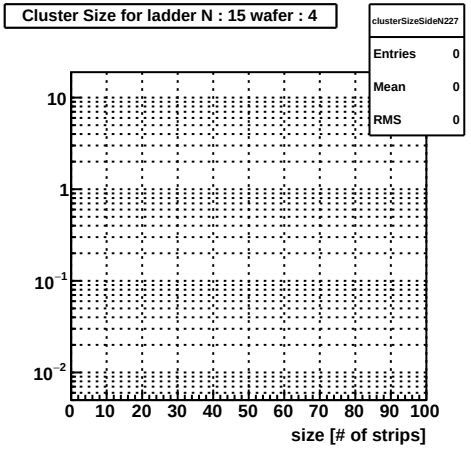

Cluster Size for ladder N : 15 wafer : 3
clusterSizeSideN226
Entries 0
Mean 0
RMS 0
size [# of strips]

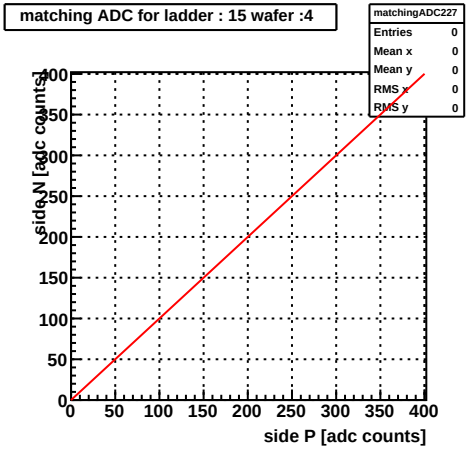

matching ADC for ladder : 15 wafer :3
matchingADC226
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

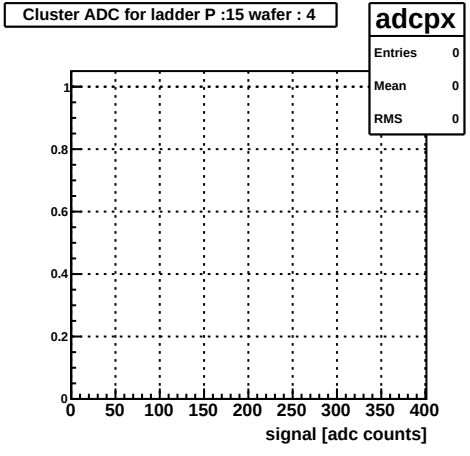

Cluster ADC for ladder P :15 wafer : 3
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

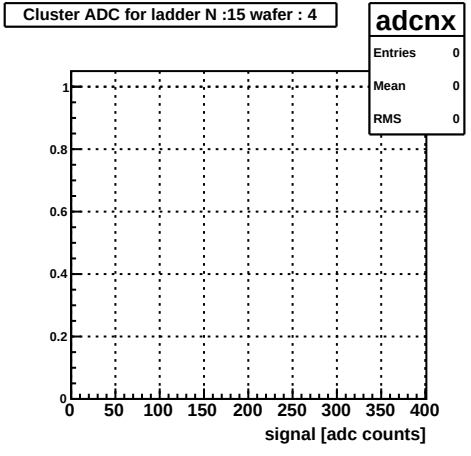

Cluster ADC for ladder N :15 wafer : 3
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

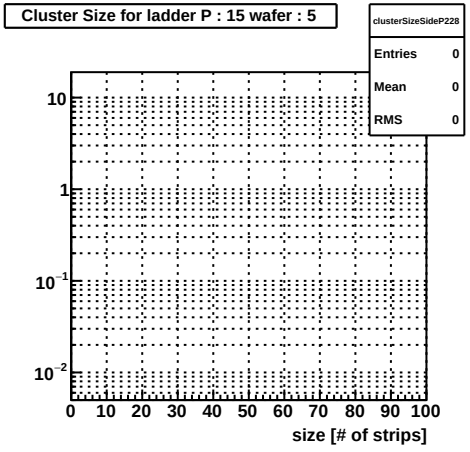

Cluster Size for ladder P : 15 wafer : 4
clusterSizeSideP227
Entries 0
Mean 0
RMS 0
size [# of strips]

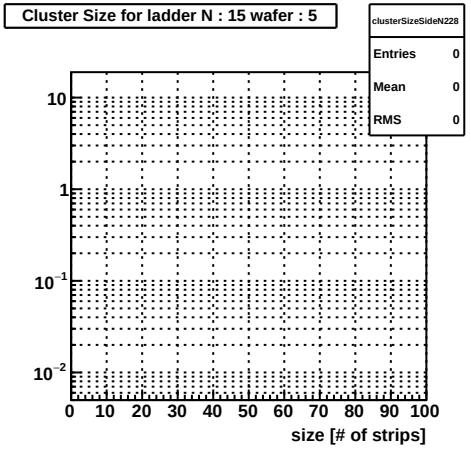

Cluster Size for ladder N : 15 wafer : 4
clusterSizeSideN227
Entries 0
Mean 0
RMS 0
size [# of strips]

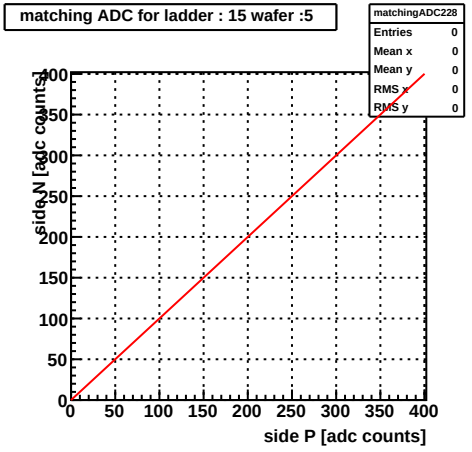

matching ADC for ladder : 15 wafer :4
matchingADC227
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

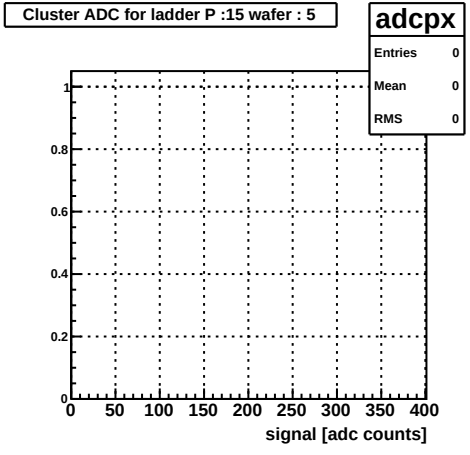

Cluster ADC for ladder P :15 wafer : 4
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

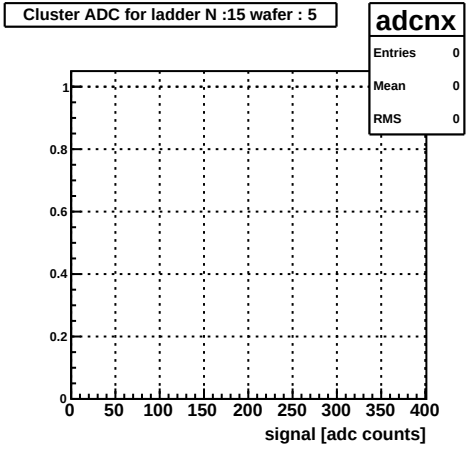

Cluster ADC for ladder N :15 wafer : 4
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

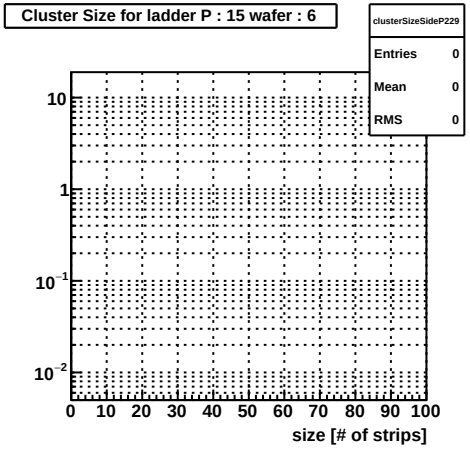

Cluster Size for ladder P : 15 wafer : 5
clusterSizeSideP228
Entries 0
Mean 0
RMS 0
size [# of strips]

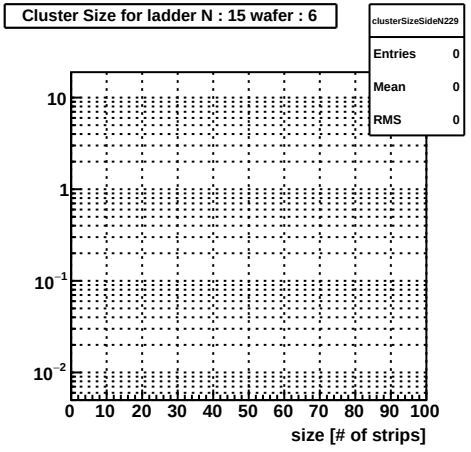

Cluster Size for ladder N : 15 wafer : 5
clusterSizeSideN228
Entries 0
Mean 0
RMS 0
size [# of strips]

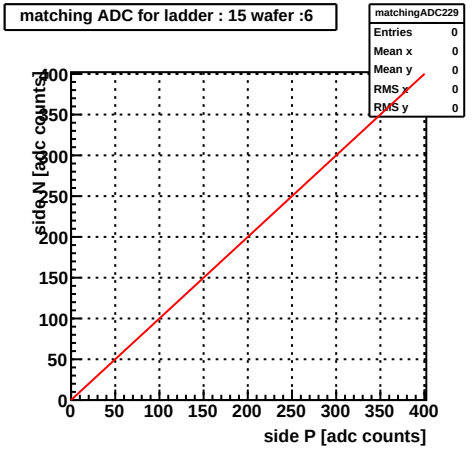

matching ADC for ladder : 15 wafer :5
matchingADC228
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

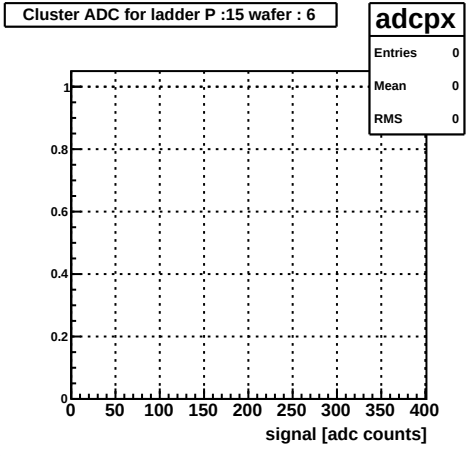

Cluster ADC for ladder P :15 wafer : 5
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

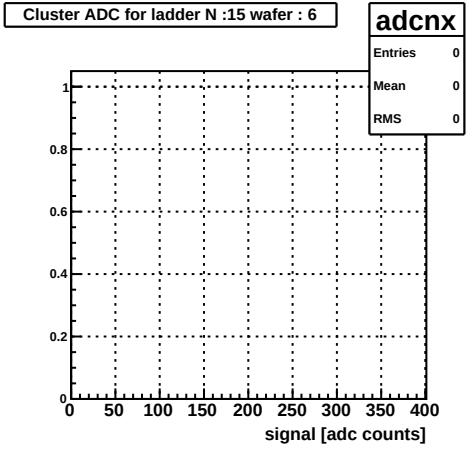

Cluster ADC for ladder N :15 wafer : 5
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

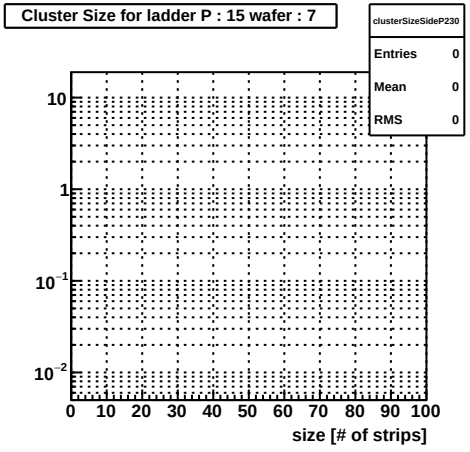

Cluster Size for ladder P : 15 wafer : 6
clusterSizeSideP229
Entries 0
Mean 0
RMS 0
size [# of strips]

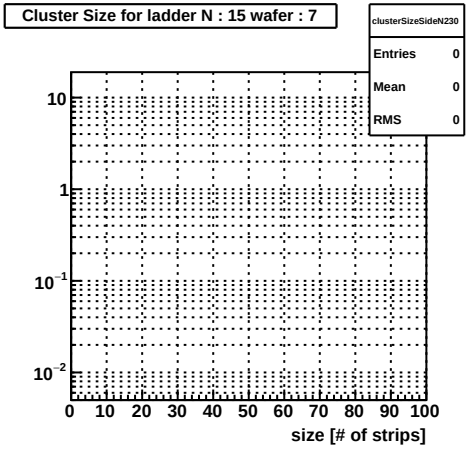

Cluster Size for ladder N : 15 wafer : 6
clusterSizeSideN229
Entries 0
Mean 0
RMS 0
size [# of strips]

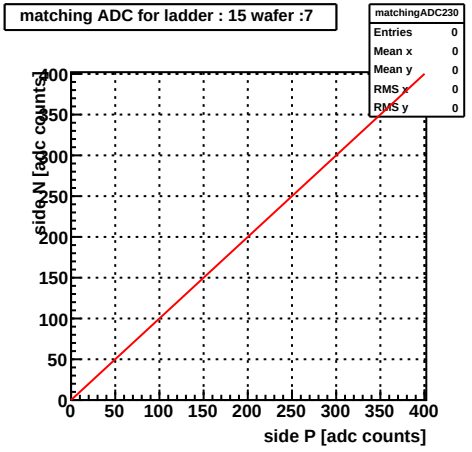

matching ADC for ladder : 15 wafer :6
matchingADC229
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

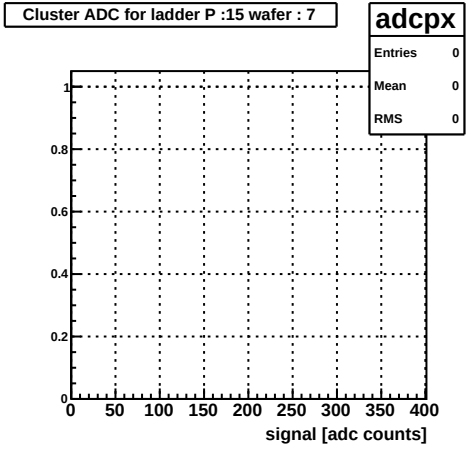

Cluster ADC for ladder P :15 wafer : 6
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

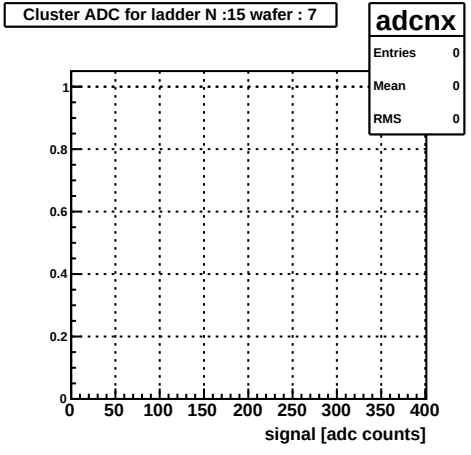

Cluster ADC for ladder N :15 wafer : 6
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

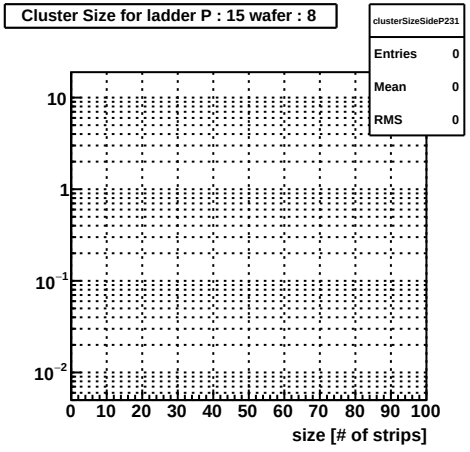

Cluster Size for ladder P : 15 wafer : 7
clusterSizeSideP230
Entries 0
Mean 0
RMS 0
size [# of strips]

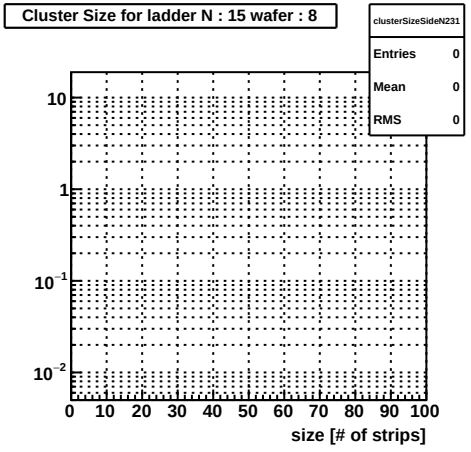

Cluster Size for ladder N : 15 wafer : 7
clusterSizeSideN230
Entries 0
Mean 0
RMS 0
size [# of strips]

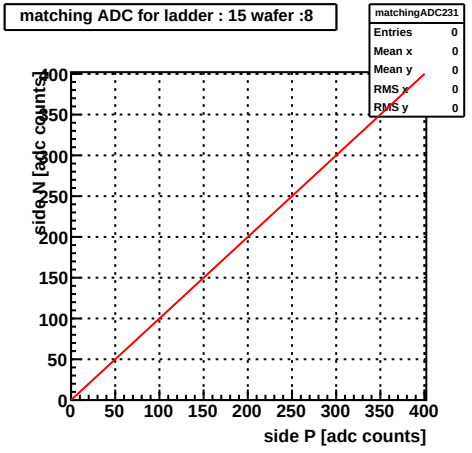

matching ADC for ladder : 15 wafer :7
matchingADC230
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

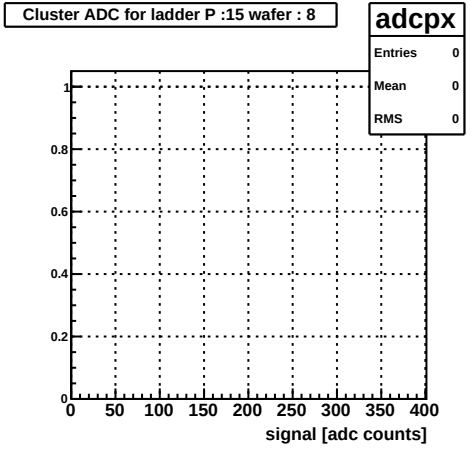

Cluster ADC for ladder P :15 wafer : 7
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

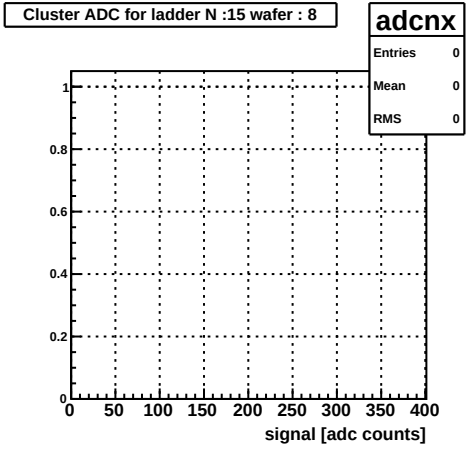

Cluster ADC for ladder N :15 wafer : 7
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

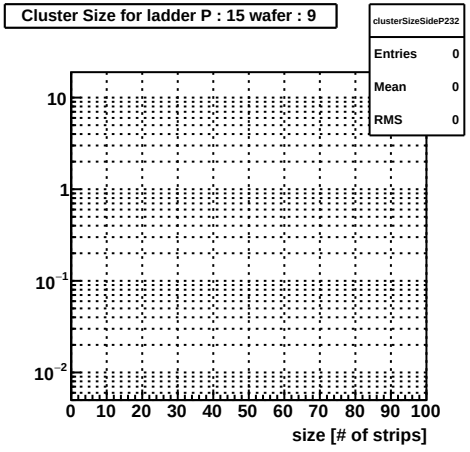

Cluster Size for ladder P : 15 wafer : 8
clusterSizeSideP231
Entries 0
Mean 0
RMS 0
size [# of strips]

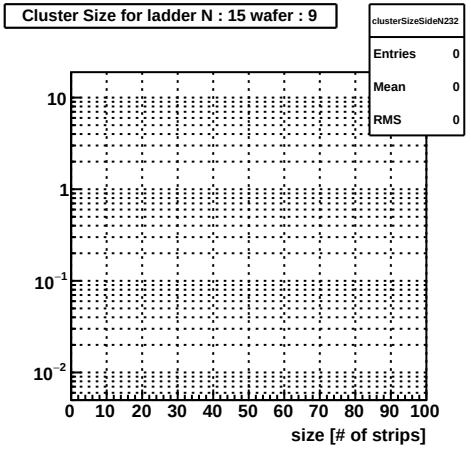

Cluster Size for ladder N : 15 wafer : 8
clusterSizeSideN231
Entries 0
Mean 0
RMS 0
size [# of strips]

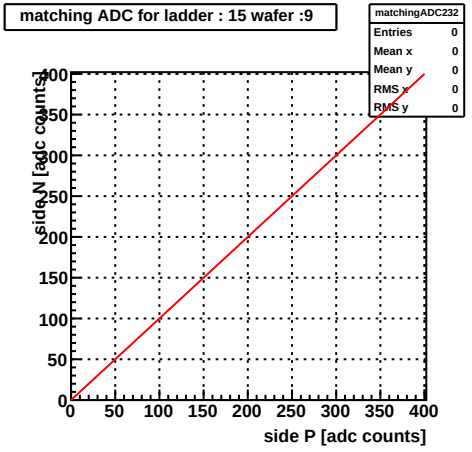

matching ADC for ladder : 15 wafer :8
matchingADC231
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

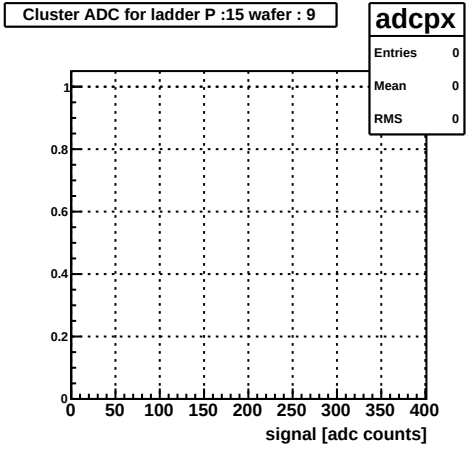

Cluster ADC for ladder P :15 wafer : 8
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

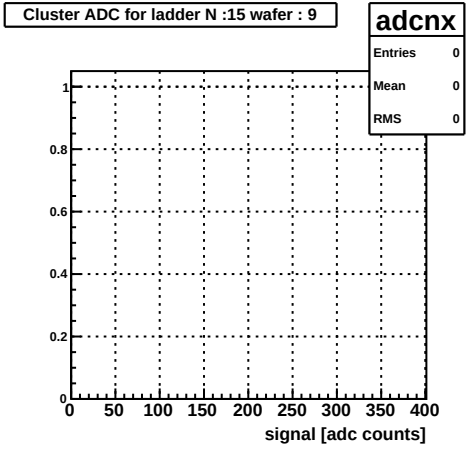

Cluster ADC for ladder N :15 wafer : 8
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

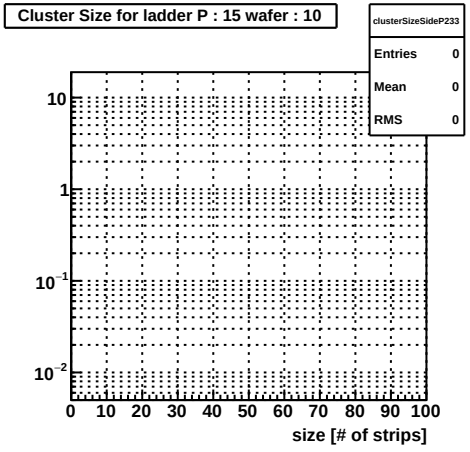

Cluster Size for ladder P : 15 wafer : 9
clusterSizeSideP232
Entries 0
Mean 0
RMS 0
size [# of strips]

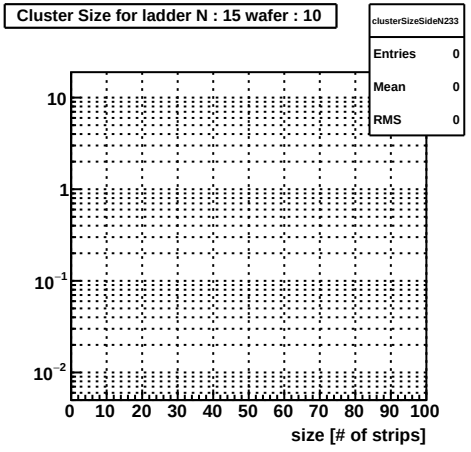

Cluster Size for ladder N : 15 wafer : 9
clusterSizeSideN232
Entries 0
Mean 0
RMS 0
size [# of strips]

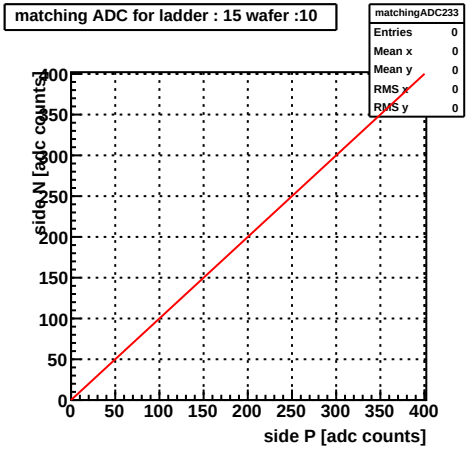

matching ADC for ladder : 15 wafer :9
matchingADC232
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

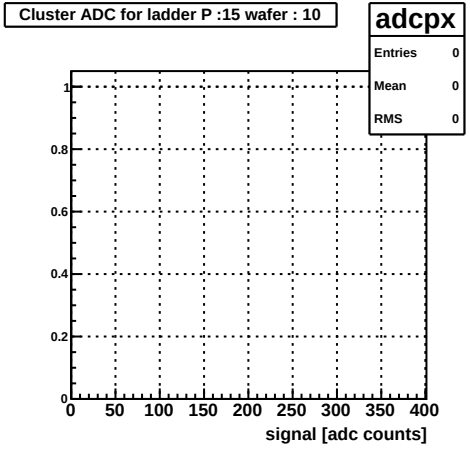

Cluster ADC for ladder P :15 wafer : 9
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

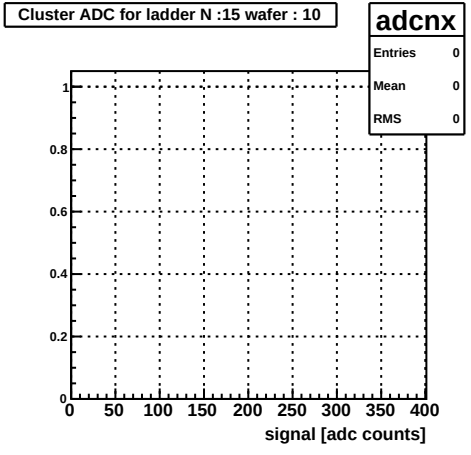

Cluster ADC for ladder N :15 wafer : 9
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

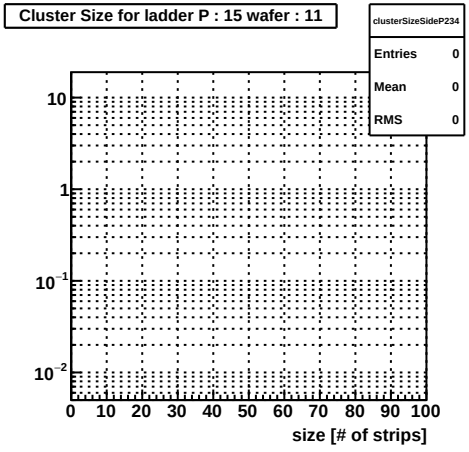

Cluster Size for ladder P : 15 wafer : 10
clusterSizeSideP233
Entries 0
Mean 0
RMS 0
size [# of strips]

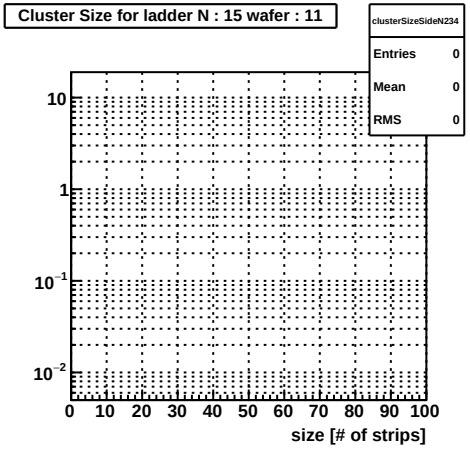

Cluster Size for ladder N : 15 wafer : 10
clusterSizeSideN233
Entries 0
Mean 0
RMS 0
size [# of strips]

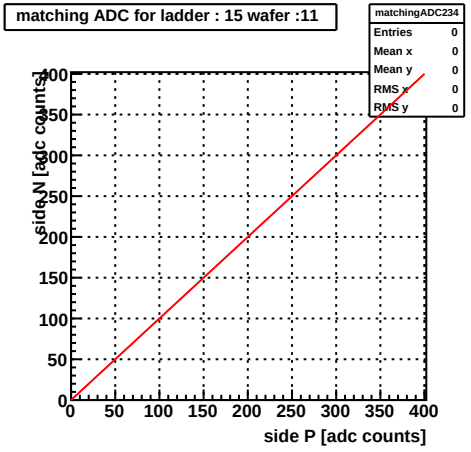

matching ADC for ladder : 15 wafer :10
matchingADC233
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

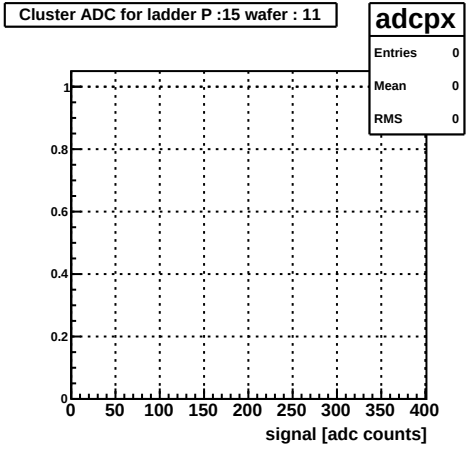

Cluster ADC for ladder P :15 wafer : 10
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

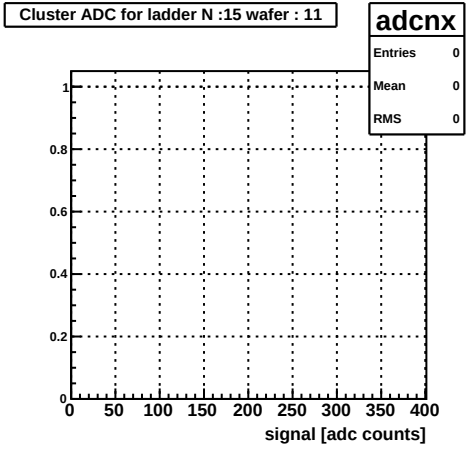

Cluster ADC for ladder N :15 wafer : 10
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

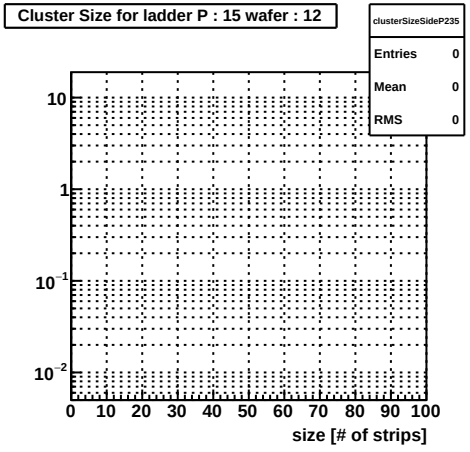

Cluster Size for ladder P : 15 wafer : 11
clusterSizeSideP234
Entries 0
Mean 0
RMS 0
size [# of strips]

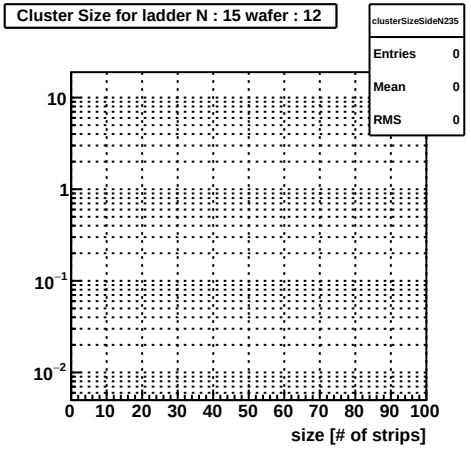

Cluster Size for ladder N : 15 wafer : 11
clusterSizeSideN234
Entries 0
Mean 0
RMS 0
size [# of strips]

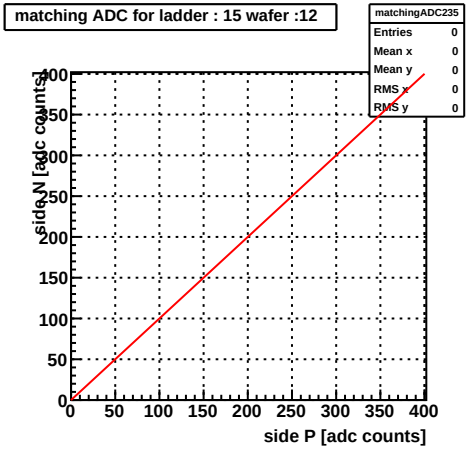

matching ADC for ladder : 15 wafer :11
matchingADC234
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

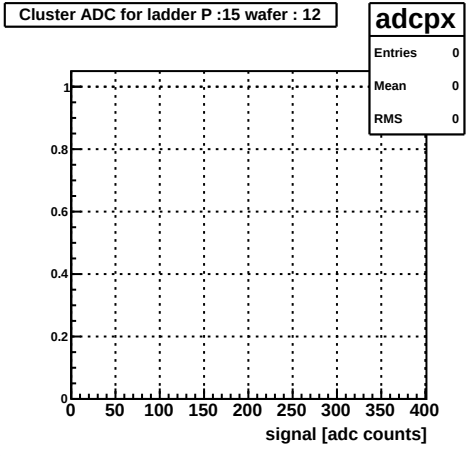

Cluster ADC for ladder P :15 wafer : 11
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

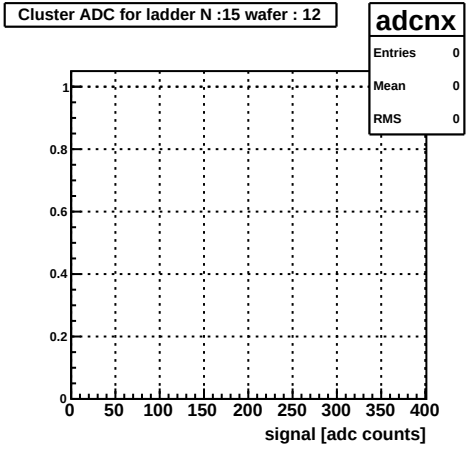

Cluster ADC for ladder N :15 wafer : 11
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

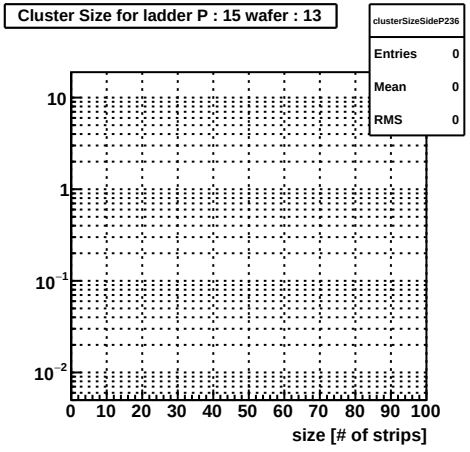

Cluster Size for ladder P : 15 wafer : 12
clusterSizeSideP235
Entries 0
Mean 0
RMS 0
size [# of strips]

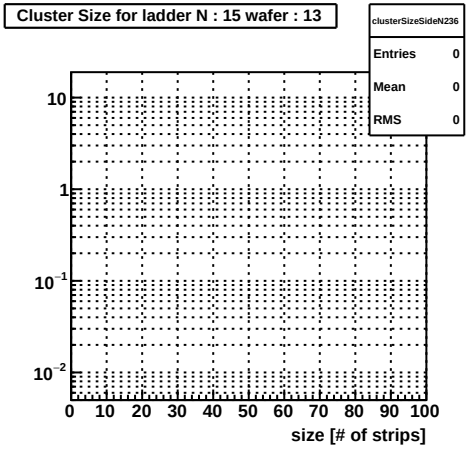

Cluster Size for ladder N : 15 wafer : 12
clusterSizeSideN235
Entries 0
Mean 0
RMS 0
size [# of strips]

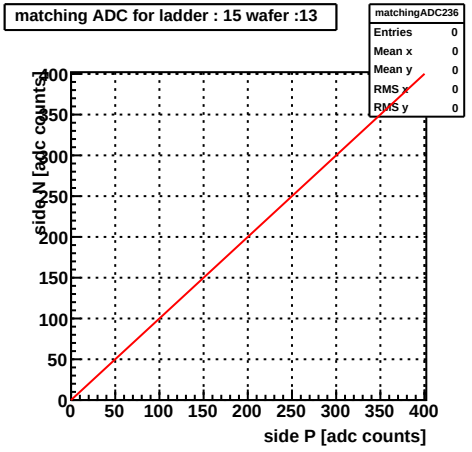

matching ADC for ladder : 15 wafer :12
matchingADC235
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

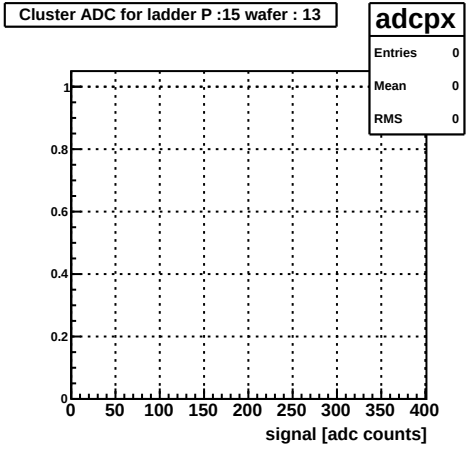

Cluster ADC for ladder P :15 wafer : 12
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

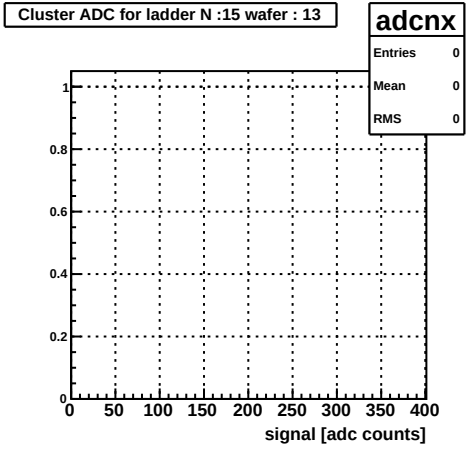

Cluster ADC for ladder N :15 wafer : 12
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

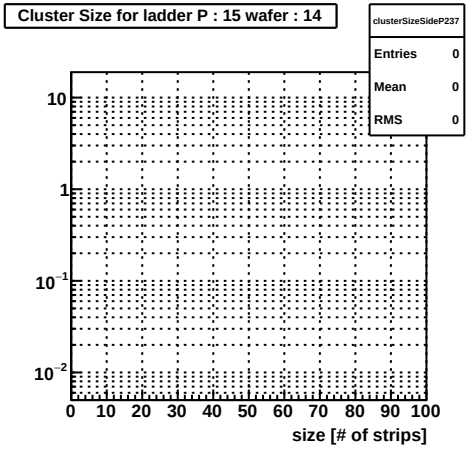

Cluster Size for ladder P : 15 wafer : 13
clusterSizeSideP236
Entries 0
Mean 0
RMS 0
size [# of strips]

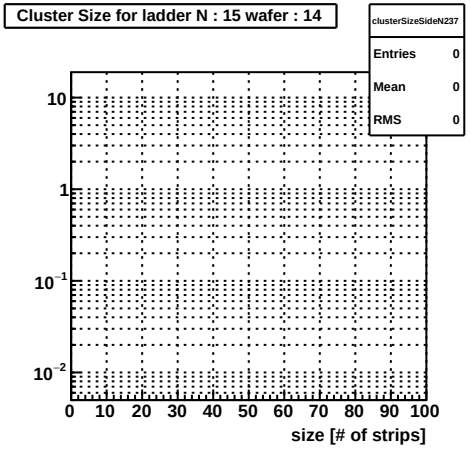

Cluster Size for ladder N : 15 wafer : 13
clusterSizeSideN236
Entries 0
Mean 0
RMS 0
size [# of strips]

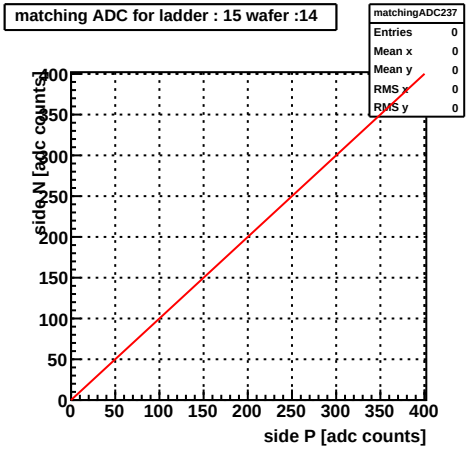

matching ADC for ladder : 15 wafer :13
matchingADC236
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

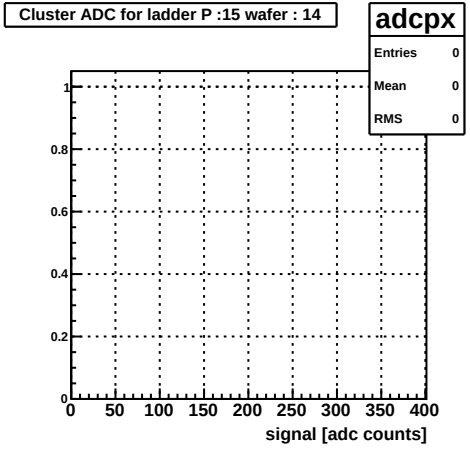

Cluster ADC for ladder P :15 wafer : 13
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

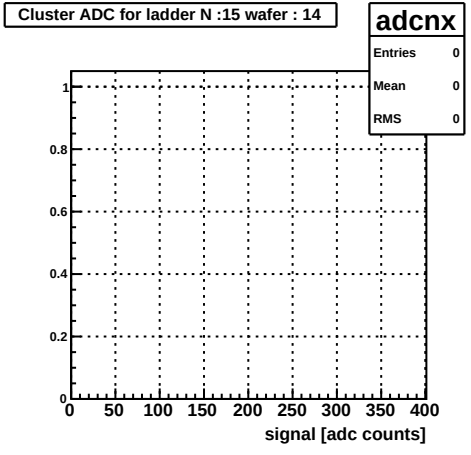

Cluster ADC for ladder N :15 wafer : 13
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

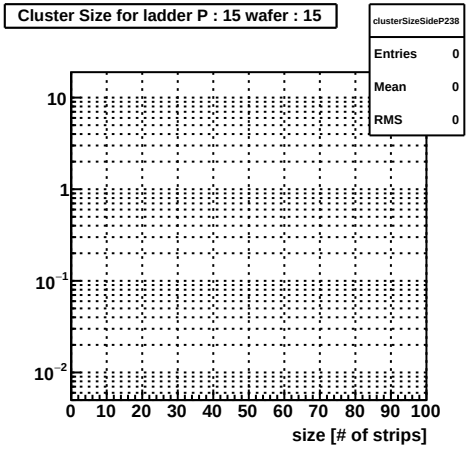

Cluster Size for ladder P : 15 wafer : 14
clusterSizeSideP237
Entries 0
Mean 0
RMS 0
size [# of strips]

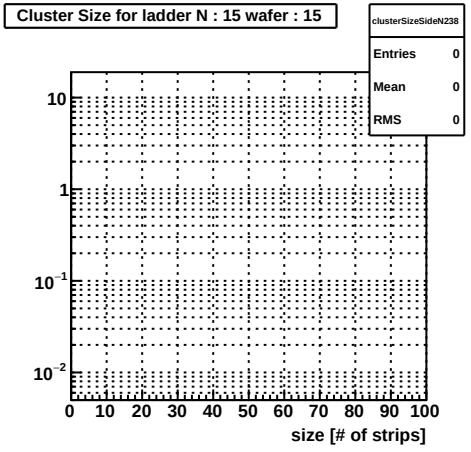

Cluster Size for ladder N : 15 wafer : 14
clusterSizeSideN237
Entries 0
Mean 0
RMS 0
size [# of strips]

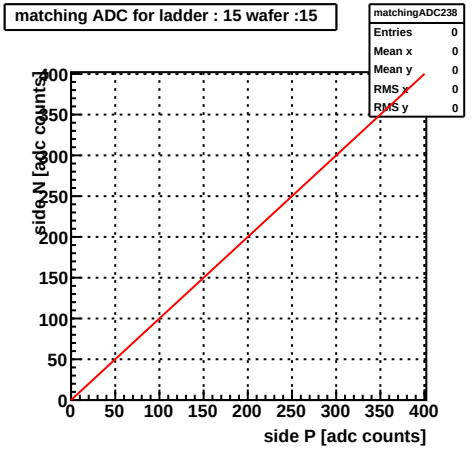

matching ADC for ladder : 15 wafer :14
matchingADC237
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

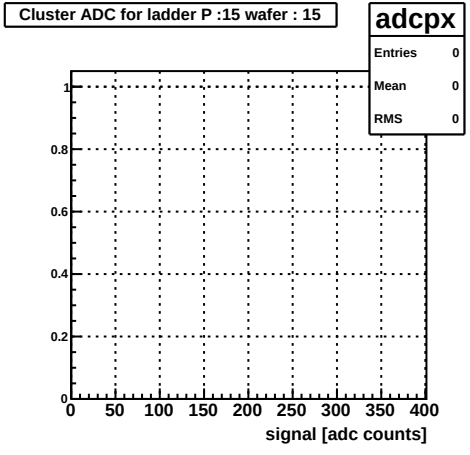

Cluster ADC for ladder P :15 wafer : 14
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

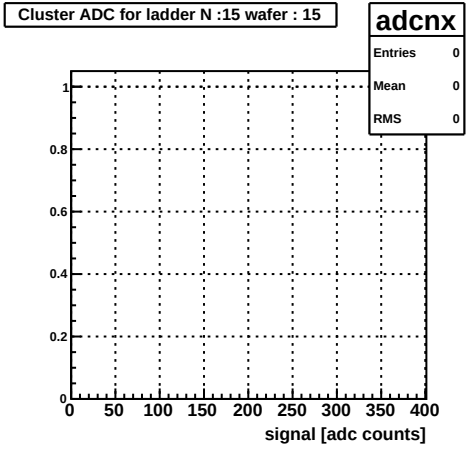

Cluster ADC for ladder N :15 wafer : 14
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

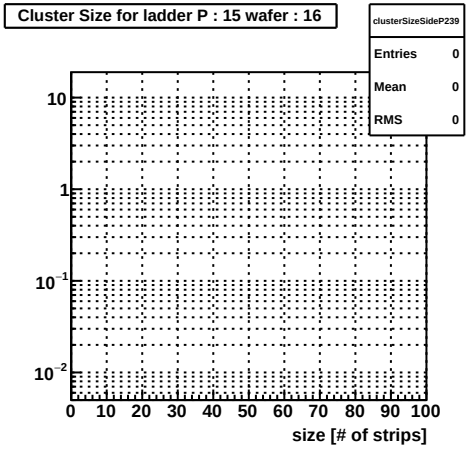

Cluster Size for ladder P : 15 wafer : 15
clusterSizeSideP238
Entries 0
Mean 0
RMS 0
size [# of strips]

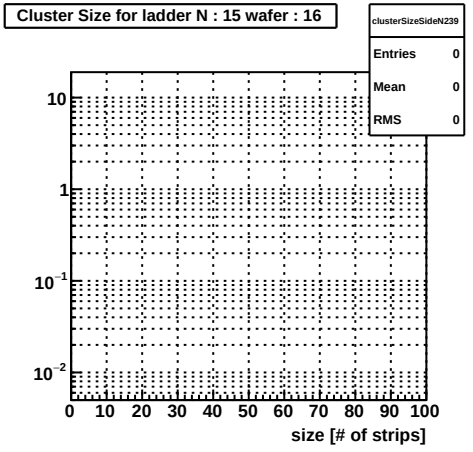

Cluster Size for ladder N : 15 wafer : 15
clusterSizeSideN238
Entries 0
Mean 0
RMS 0
size [# of strips]

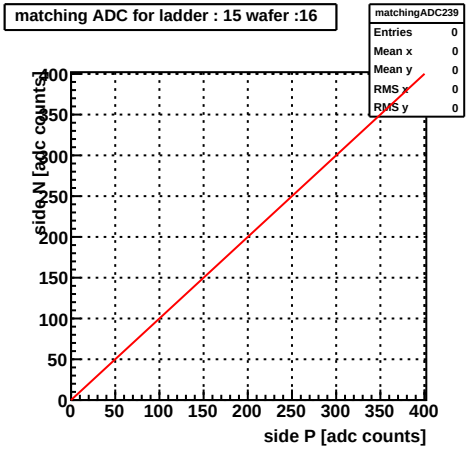

matching ADC for ladder : 15 wafer :15
matchingADC238
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

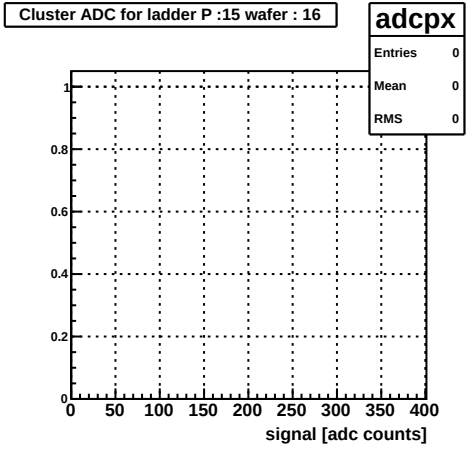

Cluster ADC for ladder P :15 wafer : 15
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

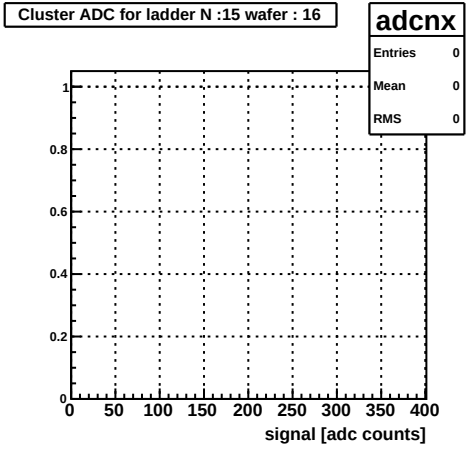

Cluster ADC for ladder N :15 wafer : 15
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

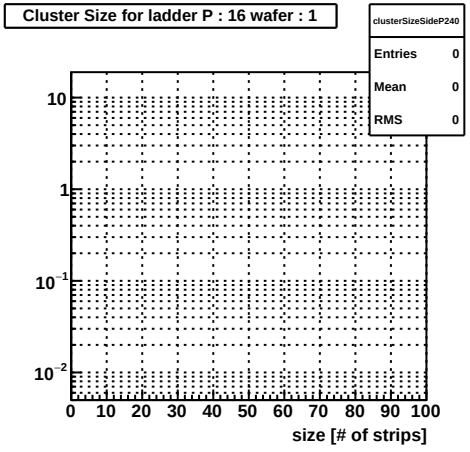

Cluster Size for ladder P : 15 wafer : 16
clusterSizeSideP239
Entries 0
Mean 0
RMS 0
size [# of strips]

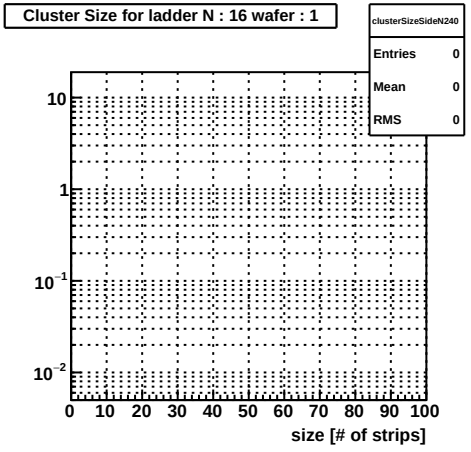

Cluster Size for ladder N : 15 wafer : 16
clusterSizeSideN239
Entries 0
Mean 0
RMS 0
size [# of strips]

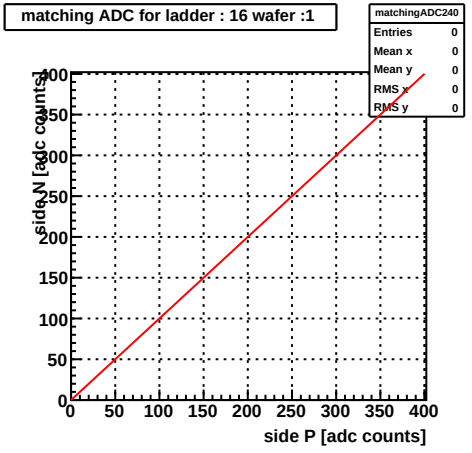

matching ADC for ladder : 15 wafer :16
matchingADC239
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

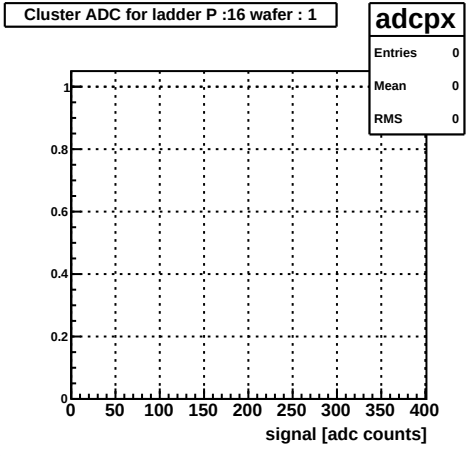

Cluster ADC for ladder P :15 wafer : 16
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

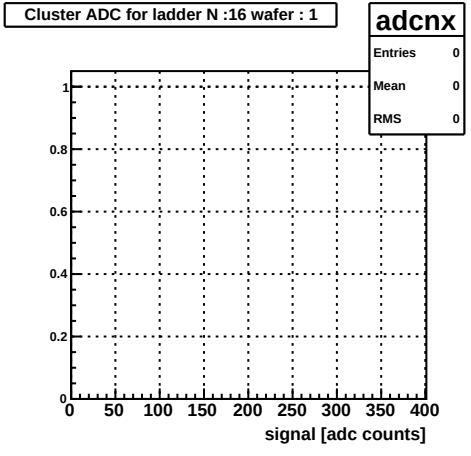

Cluster ADC for ladder N :15 wafer : 16
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

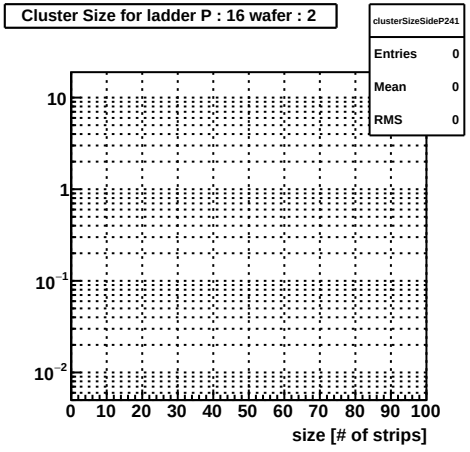

Cluster Size for ladder P : 16 wafer : 1
clusterSizeSideP240
Entries 0
Mean 0
RMS 0
size [# of strips]

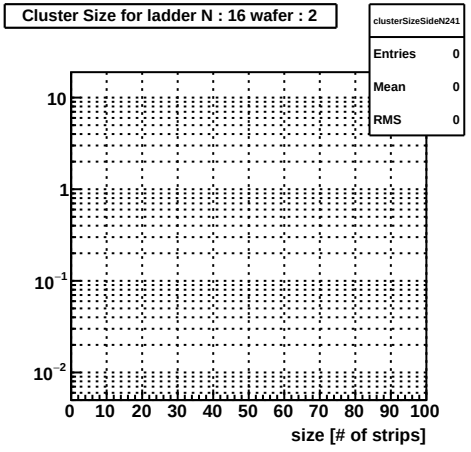

Cluster Size for ladder N : 16 wafer : 1
clusterSizeSideN240
Entries 0
Mean 0
RMS 0
size [# of strips]

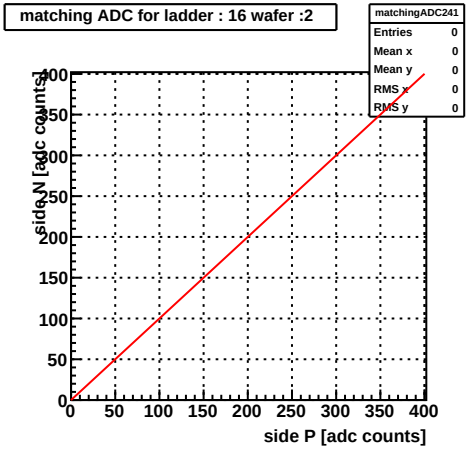

matching ADC for ladder : 16 wafer :1
matchingADC240
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

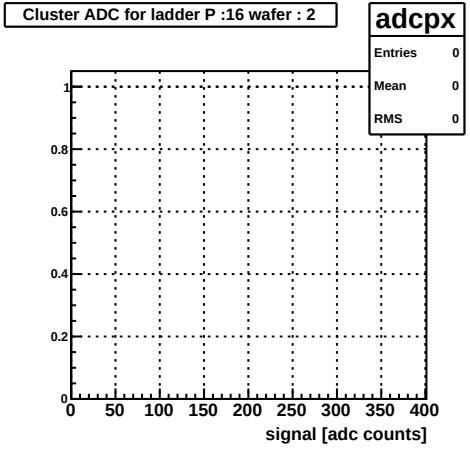

Cluster ADC for ladder P :16 wafer : 1
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

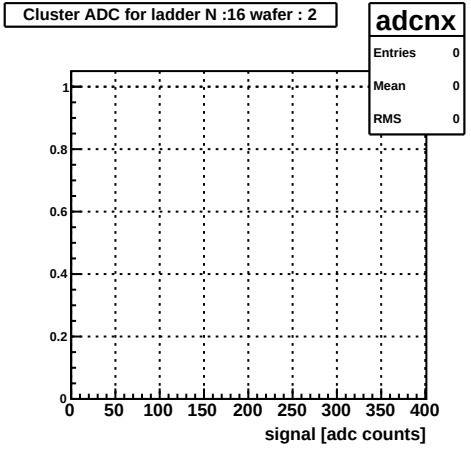

Cluster ADC for ladder N :16 wafer : 1
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

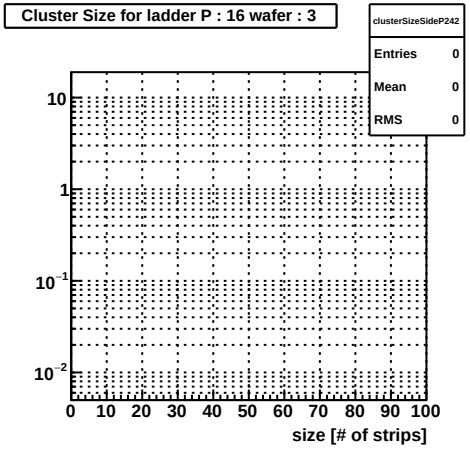

Cluster Size for ladder P : 16 wafer : 2
clusterSizeSideP241
Entries 0
Mean 0
RMS 0
size [# of strips]

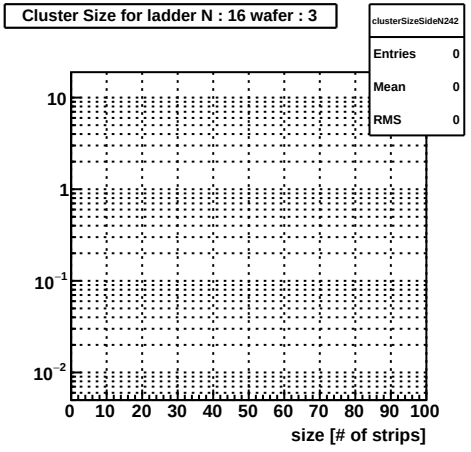

Cluster Size for ladder N : 16 wafer : 2
clusterSizeSideN241
Entries 0
Mean 0
RMS 0
size [# of strips]

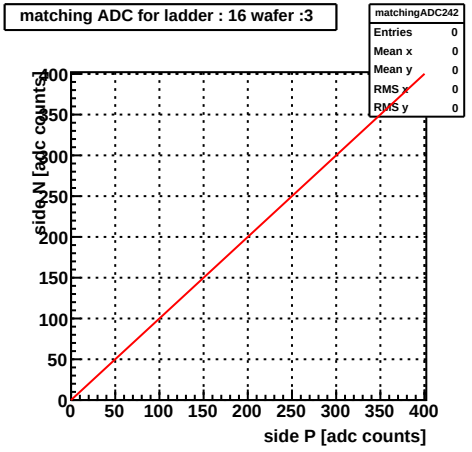

matching ADC for ladder : 16 wafer :2
matchingADC241
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

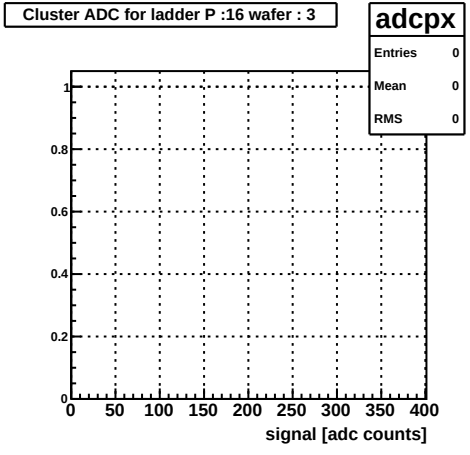

Cluster ADC for ladder P :16 wafer : 2
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

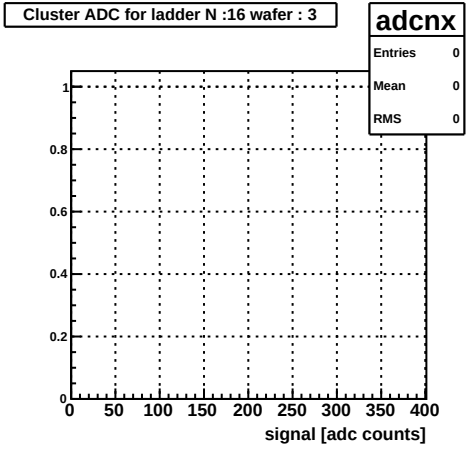

Cluster ADC for ladder N :16 wafer : 2
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

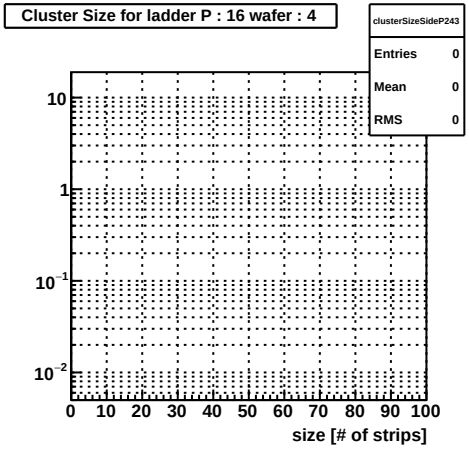

Cluster Size for ladder P : 16 wafer : 3
clusterSizeSideP242
Entries 0
Mean 0
RMS 0
size [# of strips]

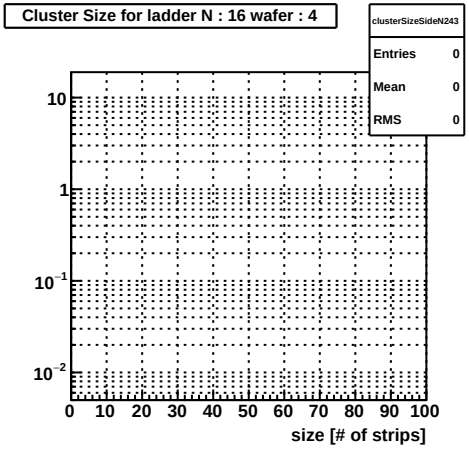

Cluster Size for ladder N : 16 wafer : 3
clusterSizeSideN242
Entries 0
Mean 0
RMS 0
size [# of strips]

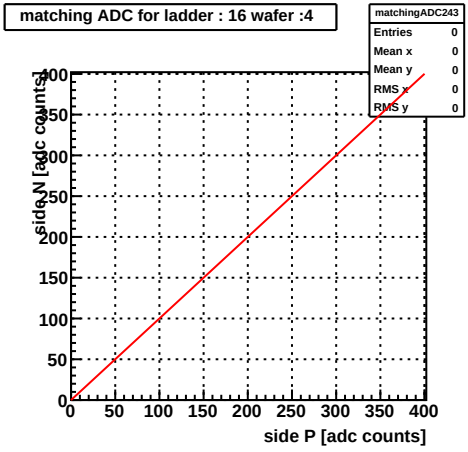

matching ADC for ladder : 16 wafer :3
matchingADC242
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

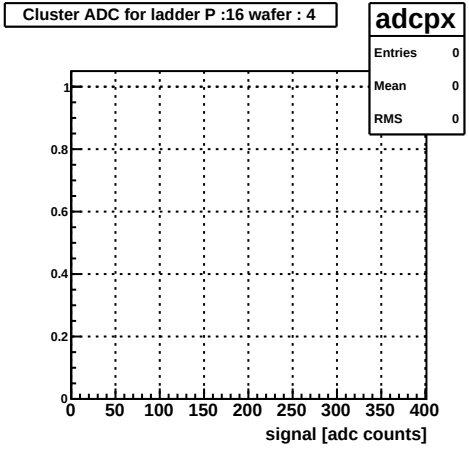

Cluster ADC for ladder P :16 wafer : 3
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

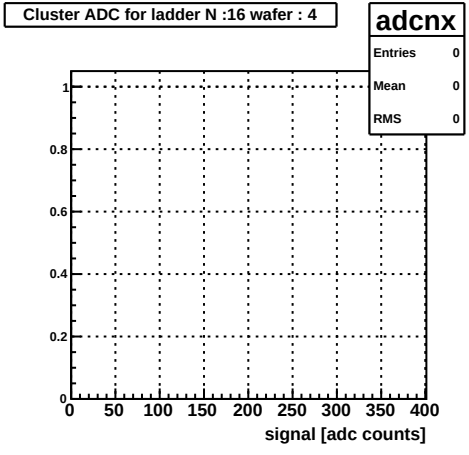

Cluster ADC for ladder N :16 wafer : 3
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

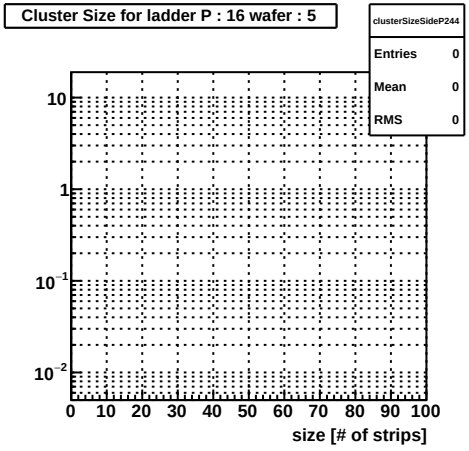

Cluster Size for ladder P : 16 wafer : 4
clusterSizeSideP243
Entries 0
Mean 0
RMS 0
size [# of strips]

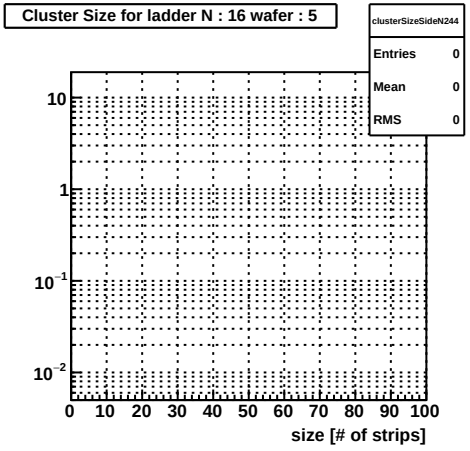

Cluster Size for ladder N : 16 wafer : 4
clusterSizeSideN243
Entries 0
Mean 0
RMS 0
size [# of strips]

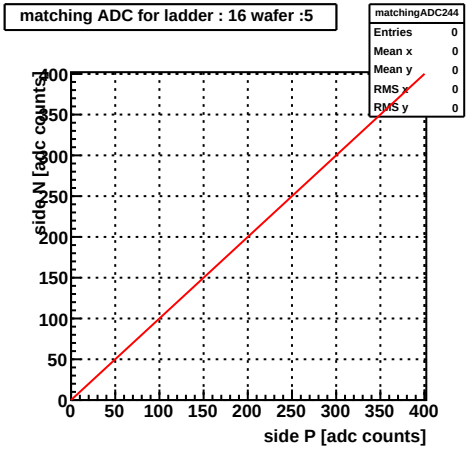

matching ADC for ladder : 16 wafer :4
matchingADC243
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

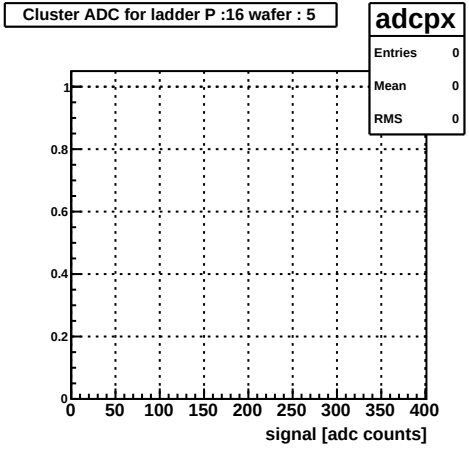

Cluster ADC for ladder P :16 wafer : 4
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

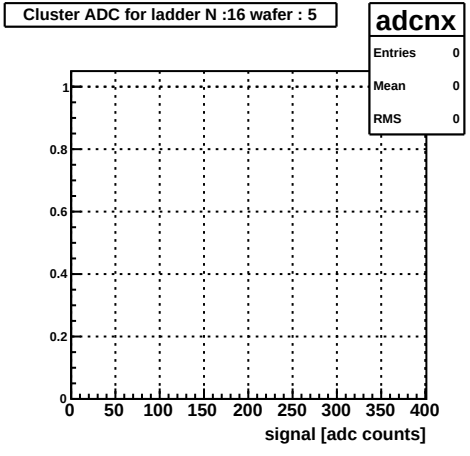

Cluster ADC for ladder N :16 wafer : 4
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

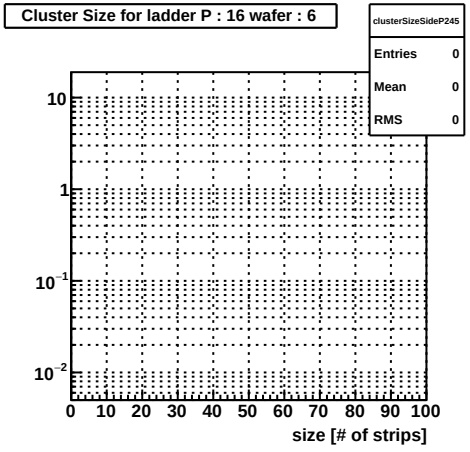

Cluster Size for ladder P : 16 wafer : 5
clusterSizeSideP244
Entries 0
Mean 0
RMS 0
size [# of strips]

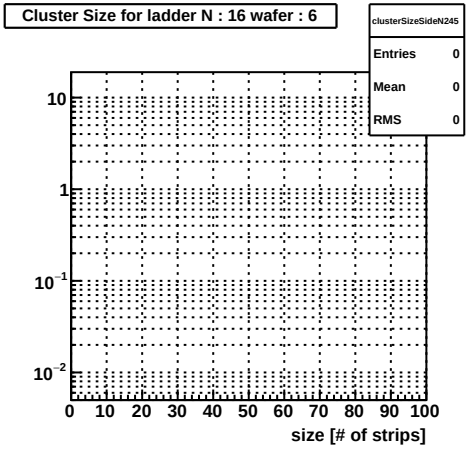

Cluster Size for ladder N : 16 wafer : 5
clusterSizeSideN244
Entries 0
Mean 0
RMS 0
size [# of strips]

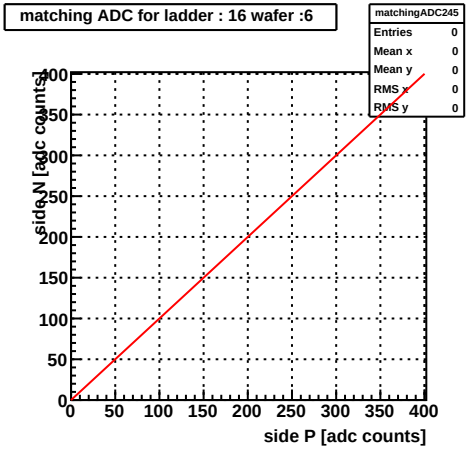

matching ADC for ladder : 16 wafer :5
matchingADC244
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

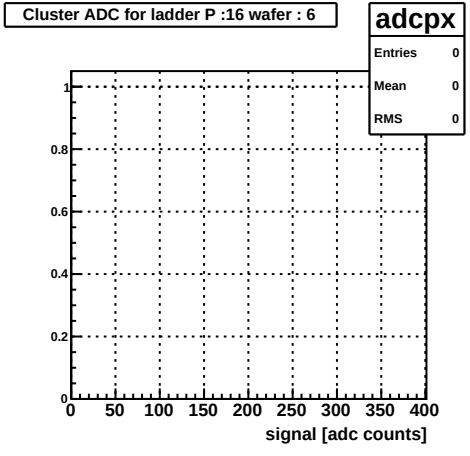

Cluster ADC for ladder P :16 wafer : 5
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

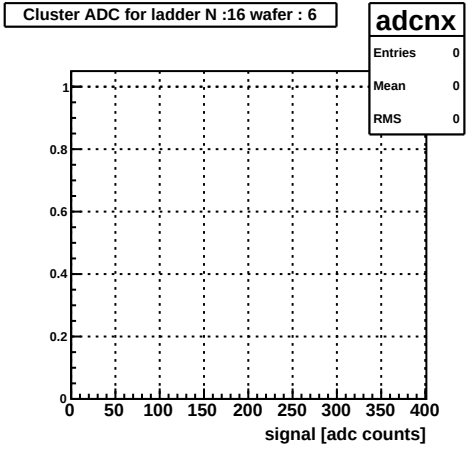

Cluster ADC for ladder N :16 wafer : 5
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

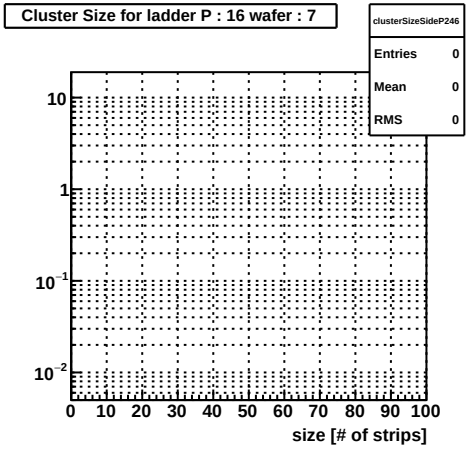

Cluster Size for ladder P : 16 wafer : 6
clusterSizeSideP245
Entries 0
Mean 0
RMS 0
size [# of strips]

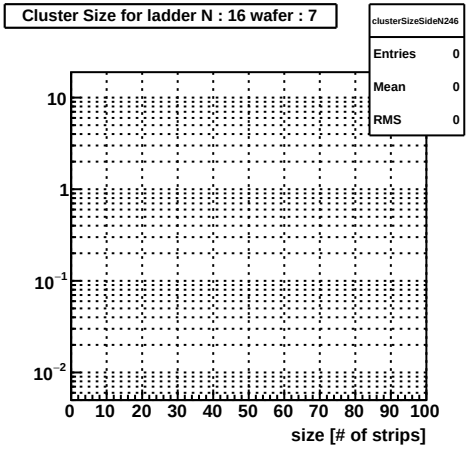

Cluster Size for ladder N : 16 wafer : 6
clusterSizeSideN245
Entries 0
Mean 0
RMS 0
size [# of strips]

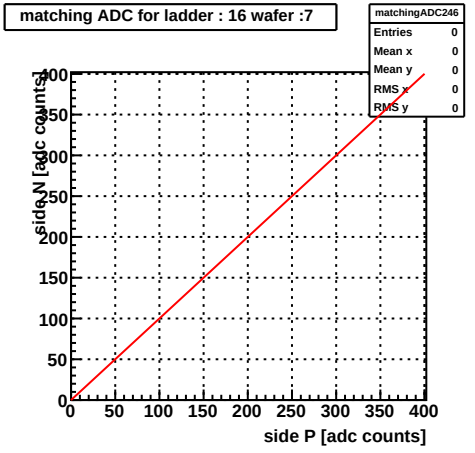

matching ADC for ladder : 16 wafer :6
matchingADC245
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

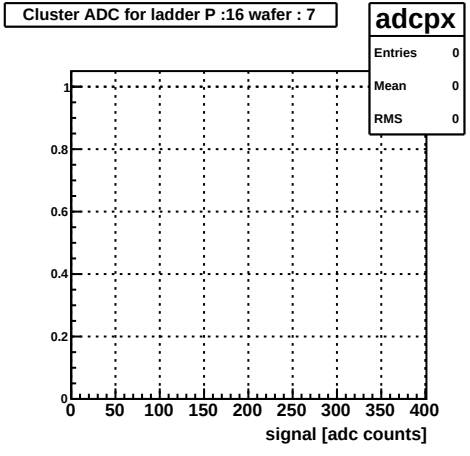

Cluster ADC for ladder P :16 wafer : 6
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

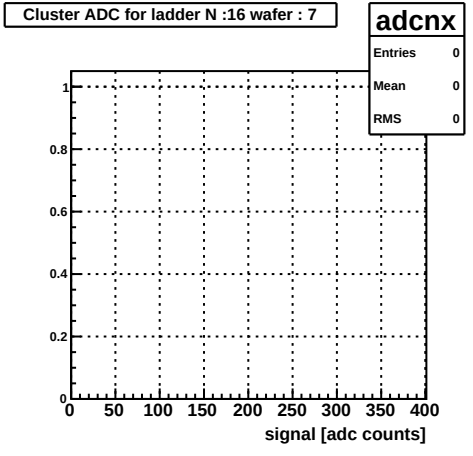

Cluster ADC for ladder N :16 wafer : 6
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

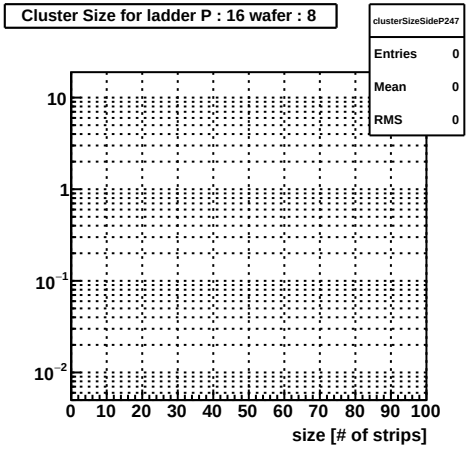

Cluster Size for ladder P : 16 wafer : 7
clusterSizeSideP246
Entries 0
Mean 0
RMS 0
size [# of strips]

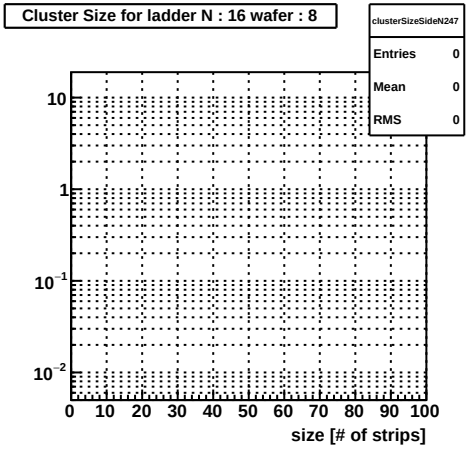

Cluster Size for ladder N : 16 wafer : 7
clusterSizeSideN246
Entries 0
Mean 0
RMS 0
size [# of strips]

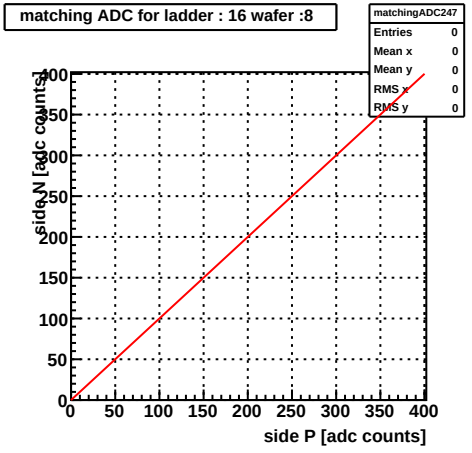

matching ADC for ladder : 16 wafer :7
matchingADC246
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

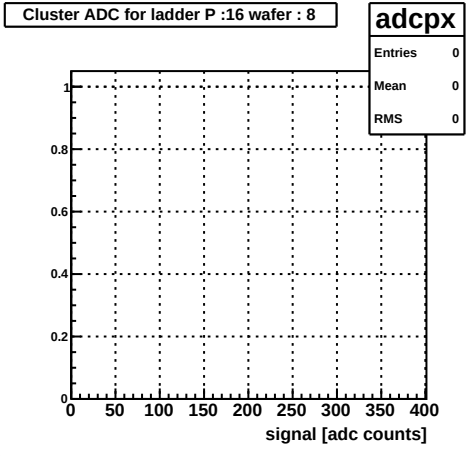

Cluster ADC for ladder P :16 wafer : 7
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

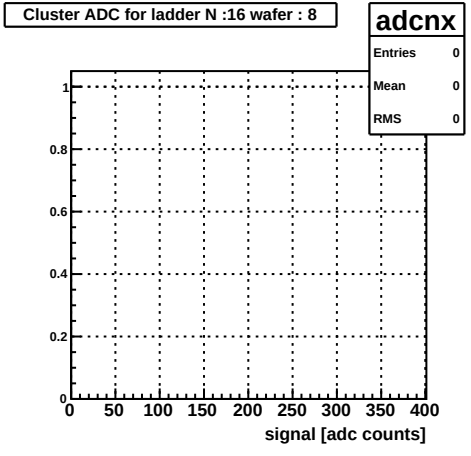

Cluster ADC for ladder N :16 wafer : 7
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

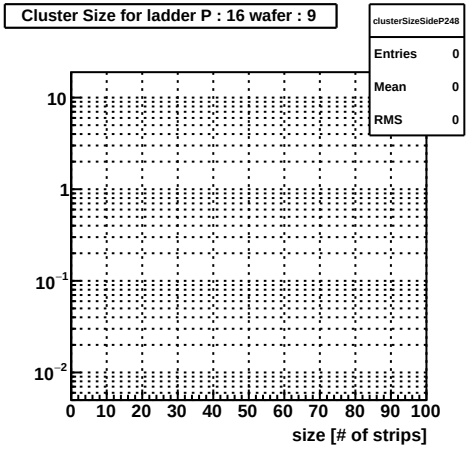

Cluster Size for ladder P : 16 wafer : 8
clusterSizeSideP247
Entries 0
Mean 0
RMS 0
size [# of strips]

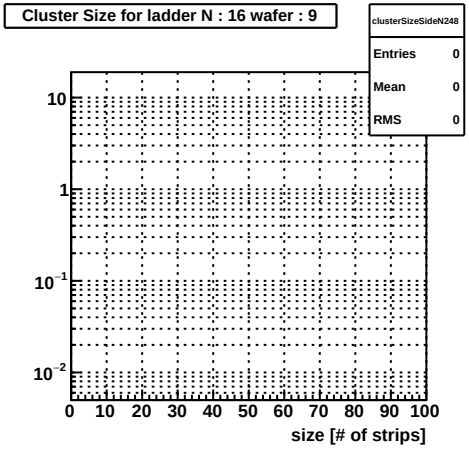

Cluster Size for ladder N : 16 wafer : 8
clusterSizeSideN247
Entries 0
Mean 0
RMS 0
size [# of strips]

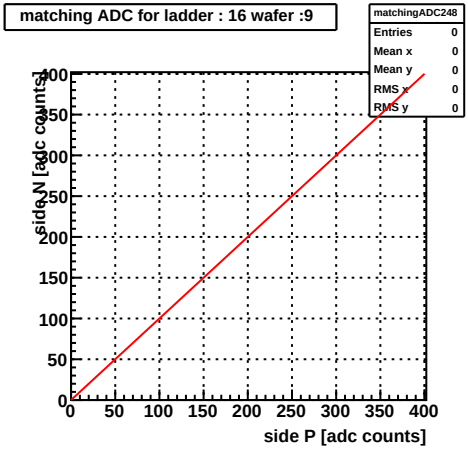

matching ADC for ladder : 16 wafer :8
matchingADC247
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

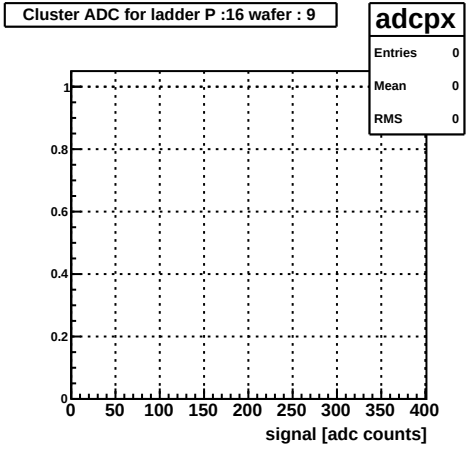

Cluster ADC for ladder P :16 wafer : 8
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

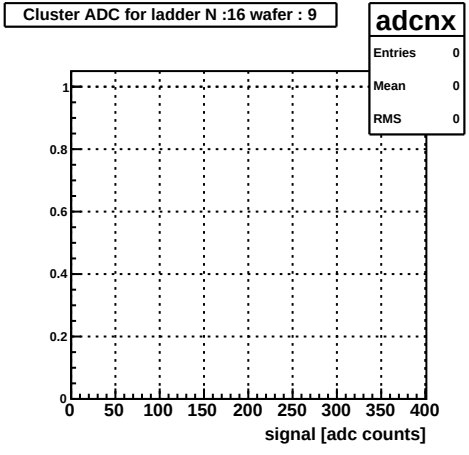

Cluster ADC for ladder N :16 wafer : 8
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

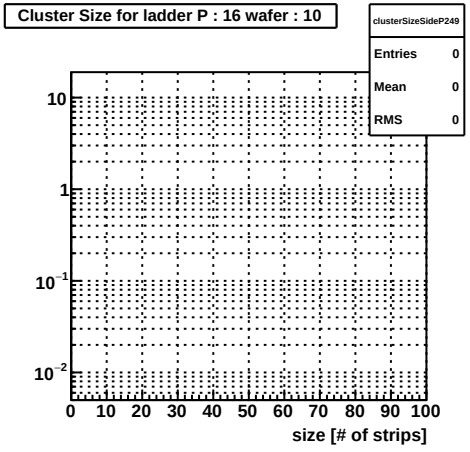

Cluster Size for ladder P : 16 wafer : 9
clusterSizeSideP248
Entries 0
Mean 0
RMS 0
size [# of strips]

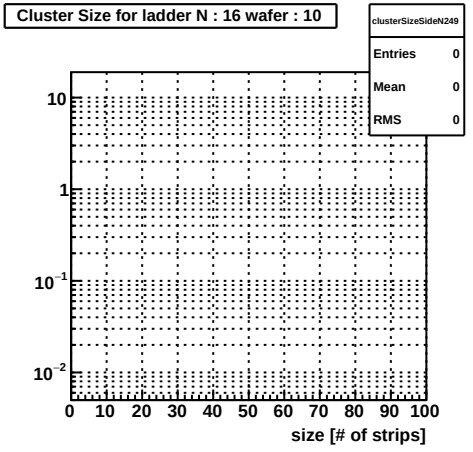

Cluster Size for ladder N : 16 wafer : 9
clusterSizeSideN248
Entries 0
Mean 0
RMS 0
size [# of strips]

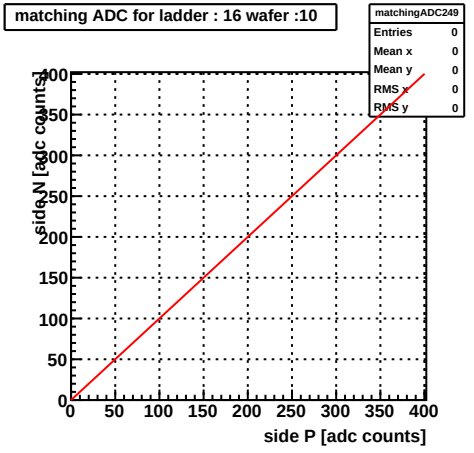

matching ADC for ladder : 16 wafer :9
matchingADC248
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

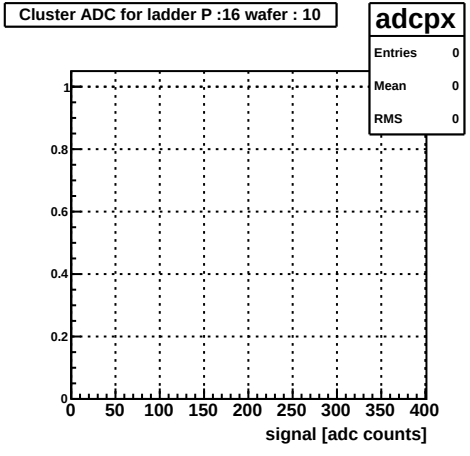

Cluster ADC for ladder P :16 wafer : 9
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

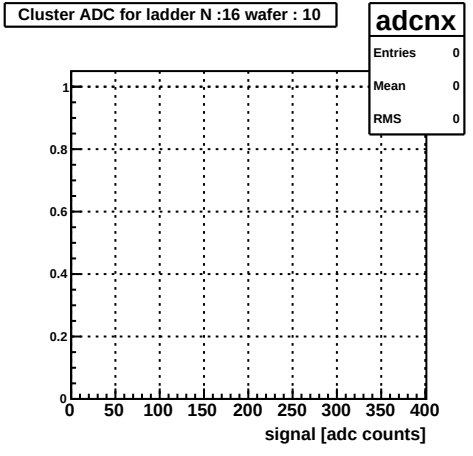

Cluster ADC for ladder N :16 wafer : 9
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

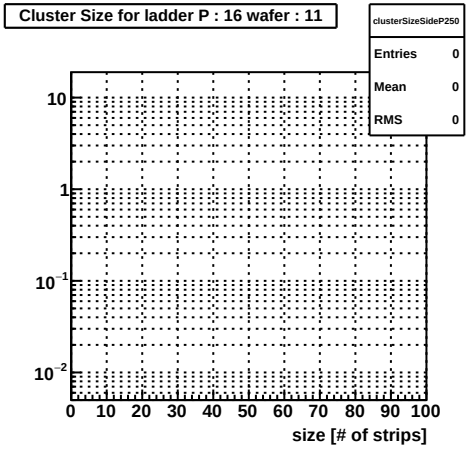

Cluster Size for ladder P : 16 wafer : 10
clusterSizeSideP249
Entries 0
Mean 0
RMS 0
size [# of strips]

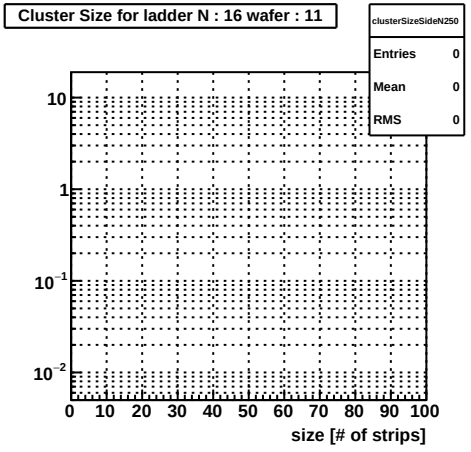

Cluster Size for ladder N : 16 wafer : 10
clusterSizeSideN249
Entries 0
Mean 0
RMS 0
size [# of strips]

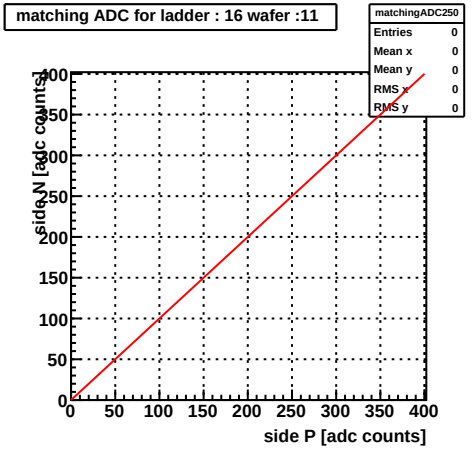

matching ADC for ladder : 16 wafer :10
matchingADC249
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

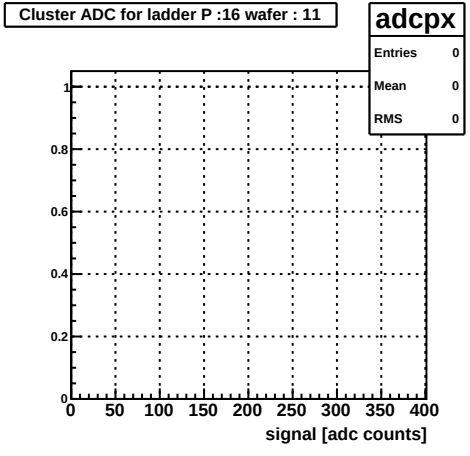

Cluster ADC for ladder P :16 wafer : 10
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

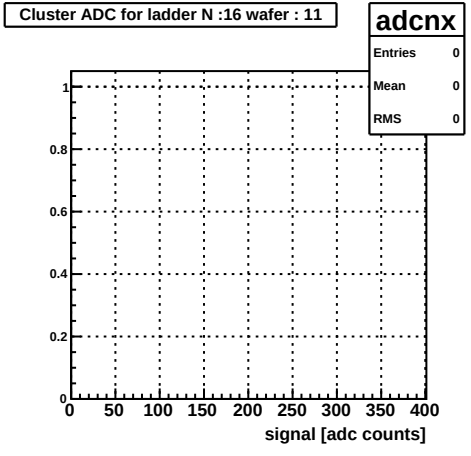

Cluster ADC for ladder N :16 wafer : 10
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

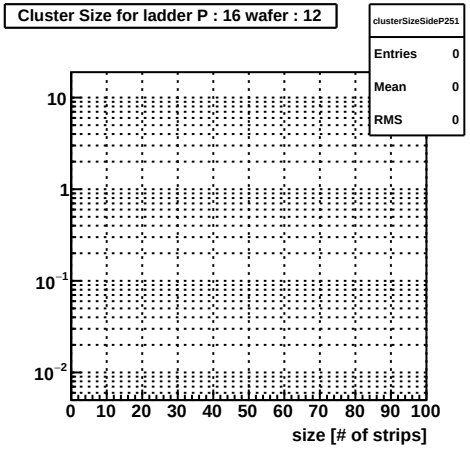

Cluster Size for ladder P : 16 wafer : 11
clusterSizeSideP250
Entries 0
Mean 0
RMS 0
size [# of strips]

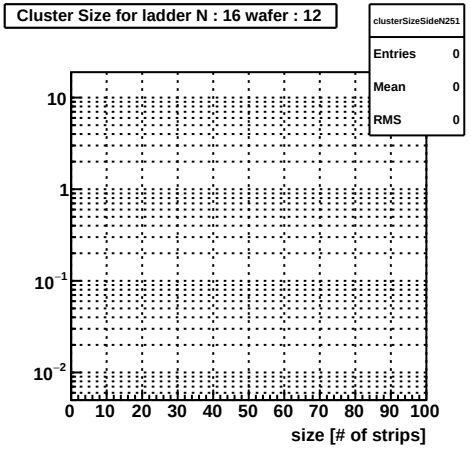

Cluster Size for ladder N : 16 wafer : 11
clusterSizeSideN250
Entries 0
Mean 0
RMS 0
size [# of strips]

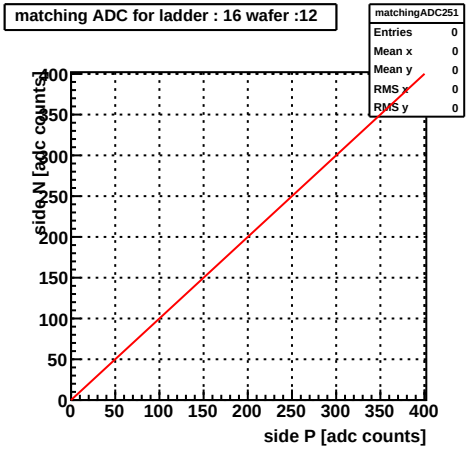

matching ADC for ladder : 16 wafer :11
matchingADC250
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

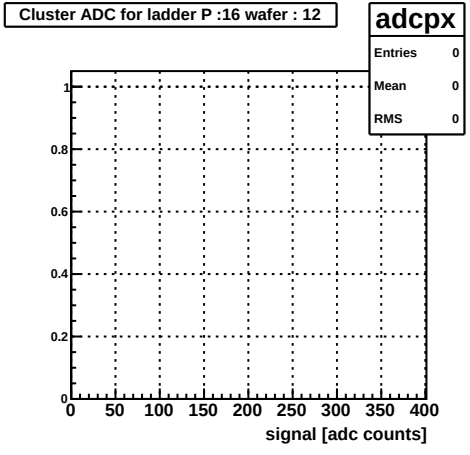

Cluster ADC for ladder P :16 wafer : 11
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

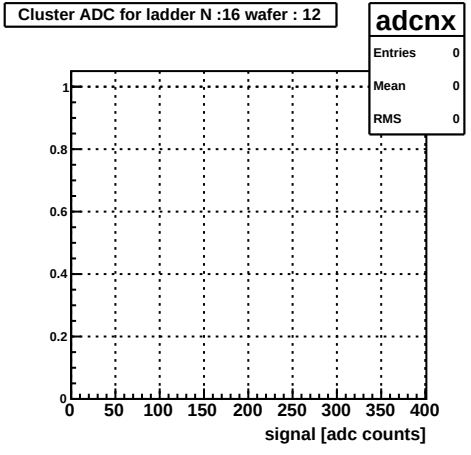

Cluster ADC for ladder N :16 wafer : 11
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

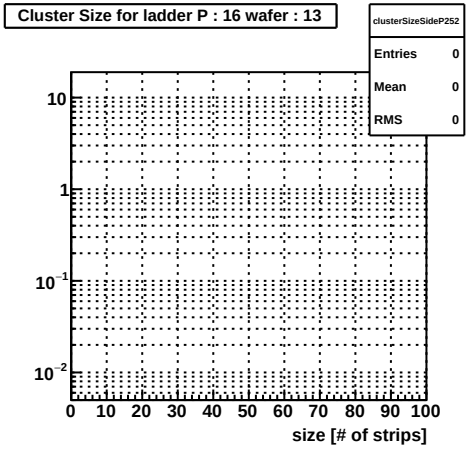

Cluster Size for ladder P : 16 wafer : 12
clusterSizeSideP251
Entries 0
Mean 0
RMS 0
size [# of strips]

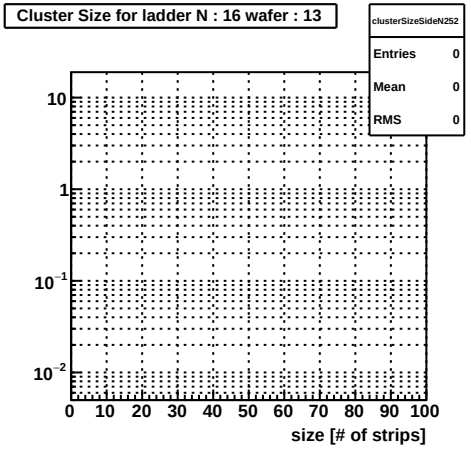

Cluster Size for ladder N : 16 wafer : 12
clusterSizeSideN251
Entries 0
Mean 0
RMS 0
size [# of strips]

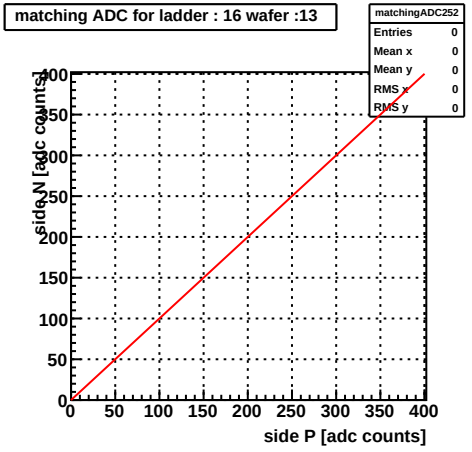

matching ADC for ladder : 16 wafer :12
matchingADC251
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

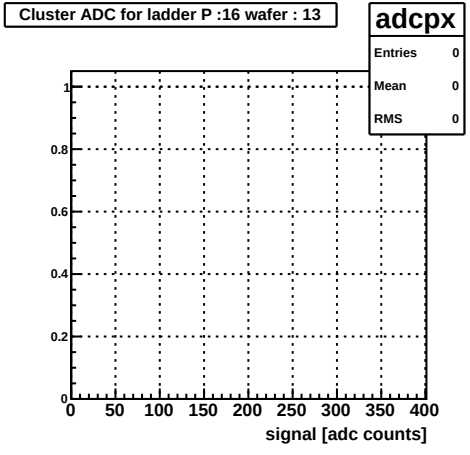

Cluster ADC for ladder P :16 wafer : 12
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

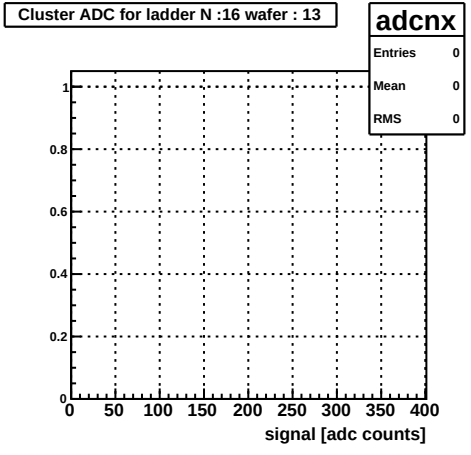

Cluster ADC for ladder N :16 wafer : 12
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

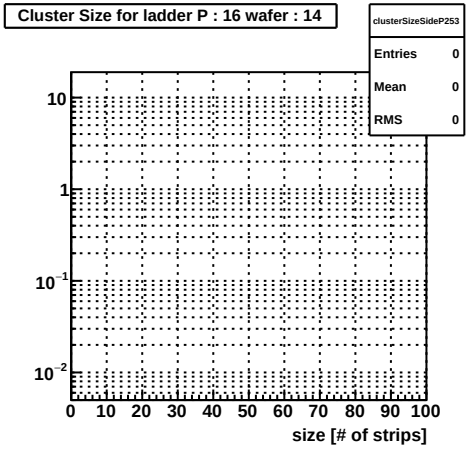

Cluster Size for ladder P : 16 wafer : 13
clusterSizeSideP252
Entries 0
Mean 0
RMS 0
size [# of strips]

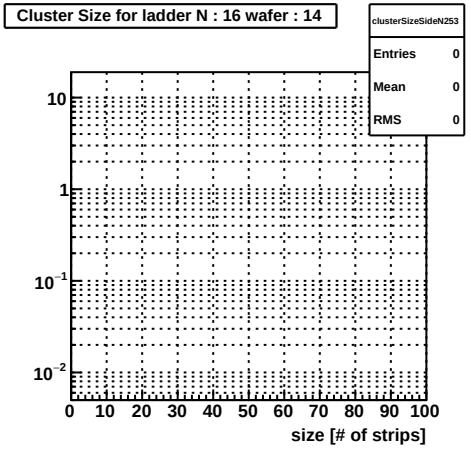

Cluster Size for ladder N : 16 wafer : 13
clusterSizeSideN252
Entries 0
Mean 0
RMS 0
size [# of strips]

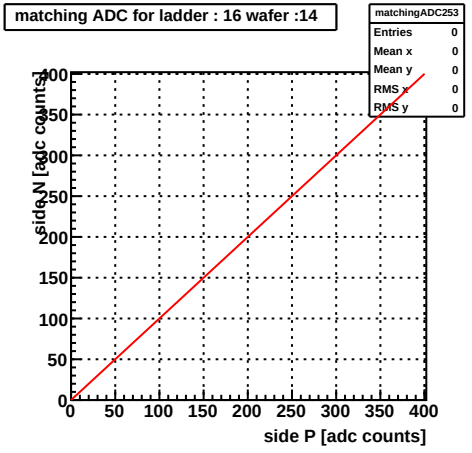

matching ADC for ladder : 16 wafer :13
matchingADC252
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

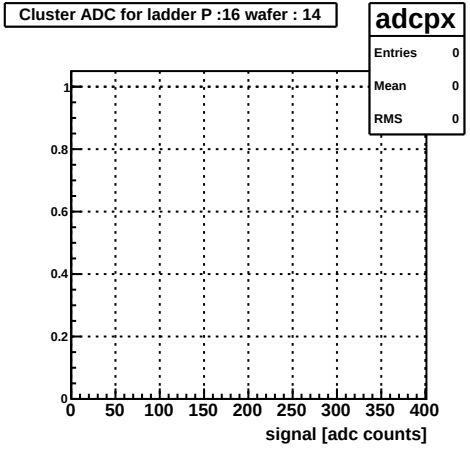

Cluster ADC for ladder P :16 wafer : 13
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

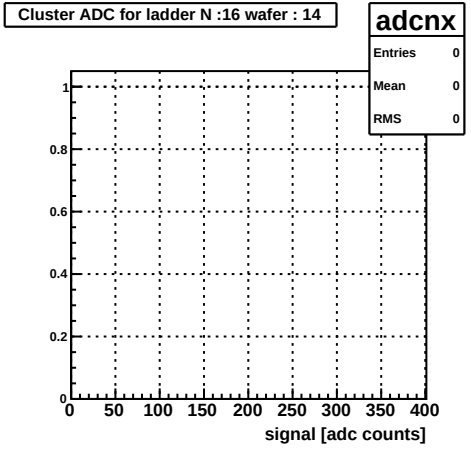

Cluster ADC for ladder N :16 wafer : 13
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

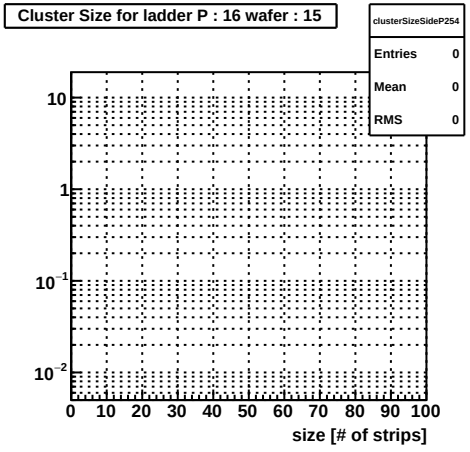

Cluster Size for ladder P : 16 wafer : 14
clusterSizeSideP253
Entries 0
Mean 0
RMS 0
size [# of strips]

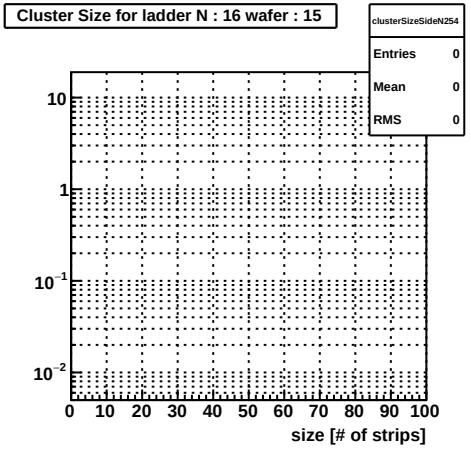

Cluster Size for ladder N : 16 wafer : 14
clusterSizeSideN253
Entries 0
Mean 0
RMS 0
size [# of strips]

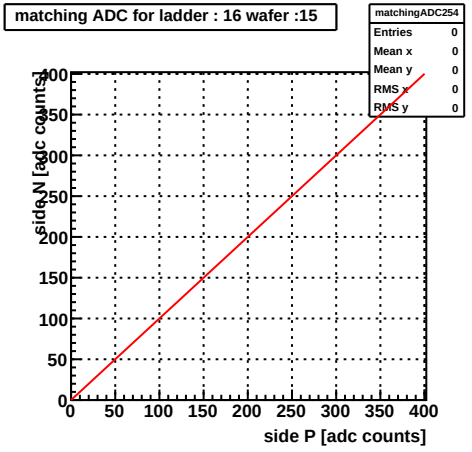

matching ADC for ladder : 16 wafer :14
matchingADC253
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

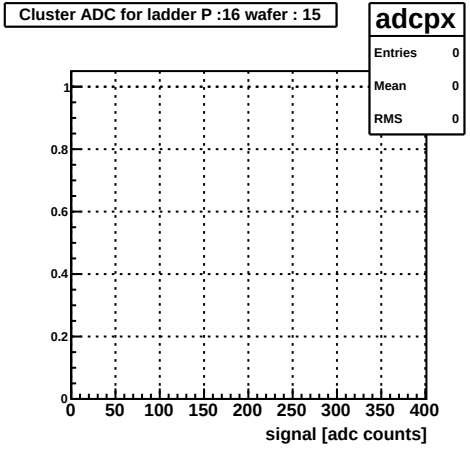

Cluster ADC for ladder P :16 wafer : 14
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

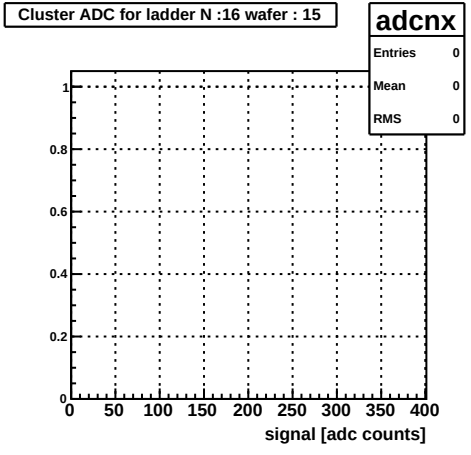

Cluster ADC for ladder N :16 wafer : 14
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

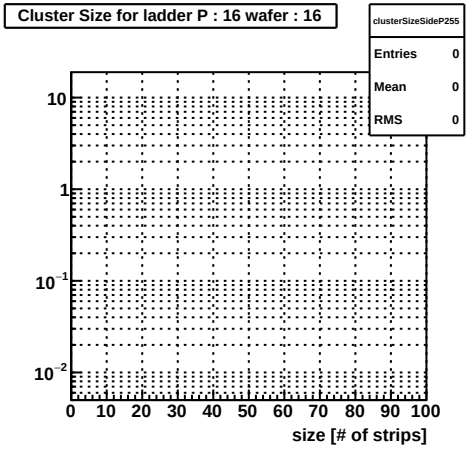

Cluster Size for ladder P : 16 wafer : 15
clusterSizeSideP254
Entries 0
Mean 0
RMS 0
size [# of strips]

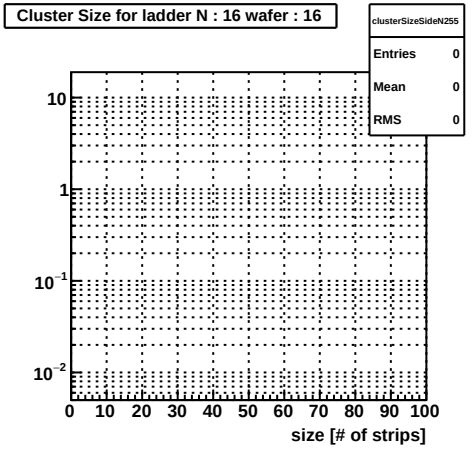

Cluster Size for ladder N : 16 wafer : 15
clusterSizeSideN254
Entries 0
Mean 0
RMS 0
size [# of strips]

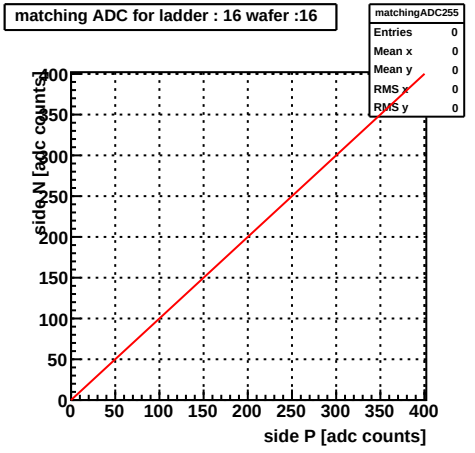

matching ADC for ladder : 16 wafer :15
matchingADC254
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

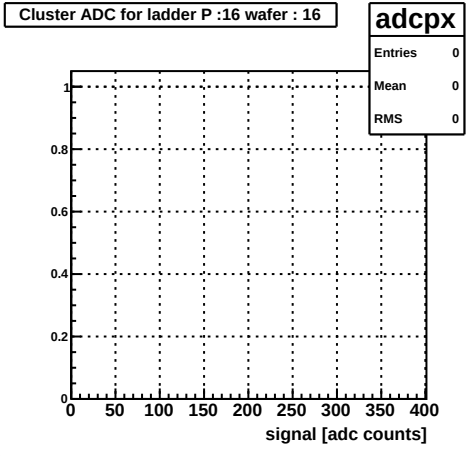

Cluster ADC for ladder P :16 wafer : 15
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

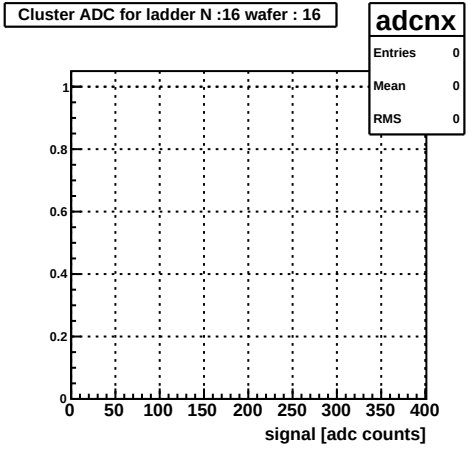

Cluster ADC for ladder N :16 wafer : 15
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

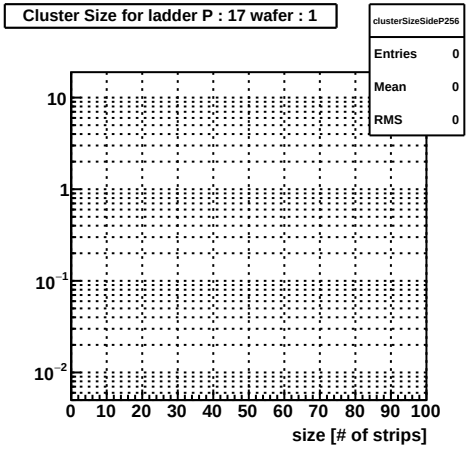

Cluster Size for ladder P : 16 wafer : 16
clusterSizeSideP255
Entries 0
Mean 0
RMS 0
size [# of strips]

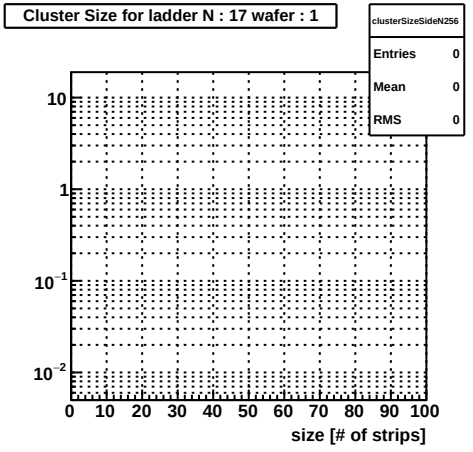

Cluster Size for ladder N : 16 wafer : 16
clusterSizeSideN255
Entries 0
Mean 0
RMS 0
size [# of strips]

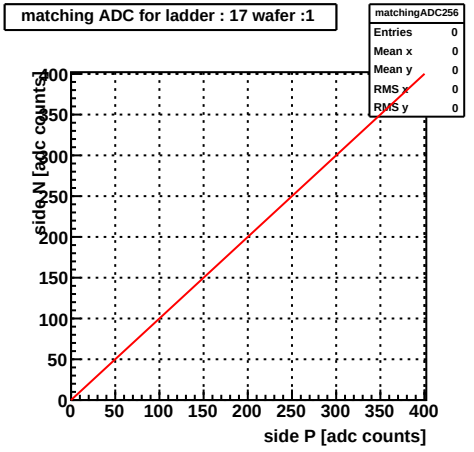

matching ADC for ladder : 16 wafer :16
matchingADC255
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

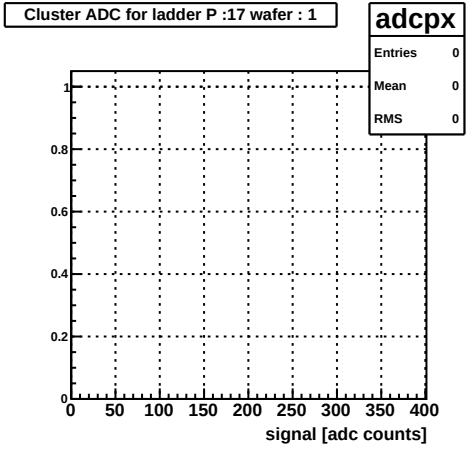

Cluster ADC for ladder P :16 wafer : 16
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

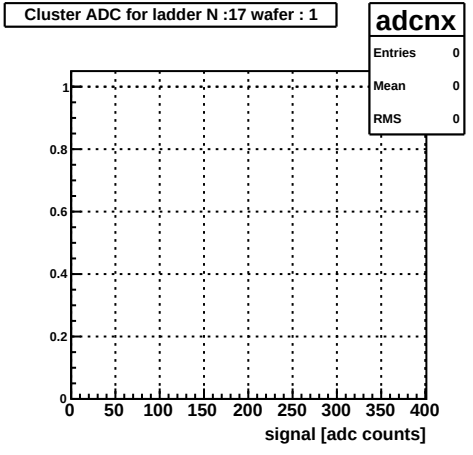

Cluster ADC for ladder N :16 wafer : 16
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

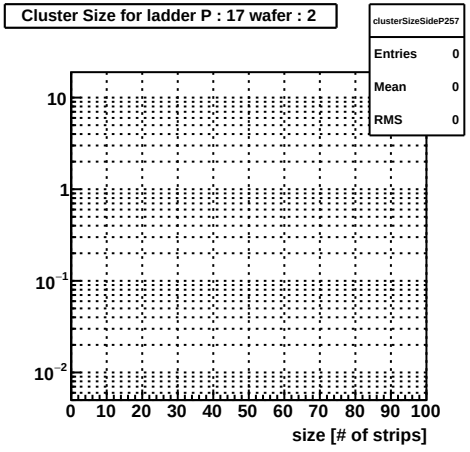

Cluster Size for ladder P : 17 wafer : 1
clusterSizeSideP256
Entries 0
Mean 0
RMS 0
size [# of strips]

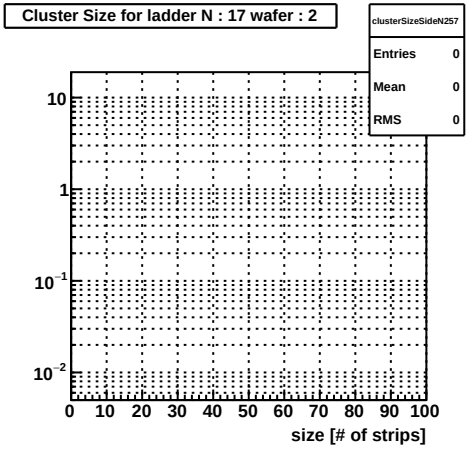

Cluster Size for ladder N : 17 wafer : 1
clusterSizeSideN256
Entries 0
Mean 0
RMS 0
size [# of strips]

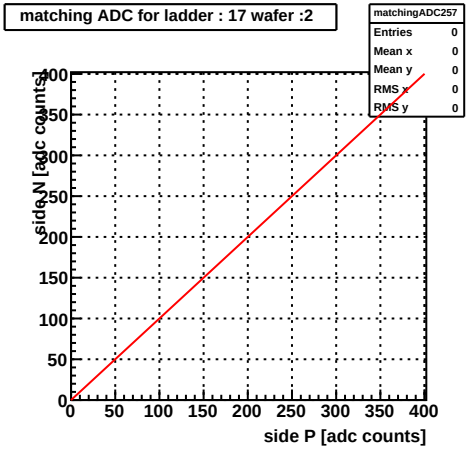

matching ADC for ladder : 17 wafer :1
matchingADC256
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

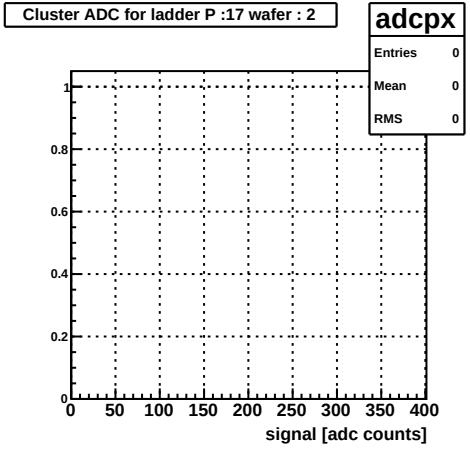

Cluster ADC for ladder P :17 wafer : 1
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

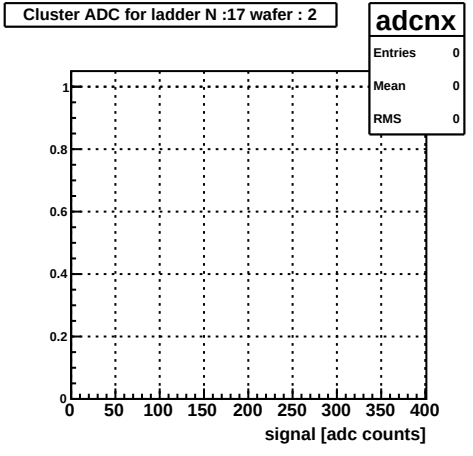

Cluster ADC for ladder N :17 wafer : 1
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

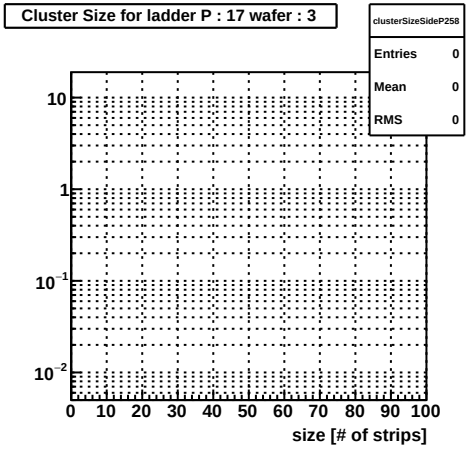

Cluster Size for ladder P : 17 wafer : 2
clusterSizeSideP257
Entries 0
Mean 0
RMS 0
size [# of strips]

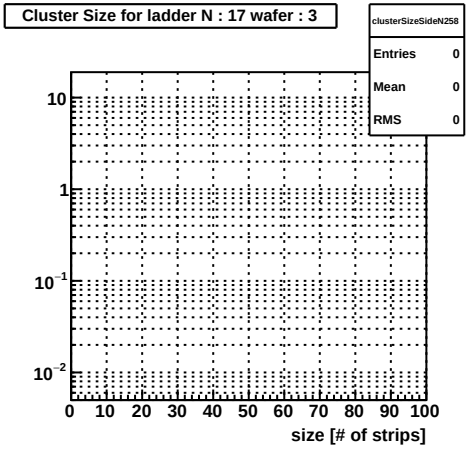

Cluster Size for ladder N : 17 wafer : 2
clusterSizeSideN257
Entries 0
Mean 0
RMS 0
size [# of strips]

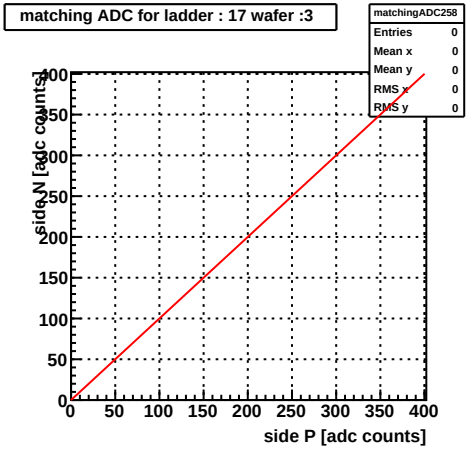

matching ADC for ladder : 17 wafer :2
matchingADC257
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

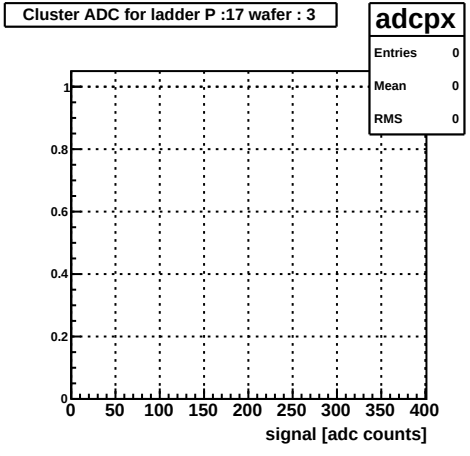

Cluster ADC for ladder P :17 wafer : 2
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

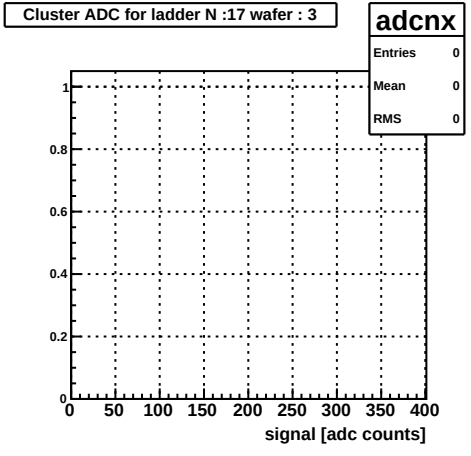

Cluster ADC for ladder N :17 wafer : 2
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

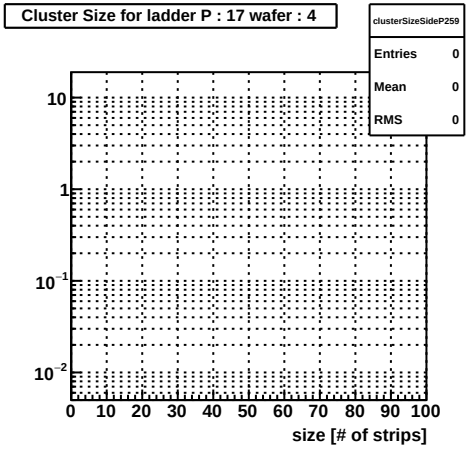

Cluster Size for ladder P : 17 wafer : 3
clusterSizeSideP258
Entries 0
Mean 0
RMS 0
size [# of strips]

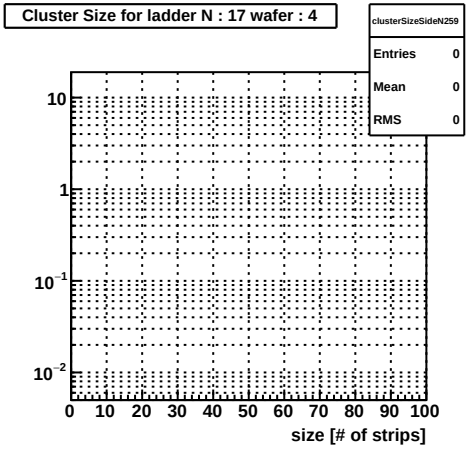

Cluster Size for ladder N : 17 wafer : 3
clusterSizeSideN258
Entries 0
Mean 0
RMS 0
size [# of strips]

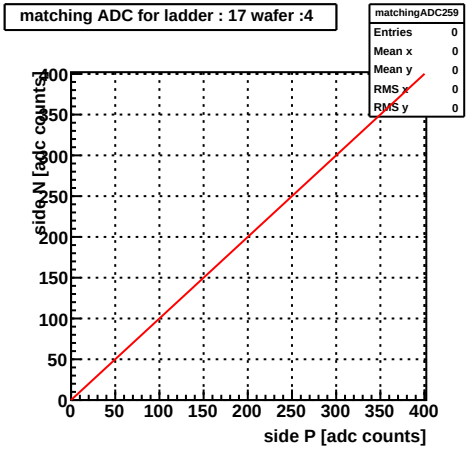

matching ADC for ladder : 17 wafer :3
matchingADC258
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

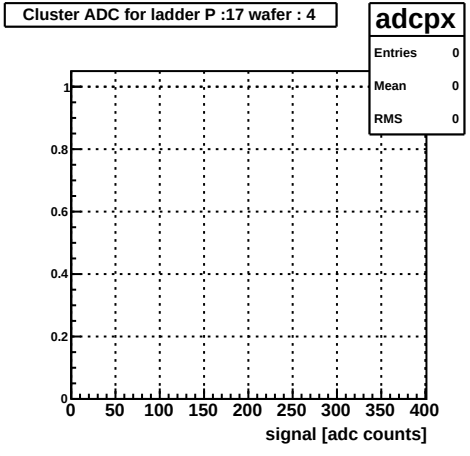

Cluster ADC for ladder P :17 wafer : 3
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

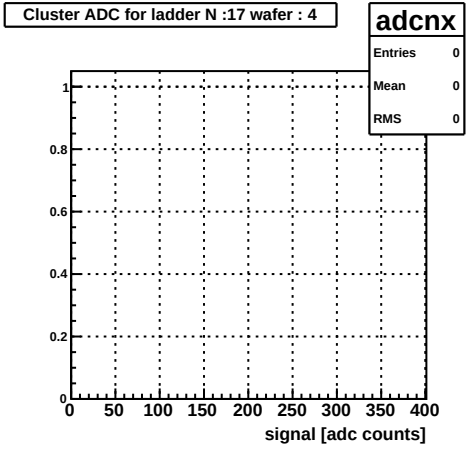

Cluster ADC for ladder N :17 wafer : 3
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

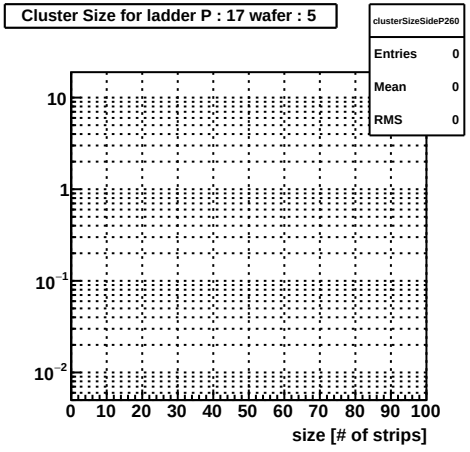

Cluster Size for ladder P : 17 wafer : 4
clusterSizeSideP259
Entries 0
Mean 0
RMS 0
size [# of strips]

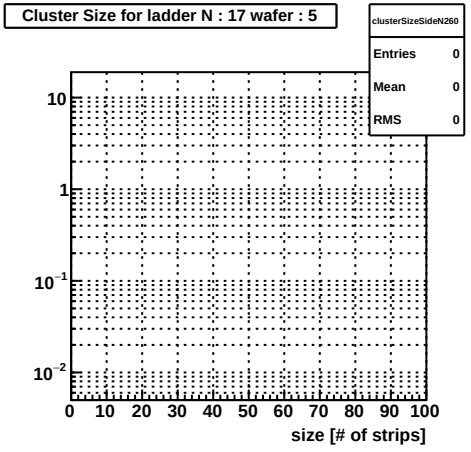

Cluster Size for ladder N : 17 wafer : 4
clusterSizeSideN259
Entries 0
Mean 0
RMS 0
size [# of strips]

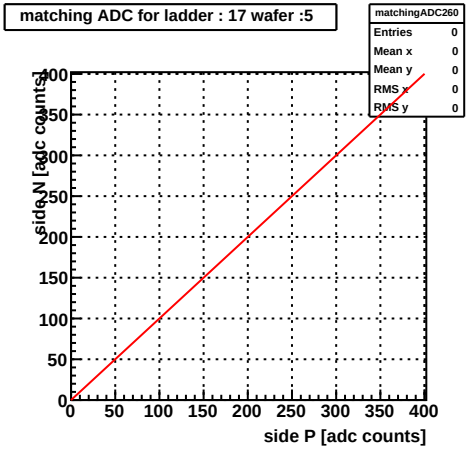

matching ADC for ladder : 17 wafer :4
matchingADC259
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

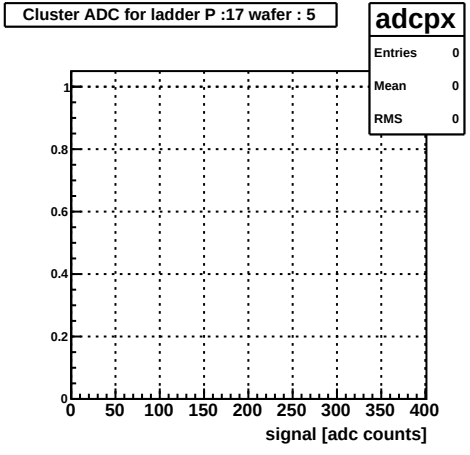

Cluster ADC for ladder P :17 wafer : 4
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

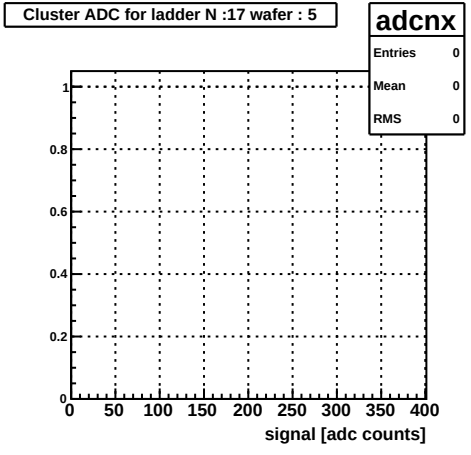

Cluster ADC for ladder N :17 wafer : 4
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

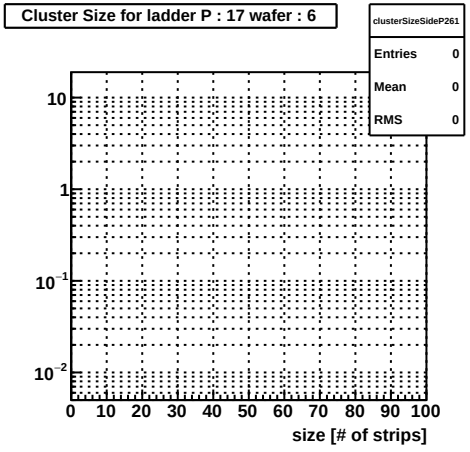

Cluster Size for ladder P : 17 wafer : 5
clusterSizeSideP260
Entries 0
Mean 0
RMS 0
size [# of strips]

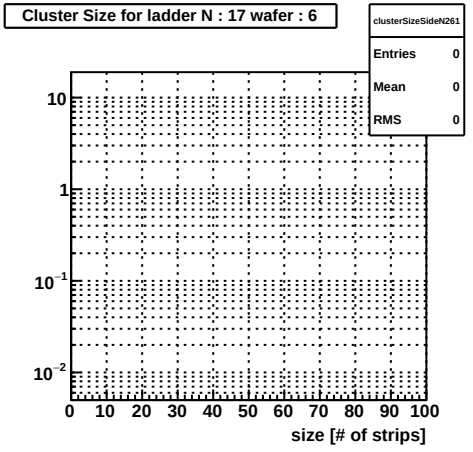

Cluster Size for ladder N : 17 wafer : 5
clusterSizeSideN260
Entries 0
Mean 0
RMS 0
size [# of strips]

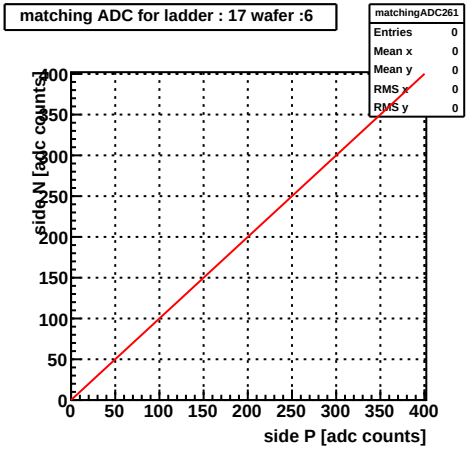

matching ADC for ladder : 17 wafer :5
matchingADC260
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

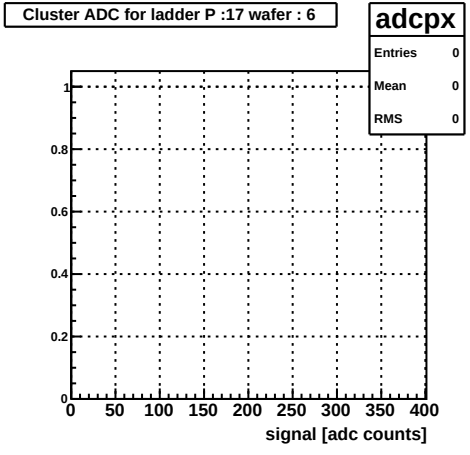

Cluster ADC for ladder P :17 wafer : 5
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

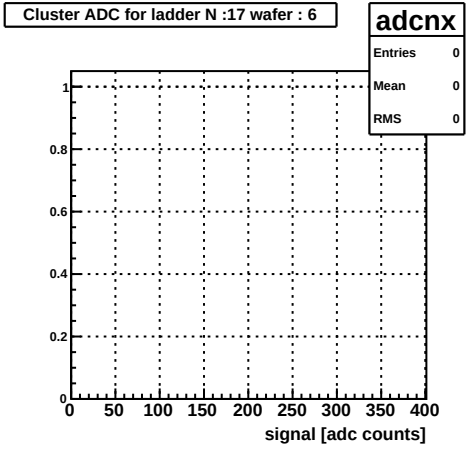

Cluster ADC for ladder N :17 wafer : 5
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

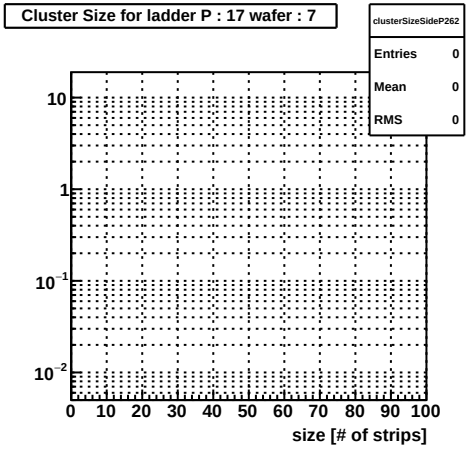

Cluster Size for ladder P : 17 wafer : 6
clusterSizeSideP261
Entries 0
Mean 0
RMS 0
size [# of strips]

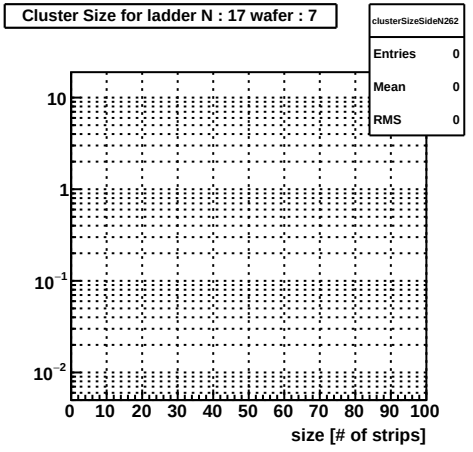

Cluster Size for ladder N : 17 wafer : 6
clusterSizeSideN261
Entries 0
Mean 0
RMS 0
size [# of strips]

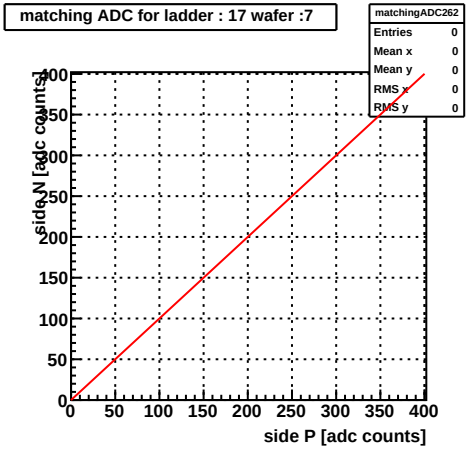

matching ADC for ladder : 17 wafer :6
matchingADC261
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

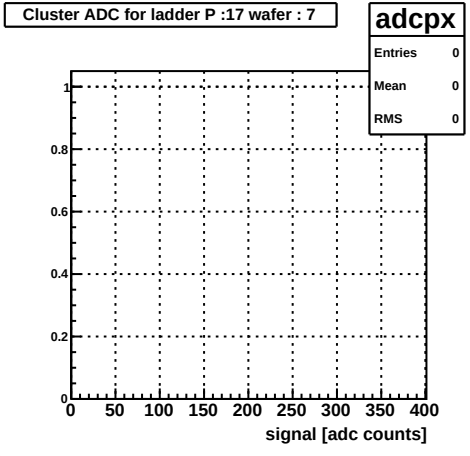

Cluster ADC for ladder P :17 wafer : 6
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

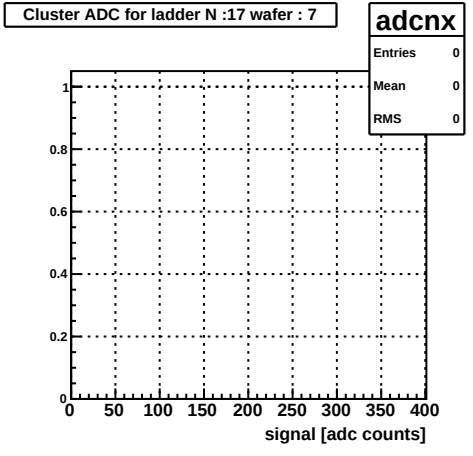

Cluster ADC for ladder N :17 wafer : 6
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

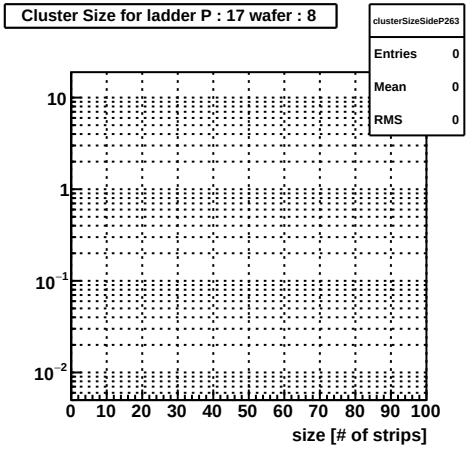

Cluster Size for ladder P : 17 wafer : 7
clusterSizeSideP262
Entries 0
Mean 0
RMS 0
size [# of strips]

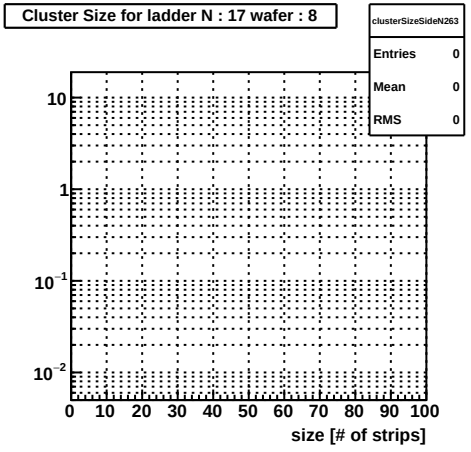

Cluster Size for ladder N : 17 wafer : 7
clusterSizeSideN262
Entries 0
Mean 0
RMS 0
size [# of strips]

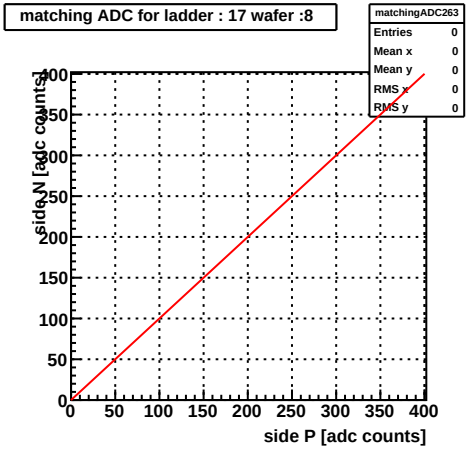

matching ADC for ladder : 17 wafer :7
matchingADC262
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

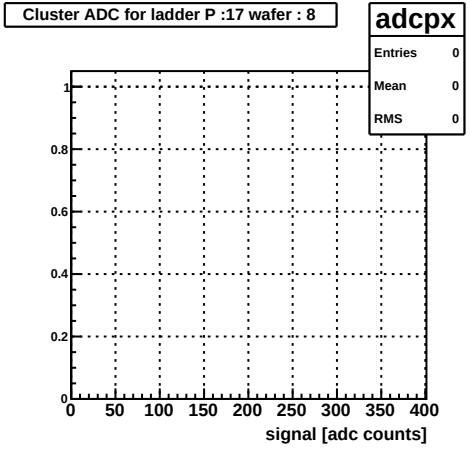

Cluster ADC for ladder P :17 wafer : 7
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

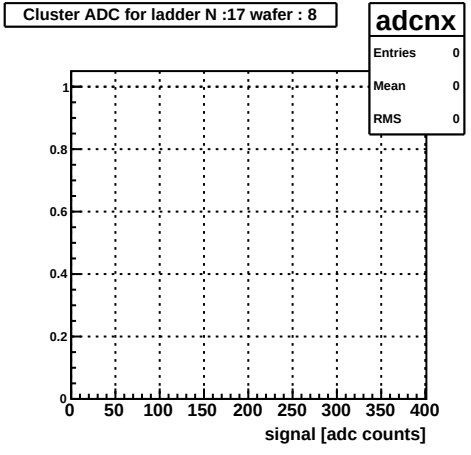

Cluster ADC for ladder N :17 wafer : 7
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

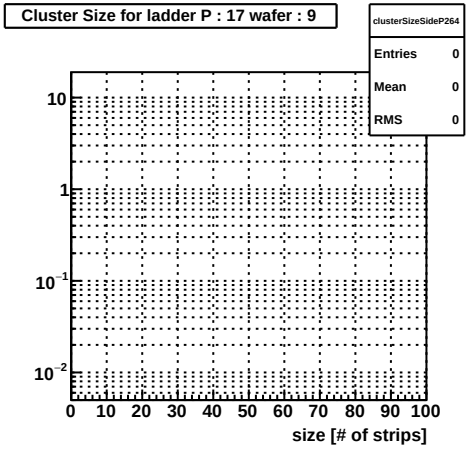

Cluster Size for ladder P : 17 wafer : 8
clusterSizeSideP263
Entries 0
Mean 0
RMS 0
size [# of strips]

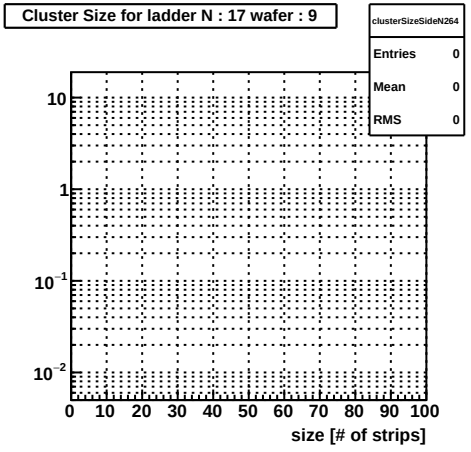

Cluster Size for ladder N : 17 wafer : 8
clusterSizeSideN263
Entries 0
Mean 0
RMS 0
size [# of strips]

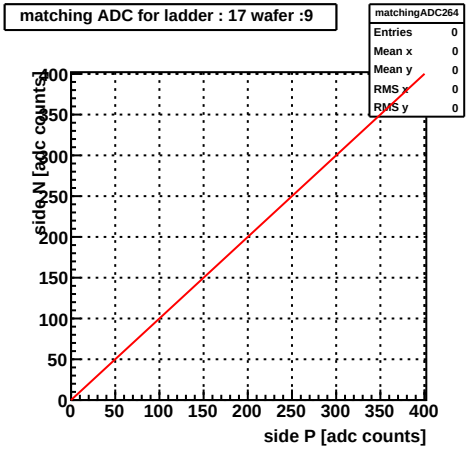

matching ADC for ladder : 17 wafer :8
matchingADC263
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

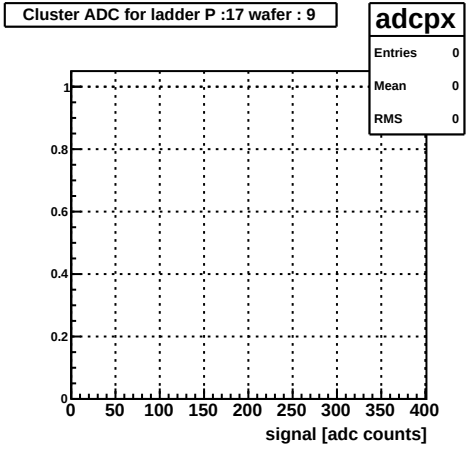

Cluster ADC for ladder P :17 wafer : 8
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

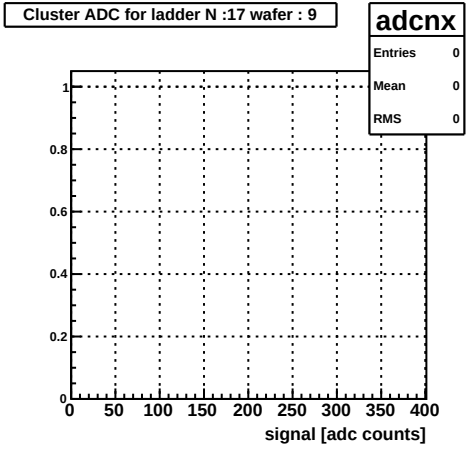

Cluster ADC for ladder N :17 wafer : 8
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

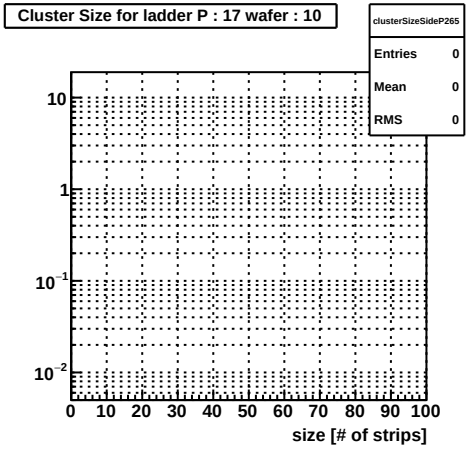

Cluster Size for ladder P : 17 wafer : 9
clusterSizeSideP264
Entries 0
Mean 0
RMS 0
size [# of strips]

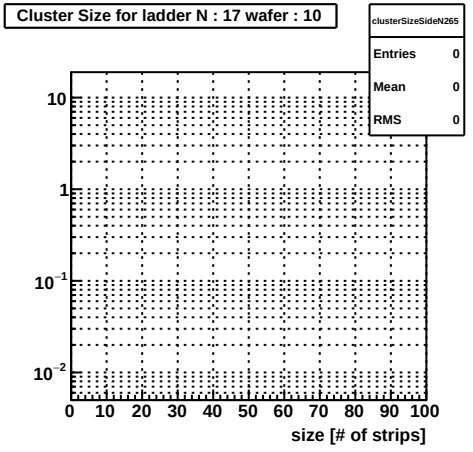

Cluster Size for ladder N : 17 wafer : 9
clusterSizeSideN264
Entries 0
Mean 0
RMS 0
size [# of strips]

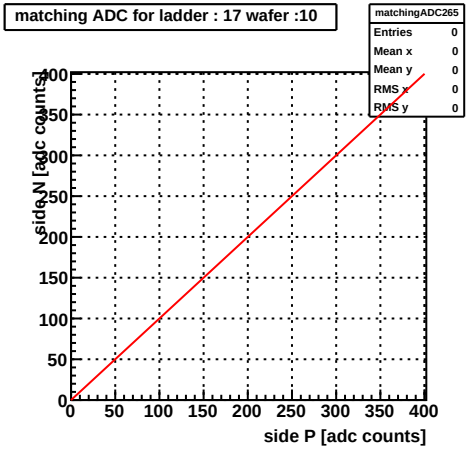

matching ADC for ladder : 17 wafer :9
matchingADC264
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

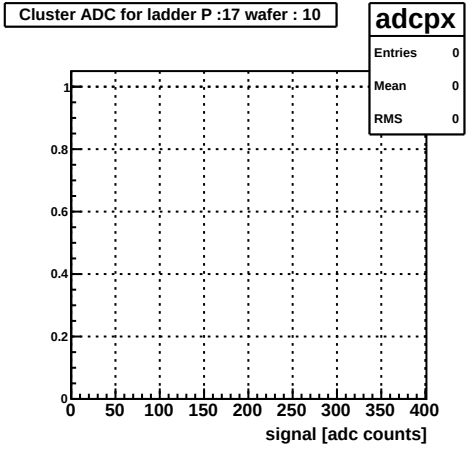

Cluster ADC for ladder P :17 wafer : 9
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

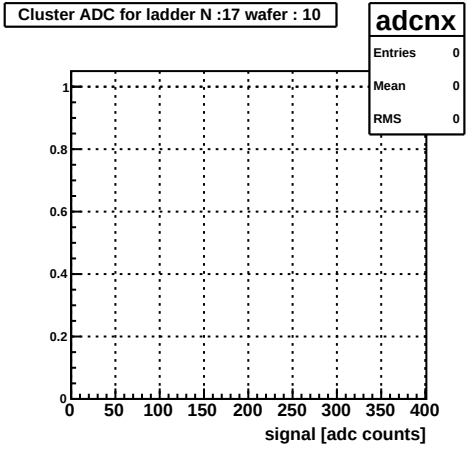

Cluster ADC for ladder N :17 wafer : 9
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

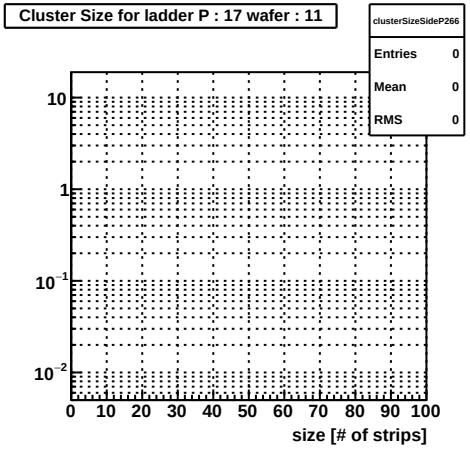

Cluster Size for ladder P : 17 wafer : 10
clusterSizeSideP265
Entries 0
Mean 0
RMS 0
size [# of strips]

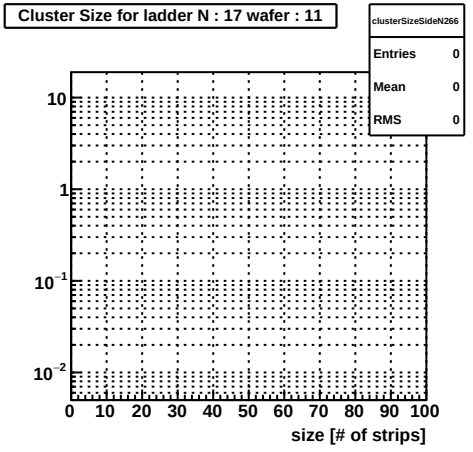

Cluster Size for ladder N : 17 wafer : 10
clusterSizeSideN265
Entries 0
Mean 0
RMS 0
size [# of strips]

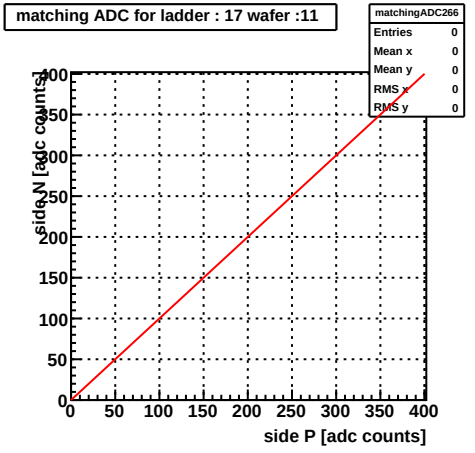

matching ADC for ladder : 17 wafer :10
matchingADC265
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

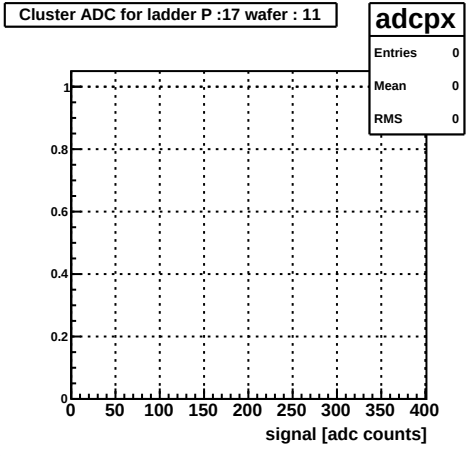

Cluster ADC for ladder P :17 wafer : 10
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

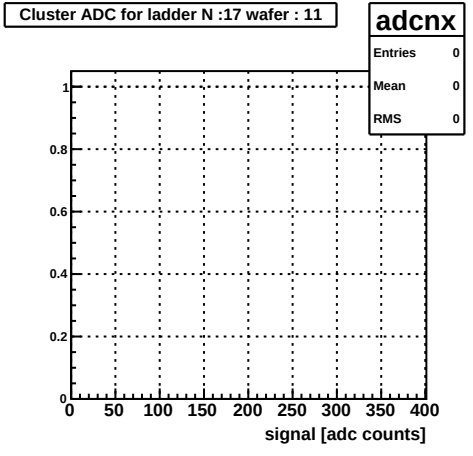

Cluster ADC for ladder N :17 wafer : 10
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

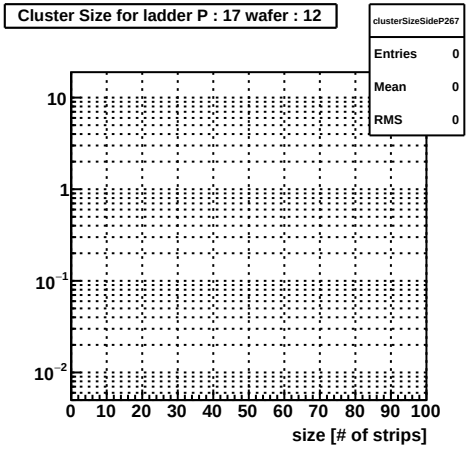

Cluster Size for ladder P : 17 wafer : 11
clusterSizeSideP266
Entries 0
Mean 0
RMS 0
size [# of strips]

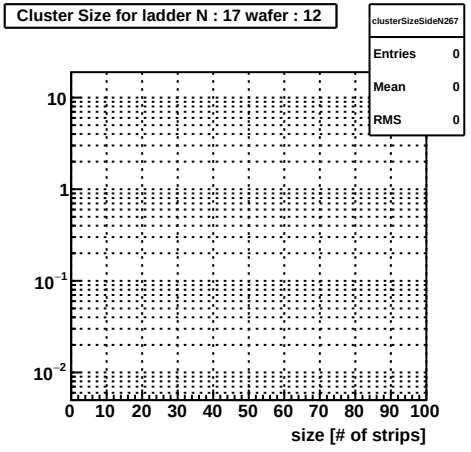

Cluster Size for ladder N : 17 wafer : 11
clusterSizeSideN266
Entries 0
Mean 0
RMS 0
size [# of strips]

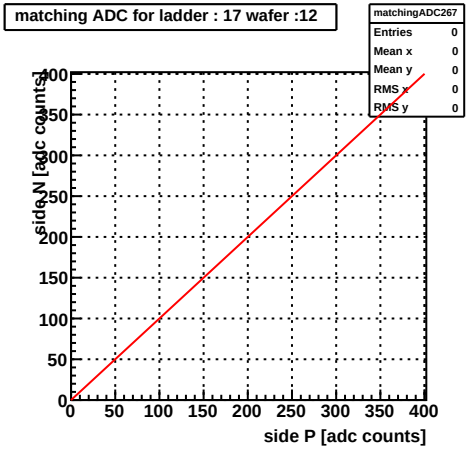

matching ADC for ladder : 17 wafer :11
matchingADC266
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

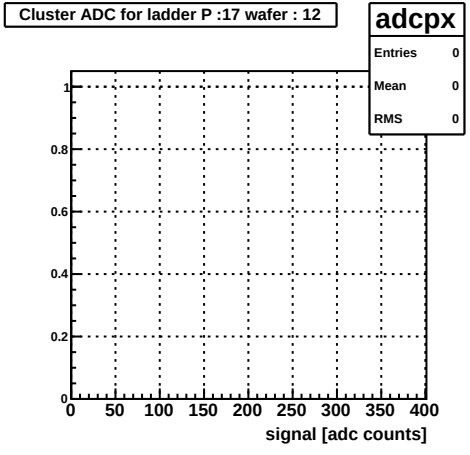

Cluster ADC for ladder P :17 wafer : 11
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

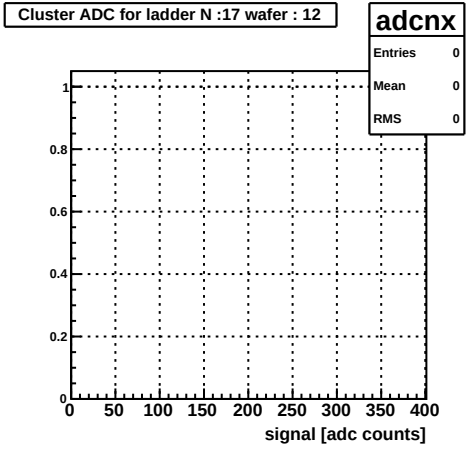

Cluster ADC for ladder N :17 wafer : 11
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

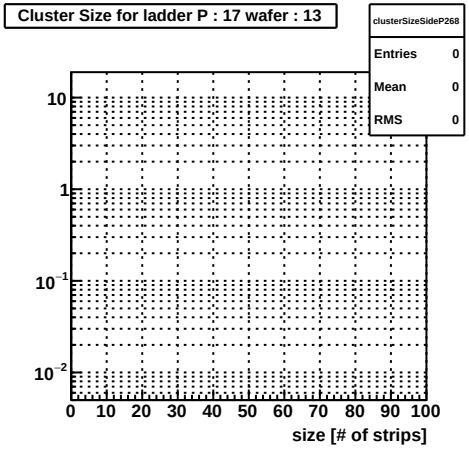

Cluster Size for ladder P : 17 wafer : 12
clusterSizeSideP267
Entries 0
Mean 0
RMS 0
size [# of strips]

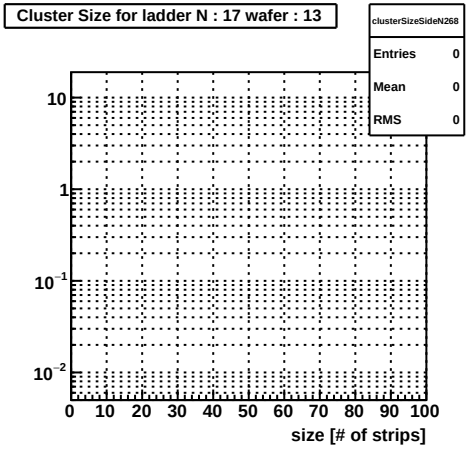

Cluster Size for ladder N : 17 wafer : 12
clusterSizeSideN267
Entries 0
Mean 0
RMS 0
size [# of strips]

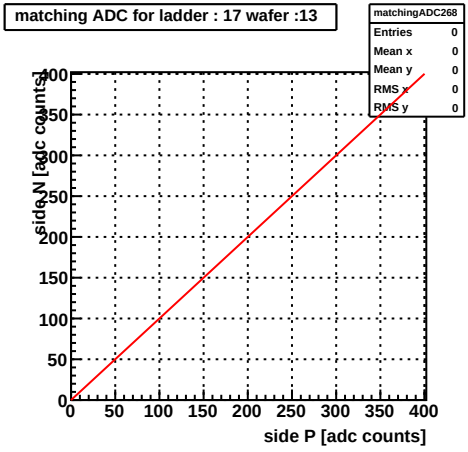

matching ADC for ladder : 17 wafer :12
matchingADC267
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

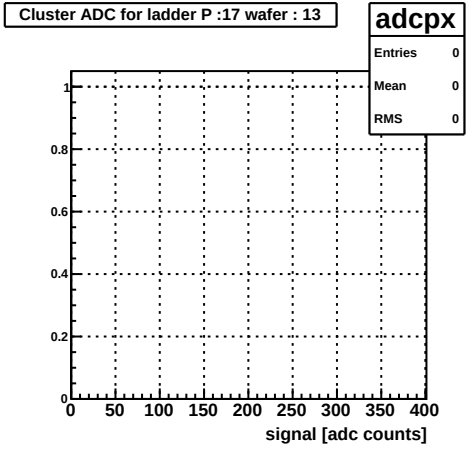

Cluster ADC for ladder P :17 wafer : 12
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

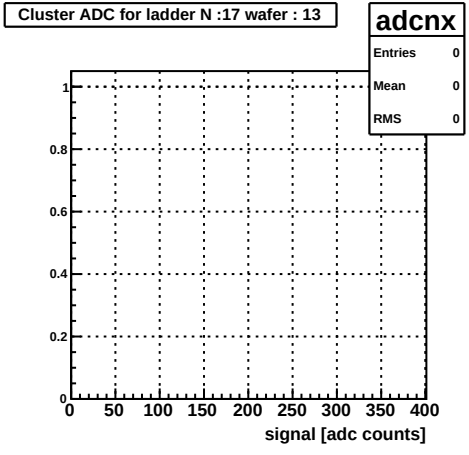

Cluster ADC for ladder N :17 wafer : 12
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

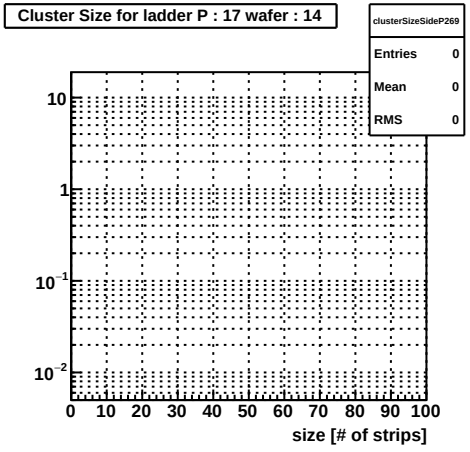

Cluster Size for ladder P : 17 wafer : 13
clusterSizeSideP268
Entries 0
Mean 0
RMS 0
size [# of strips]

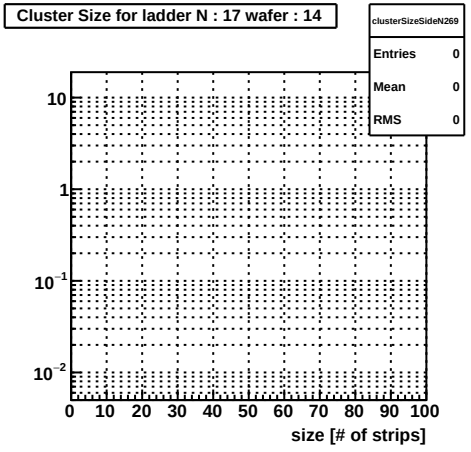

Cluster Size for ladder N : 17 wafer : 13
clusterSizeSideN268
Entries 0
Mean 0
RMS 0
size [# of strips]

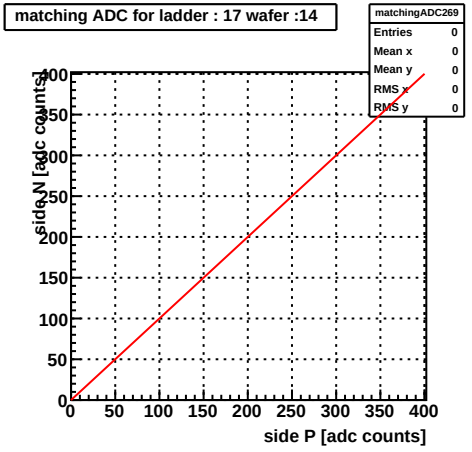

matching ADC for ladder : 17 wafer :13
matchingADC268
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

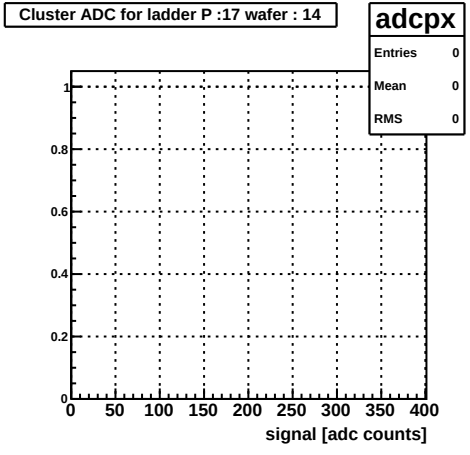

Cluster ADC for ladder P :17 wafer : 13
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

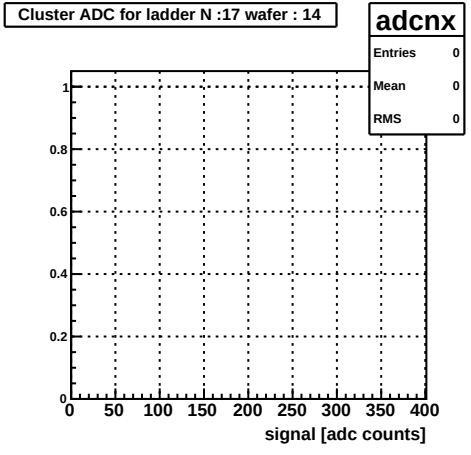

Cluster ADC for ladder N :17 wafer : 13
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

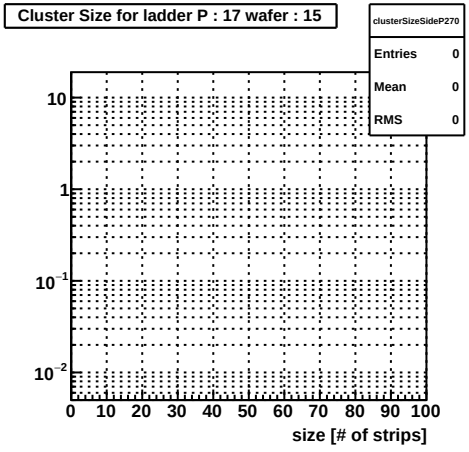

Cluster Size for ladder P : 17 wafer : 14
clusterSizeSideP269
Entries 0
Mean 0
RMS 0
size [# of strips]

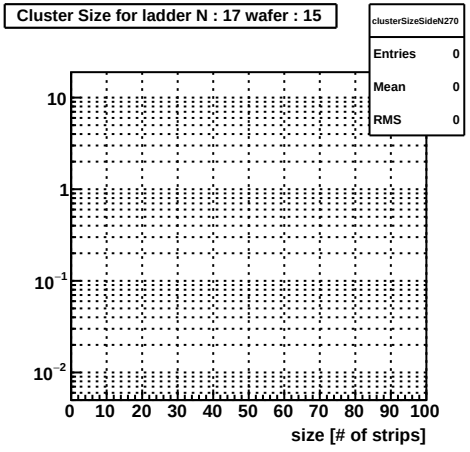

Cluster Size for ladder N : 17 wafer : 14
clusterSizeSideN269
Entries 0
Mean 0
RMS 0
size [# of strips]

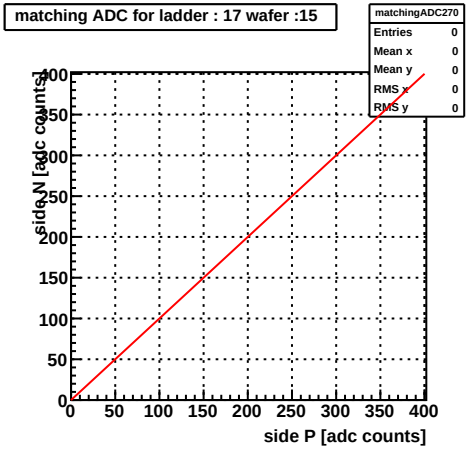

matching ADC for ladder : 17 wafer :14
matchingADC269
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

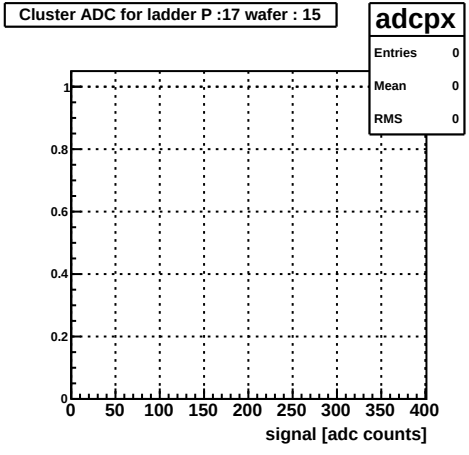

Cluster ADC for ladder P :17 wafer : 14
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

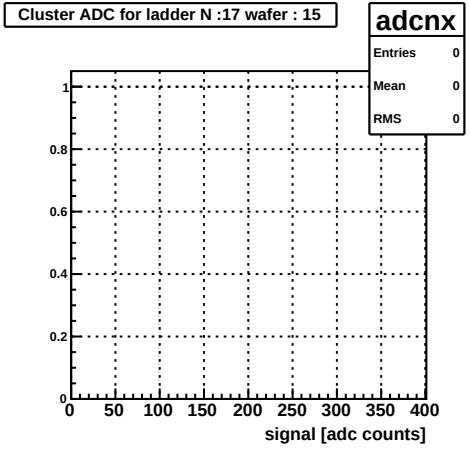

Cluster ADC for ladder N :17 wafer : 14
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

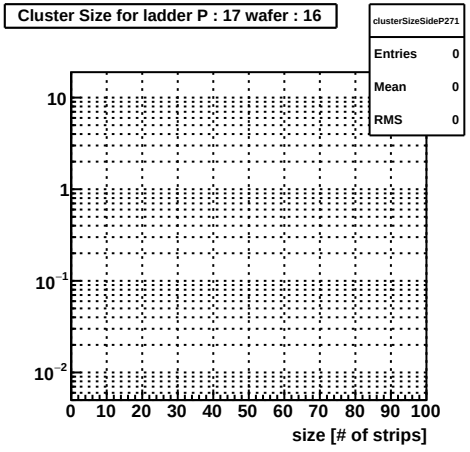

Cluster Size for ladder P : 17 wafer : 15
clusterSizeSideP270
Entries 0
Mean 0
RMS 0
size [# of strips]

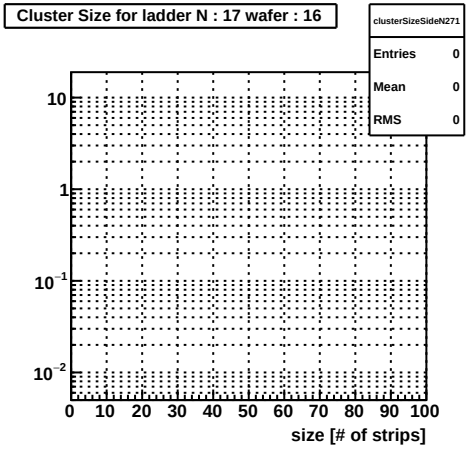

Cluster Size for ladder N : 17 wafer : 15
clusterSizeSideN270
Entries 0
Mean 0
RMS 0
size [# of strips]

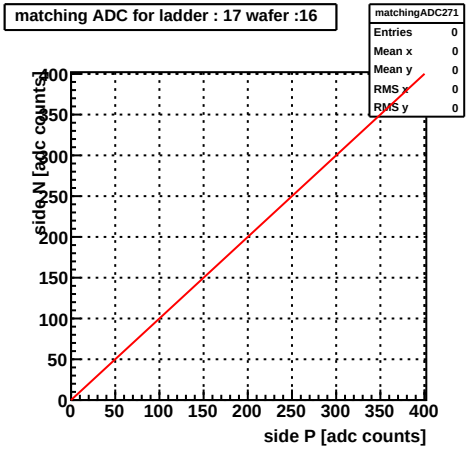

matching ADC for ladder : 17 wafer :15
matchingADC270
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

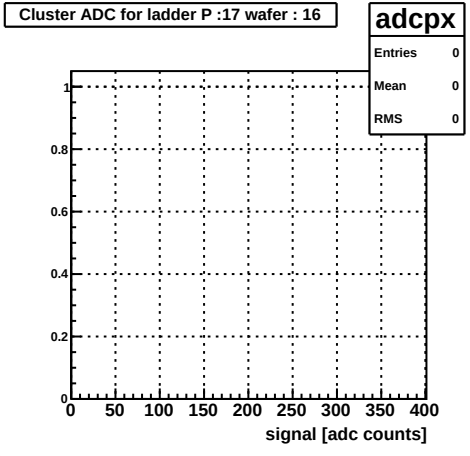

Cluster ADC for ladder P :17 wafer : 15
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

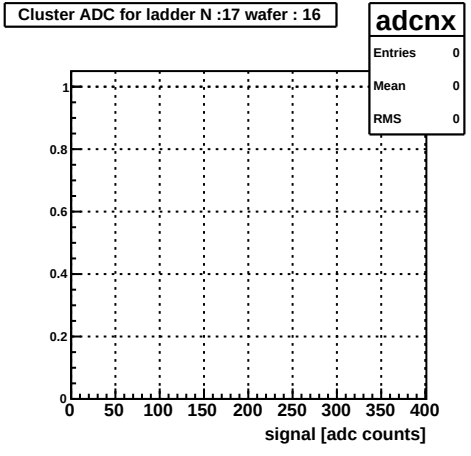

Cluster ADC for ladder N :17 wafer : 15
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

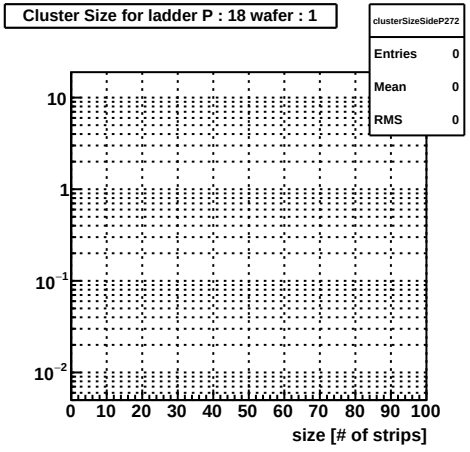

Cluster Size for ladder P : 17 wafer : 16
clusterSizeSideP271
Entries 0
Mean 0
RMS 0
size [# of strips]

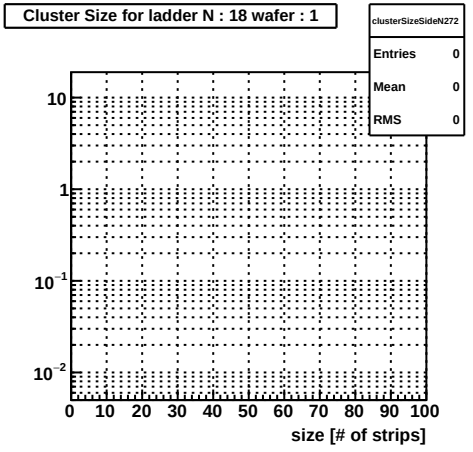

Cluster Size for ladder N : 17 wafer : 16
clusterSizeSideN271
Entries 0
Mean 0
RMS 0
size [# of strips]

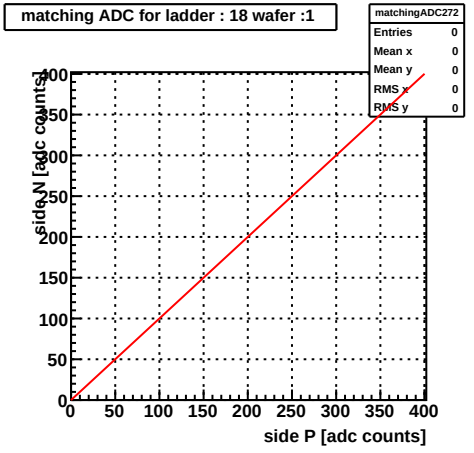

matching ADC for ladder : 17 wafer :16
matchingADC271
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

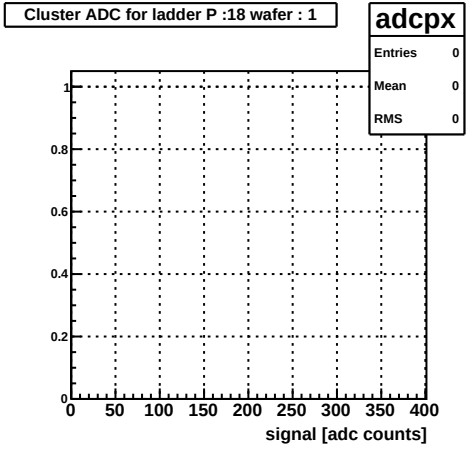

Cluster ADC for ladder P :17 wafer : 16
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

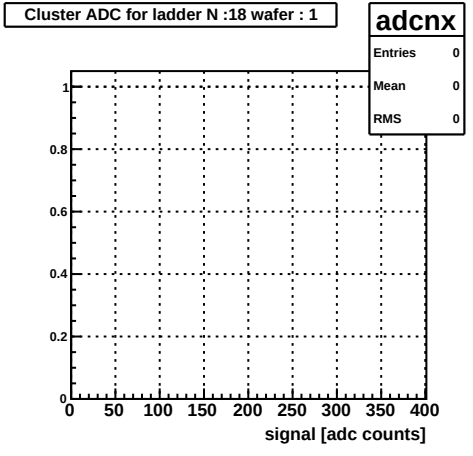

Cluster ADC for ladder N :17 wafer : 16
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

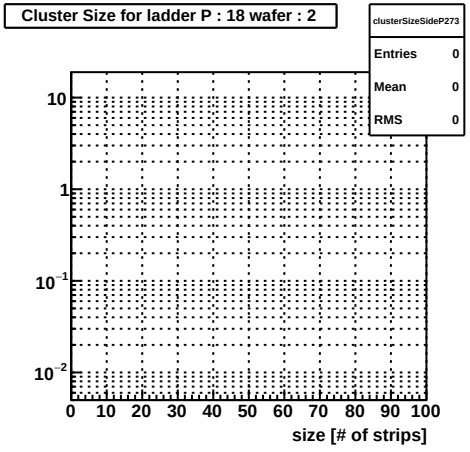

Cluster Size for ladder P : 18 wafer : 1
clusterSizeSideP272
Entries 0
Mean 0
RMS 0
size [# of strips]

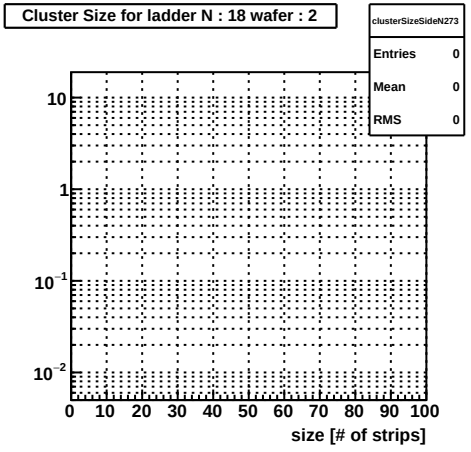

Cluster Size for ladder N : 18 wafer : 1
clusterSizeSideN272
Entries 0
Mean 0
RMS 0
size [# of strips]

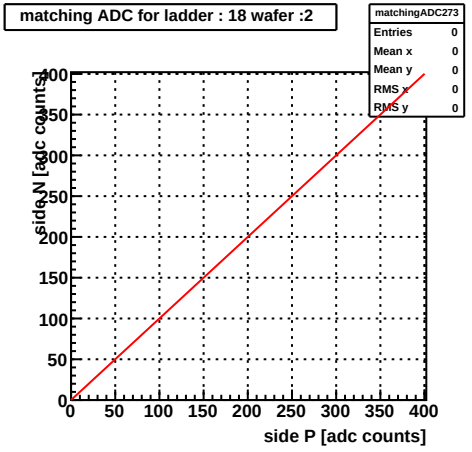

matching ADC for ladder : 18 wafer :1
matchingADC272
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

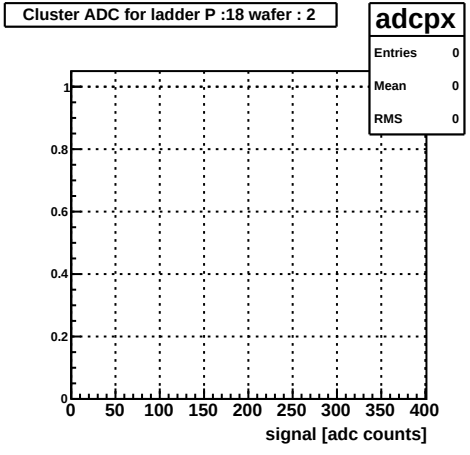

Cluster ADC for ladder P :18 wafer : 1
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

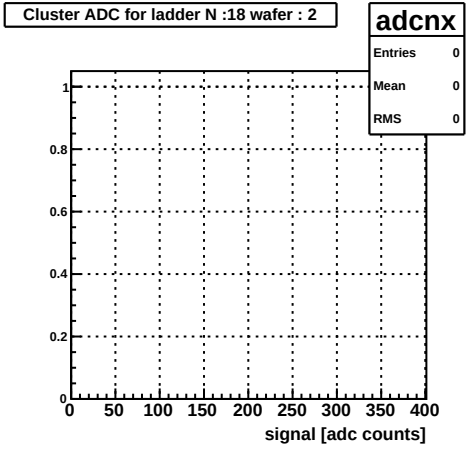

Cluster ADC for ladder N :18 wafer : 1
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

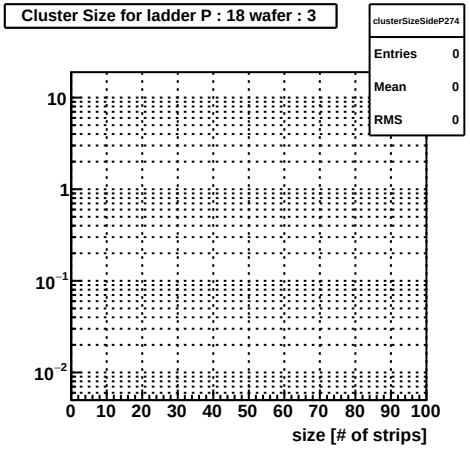

Cluster Size for ladder P : 18 wafer : 2
clusterSizeSideP273
Entries 0
Mean 0
RMS 0
size [# of strips]

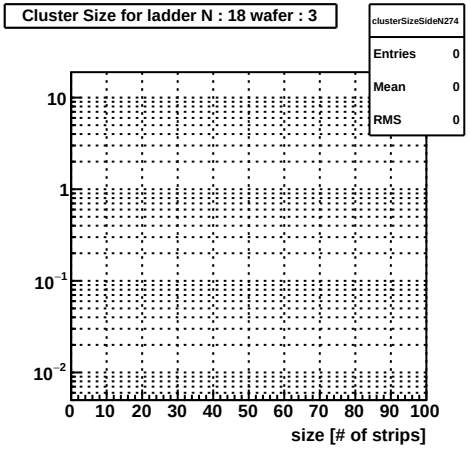

Cluster Size for ladder N : 18 wafer : 2
clusterSizeSideN273
Entries 0
Mean 0
RMS 0
size [# of strips]

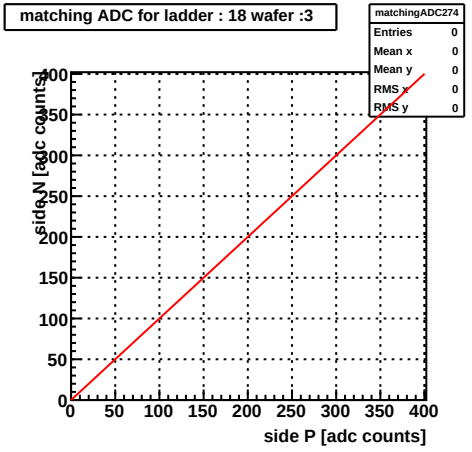

matching ADC for ladder : 18 wafer :2
matchingADC273
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

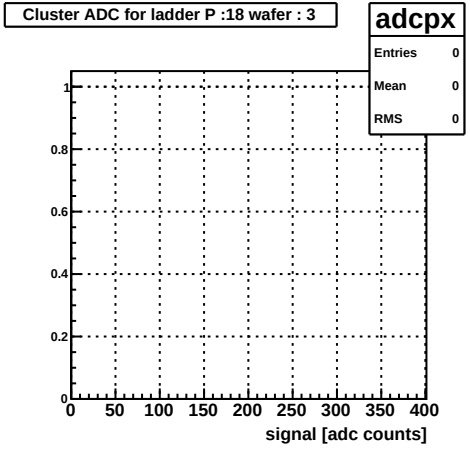

Cluster ADC for ladder P :18 wafer : 2
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

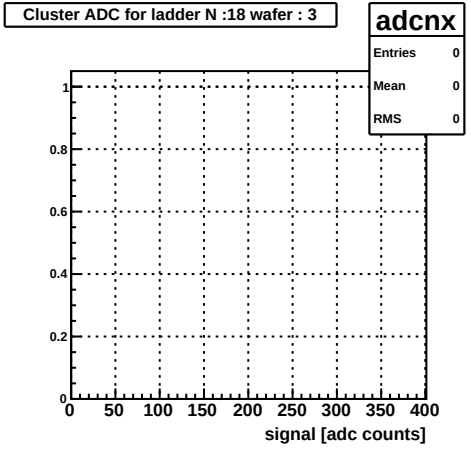

Cluster ADC for ladder N :18 wafer : 2
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

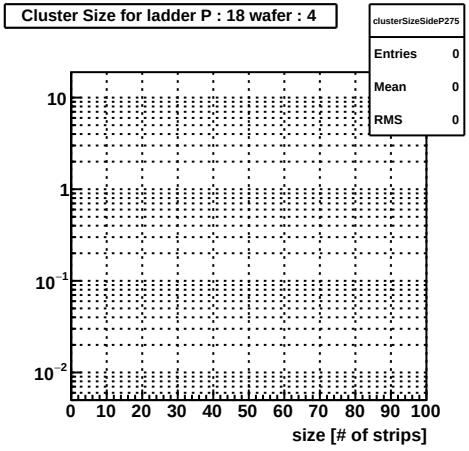

Cluster Size for ladder P : 18 wafer : 3
clusterSizeSideP274
Entries 0
Mean 0
RMS 0
size [# of strips]

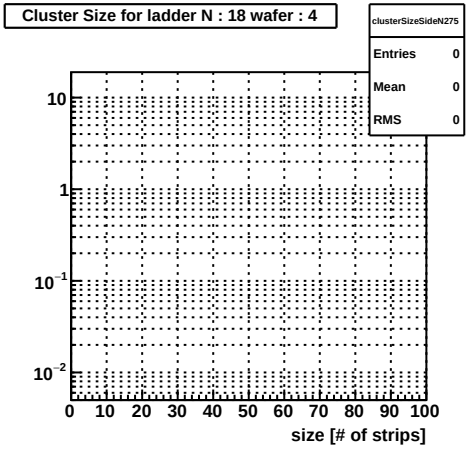

Cluster Size for ladder N : 18 wafer : 3
clusterSizeSideN274
Entries 0
Mean 0
RMS 0
size [# of strips]

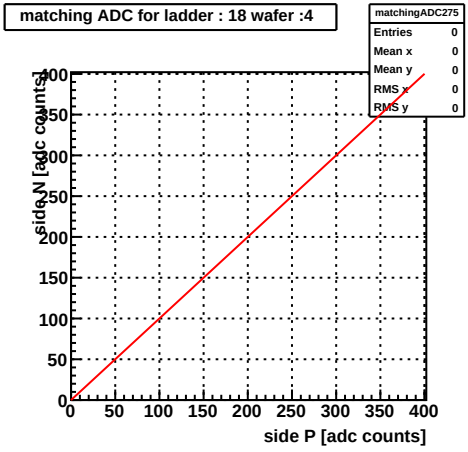

matching ADC for ladder : 18 wafer :3
matchingADC274
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

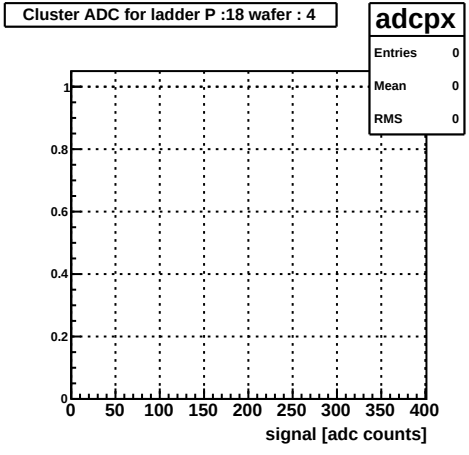

Cluster ADC for ladder P :18 wafer : 3
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

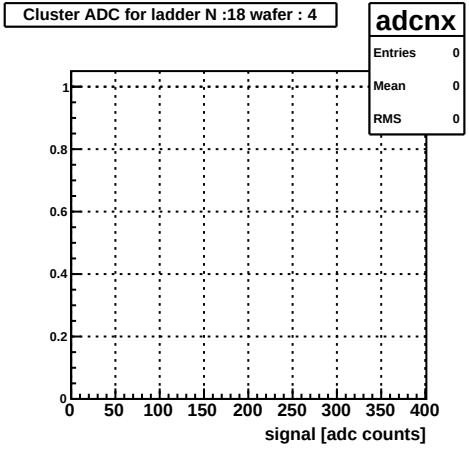

Cluster ADC for ladder N :18 wafer : 3
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

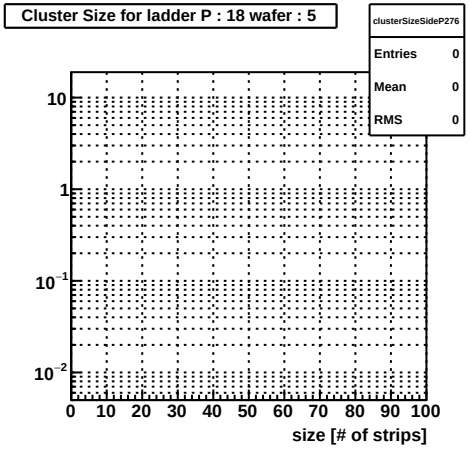

Cluster Size for ladder P : 18 wafer : 4
clusterSizeSideP275
Entries 0
Mean 0
RMS 0
size [# of strips]

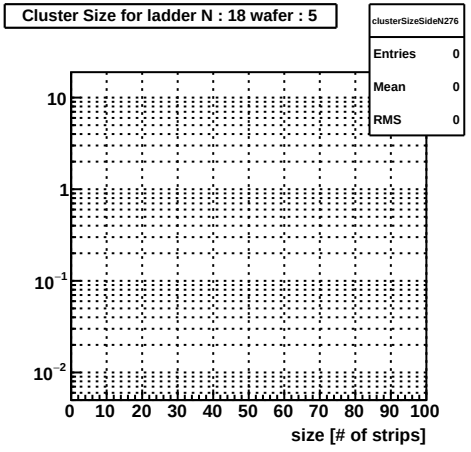

Cluster Size for ladder N : 18 wafer : 4
clusterSizeSideN275
Entries 0
Mean 0
RMS 0
size [# of strips]

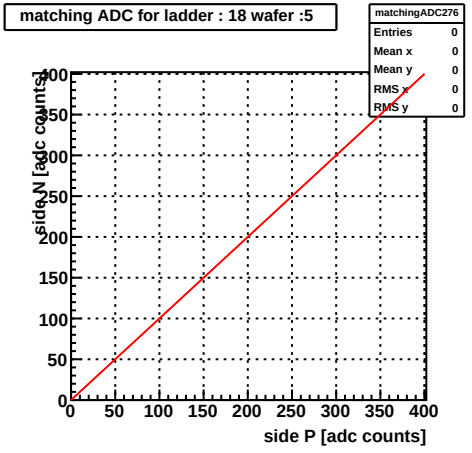

matching ADC for ladder : 18 wafer :4
matchingADC275
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

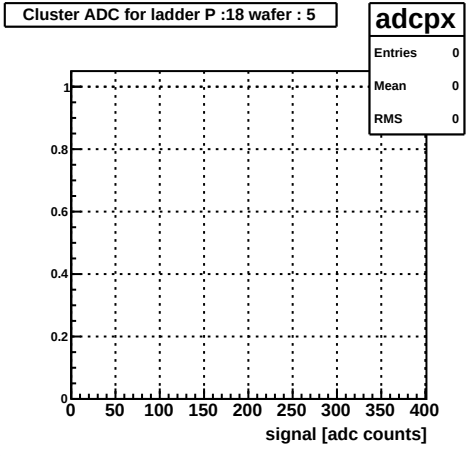

Cluster ADC for ladder P :18 wafer : 4
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

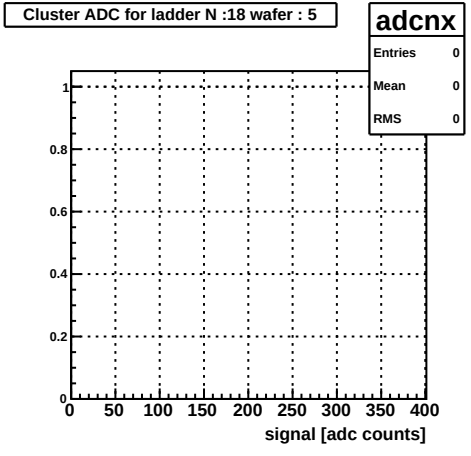

Cluster ADC for ladder N :18 wafer : 4
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

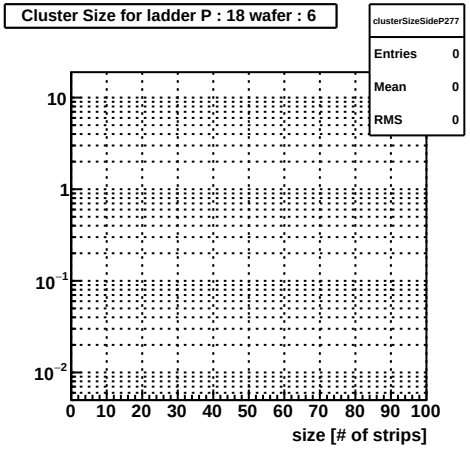

Cluster Size for ladder P : 18 wafer : 5
clusterSizeSideP276
Entries 0
Mean 0
RMS 0
size [# of strips]

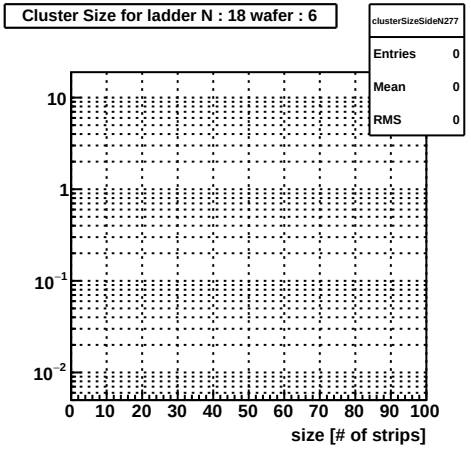

Cluster Size for ladder N : 18 wafer : 5
clusterSizeSideN276
Entries 0
Mean 0
RMS 0
size [# of strips]

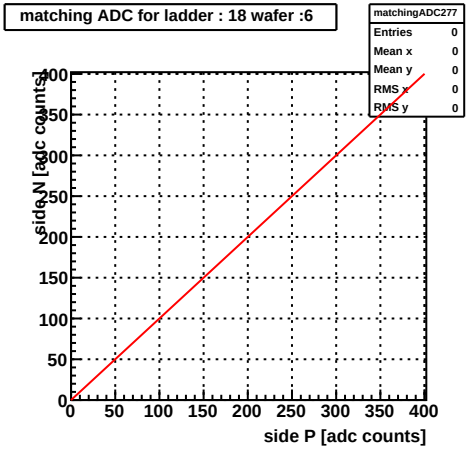

matching ADC for ladder : 18 wafer :5
matchingADC276
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

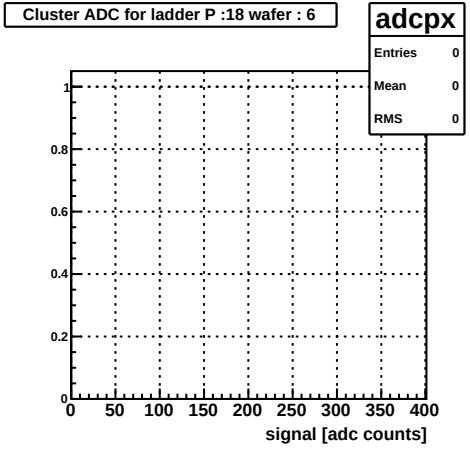

Cluster ADC for ladder P :18 wafer : 5
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

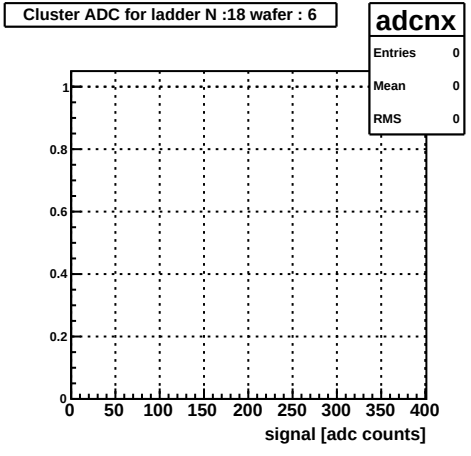

Cluster ADC for ladder N :18 wafer : 5
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

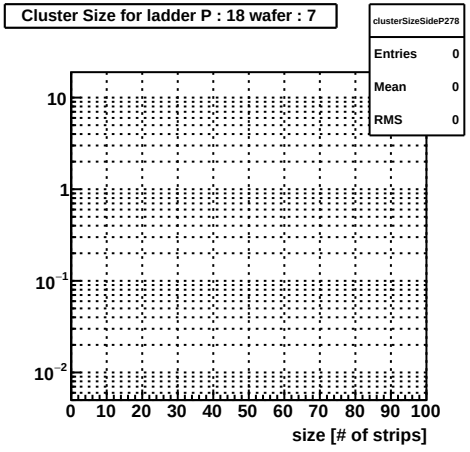

Cluster Size for ladder P : 18 wafer : 6
clusterSizeSideP277
Entries 0
Mean 0
RMS 0
size [# of strips]

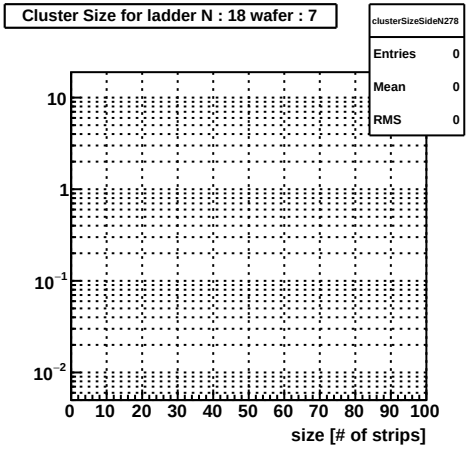

Cluster Size for ladder N : 18 wafer : 6
clusterSizeSideN277
Entries 0
Mean 0
RMS 0
size [# of strips]

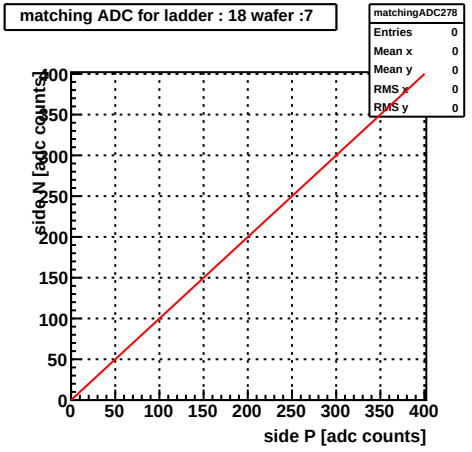

matching ADC for ladder : 18 wafer :6
matchingADC277
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

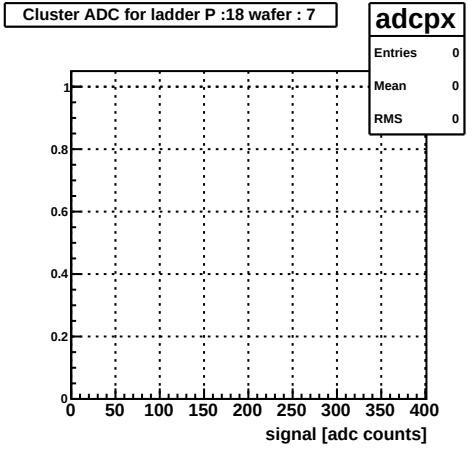

Cluster ADC for ladder P :18 wafer : 6
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

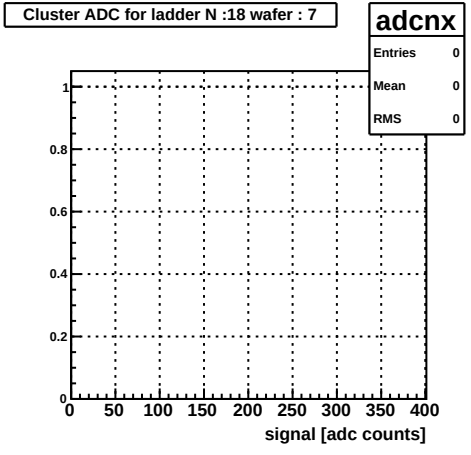

Cluster ADC for ladder N :18 wafer : 6
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

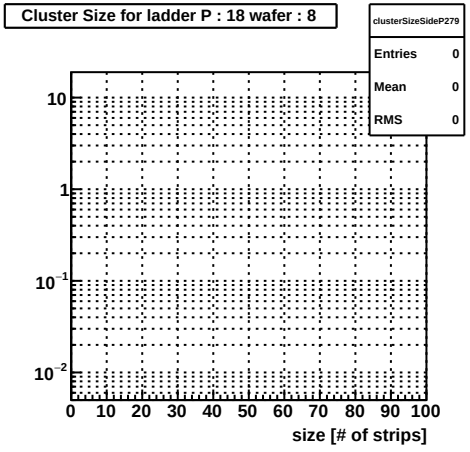

Cluster Size for ladder P : 18 wafer : 7
clusterSizeSideP278
Entries 0
Mean 0
RMS 0
size [# of strips]

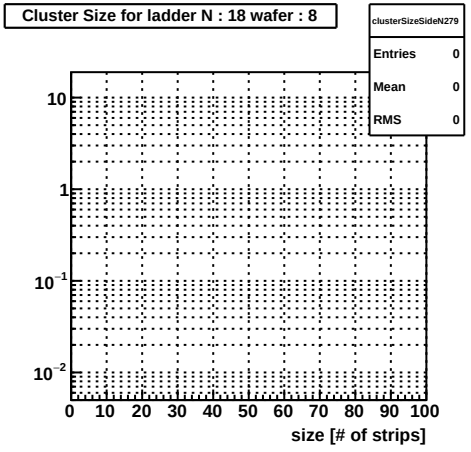

Cluster Size for ladder N : 18 wafer : 7
clusterSizeSideN278
Entries 0
Mean 0
RMS 0
size [# of strips]

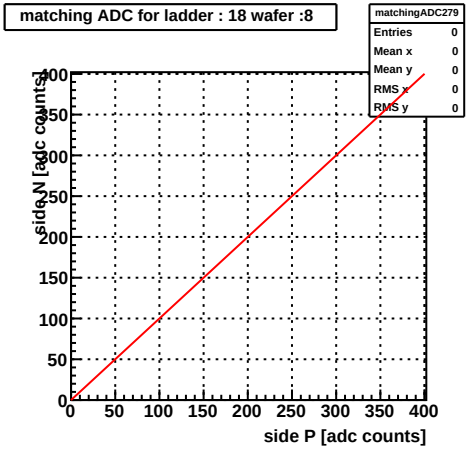

matching ADC for ladder : 18 wafer :7
matchingADC278
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

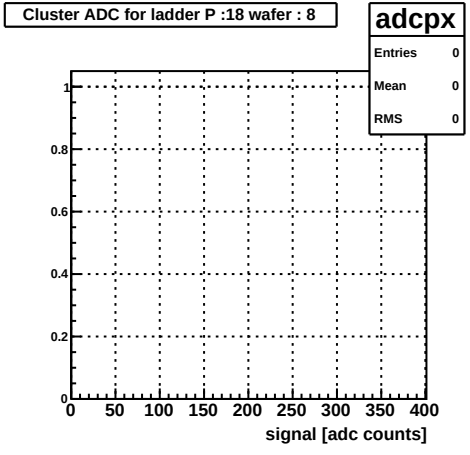

Cluster ADC for ladder P :18 wafer : 7
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

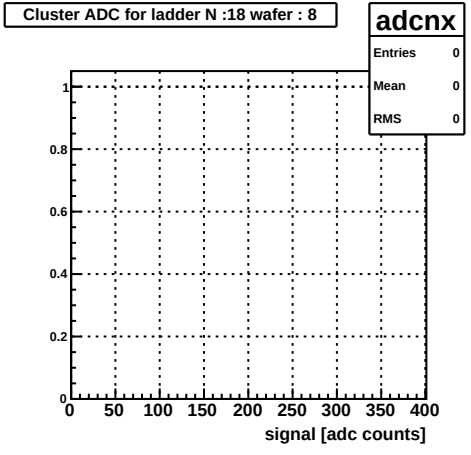

Cluster ADC for ladder N :18 wafer : 7
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

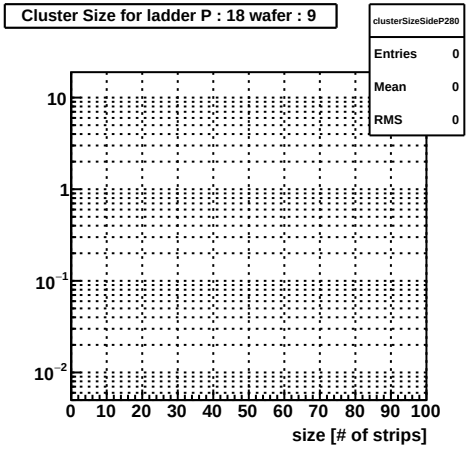

Cluster Size for ladder P : 18 wafer : 8
clusterSizeSideP279
Entries 0
Mean 0
RMS 0
size [# of strips]

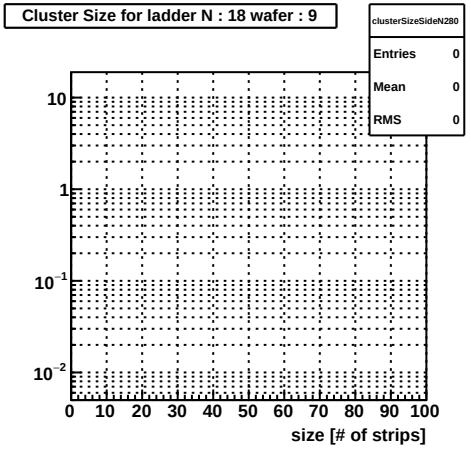

Cluster Size for ladder N : 18 wafer : 8
clusterSizeSideN279
Entries 0
Mean 0
RMS 0
size [# of strips]

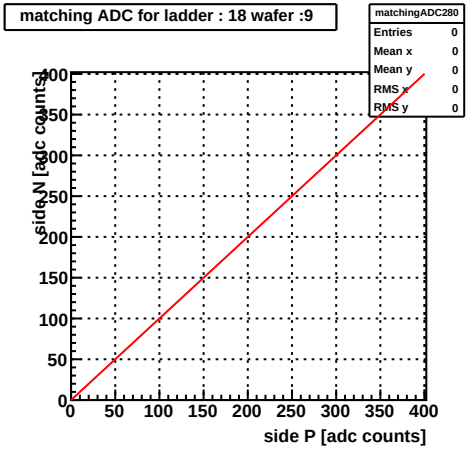

matching ADC for ladder : 18 wafer :8
matchingADC279
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

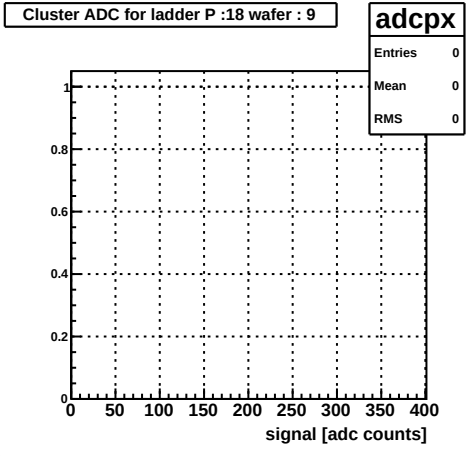

Cluster ADC for ladder P :18 wafer : 8
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

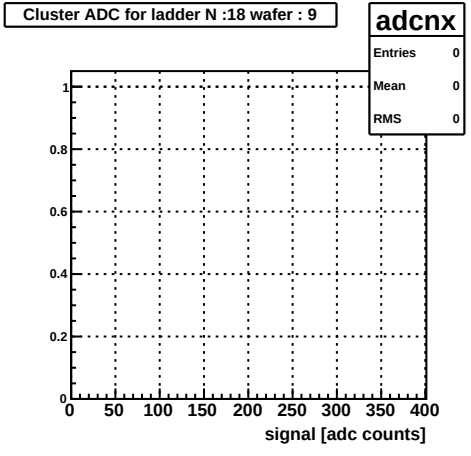

Cluster ADC for ladder N :18 wafer : 8
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

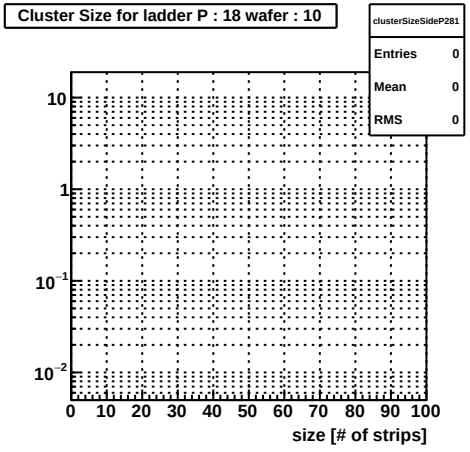

Cluster Size for ladder P : 18 wafer : 9
clusterSizeSideP280
Entries 0
Mean 0
RMS 0
size [# of strips]

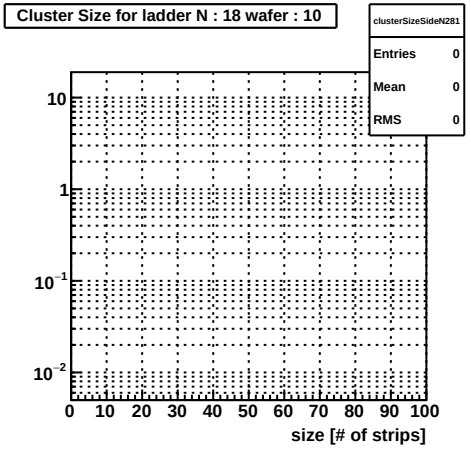

Cluster Size for ladder N : 18 wafer : 9
clusterSizeSideN280
Entries 0
Mean 0
RMS 0
size [# of strips]

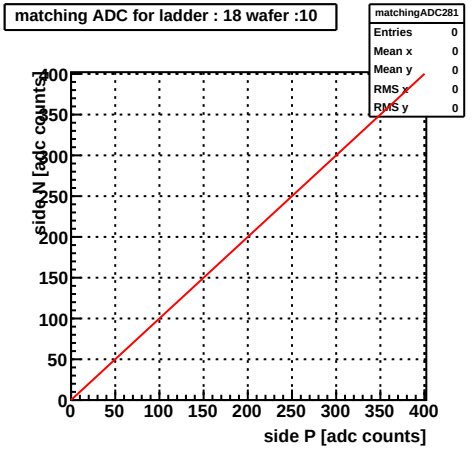

matching ADC for ladder : 18 wafer :9
matchingADC280
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

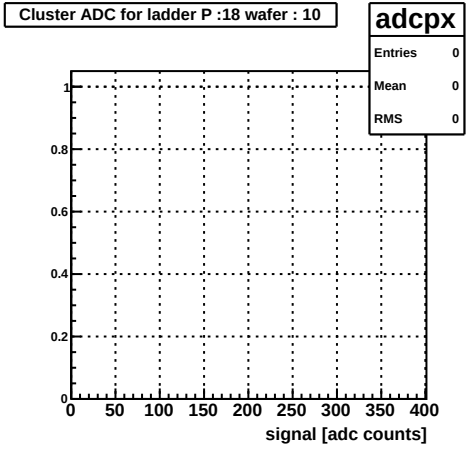

Cluster ADC for ladder P :18 wafer : 9
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

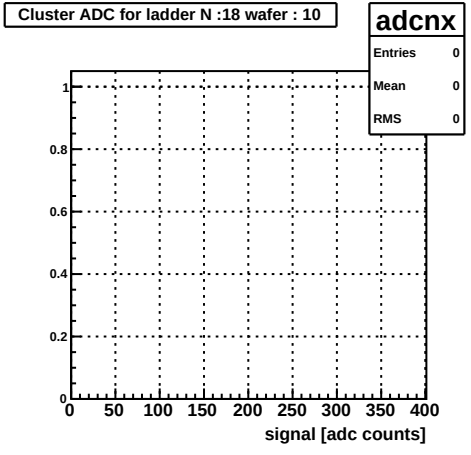

Cluster ADC for ladder N :18 wafer : 9
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

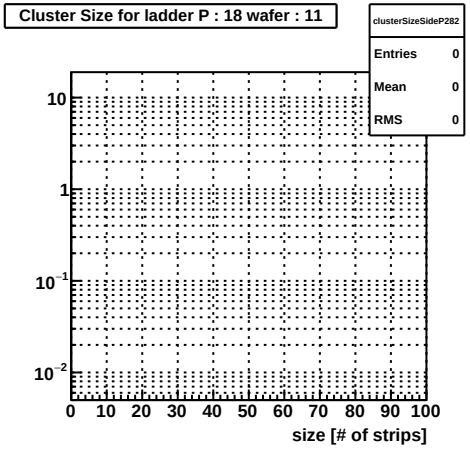

Cluster Size for ladder P : 18 wafer : 10
clusterSizeSideP281
Entries 0
Mean 0
RMS 0
size [# of strips]

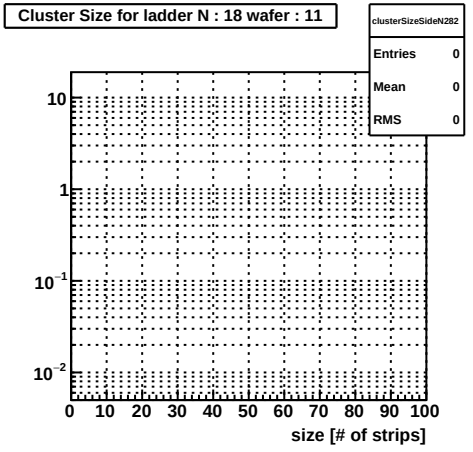

Cluster Size for ladder N : 18 wafer : 10
clusterSizeSideN281
Entries 0
Mean 0
RMS 0
size [# of strips]

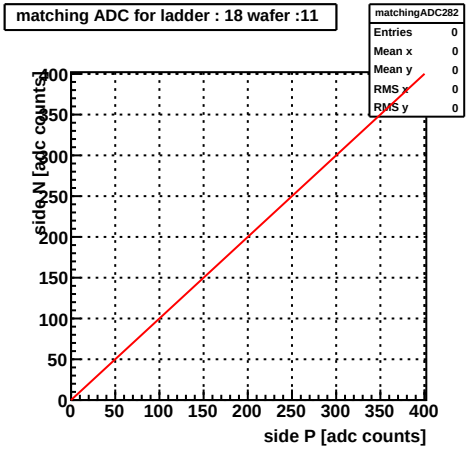

matching ADC for ladder : 18 wafer :10
matchingADC281
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

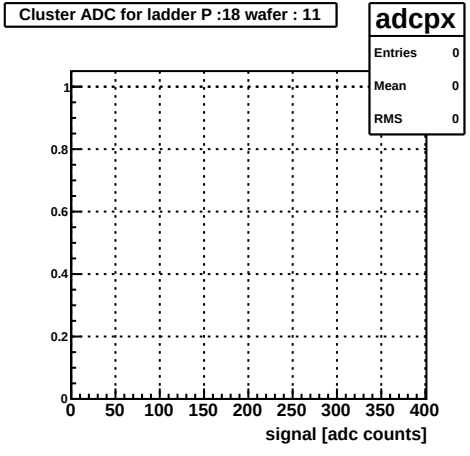

Cluster ADC for ladder P :18 wafer : 10
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

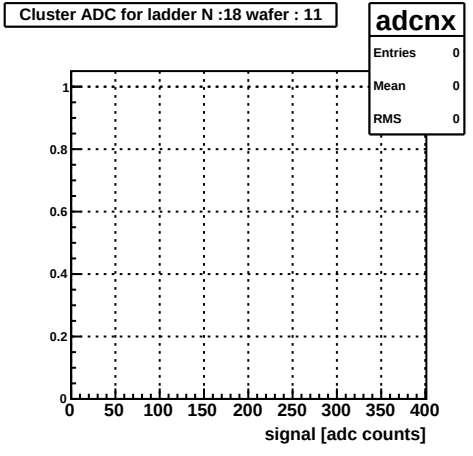

Cluster ADC for ladder N :18 wafer : 10
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

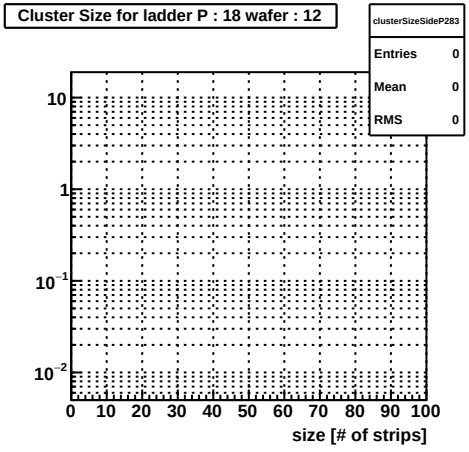

Cluster Size for ladder P : 18 wafer : 11
clusterSizeSideP282
Entries 0
Mean 0
RMS 0
size [# of strips]

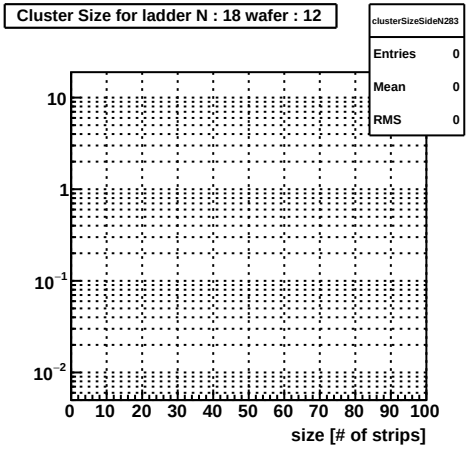

Cluster Size for ladder N : 18 wafer : 11
clusterSizeSideN282
Entries 0
Mean 0
RMS 0
size [# of strips]

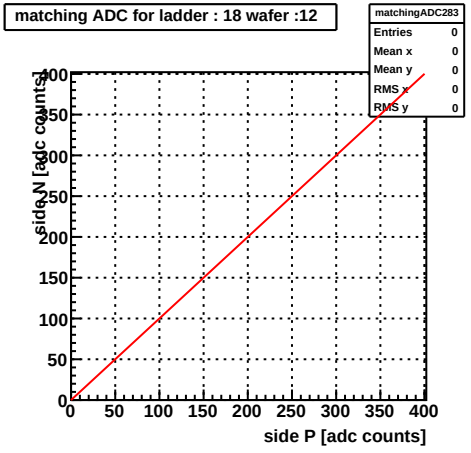

matching ADC for ladder : 18 wafer :11
matchingADC282
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

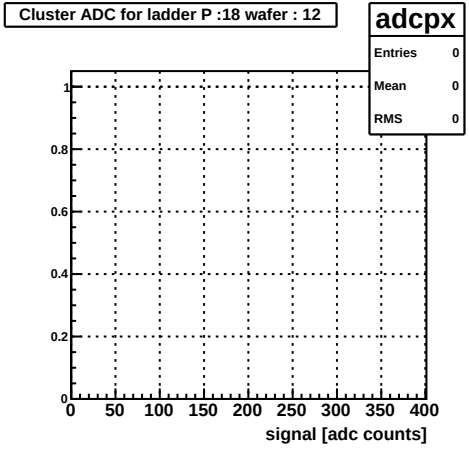

Cluster ADC for ladder P :18 wafer : 11
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

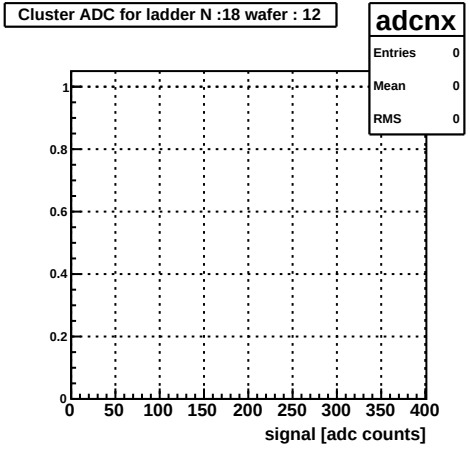

Cluster ADC for ladder N :18 wafer : 11
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

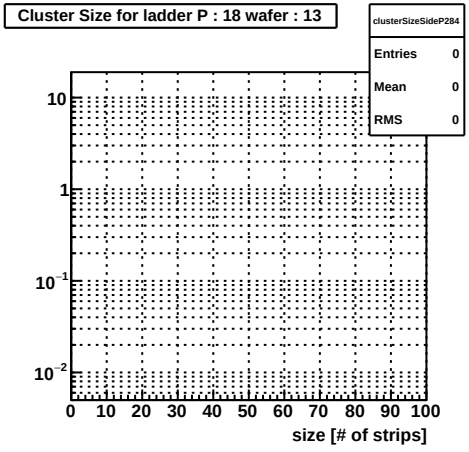

Cluster Size for ladder P : 18 wafer : 12
clusterSizeSideP283
Entries 0
Mean 0
RMS 0
size [# of strips]

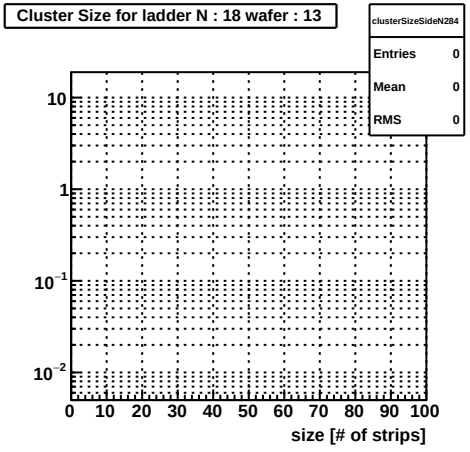

Cluster Size for ladder N : 18 wafer : 12
clusterSizeSideN283
Entries 0
Mean 0
RMS 0
size [# of strips]

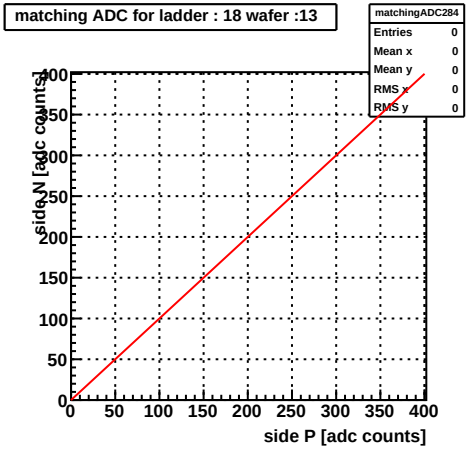

matching ADC for ladder : 18 wafer :12
matchingADC283
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

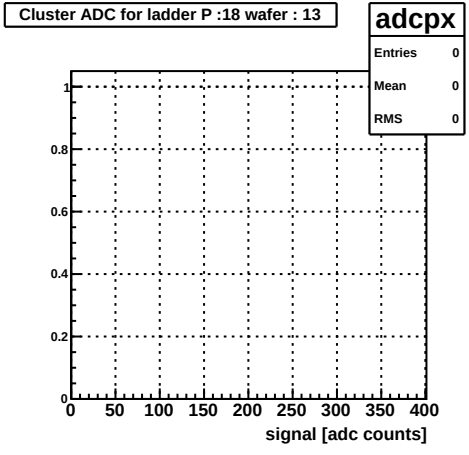

Cluster ADC for ladder P :18 wafer : 12
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

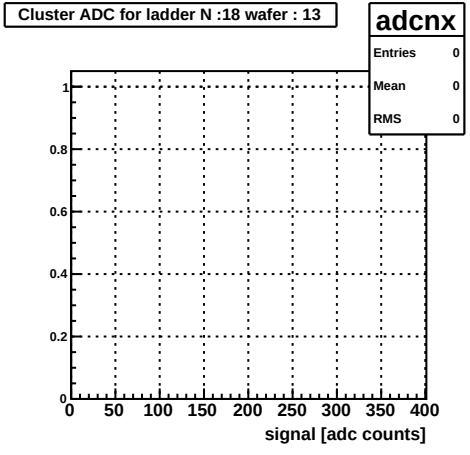

Cluster ADC for ladder N :18 wafer : 12
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

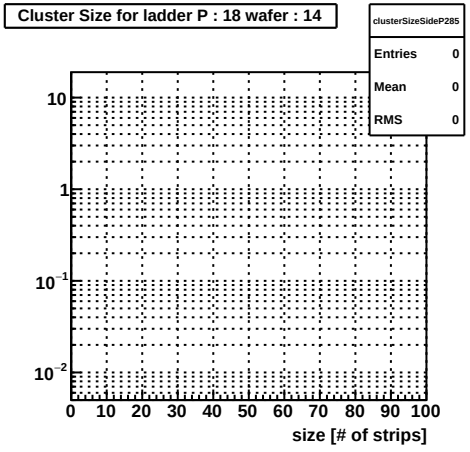

Cluster Size for ladder P : 18 wafer : 13
clusterSizeSideP284
Entries 0
Mean 0
RMS 0
size [# of strips]

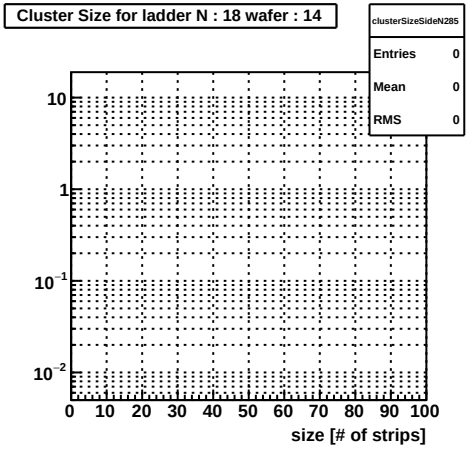

Cluster Size for ladder N : 18 wafer : 13
clusterSizeSideN284
Entries 0
Mean 0
RMS 0
size [# of strips]

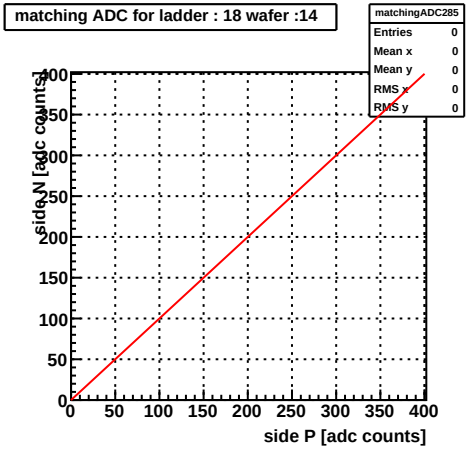

matching ADC for ladder : 18 wafer :13
matchingADC284
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

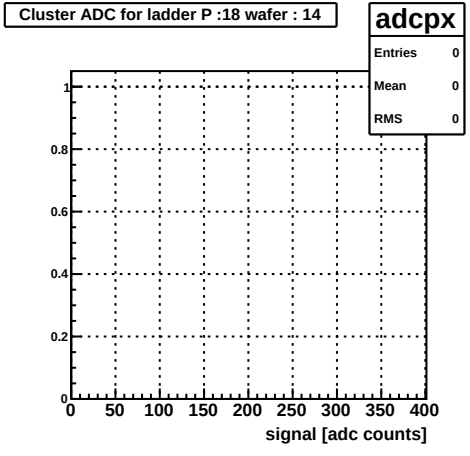

Cluster ADC for ladder P :18 wafer : 13
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

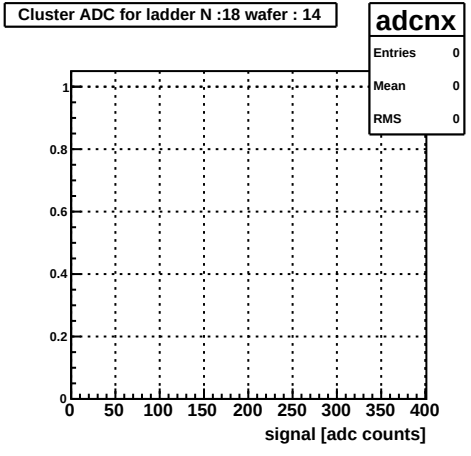

Cluster ADC for ladder N :18 wafer : 13
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

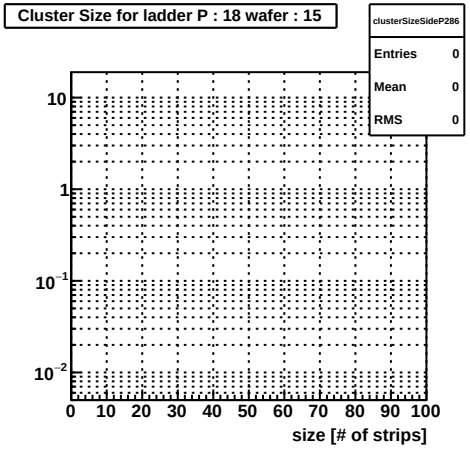

Cluster Size for ladder P : 18 wafer : 14
clusterSizeSideP285
Entries 0
Mean 0
RMS 0
size [# of strips]

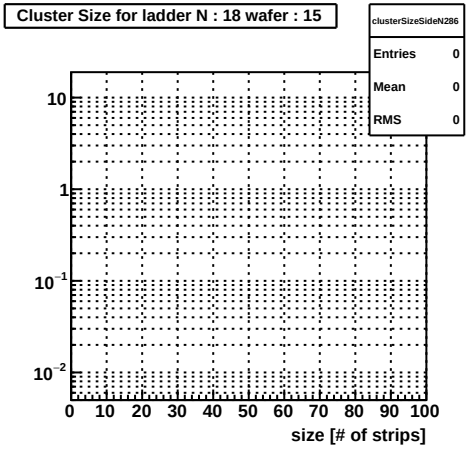

Cluster Size for ladder N : 18 wafer : 14
clusterSizeSideN285
Entries 0
Mean 0
RMS 0
size [# of strips]

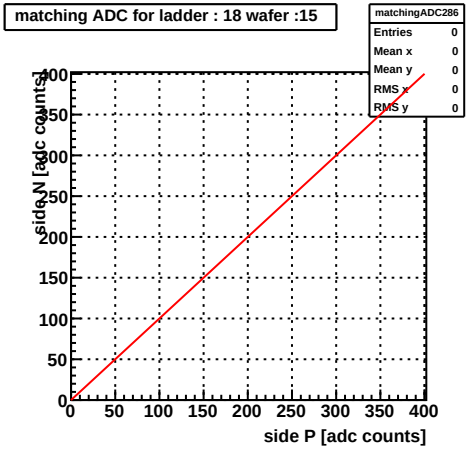

matching ADC for ladder : 18 wafer :14
matchingADC285
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

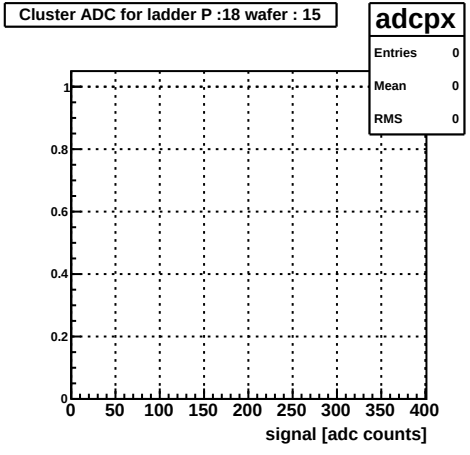

Cluster ADC for ladder P :18 wafer : 14
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

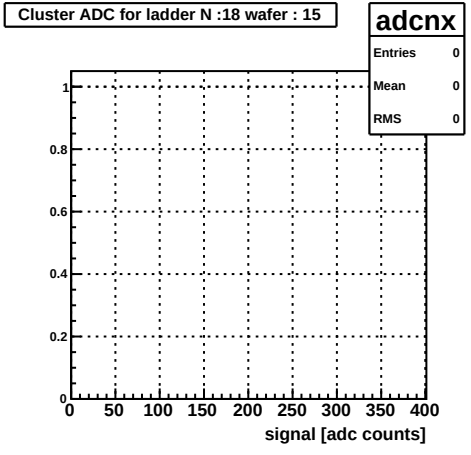

Cluster ADC for ladder N :18 wafer : 14
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

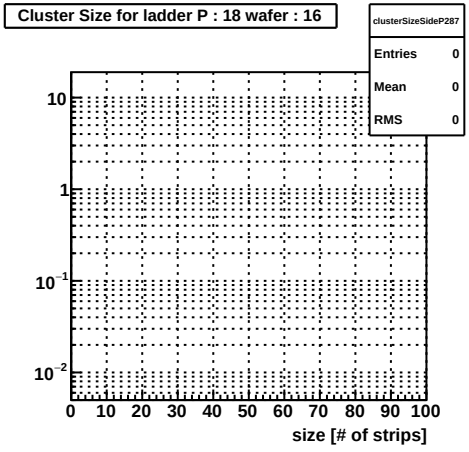

Cluster Size for ladder P : 18 wafer : 15
clusterSizeSideP286
Entries 0
Mean 0
RMS 0
size [# of strips]

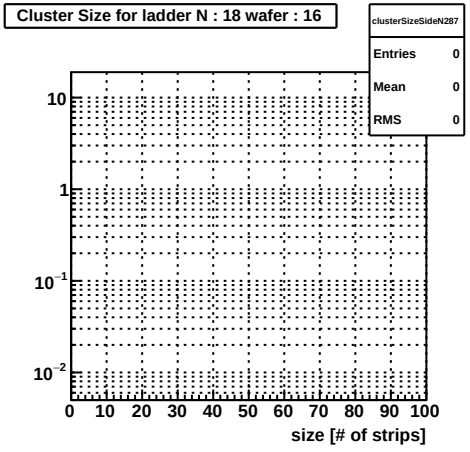

Cluster Size for ladder N : 18 wafer : 15
clusterSizeSideN286
Entries 0
Mean 0
RMS 0
size [# of strips]

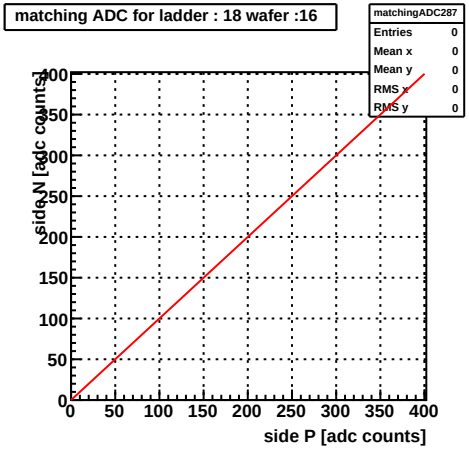

matching ADC for ladder : 18 wafer :15
matchingADC286
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

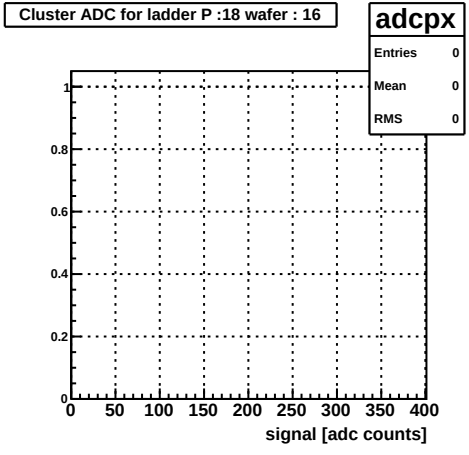

Cluster ADC for ladder P :18 wafer : 15
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

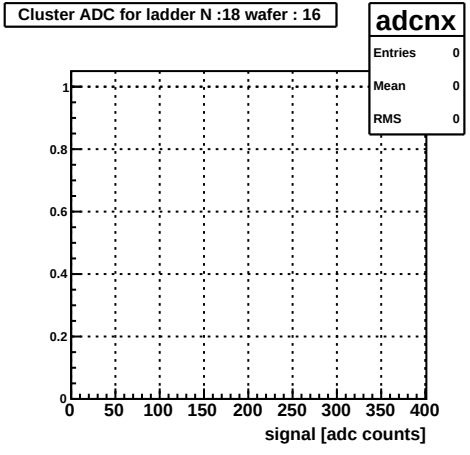

Cluster ADC for ladder N :18 wafer : 15
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

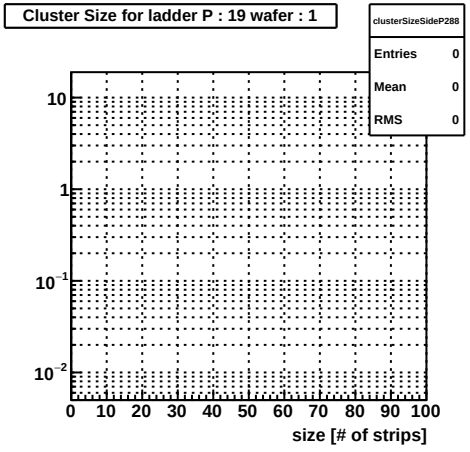

Cluster Size for ladder P : 18 wafer : 16
clusterSizeSideP287
Entries 0
Mean 0
RMS 0
size [# of strips]

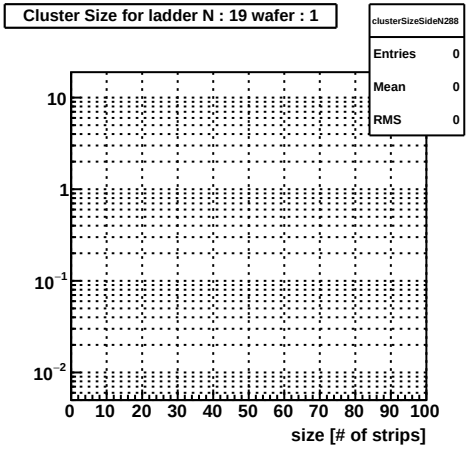

Cluster Size for ladder N : 18 wafer : 16
clusterSizeSideN287
Entries 0
Mean 0
RMS 0
size [# of strips]

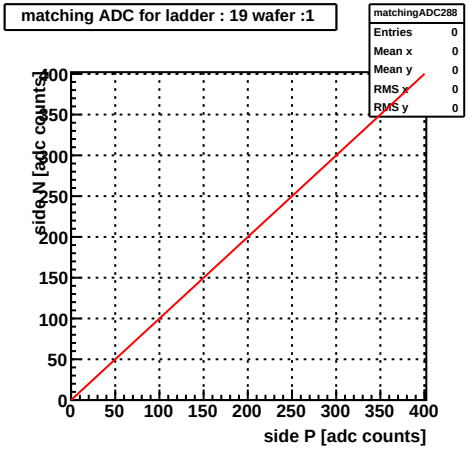

matching ADC for ladder : 18 wafer :16
matchingADC287
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

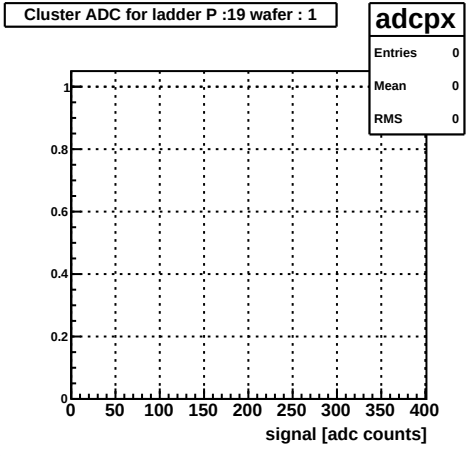

Cluster ADC for ladder P :18 wafer : 16
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

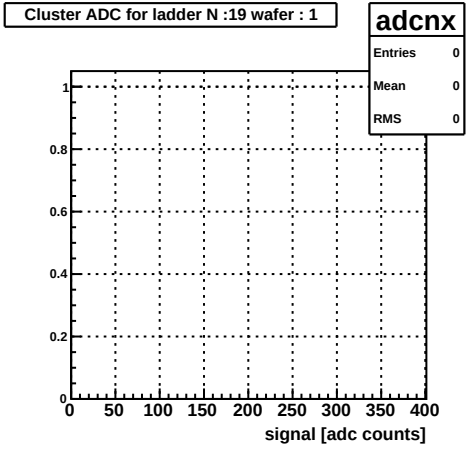

Cluster ADC for ladder N :18 wafer : 16
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

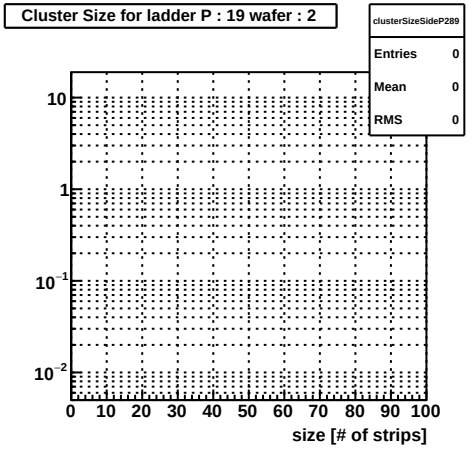

Cluster Size for ladder P : 19 wafer : 1
clusterSizeSideP288
Entries 0
Mean 0
RMS 0
size [# of strips]

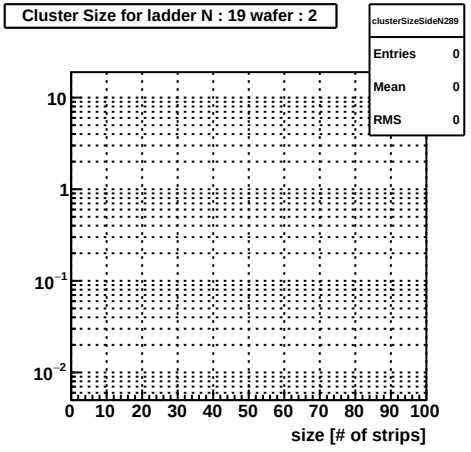

Cluster Size for ladder N : 19 wafer : 1
clusterSizeSideN288
Entries 0
Mean 0
RMS 0
size [# of strips]

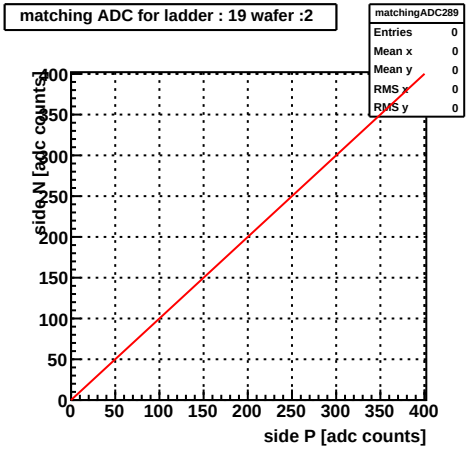

matching ADC for ladder : 19 wafer :1
matchingADC288
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

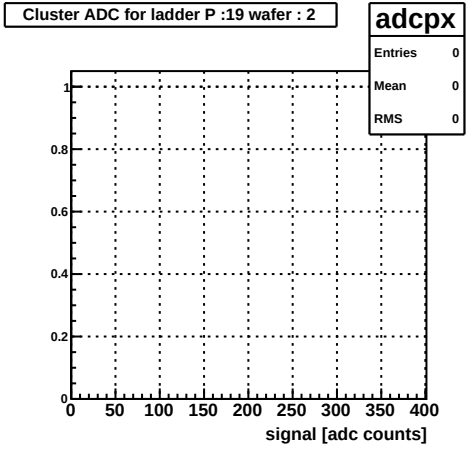

Cluster ADC for ladder P :19 wafer : 1
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

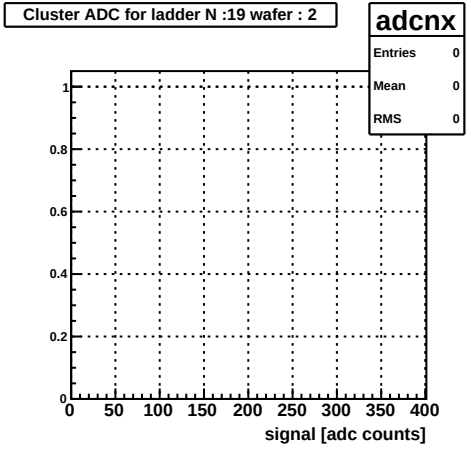

Cluster ADC for ladder N :19 wafer : 1
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

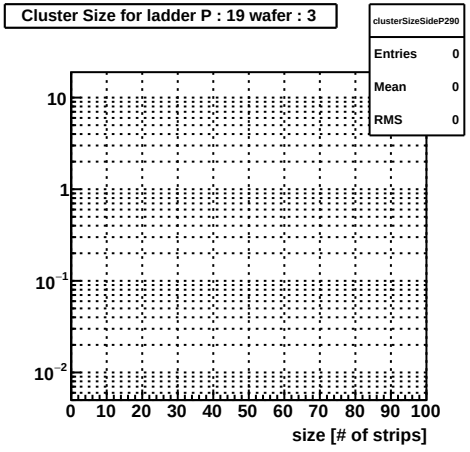

Cluster Size for ladder P : 19 wafer : 2
clusterSizeSideP289
Entries 0
Mean 0
RMS 0
size [# of strips]

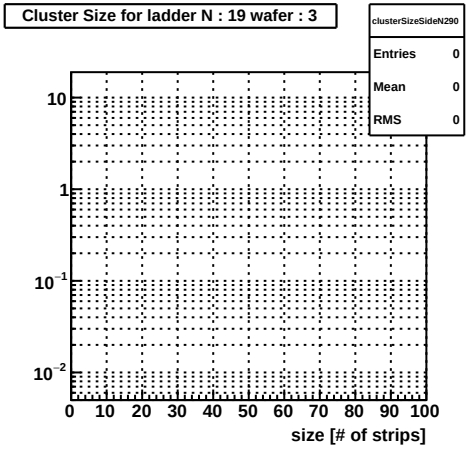

Cluster Size for ladder N : 19 wafer : 2
clusterSizeSideN289
Entries 0
Mean 0
RMS 0
size [# of strips]

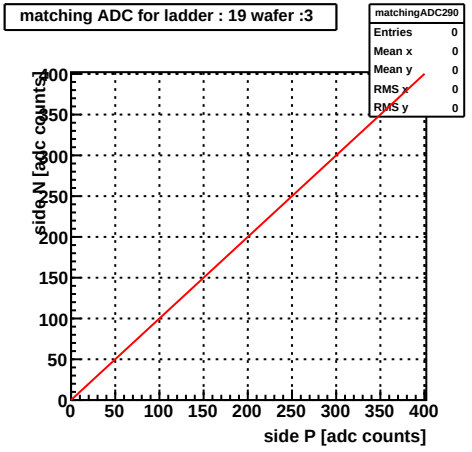

matching ADC for ladder : 19 wafer :2
matchingADC289
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

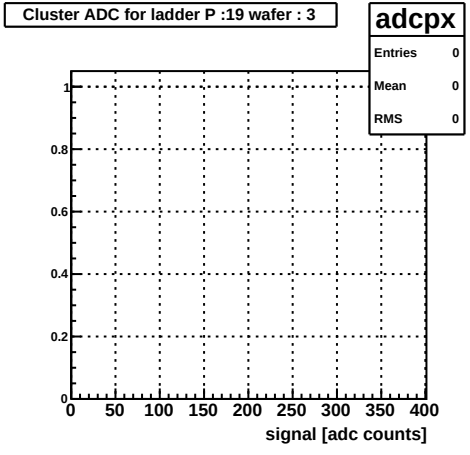

Cluster ADC for ladder P :19 wafer : 2
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

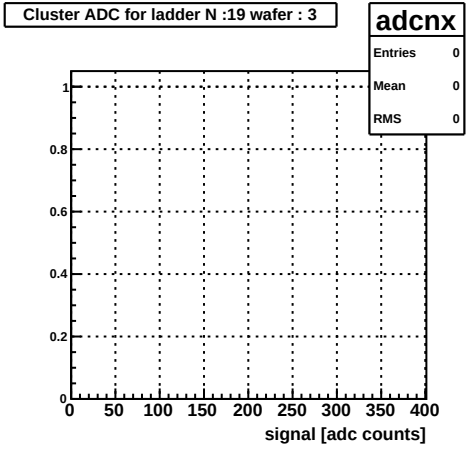

Cluster ADC for ladder N :19 wafer : 2
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

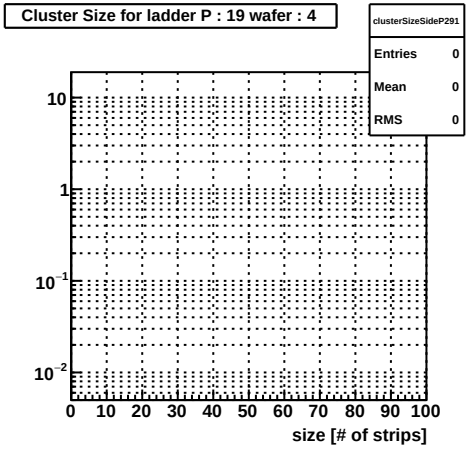

Cluster Size for ladder P : 19 wafer : 3
clusterSizeSideP290
Entries 0
Mean 0
RMS 0
size [# of strips]

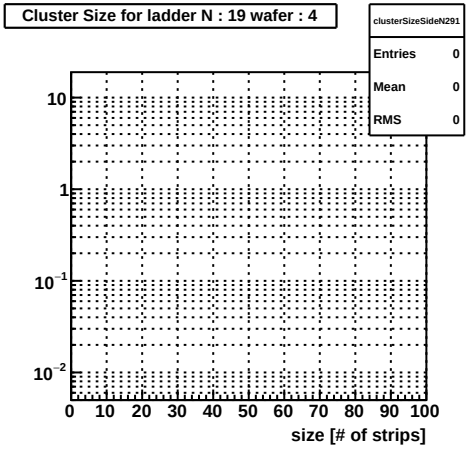

Cluster Size for ladder N : 19 wafer : 3
clusterSizeSideN290
Entries 0
Mean 0
RMS 0
size [# of strips]

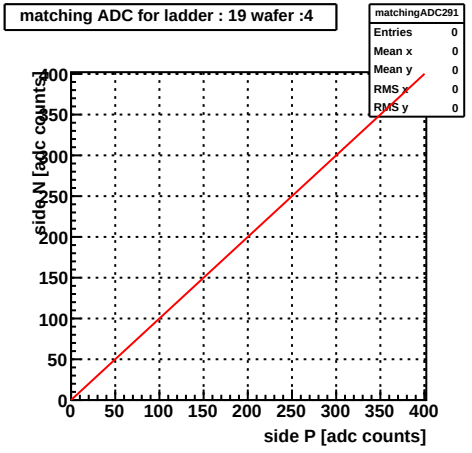

matching ADC for ladder : 19 wafer :3
matchingADC290
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

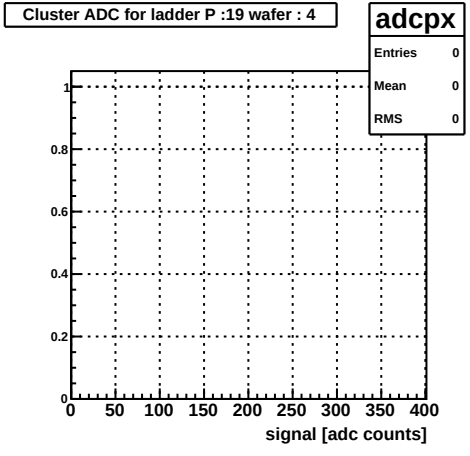

Cluster ADC for ladder P :19 wafer : 3
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

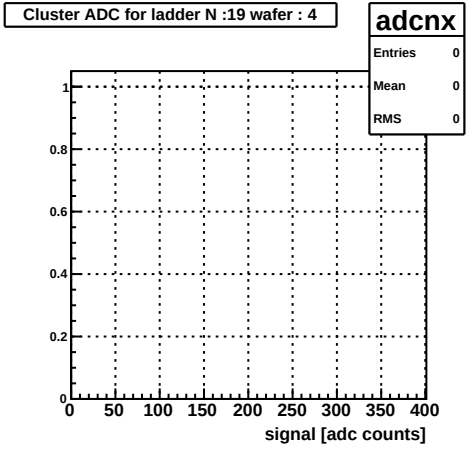

Cluster ADC for ladder N :19 wafer : 3
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

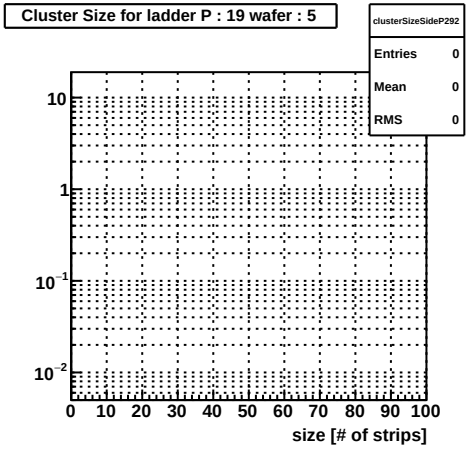

Cluster Size for ladder P : 19 wafer : 4
clusterSizeSideP291
Entries 0
Mean 0
RMS 0
size [# of strips]

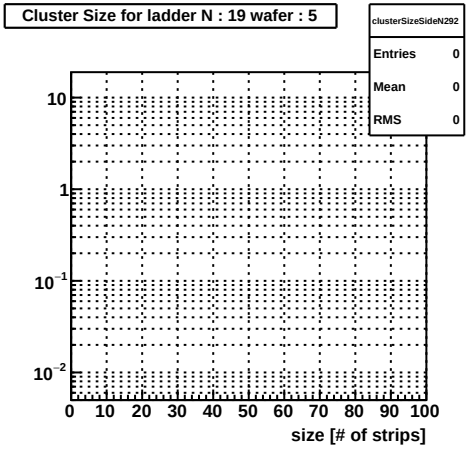

Cluster Size for ladder N : 19 wafer : 4
clusterSizeSideN291
Entries 0
Mean 0
RMS 0
size [# of strips]

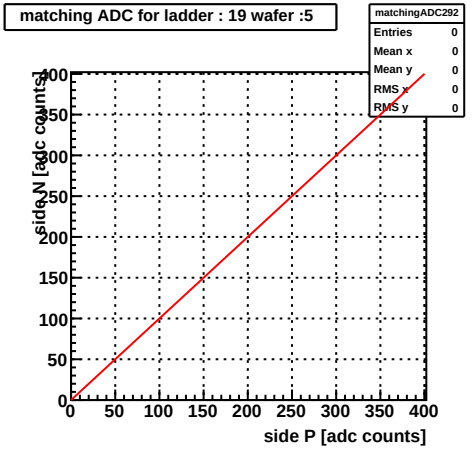

matching ADC for ladder : 19 wafer :4
matchingADC291
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

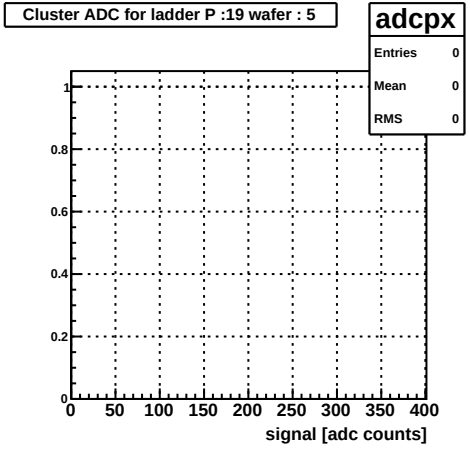

Cluster ADC for ladder P :19 wafer : 4
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

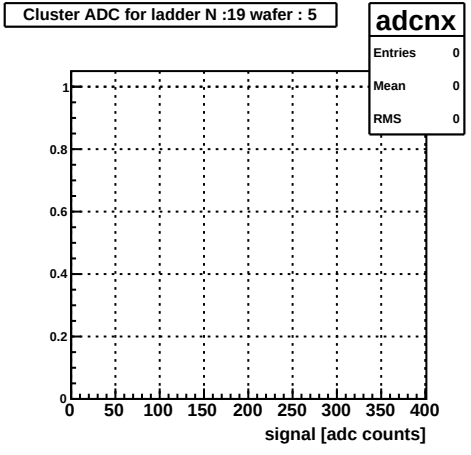

Cluster ADC for ladder N :19 wafer : 4
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

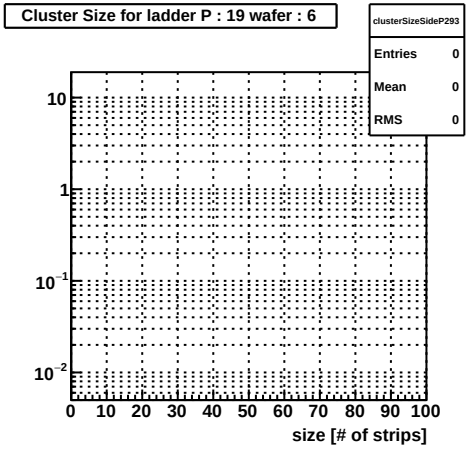

Cluster Size for ladder P : 19 wafer : 5
clusterSizeSideP292
Entries 0
Mean 0
RMS 0
size [# of strips]

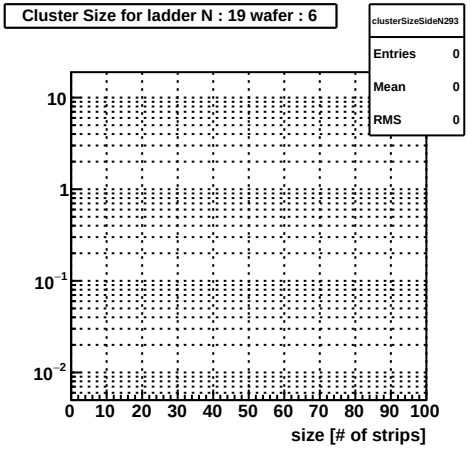

Cluster Size for ladder N : 19 wafer : 5
clusterSizeSideN292
Entries 0
Mean 0
RMS 0
size [# of strips]

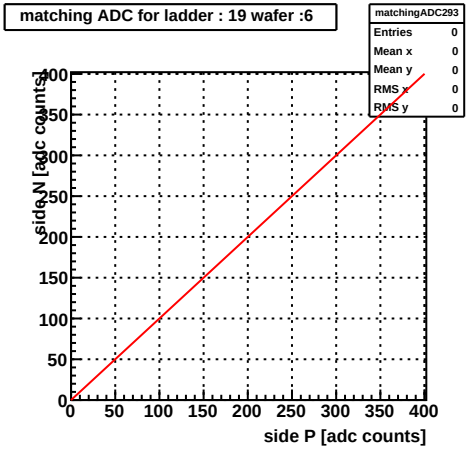

matching ADC for ladder : 19 wafer :5
matchingADC292
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

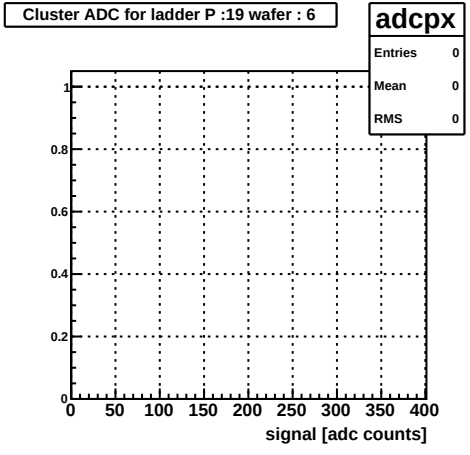

Cluster ADC for ladder P :19 wafer : 5
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

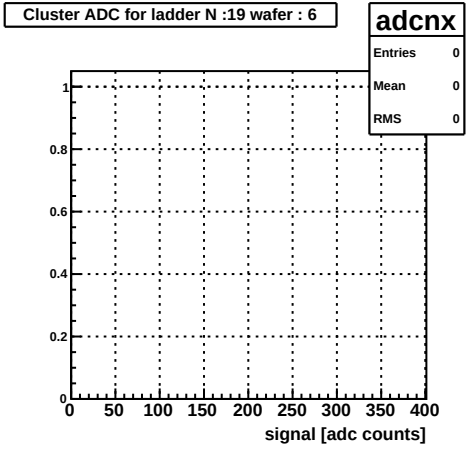

Cluster ADC for ladder N :19 wafer : 5
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

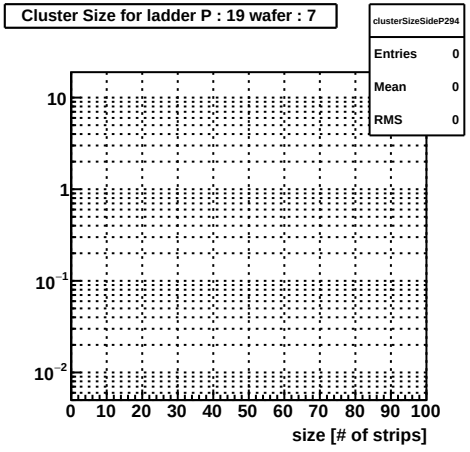

Cluster Size for ladder P : 19 wafer : 6
clusterSizeSideP293
Entries 0
Mean 0
RMS 0
size [# of strips]

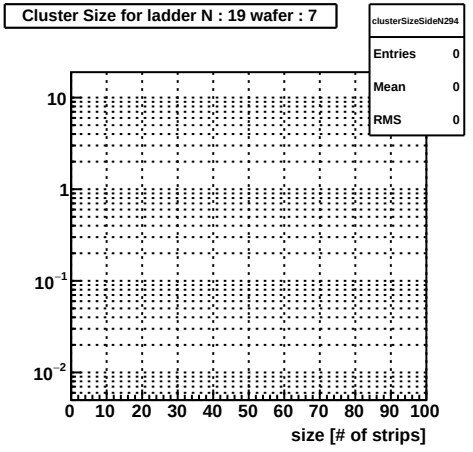

Cluster Size for ladder N : 19 wafer : 6
clusterSizeSideN293
Entries 0
Mean 0
RMS 0
size [# of strips]

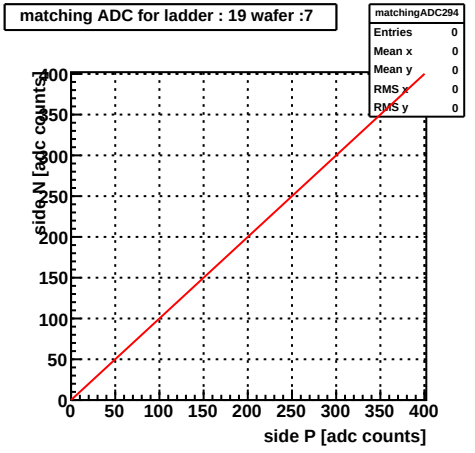

matching ADC for ladder : 19 wafer :6
matchingADC293
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

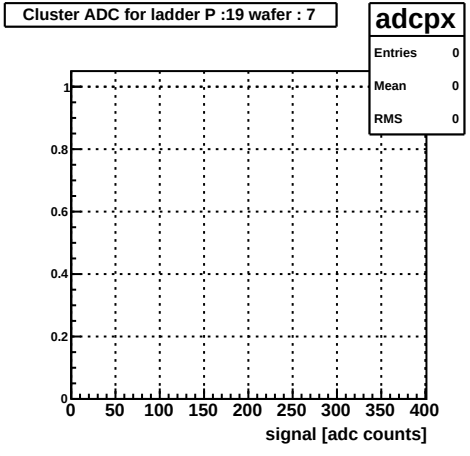

Cluster ADC for ladder P :19 wafer : 6
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

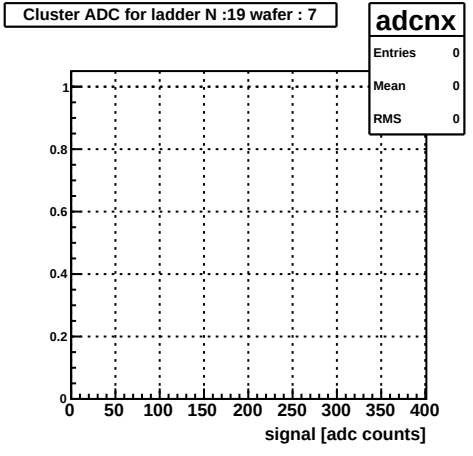

Cluster ADC for ladder N :19 wafer : 6
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

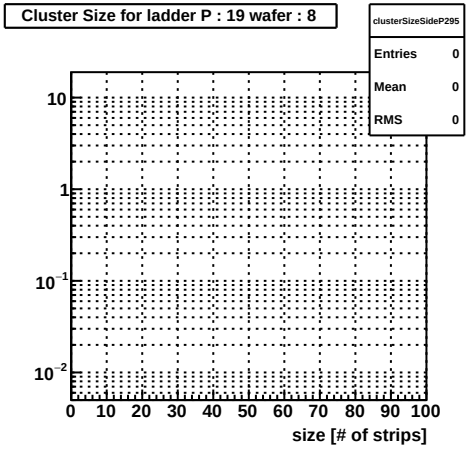

Cluster Size for ladder P : 19 wafer : 7
clusterSizeSideP294
Entries 0
Mean 0
RMS 0
size [# of strips]

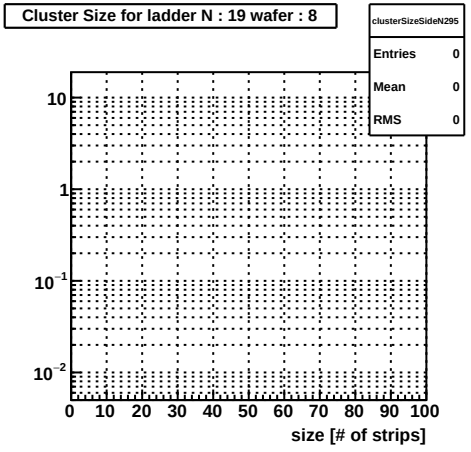

Cluster Size for ladder N : 19 wafer : 7
clusterSizeSideN294
Entries 0
Mean 0
RMS 0
size [# of strips]

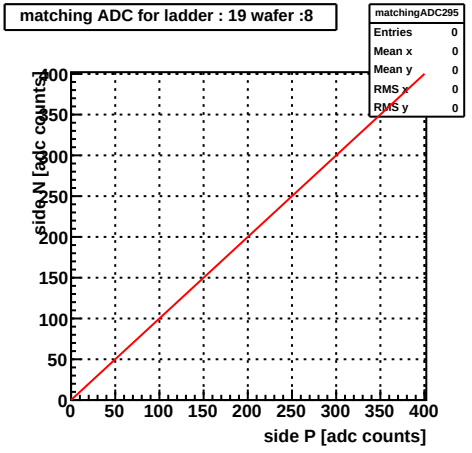

matching ADC for ladder : 19 wafer :7
matchingADC294
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

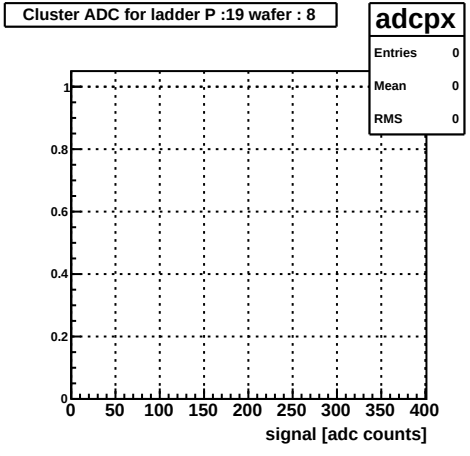

Cluster ADC for ladder P :19 wafer : 7
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

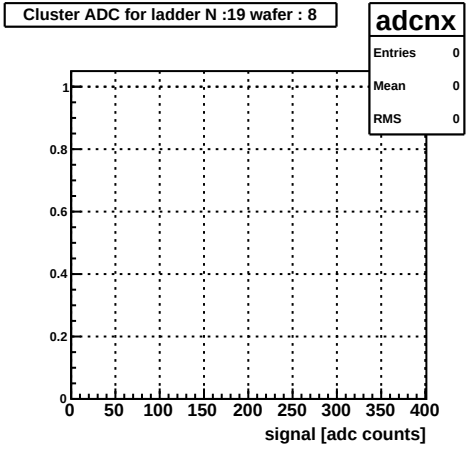

Cluster ADC for ladder N :19 wafer : 7
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

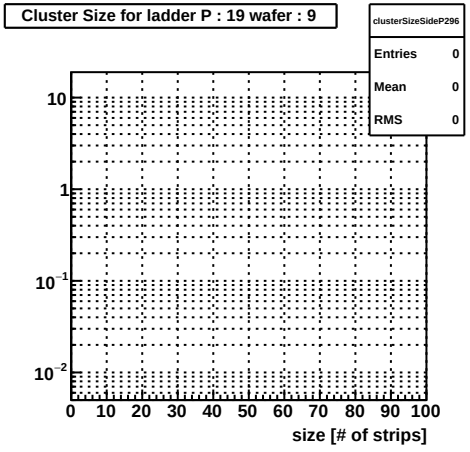

Cluster Size for ladder P : 19 wafer : 8
clusterSizeSideP295
Entries 0
Mean 0
RMS 0
size [# of strips]

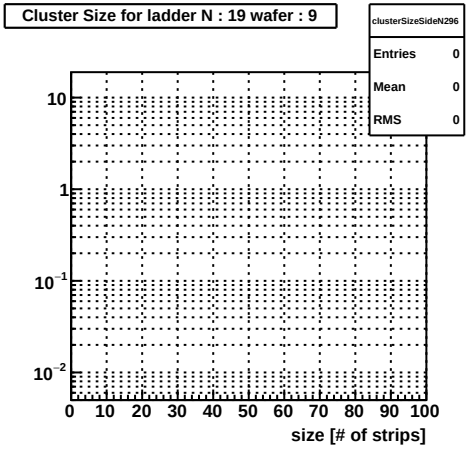

Cluster Size for ladder N : 19 wafer : 8
clusterSizeSideN295
Entries 0
Mean 0
RMS 0
size [# of strips]

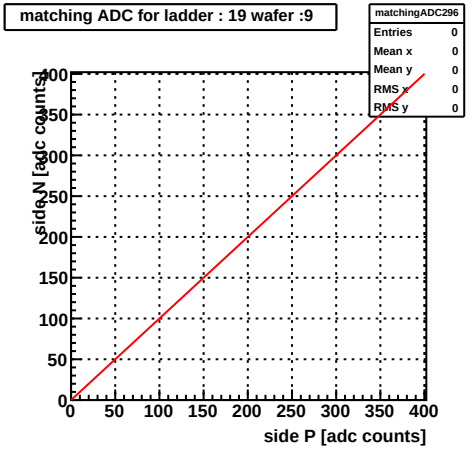

matching ADC for ladder : 19 wafer :8
matchingADC295
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

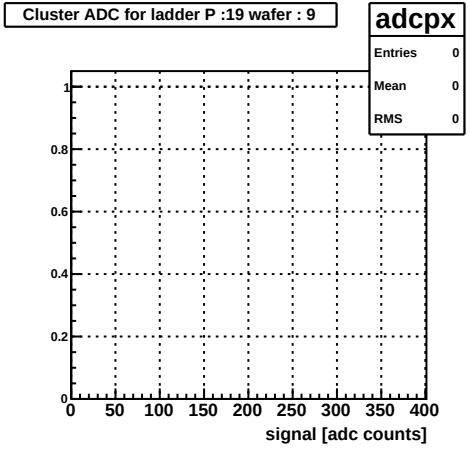

Cluster ADC for ladder P :19 wafer : 8
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

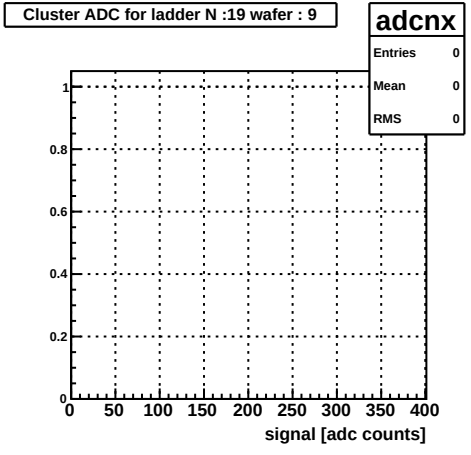

Cluster ADC for ladder N :19 wafer : 8
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

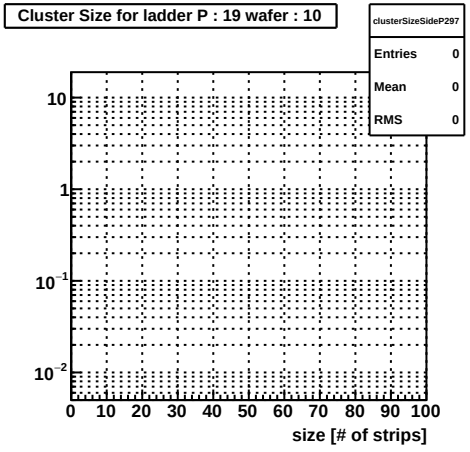

Cluster Size for ladder P : 19 wafer : 9
clusterSizeSideP296
Entries 0
Mean 0
RMS 0
size [# of strips]

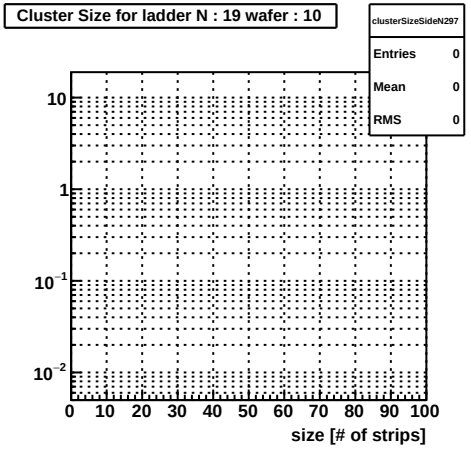

Cluster Size for ladder N : 19 wafer : 9
clusterSizeSideN296
Entries 0
Mean 0
RMS 0
size [# of strips]

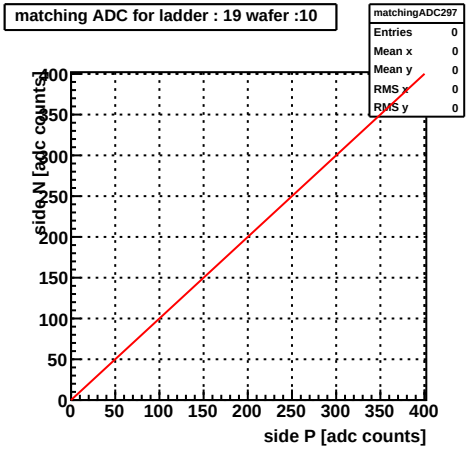

matching ADC for ladder : 19 wafer :9
matchingADC296
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

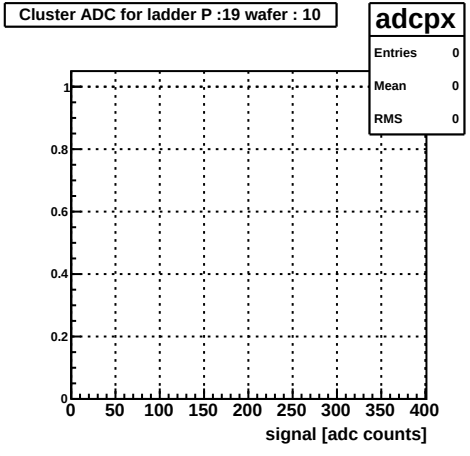

Cluster ADC for ladder P :19 wafer : 9
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

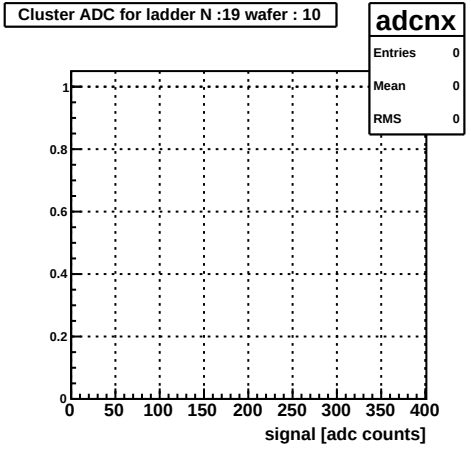

Cluster ADC for ladder N :19 wafer : 9
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

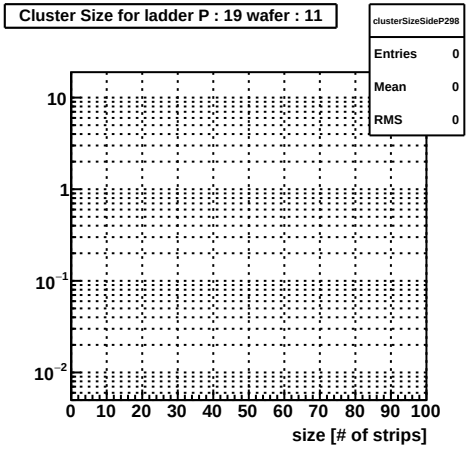

Cluster Size for ladder P : 19 wafer : 10
clusterSizeSideP297
Entries 0
Mean 0
RMS 0
size [# of strips]

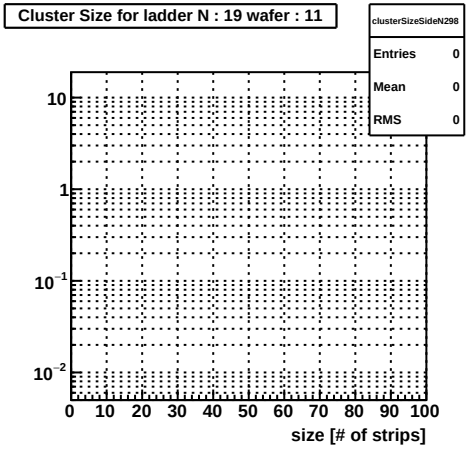

Cluster Size for ladder N : 19 wafer : 10
clusterSizeSideN297
Entries 0
Mean 0
RMS 0
size [# of strips]

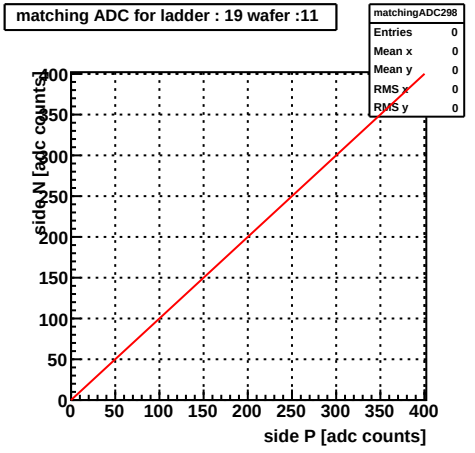

matching ADC for ladder : 19 wafer :10
matchingADC297
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

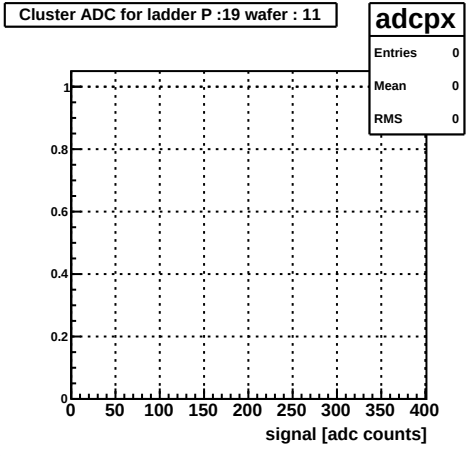

Cluster ADC for ladder P :19 wafer : 10
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

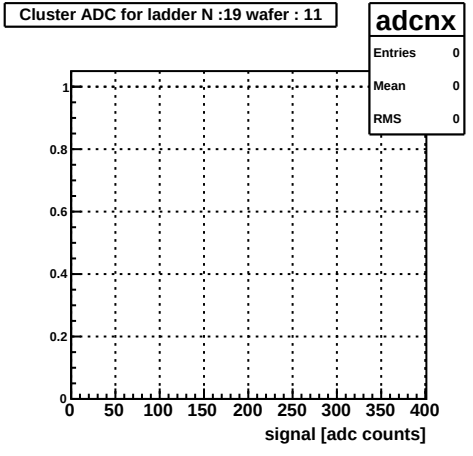

Cluster ADC for ladder N :19 wafer : 10
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

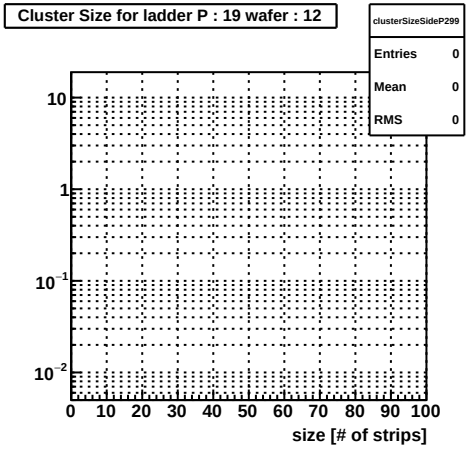

Cluster Size for ladder P : 19 wafer : 11
clusterSizeSideP298
Entries 0
Mean 0
RMS 0
size [# of strips]

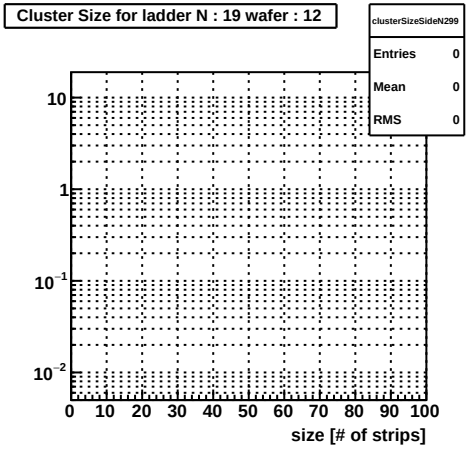

Cluster Size for ladder N : 19 wafer : 11
clusterSizeSideN298
Entries 0
Mean 0
RMS 0
size [# of strips]

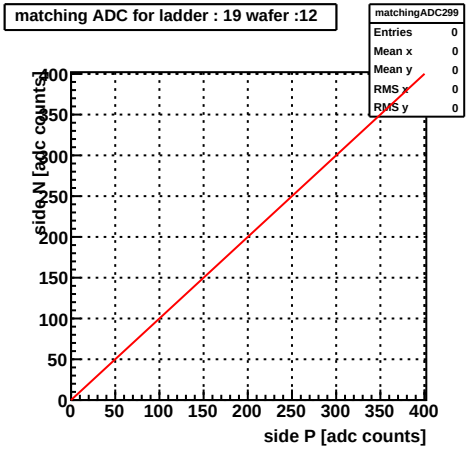

matching ADC for ladder : 19 wafer :11
matchingADC298
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

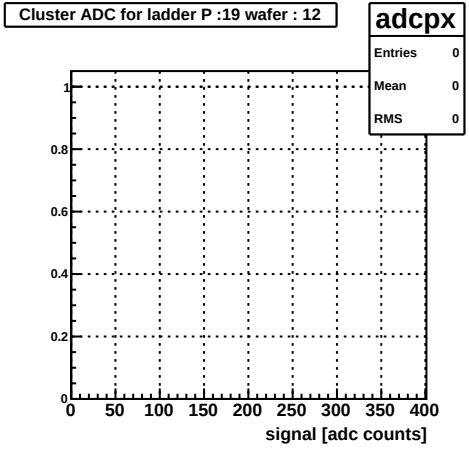

Cluster ADC for ladder P :19 wafer : 11
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

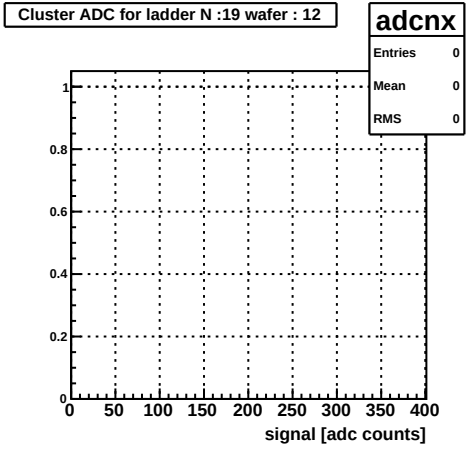

Cluster ADC for ladder N :19 wafer : 11
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

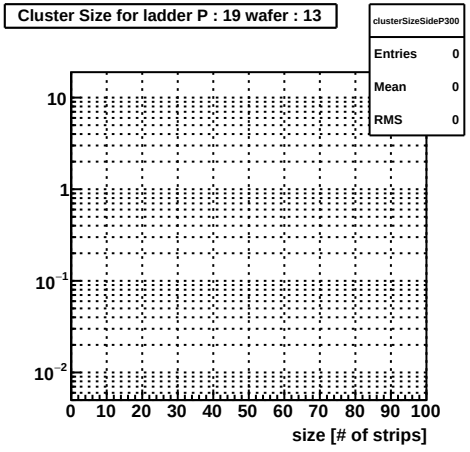

Cluster Size for ladder P : 19 wafer : 12
clusterSizeSideP299
Entries 0
Mean 0
RMS 0
size [# of strips]

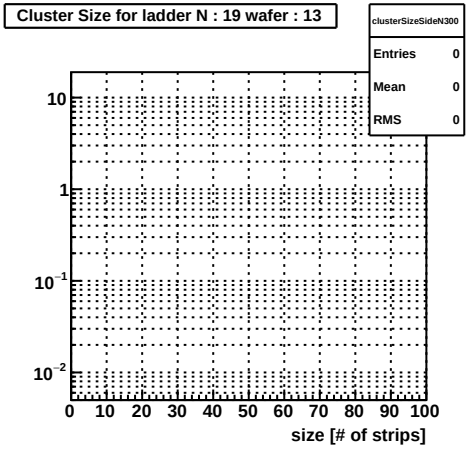

Cluster Size for ladder N : 19 wafer : 12
clusterSizeSideN299
Entries 0
Mean 0
RMS 0
size [# of strips]

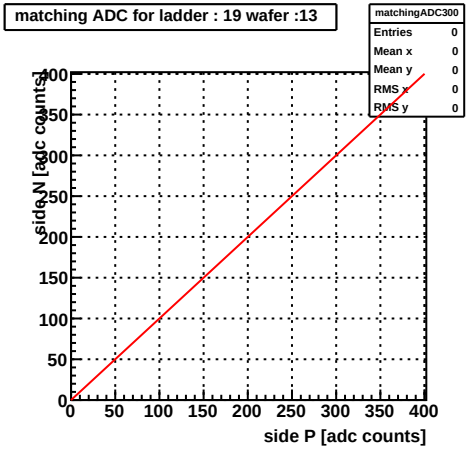

matching ADC for ladder : 19 wafer :12
matchingADC299
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

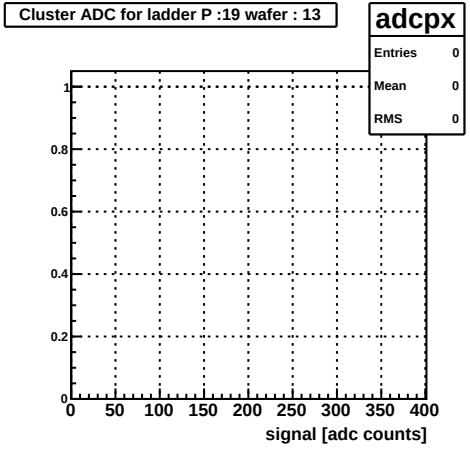

Cluster ADC for ladder P :19 wafer : 12
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

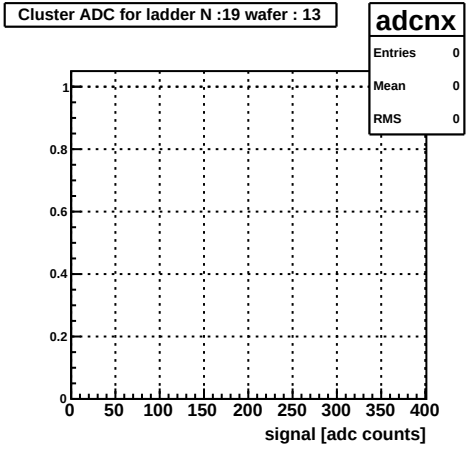

Cluster ADC for ladder N :19 wafer : 12
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

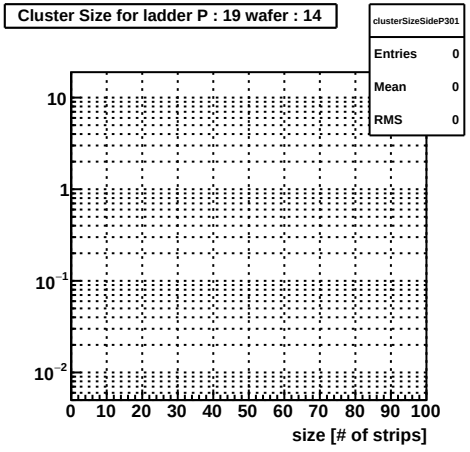

Cluster Size for ladder P : 19 wafer : 13
clusterSizeSideP300
Entries 0
Mean 0
RMS 0
size [# of strips]

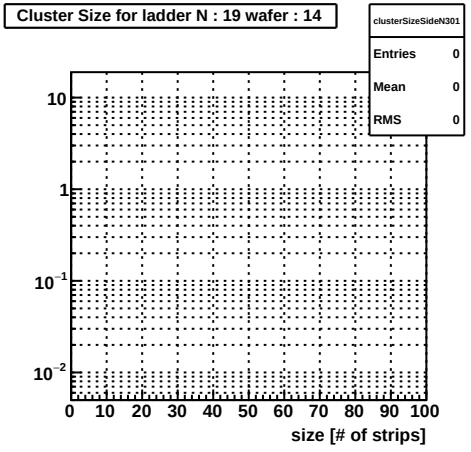

Cluster Size for ladder N : 19 wafer : 13
clusterSizeSideN300
Entries 0
Mean 0
RMS 0
size [# of strips]

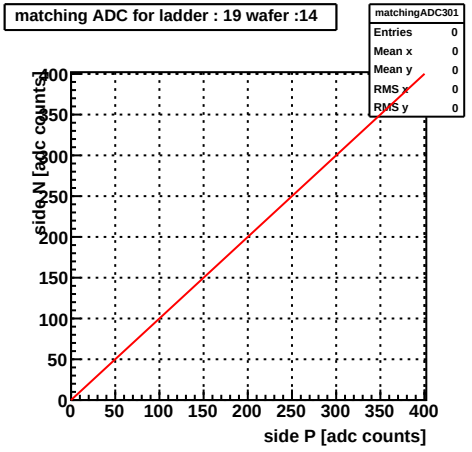

matching ADC for ladder : 19 wafer :13
matchingADC300
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

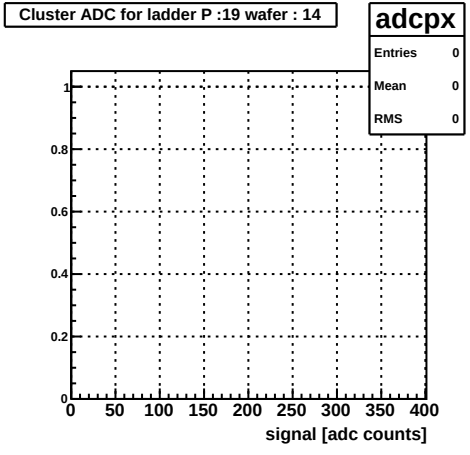

Cluster ADC for ladder P :19 wafer : 13
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

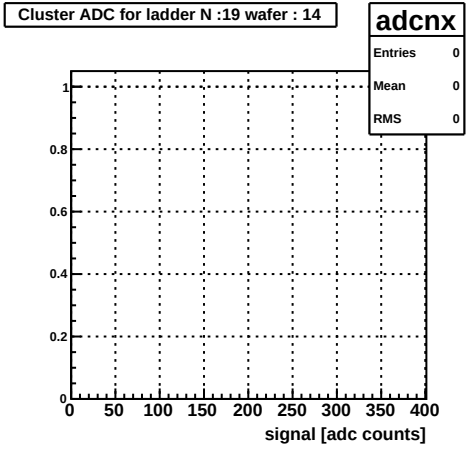

Cluster ADC for ladder N :19 wafer : 13
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

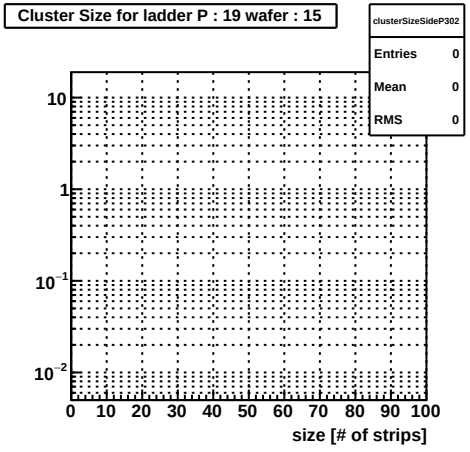

Cluster Size for ladder P : 19 wafer : 14
clusterSizeSideP301
Entries 0
Mean 0
RMS 0
size [# of strips]

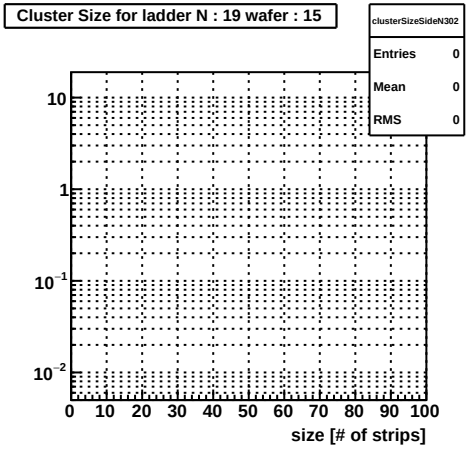

Cluster Size for ladder N : 19 wafer : 14
clusterSizeSideN301
Entries 0
Mean 0
RMS 0
size [# of strips]

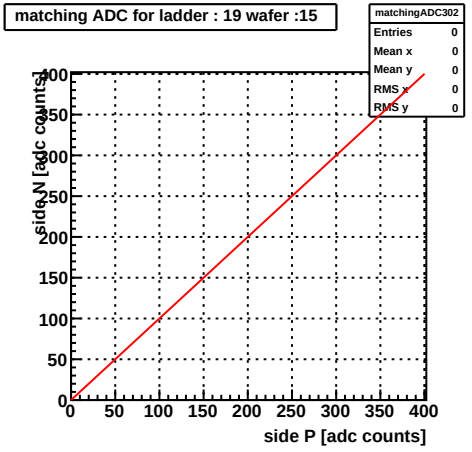

matching ADC for ladder : 19 wafer :14
matchingADC301
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

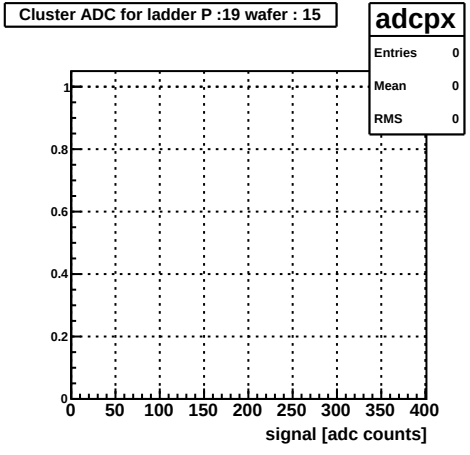

Cluster ADC for ladder P :19 wafer : 14
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

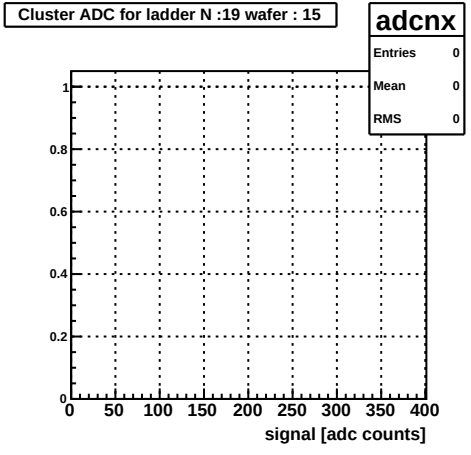

Cluster ADC for ladder N :19 wafer : 14
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

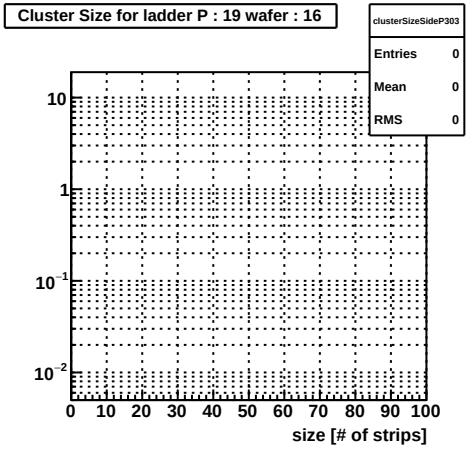

Cluster Size for ladder P : 19 wafer : 15
clusterSizeSideP302
Entries 0
Mean 0
RMS 0
size [# of strips]

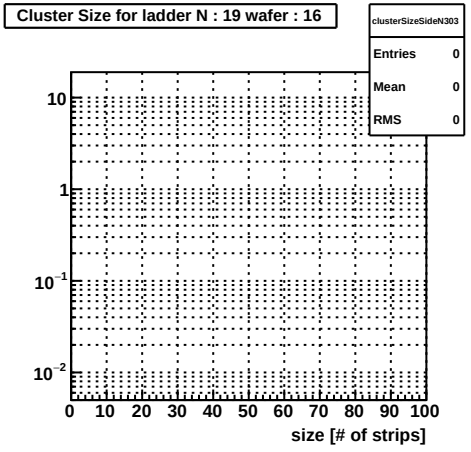

Cluster Size for ladder N : 19 wafer : 15
clusterSizeSideN302
Entries 0
Mean 0
RMS 0
size [# of strips]

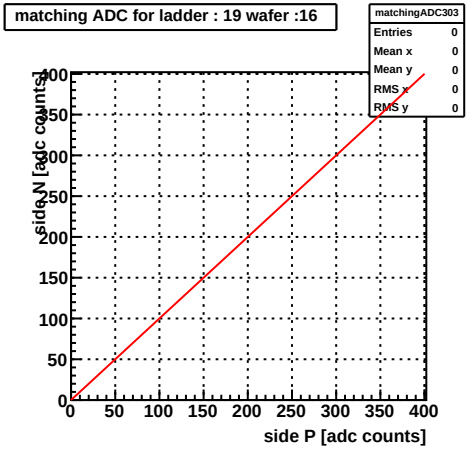

matching ADC for ladder : 19 wafer :15
matchingADC302
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

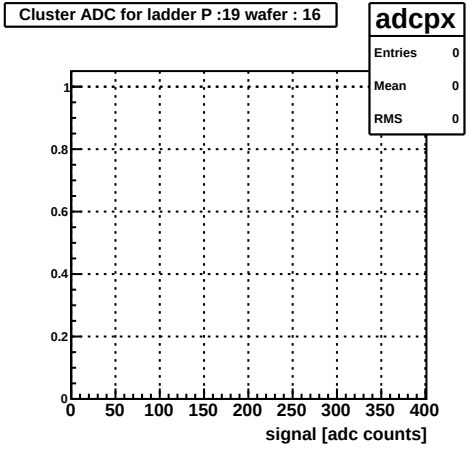

Cluster ADC for ladder P :19 wafer : 15
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

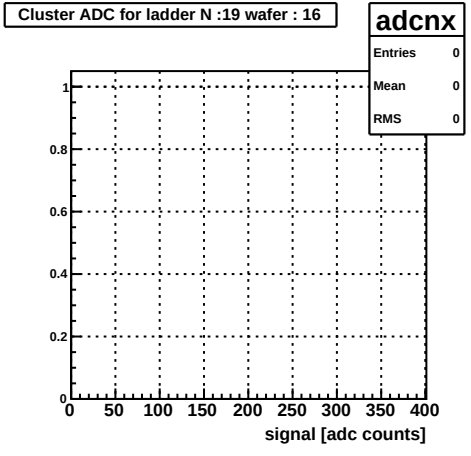

Cluster ADC for ladder N :19 wafer : 15
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

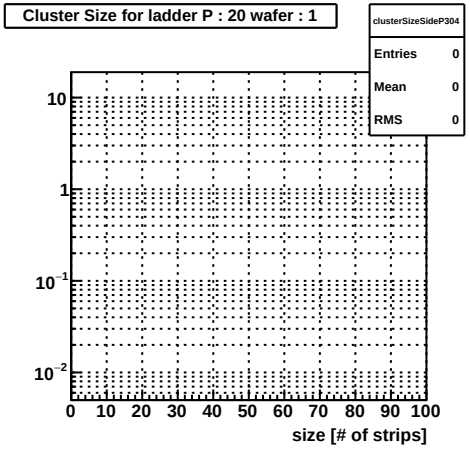

Cluster Size for ladder P : 19 wafer : 16
clusterSizeSideP303
Entries 0
Mean 0
RMS 0
size [# of strips]

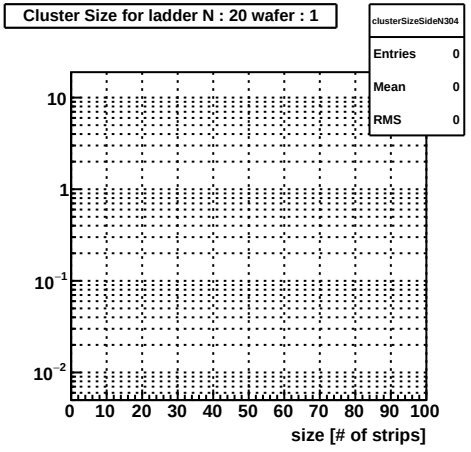

Cluster Size for ladder N : 19 wafer : 16
clusterSizeSideN303
Entries 0
Mean 0
RMS 0
size [# of strips]

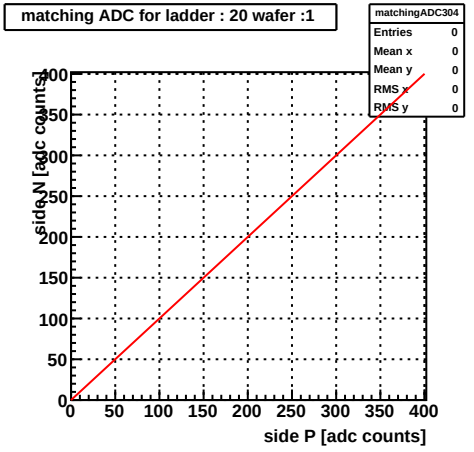

matching ADC for ladder : 19 wafer :16
matchingADC303
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

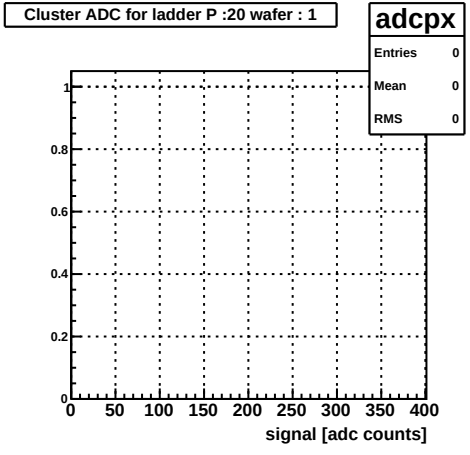

Cluster ADC for ladder P :19 wafer : 16
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

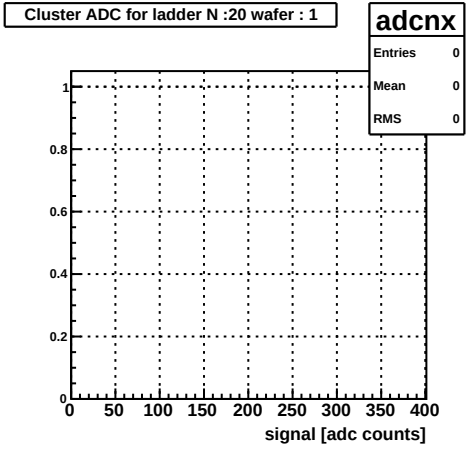

Cluster ADC for ladder N :19 wafer : 16
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

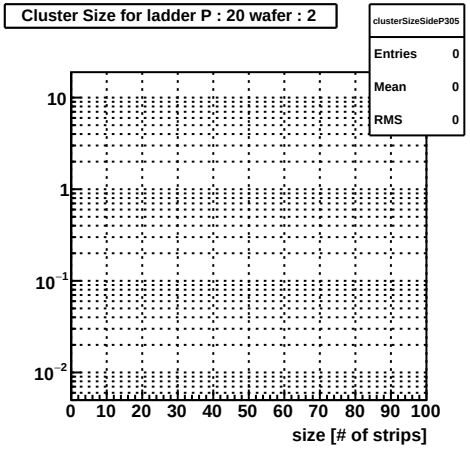

Cluster Size for ladder P : 20 wafer : 1
clusterSizeSideP304
Entries 0
Mean 0
RMS 0
size [# of strips]

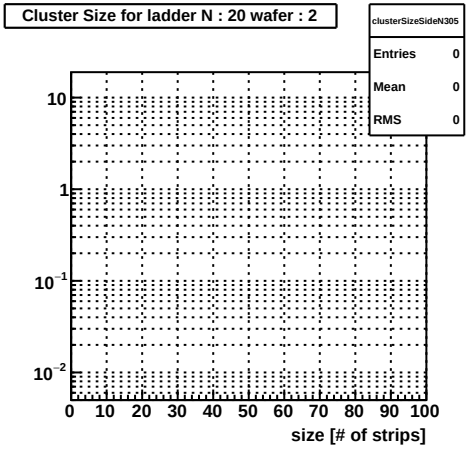

Cluster Size for ladder N : 20 wafer : 1
clusterSizeSideN304
Entries 0
Mean 0
RMS 0
size [# of strips]

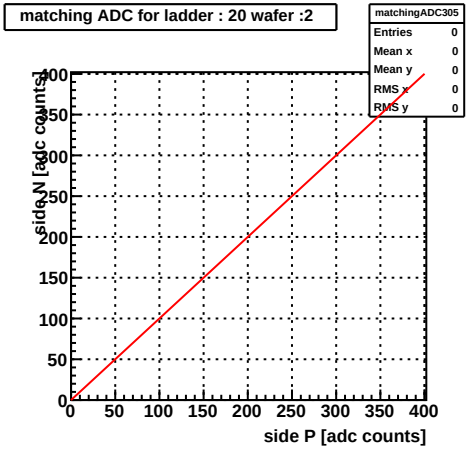

matching ADC for ladder : 20 wafer :1
matchingADC304
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

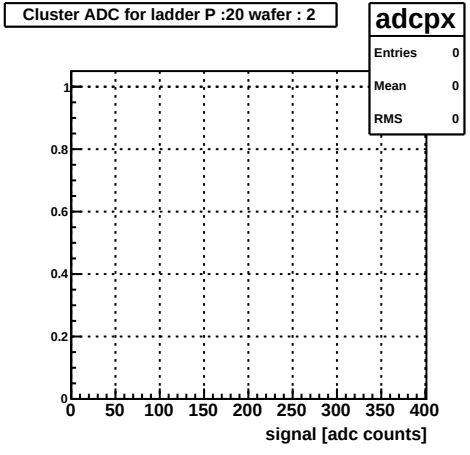

Cluster ADC for ladder P :20 wafer : 1
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

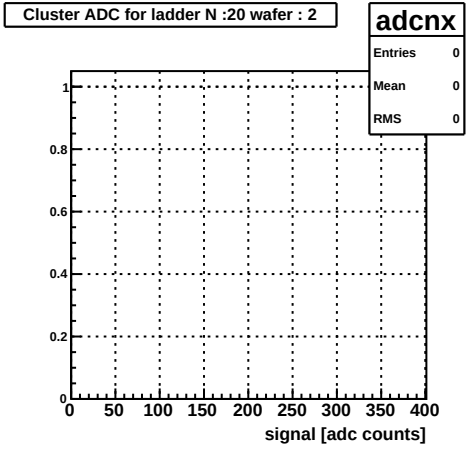

Cluster ADC for ladder N :20 wafer : 1
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

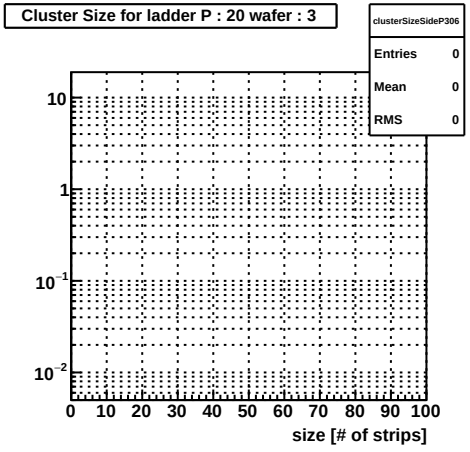

Cluster Size for ladder P : 20 wafer : 2
clusterSizeSideP305
Entries 0
Mean 0
RMS 0
size [# of strips]

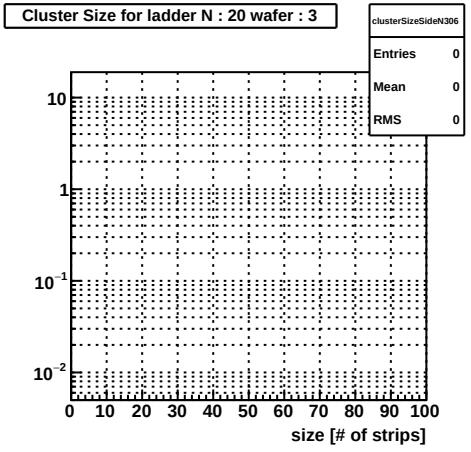

Cluster Size for ladder N : 20 wafer : 2
clusterSizeSideN305
Entries 0
Mean 0
RMS 0
size [# of strips]

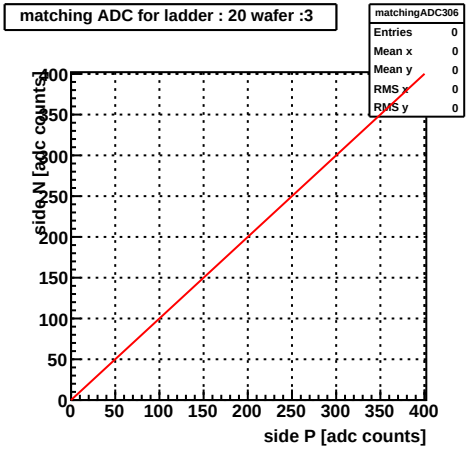

matching ADC for ladder : 20 wafer :2
matchingADC305
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

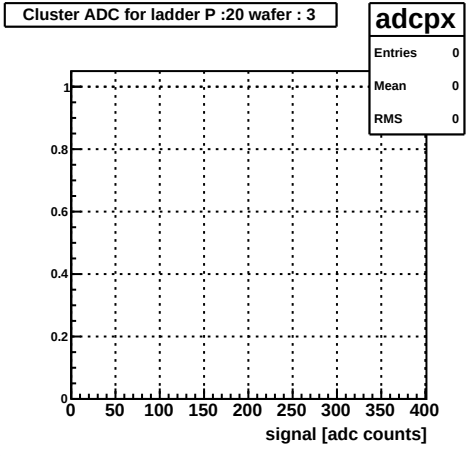

Cluster ADC for ladder P :20 wafer : 2
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

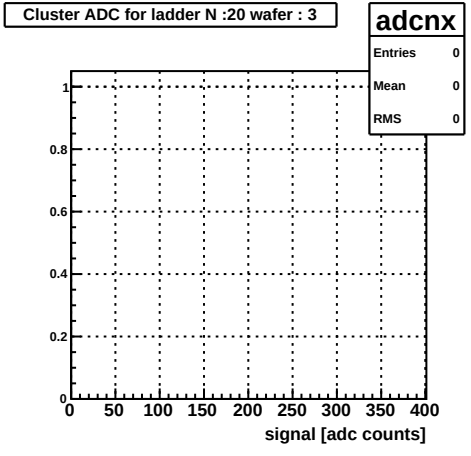

Cluster ADC for ladder N :20 wafer : 2
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

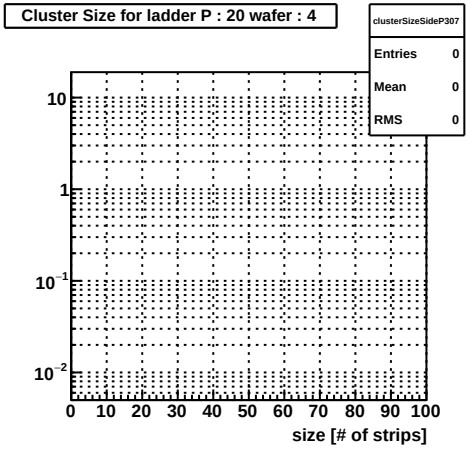

Cluster Size for ladder P : 20 wafer : 3
clusterSizeSideP306
Entries 0
Mean 0
RMS 0
size [# of strips]

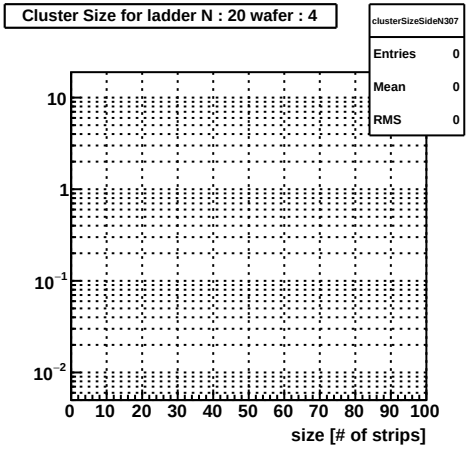

Cluster Size for ladder N : 20 wafer : 3
clusterSizeSideN306
Entries 0
Mean 0
RMS 0
size [# of strips]

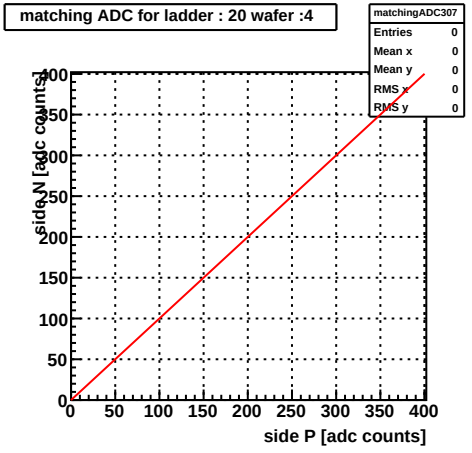

matching ADC for ladder : 20 wafer :3
matchingADC306
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

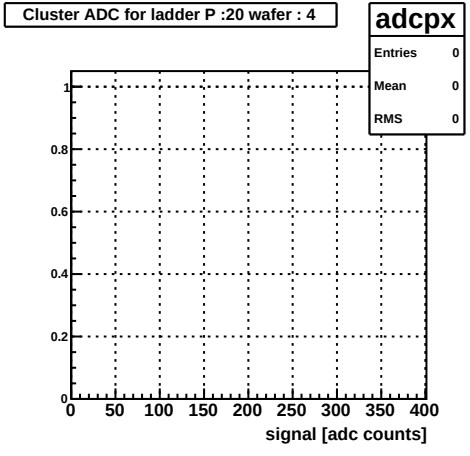

Cluster ADC for ladder P :20 wafer : 3
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

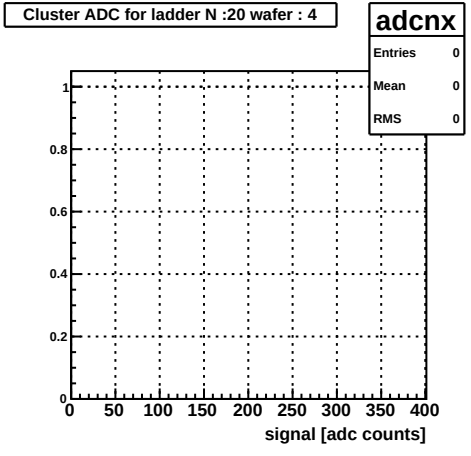

Cluster ADC for ladder N :20 wafer : 3
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

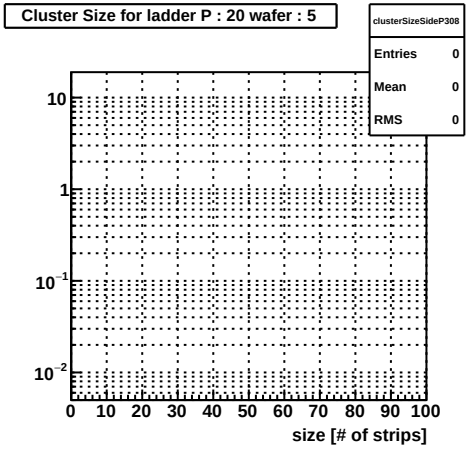

Cluster Size for ladder P : 20 wafer : 4
clusterSizeSideP307
Entries 0
Mean 0
RMS 0
size [# of strips]

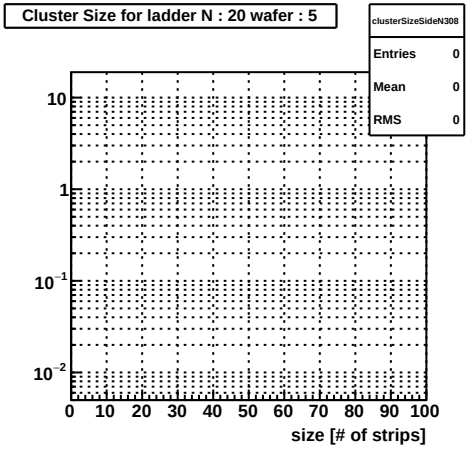

Cluster Size for ladder N : 20 wafer : 4
clusterSizeSideN307
Entries 0
Mean 0
RMS 0
size [# of strips]

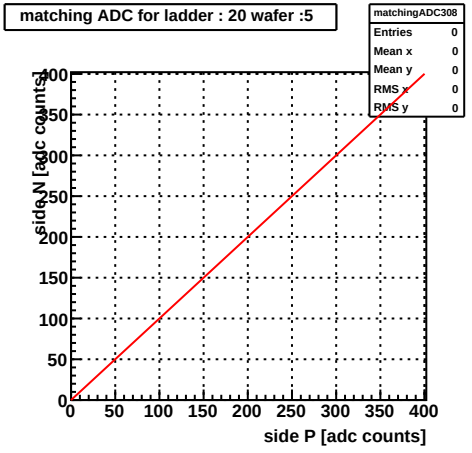

matching ADC for ladder : 20 wafer :4
matchingADC307
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

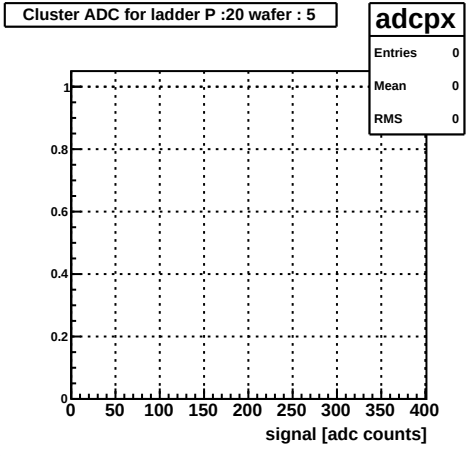

Cluster ADC for ladder P :20 wafer : 4
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

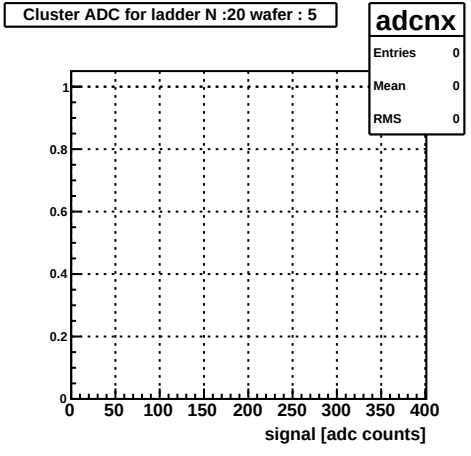

Cluster ADC for ladder N :20 wafer : 4
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

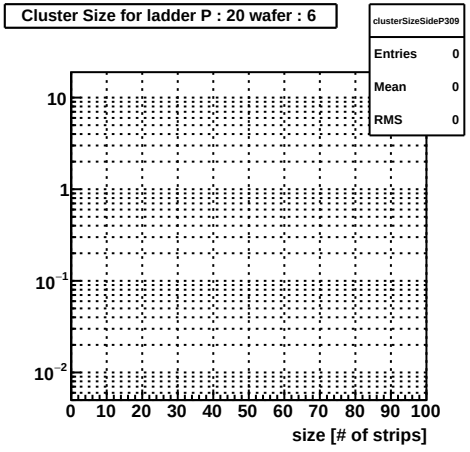

Cluster Size for ladder P : 20 wafer : 5
clusterSizeSideP308
Entries 0
Mean 0
RMS 0
size [# of strips]

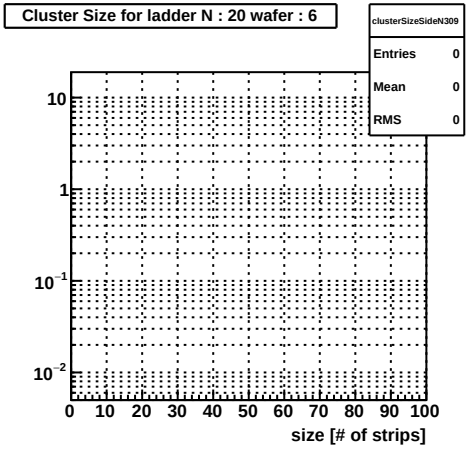

Cluster Size for ladder N : 20 wafer : 5
clusterSizeSideN308
Entries 0
Mean 0
RMS 0
size [# of strips]

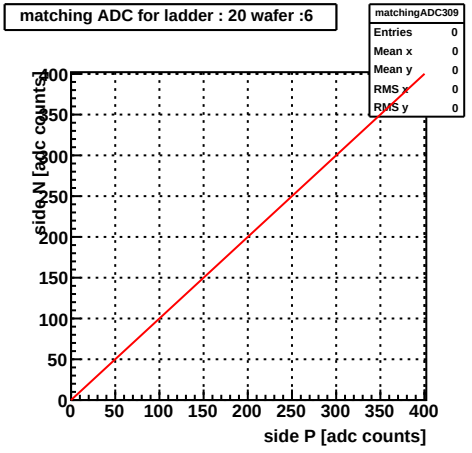

matching ADC for ladder : 20 wafer :5
matchingADC308
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

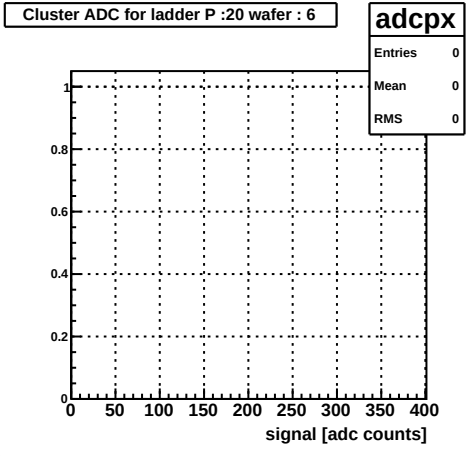

Cluster ADC for ladder P :20 wafer : 5
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

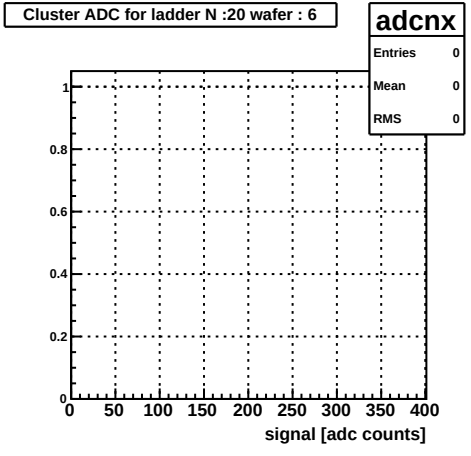

Cluster ADC for ladder N :20 wafer : 5
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

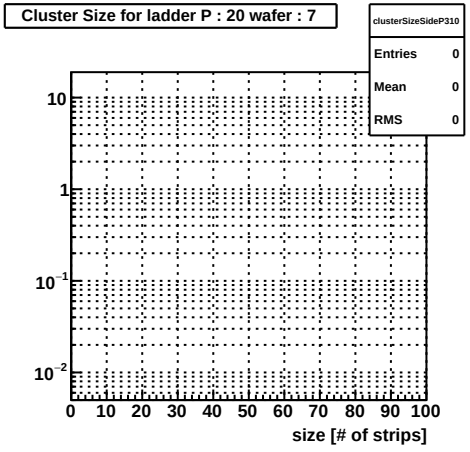

Cluster Size for ladder P : 20 wafer : 6
clusterSizeSideP309
Entries 0
Mean 0
RMS 0
size [# of strips]

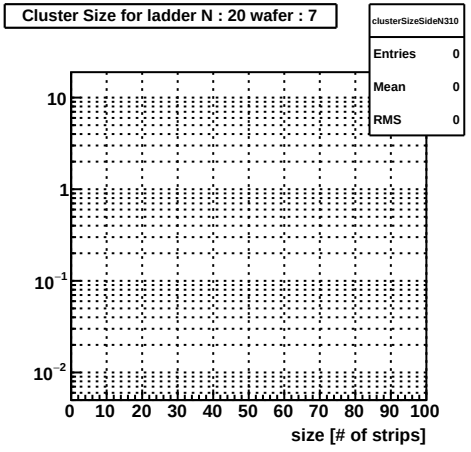

Cluster Size for ladder N : 20 wafer : 6
clusterSizeSideN309
Entries 0
Mean 0
RMS 0
size [# of strips]

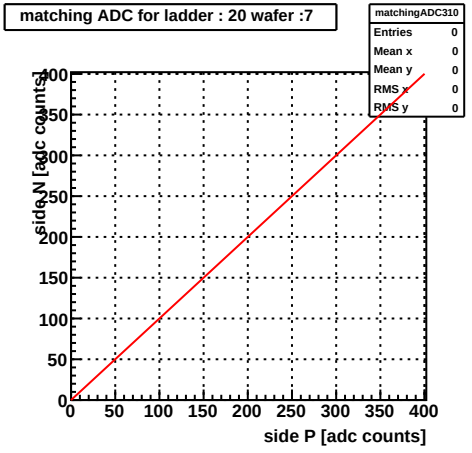

matching ADC for ladder : 20 wafer :6
matchingADC309
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

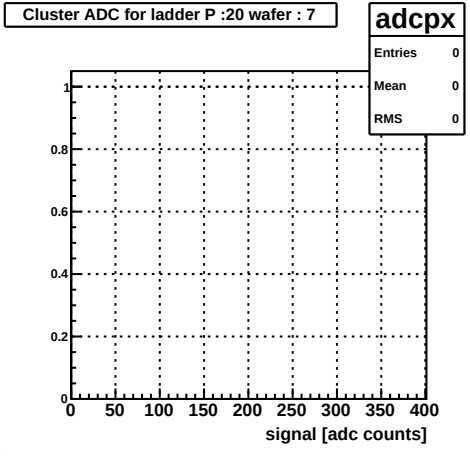

Cluster ADC for ladder P :20 wafer : 6
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

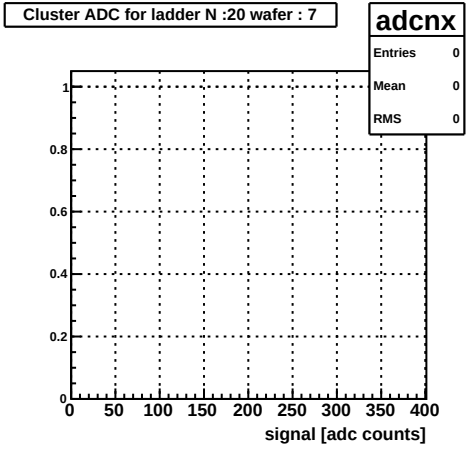

Cluster ADC for ladder N :20 wafer : 6
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

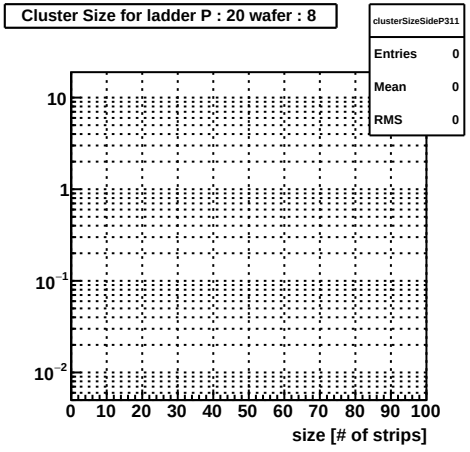

Cluster Size for ladder P : 20 wafer : 7
clusterSizeSideP310
Entries 0
Mean 0
RMS 0
size [# of strips]

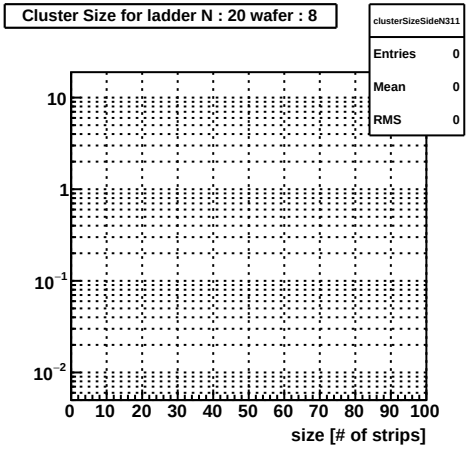

Cluster Size for ladder N : 20 wafer : 7
clusterSizeSideN310
Entries 0
Mean 0
RMS 0
size [# of strips]

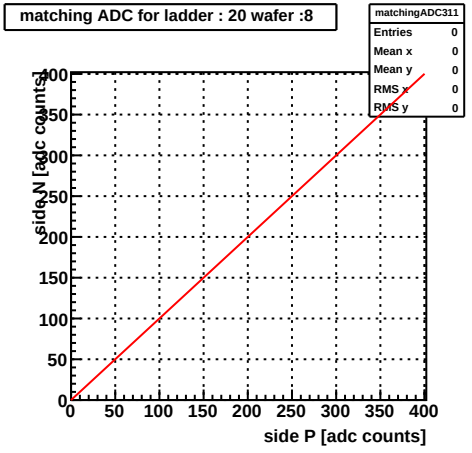

matching ADC for ladder : 20 wafer :7
matchingADC310
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

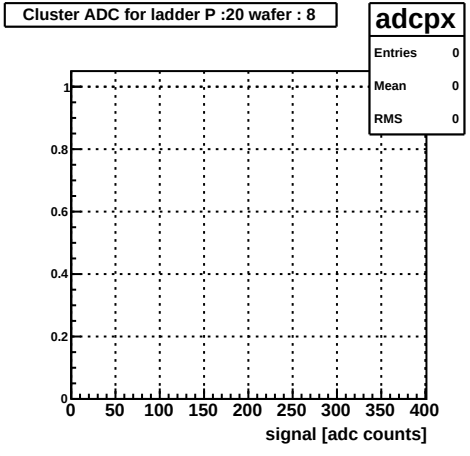

Cluster ADC for ladder P :20 wafer : 7
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

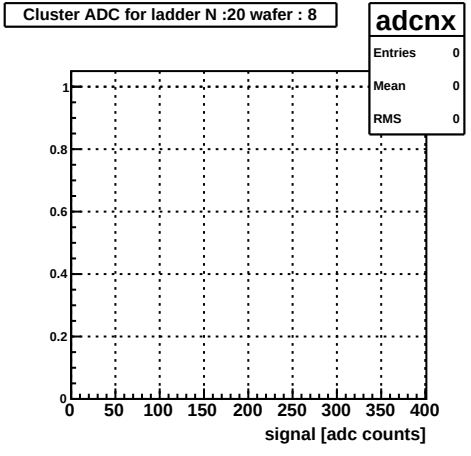

Cluster ADC for ladder N :20 wafer : 7
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

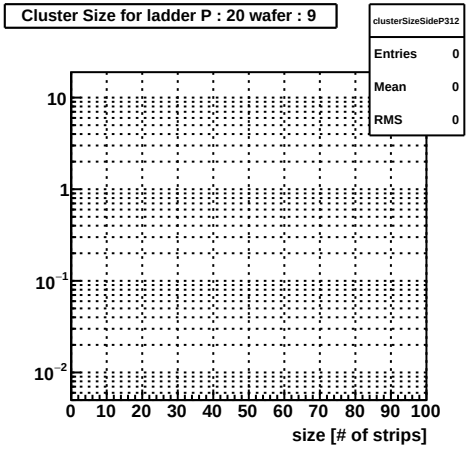

Cluster Size for ladder P : 20 wafer : 8
clusterSizeSideP311
Entries 0
Mean 0
RMS 0
size [# of strips]

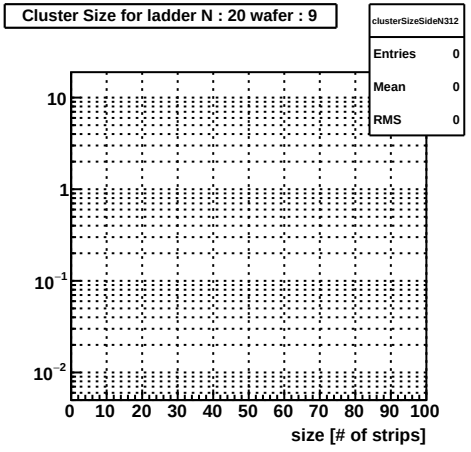

Cluster Size for ladder N : 20 wafer : 8
clusterSizeSideN311
Entries 0
Mean 0
RMS 0
size [# of strips]

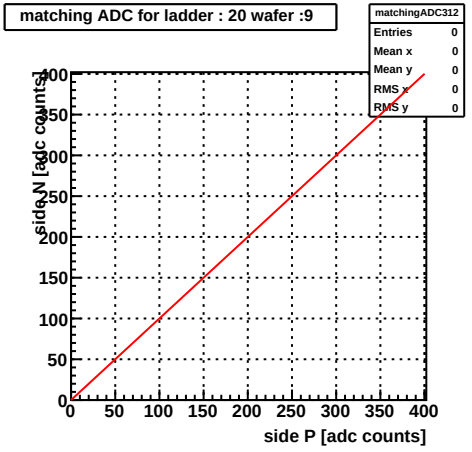

matching ADC for ladder : 20 wafer :8
matchingADC311
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

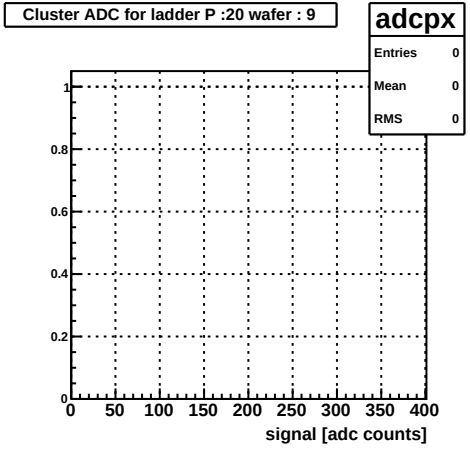

Cluster ADC for ladder P :20 wafer : 8
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

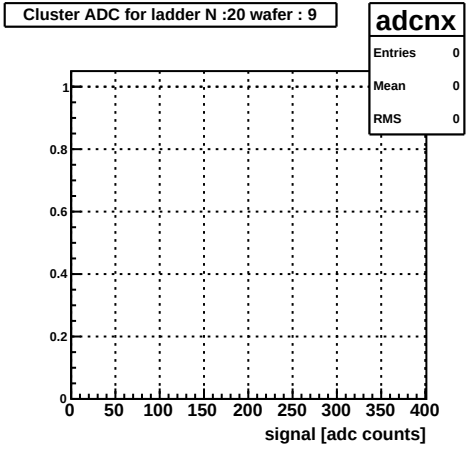

Cluster ADC for ladder N :20 wafer : 8
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

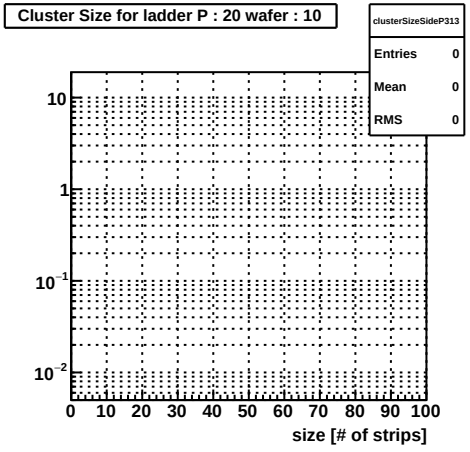

Cluster Size for ladder P : 20 wafer : 9
clusterSizeSideP312
Entries 0
Mean 0
RMS 0
size [# of strips]

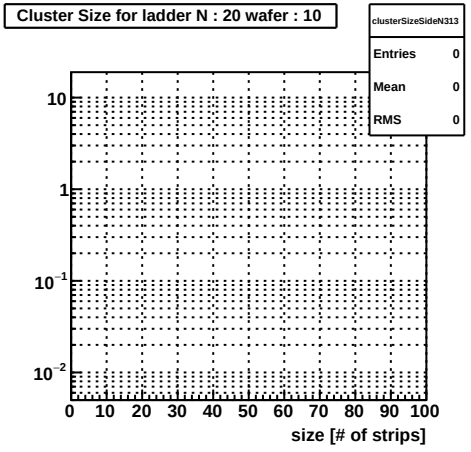

Cluster Size for ladder N : 20 wafer : 9
clusterSizeSideN312
Entries 0
Mean 0
RMS 0
size [# of strips]

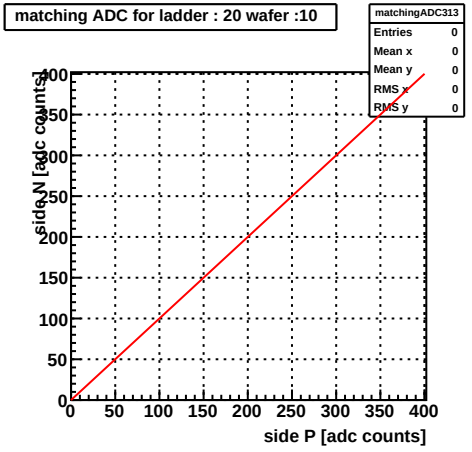

matching ADC for ladder : 20 wafer :9
matchingADC312
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

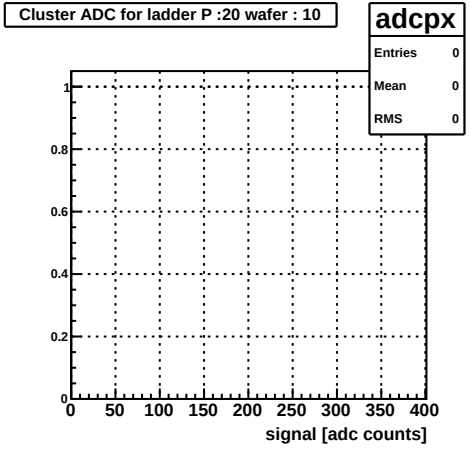

Cluster ADC for ladder P :20 wafer : 9
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

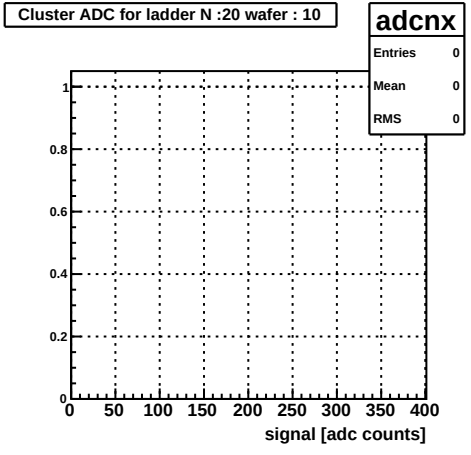

Cluster ADC for ladder N :20 wafer : 9
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

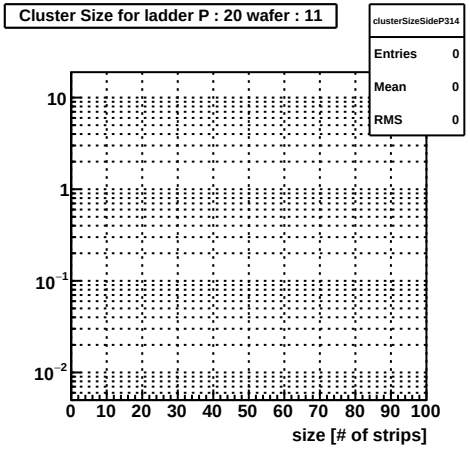

Cluster Size for ladder P : 20 wafer : 10
clusterSizeSideP313
Entries 0
Mean 0
RMS 0
size [# of strips]

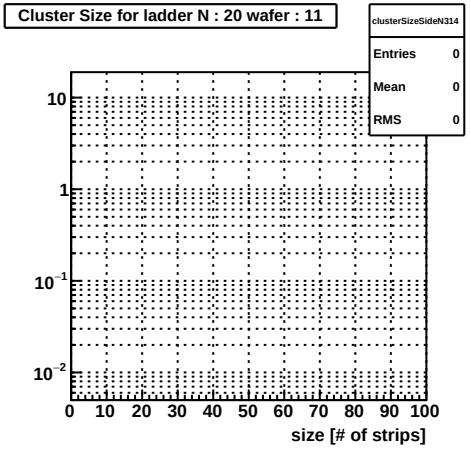

Cluster Size for ladder N : 20 wafer : 10
clusterSizeSideN313
Entries 0
Mean 0
RMS 0
size [# of strips]

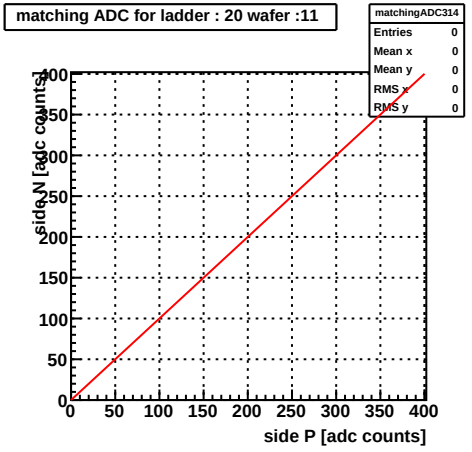

matching ADC for ladder : 20 wafer :10
matchingADC313
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

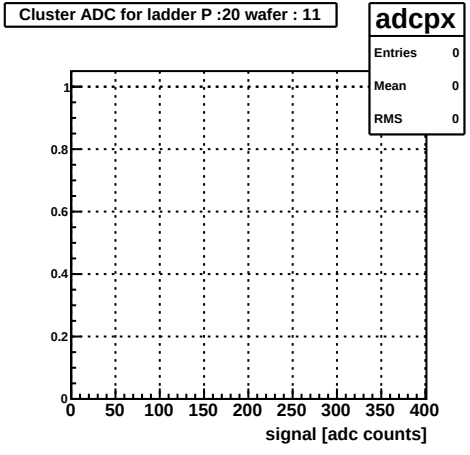

Cluster ADC for ladder P :20 wafer : 10
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

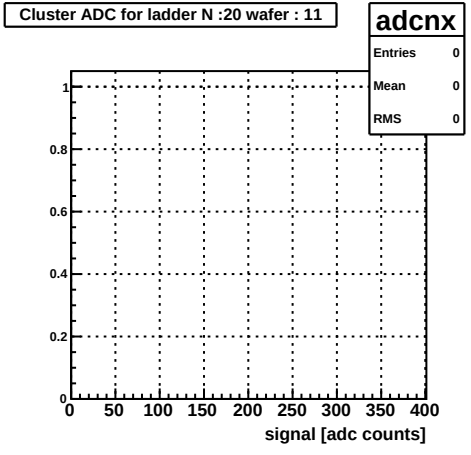

Cluster ADC for ladder N :20 wafer : 10
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

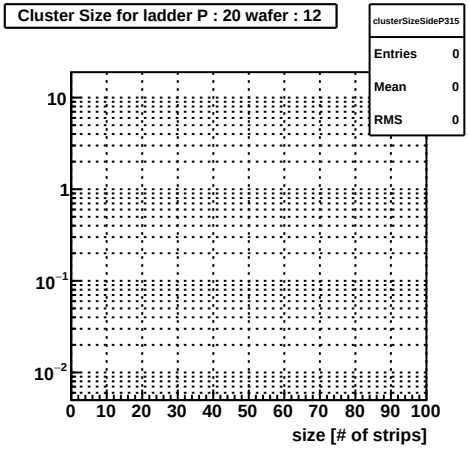

Cluster Size for ladder P : 20 wafer : 11
clusterSizeSideP314
Entries 0
Mean 0
RMS 0
size [# of strips]

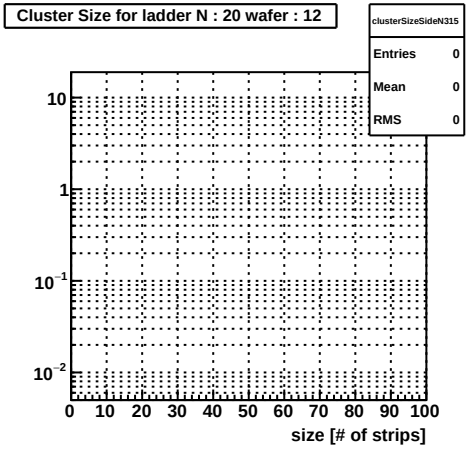

Cluster Size for ladder N : 20 wafer : 11
clusterSizeSideN314
Entries 0
Mean 0
RMS 0
size [# of strips]

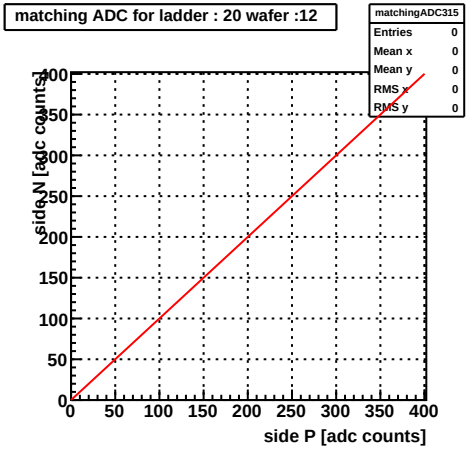

matching ADC for ladder : 20 wafer :11
matchingADC314
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

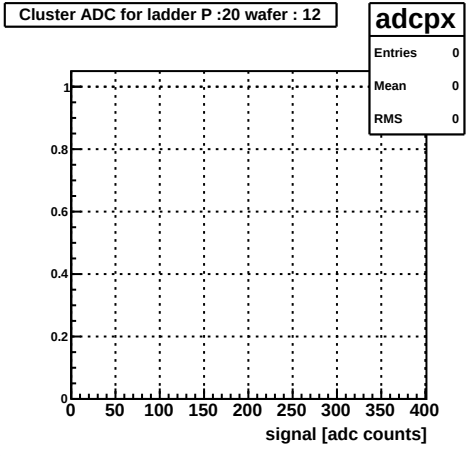

Cluster ADC for ladder P :20 wafer : 11
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

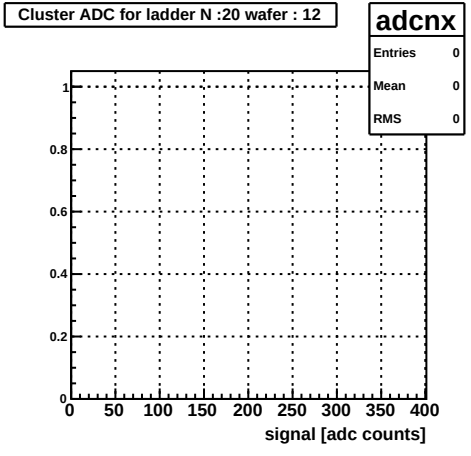

Cluster ADC for ladder N :20 wafer : 11
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

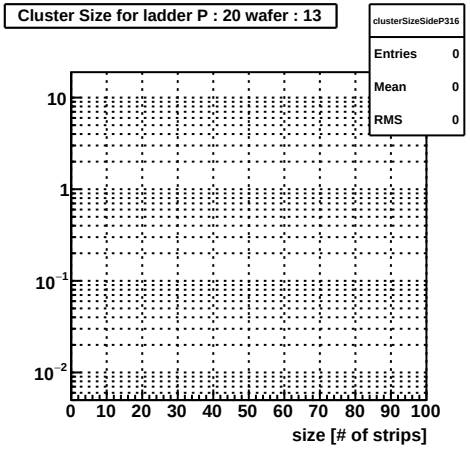

Cluster Size for ladder P : 20 wafer : 12
clusterSizeSideP315
Entries 0
Mean 0
RMS 0
size [# of strips]

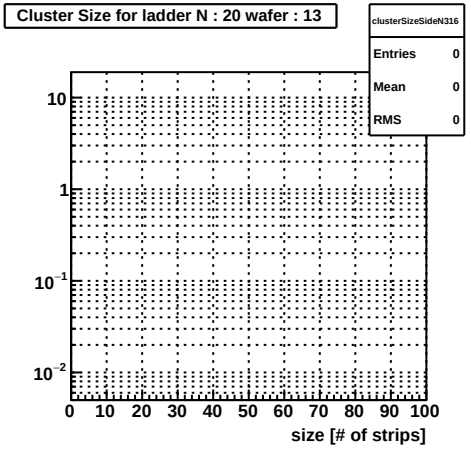

Cluster Size for ladder N : 20 wafer : 12
clusterSizeSideN315
Entries 0
Mean 0
RMS 0
size [# of strips]

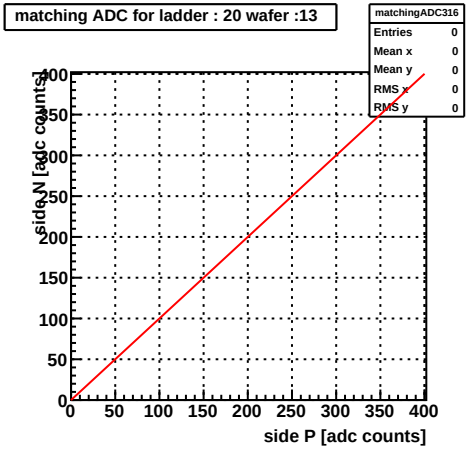

matching ADC for ladder : 20 wafer :12
matchingADC315
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

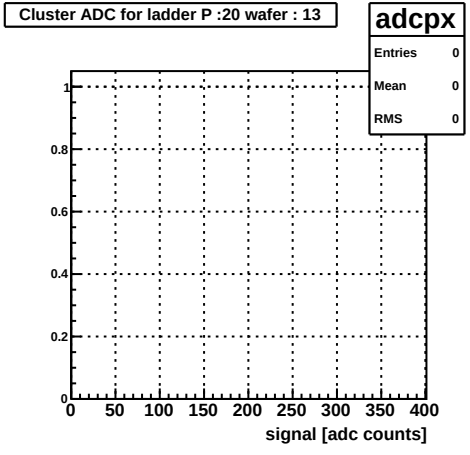

Cluster ADC for ladder P :20 wafer : 12
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

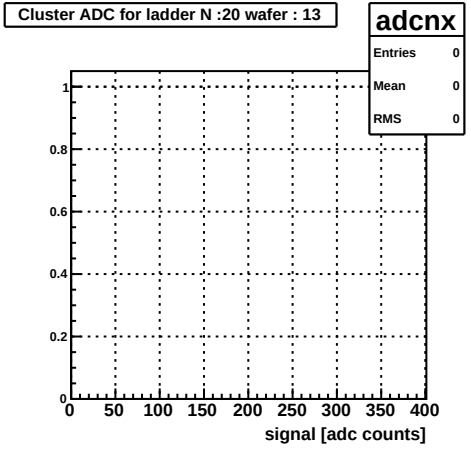

Cluster ADC for ladder N :20 wafer : 12
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

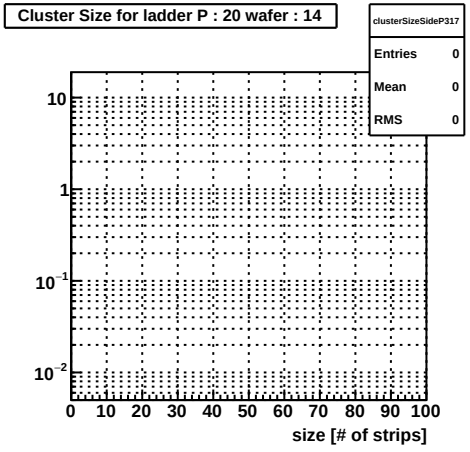

Cluster Size for ladder P : 20 wafer : 13
clusterSizeSideP316
Entries 0
Mean 0
RMS 0
size [# of strips]

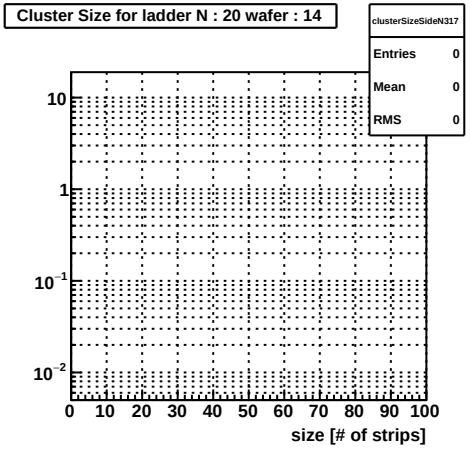

Cluster Size for ladder N : 20 wafer : 13
clusterSizeSideN316
Entries 0
Mean 0
RMS 0
size [# of strips]

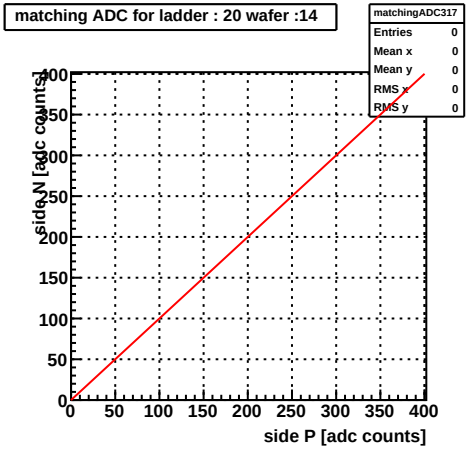

matching ADC for ladder : 20 wafer :13
matchingADC316
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

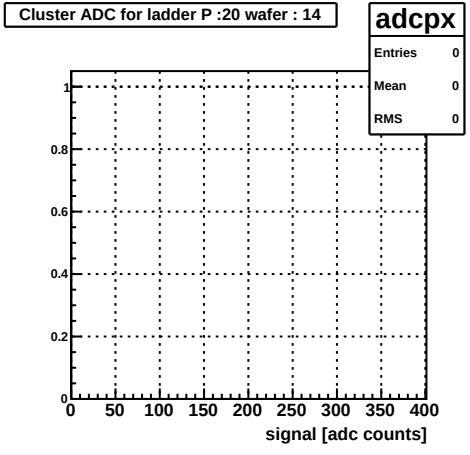

Cluster ADC for ladder P :20 wafer : 13
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

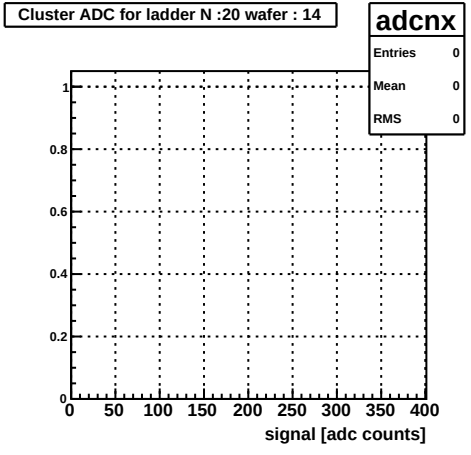

Cluster ADC for ladder N :20 wafer : 13
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

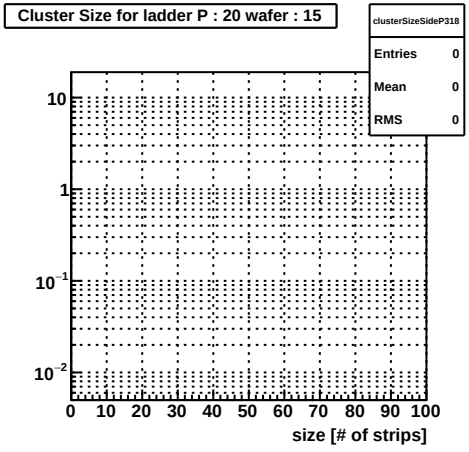

Cluster Size for ladder P : 20 wafer : 14
clusterSizeSideP317
Entries 0
Mean 0
RMS 0
size [# of strips]

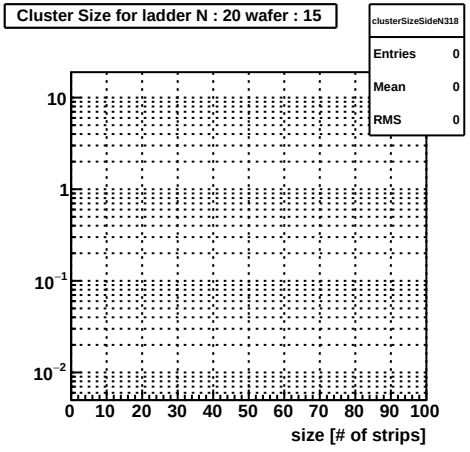

Cluster Size for ladder N : 20 wafer : 14
clusterSizeSideN317
Entries 0
Mean 0
RMS 0
size [# of strips]

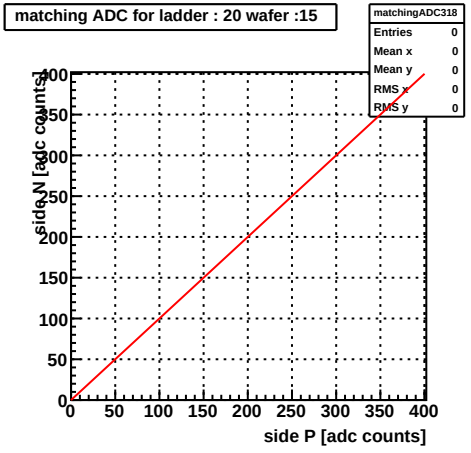

matching ADC for ladder : 20 wafer :14
matchingADC317
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

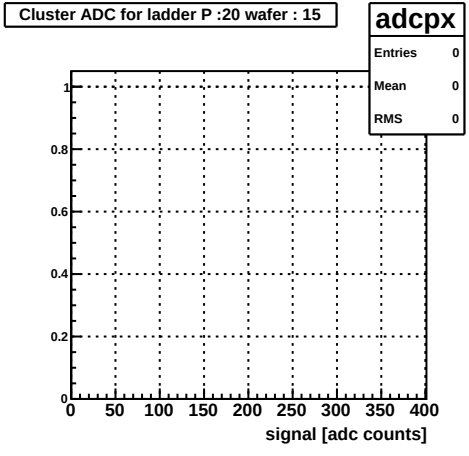

Cluster ADC for ladder P :20 wafer : 14
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

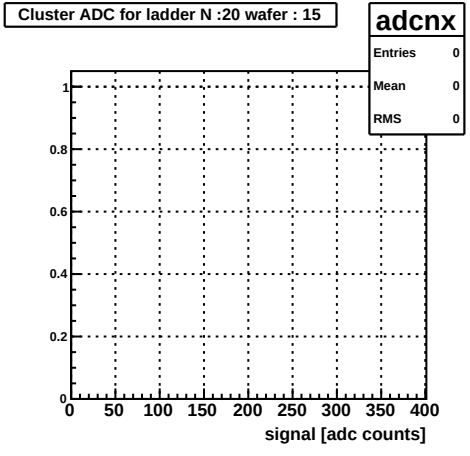

Cluster ADC for ladder N :20 wafer : 14
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

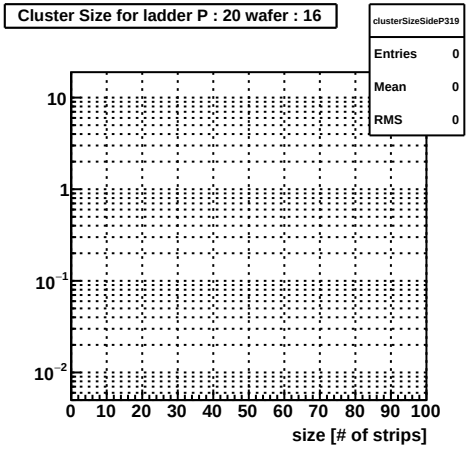

Cluster Size for ladder P : 20 wafer : 15
clusterSizeSideP318
Entries 0
Mean 0
RMS 0
size [# of strips]

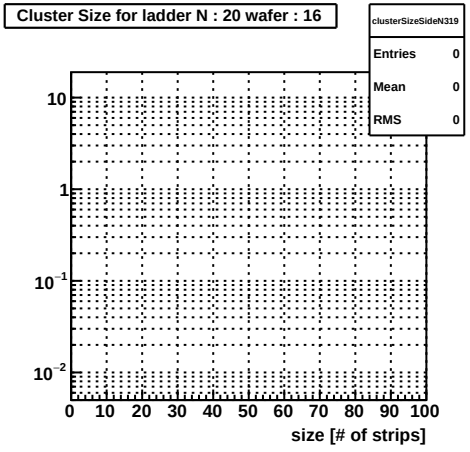

Cluster Size for ladder N : 20 wafer : 15
clusterSizeSideN318
Entries 0
Mean 0
RMS 0
size [# of strips]

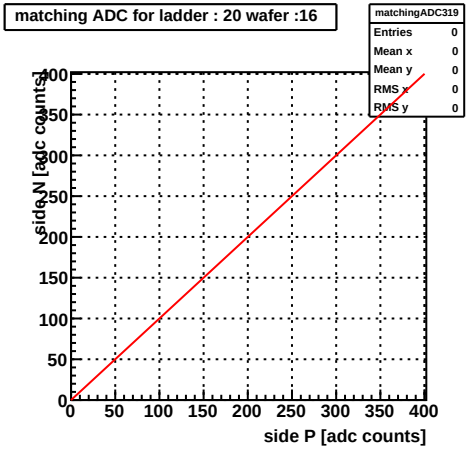

matching ADC for ladder : 20 wafer :15
matchingADC318
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]

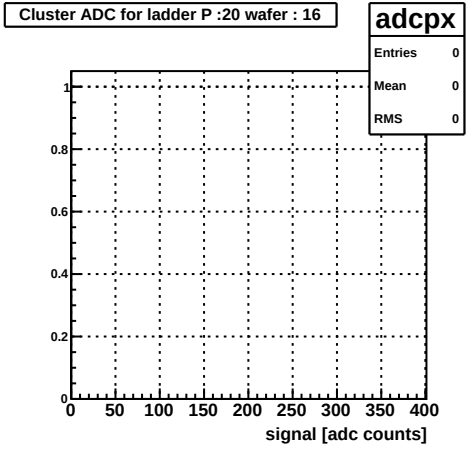

Cluster ADC for ladder P :20 wafer : 15
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]

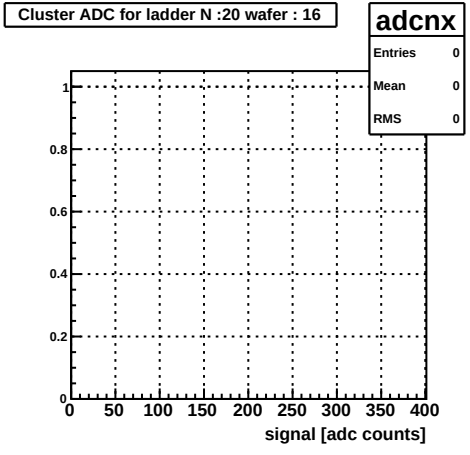

Cluster ADC for ladder N :20 wafer : 15
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]

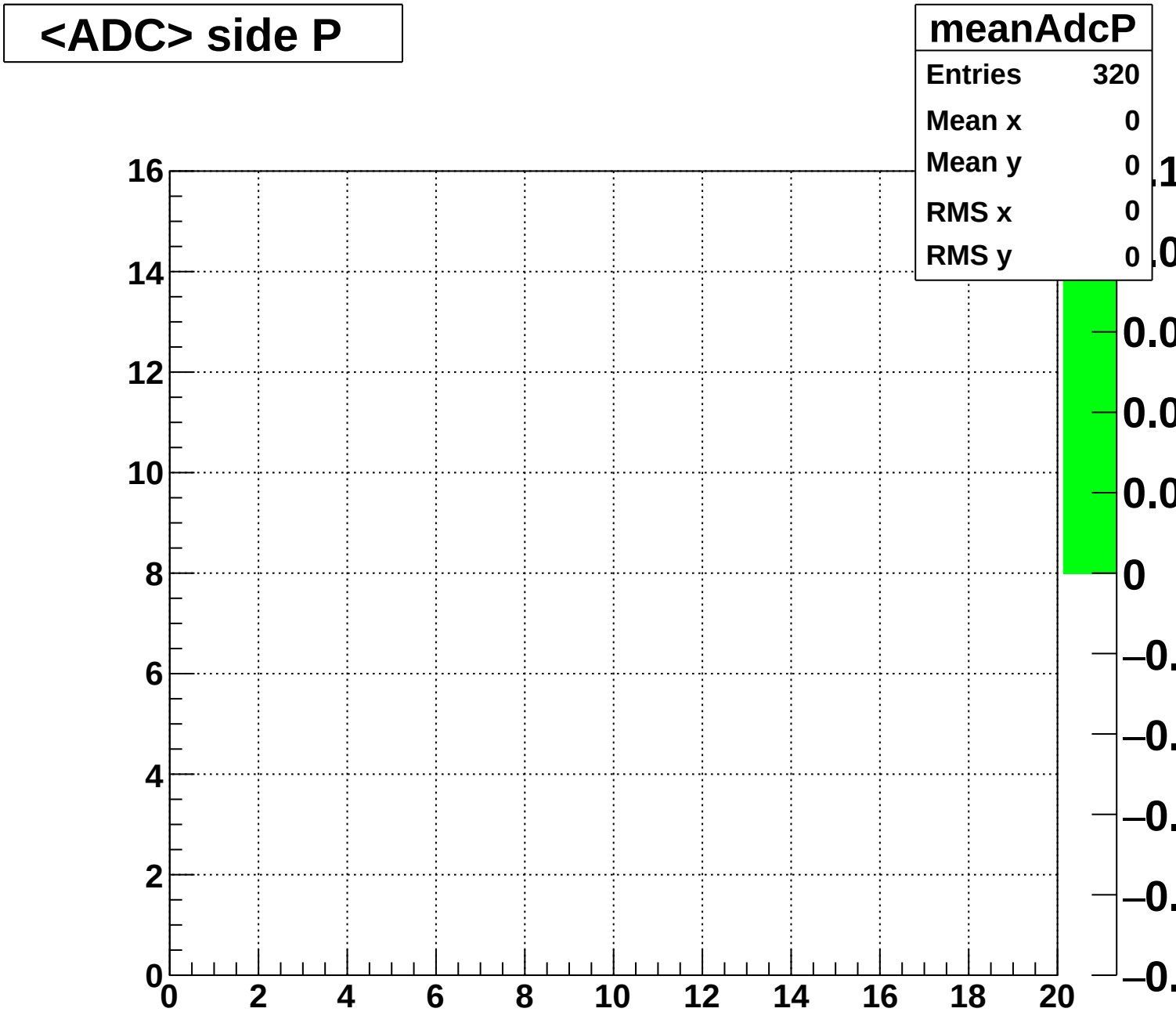

Cluster Size for ladder P : 20 wafer : 16
clusterSizeSideP319
Entries 0
Mean 0
RMS 0
size [# of strips]


Cluster Size for ladder N : 20 wafer : 16
clusterSizeSideN319
Entries 0
Mean 0
RMS 0
size [# of strips]


matching ADC for ladder : 20 wafer :16
matchingADC319
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
side N [adc counts]
side P [adc counts]


Cluster ADC for ladder P :20 wafer : 16
adcpx
Entries 0
Mean 0
RMS 0
signal [adc counts]


Cluster ADC for ladder N :20 wafer : 16
adcnx
Entries 0
Mean 0
RMS 0
signal [adc counts]



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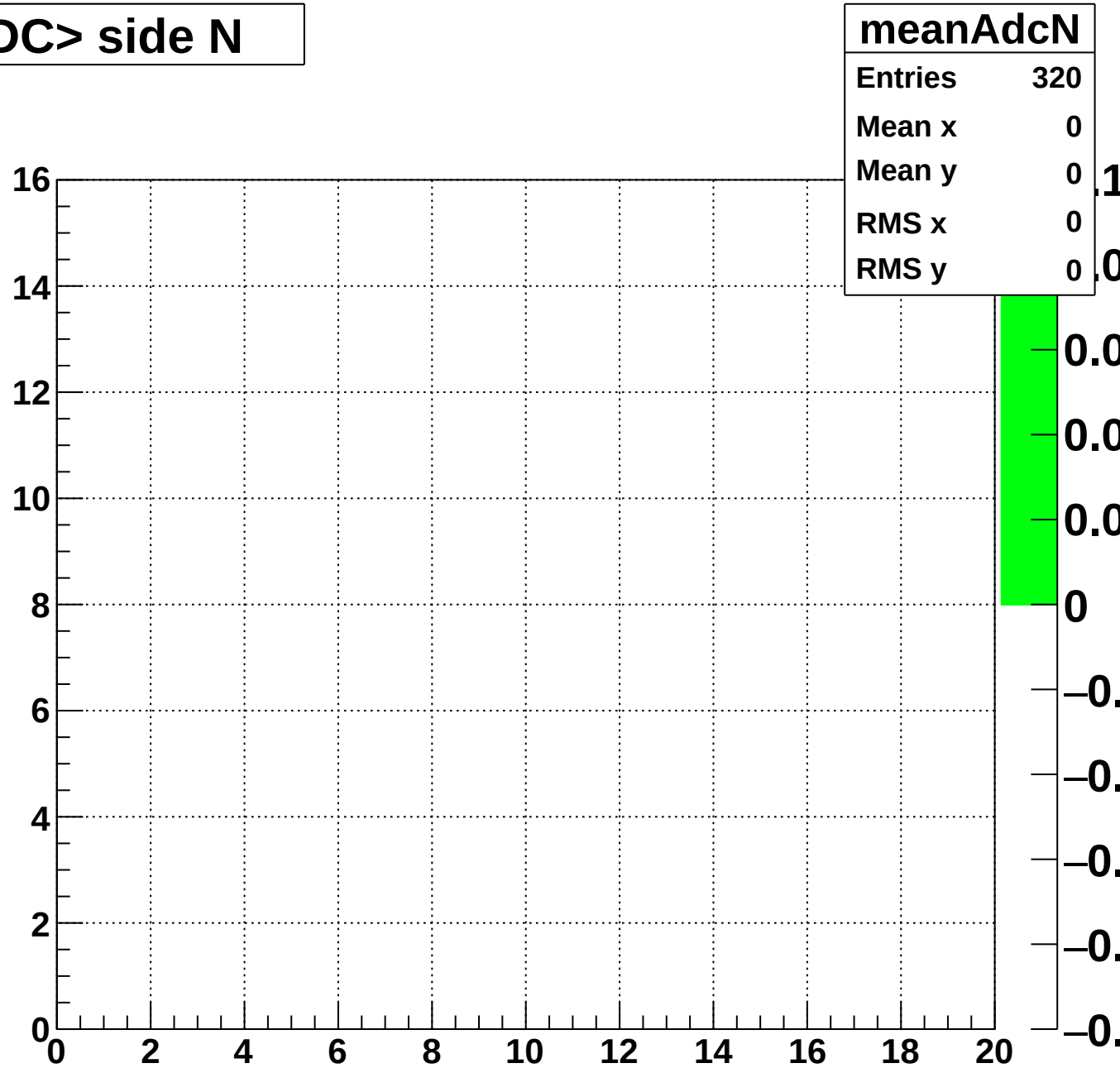
<ADC> side N



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