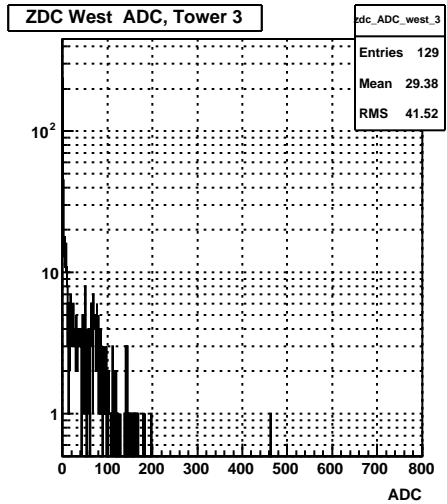
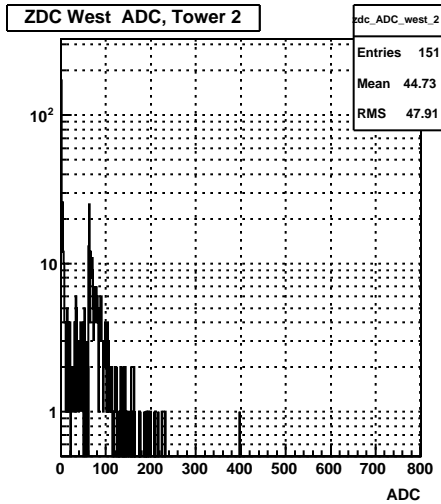
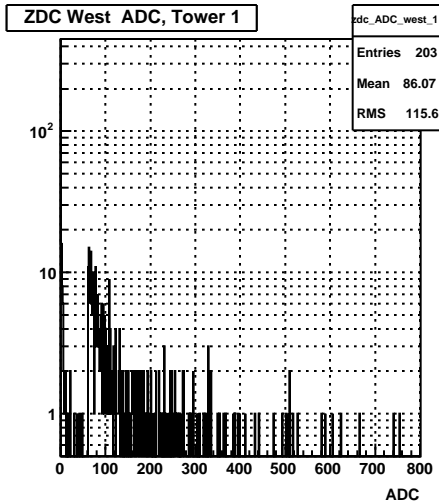
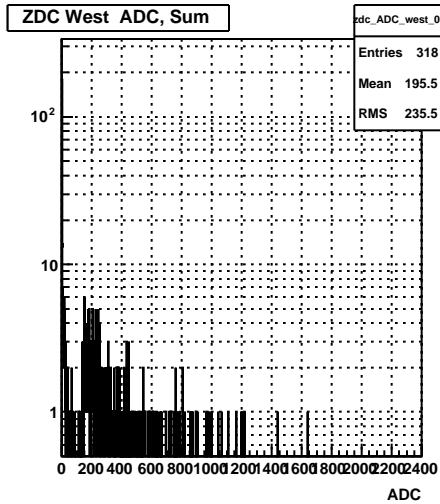
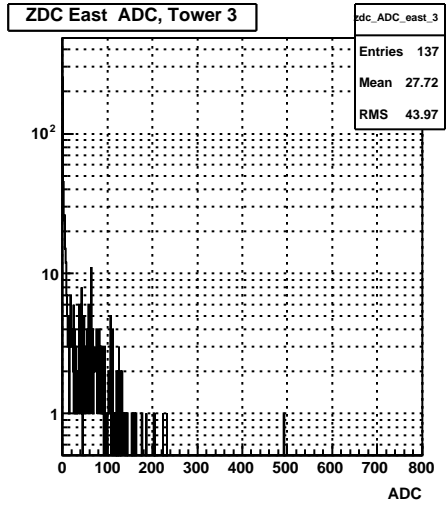
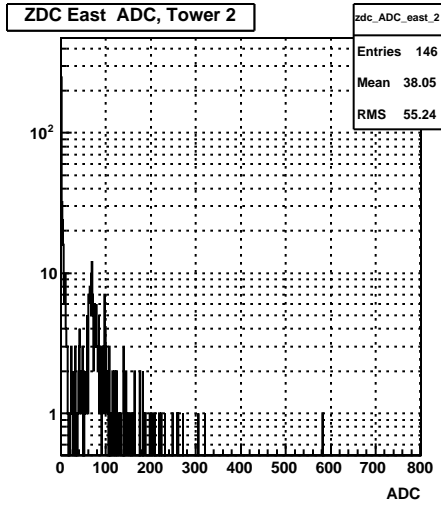
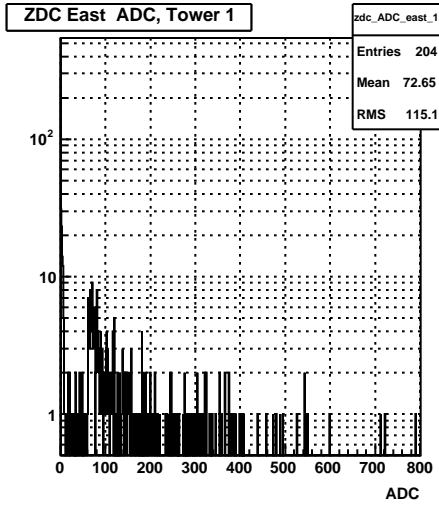
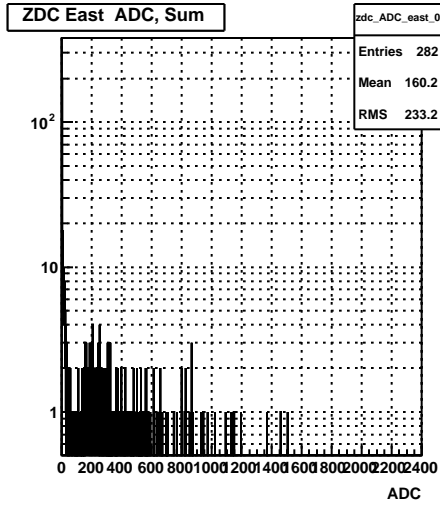


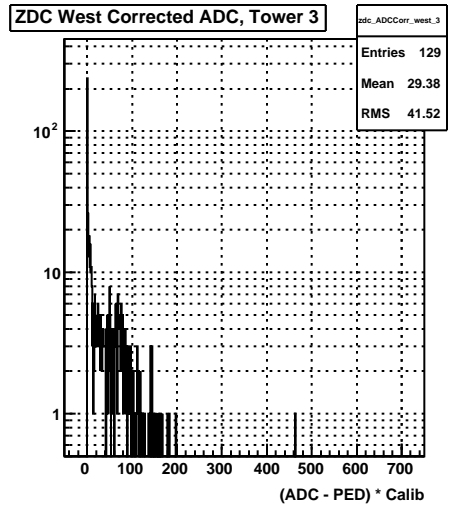
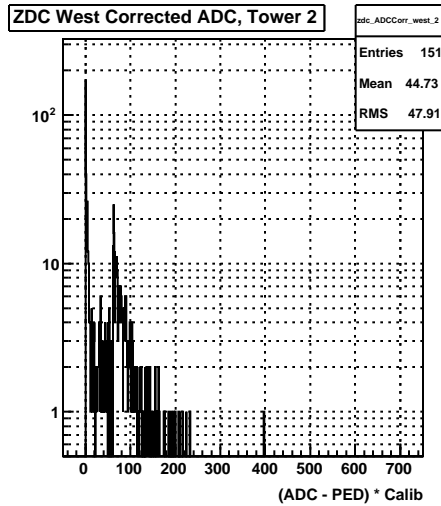
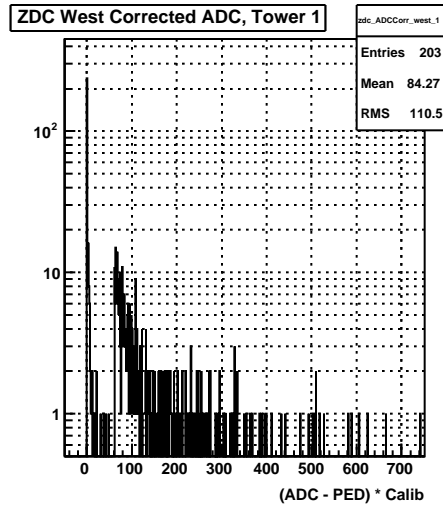
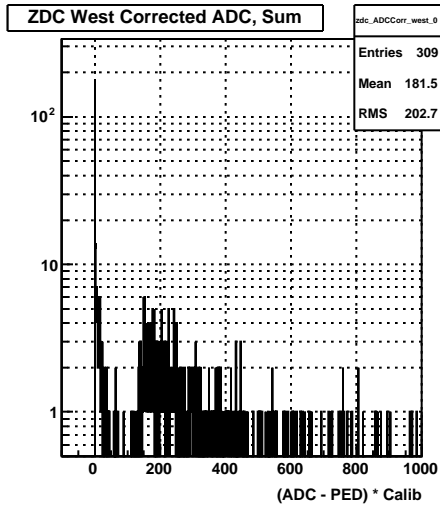
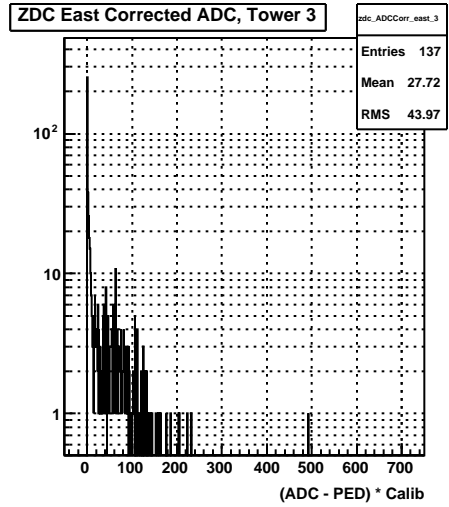
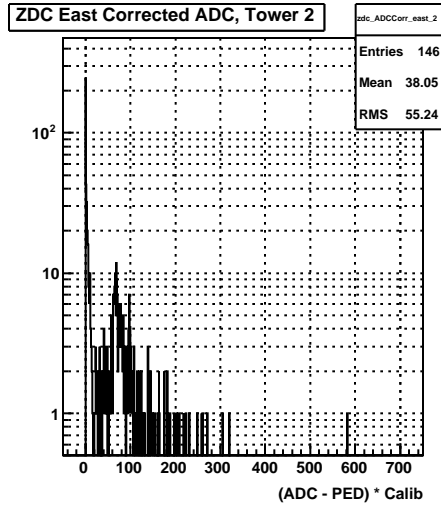
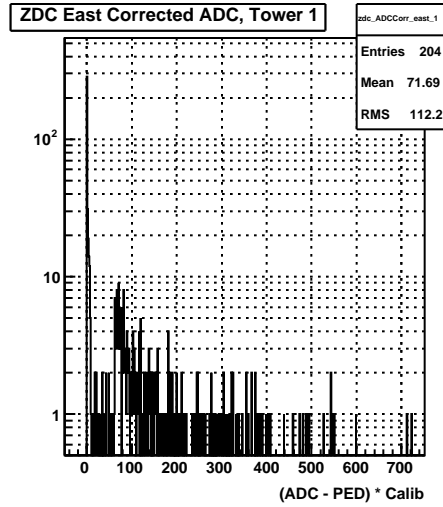
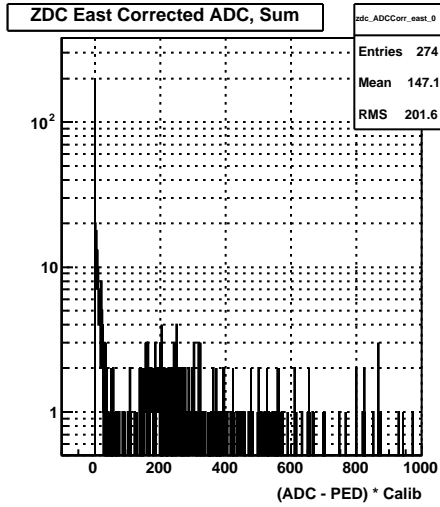
ZDC (run 10109006)

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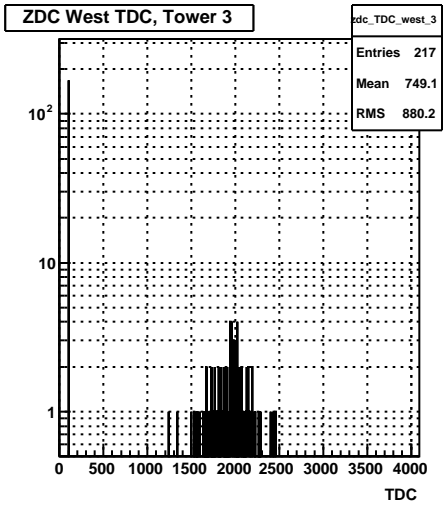
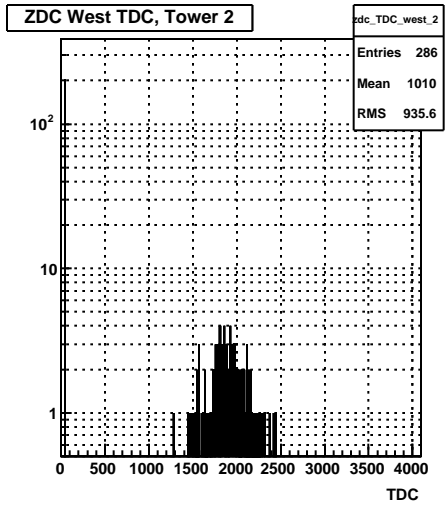
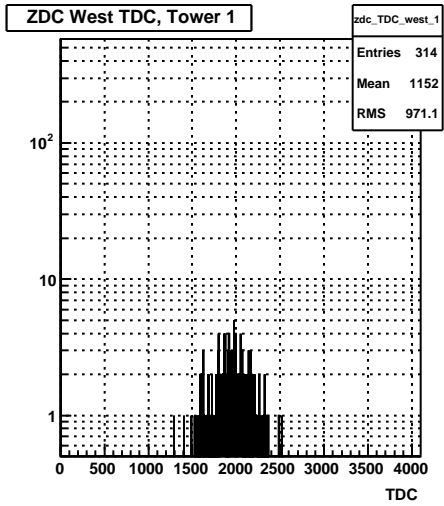
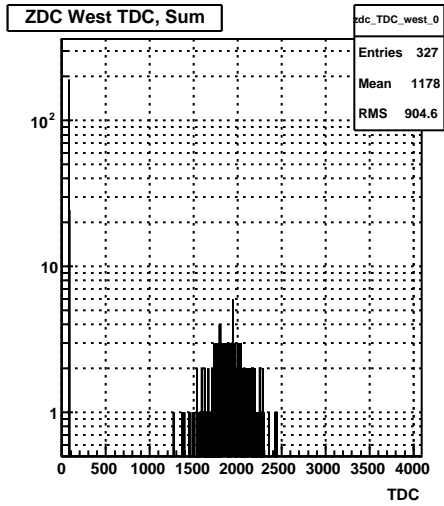
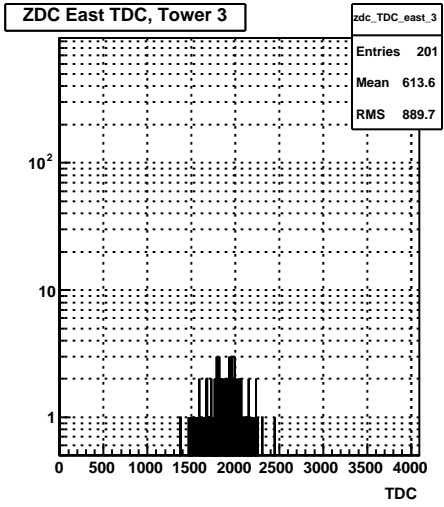
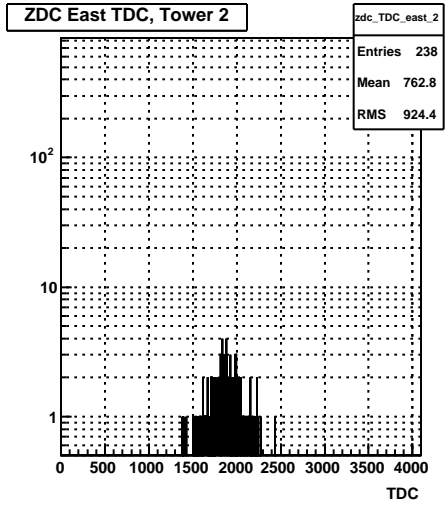
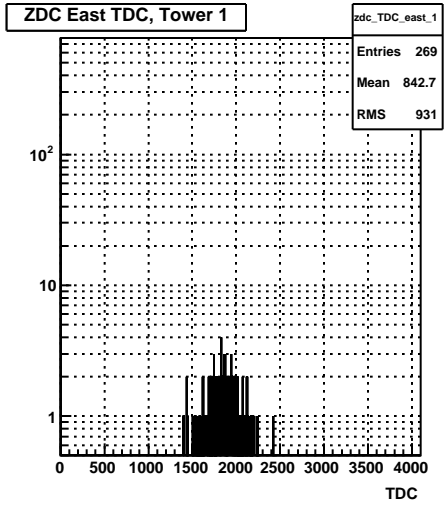
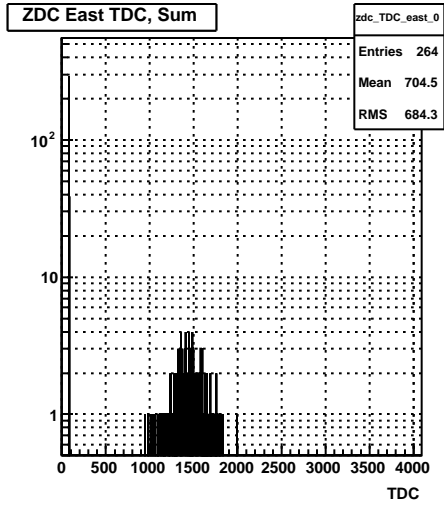
ZDC Towers (run 10109006)

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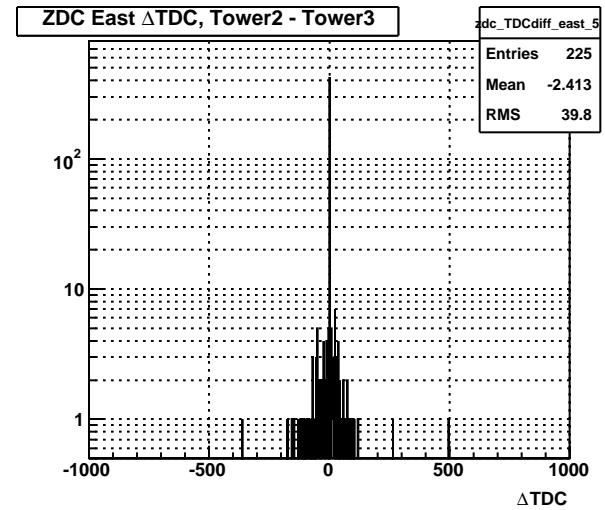
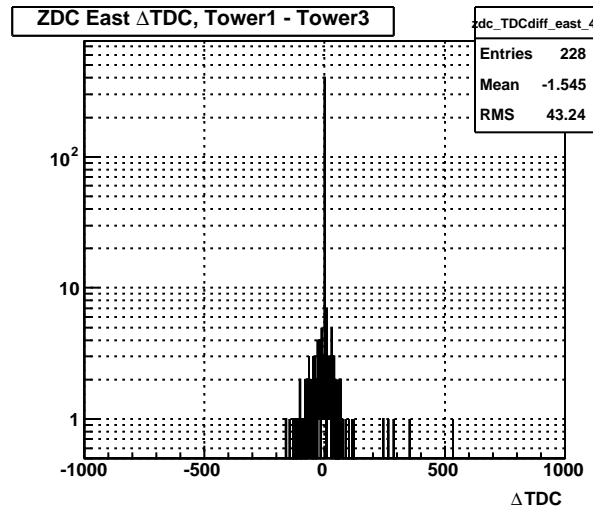
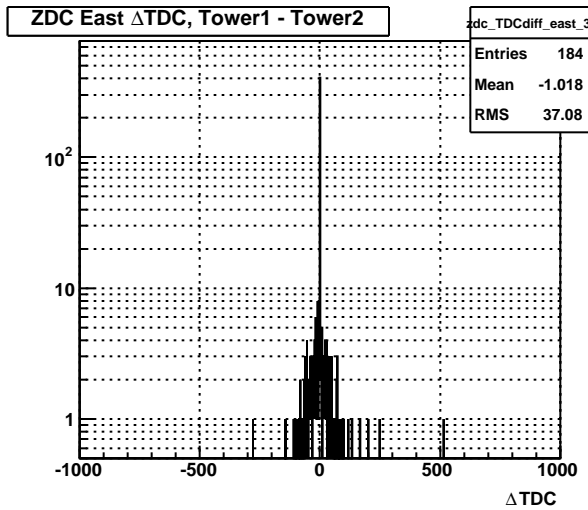
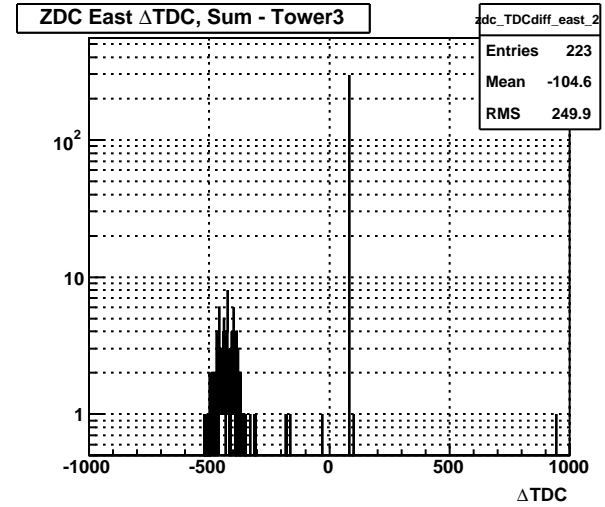
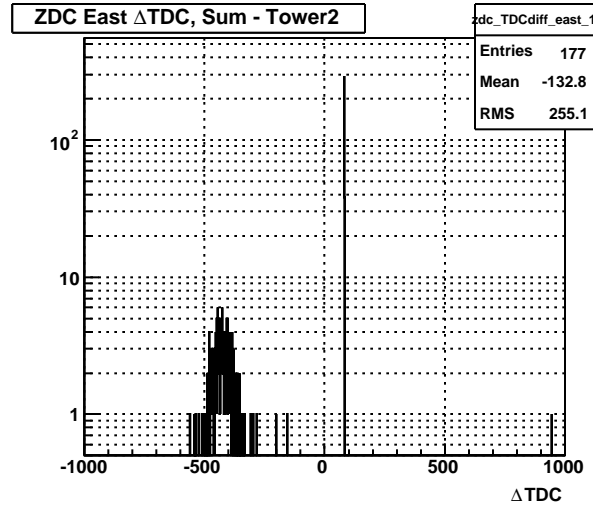
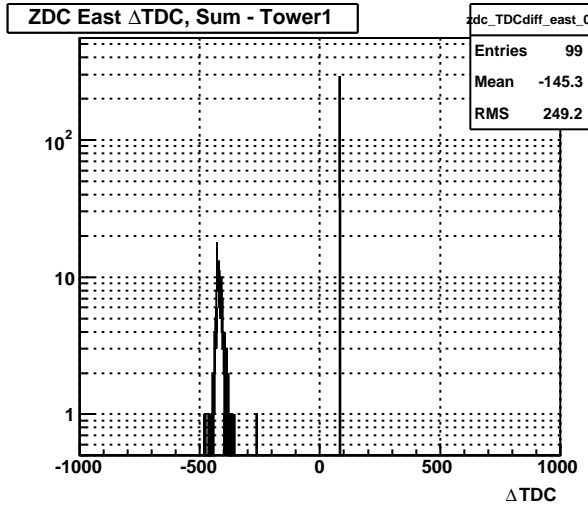
ZDC Towers Corrected (run 10109006)

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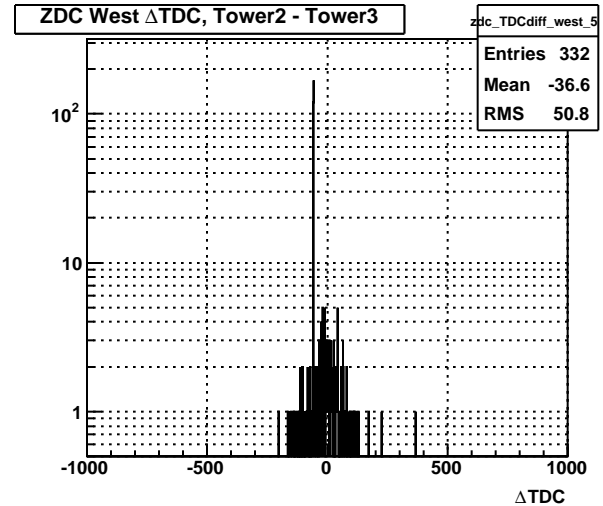
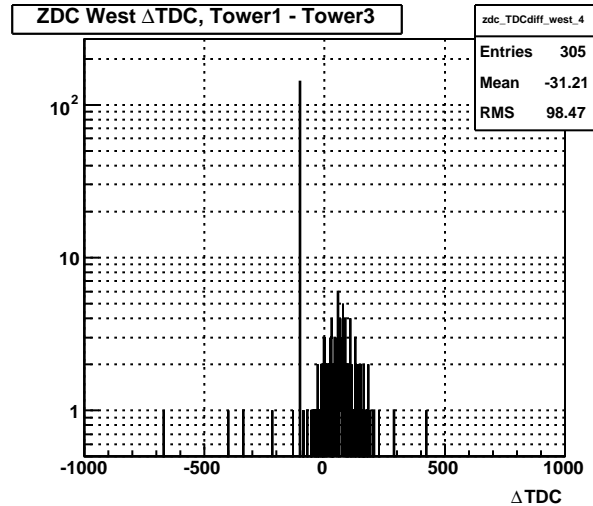
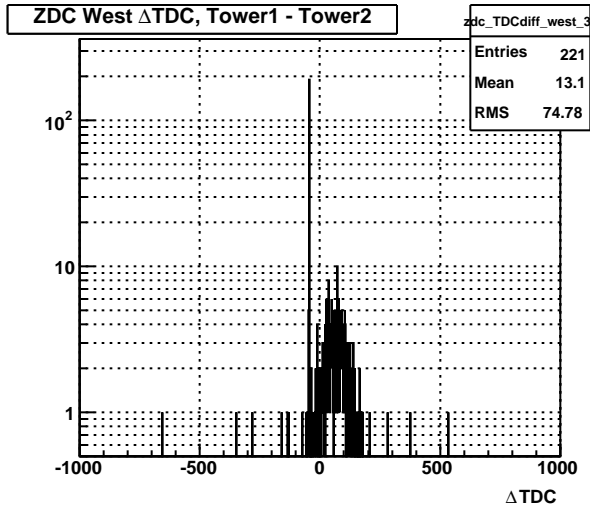
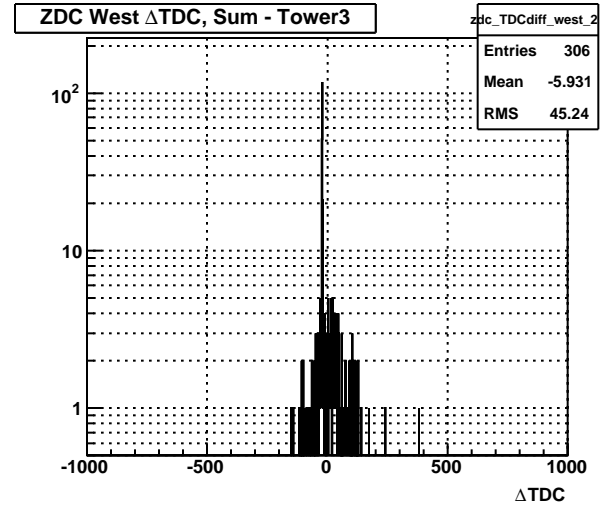
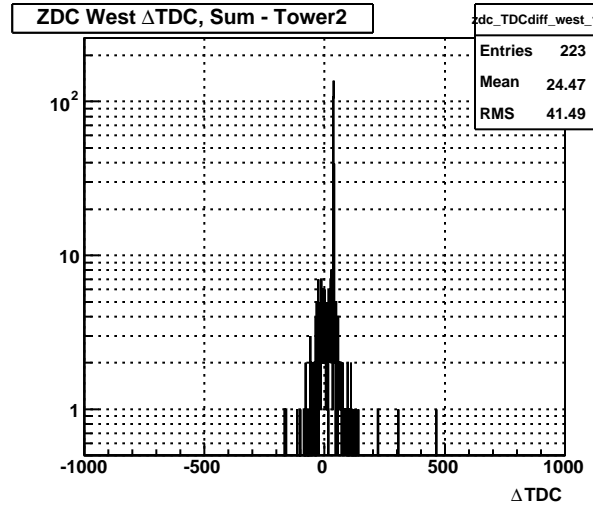
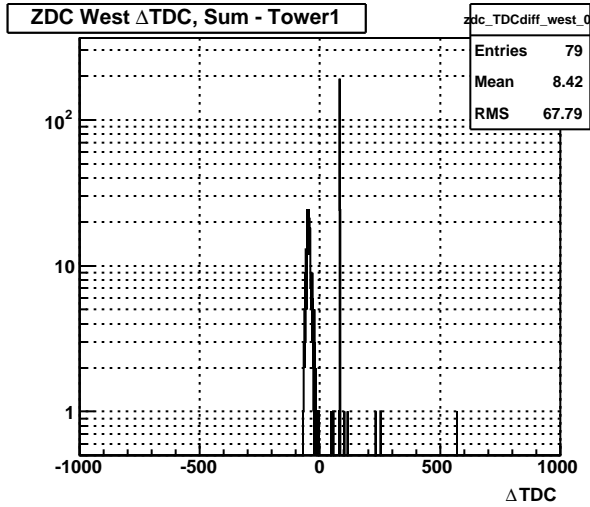
ZDC TDC (run 10109006)

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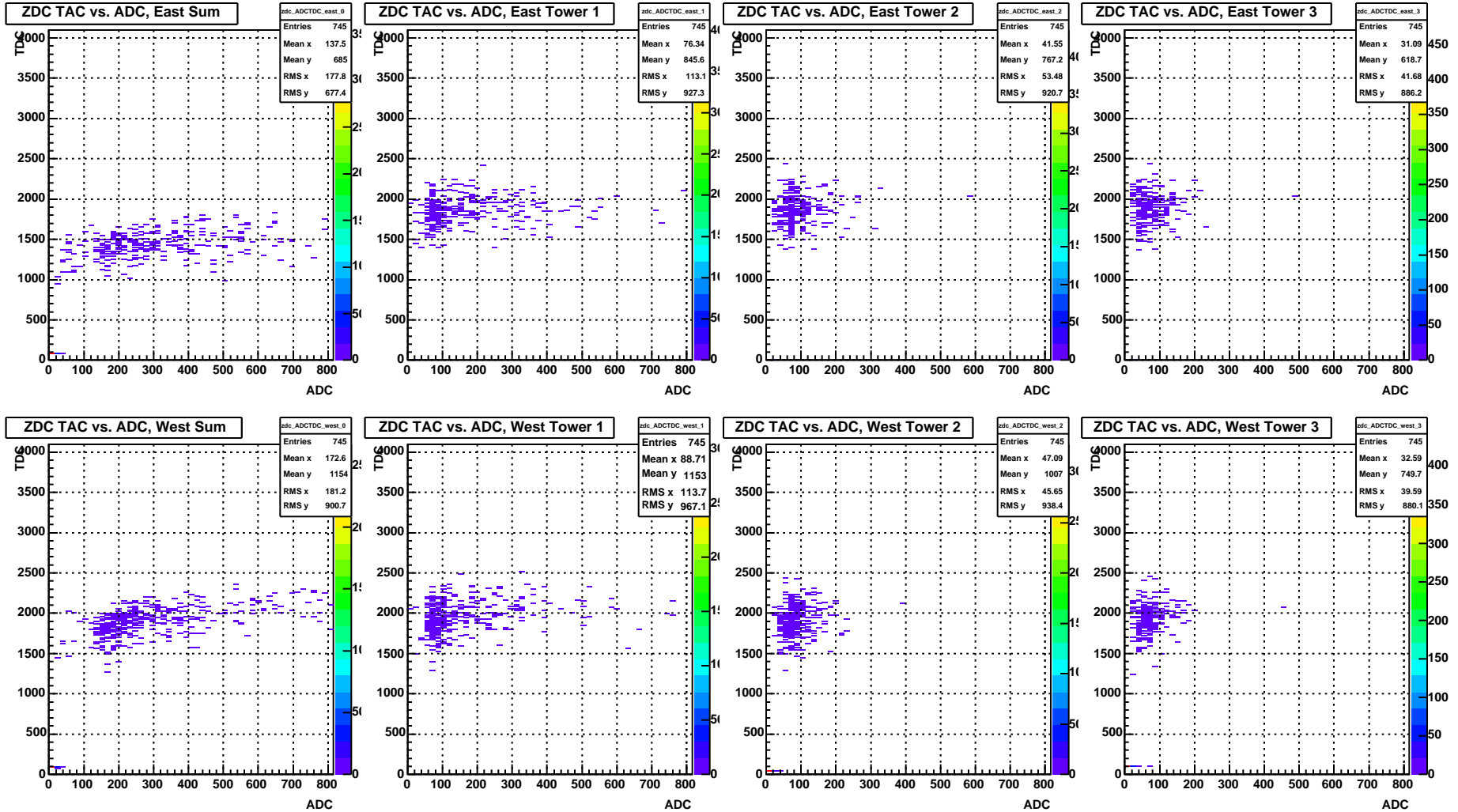
ZDC Δ TDC East (run 10109006)

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ZDC Δ TDC West (run 10109006)

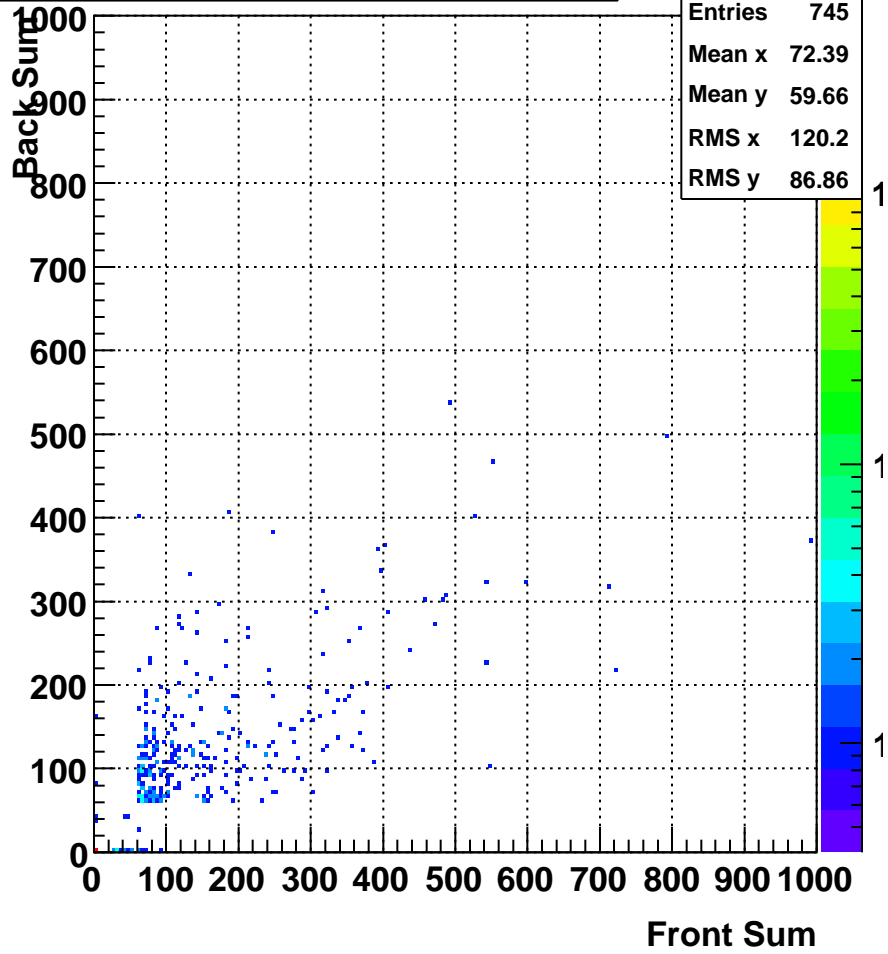
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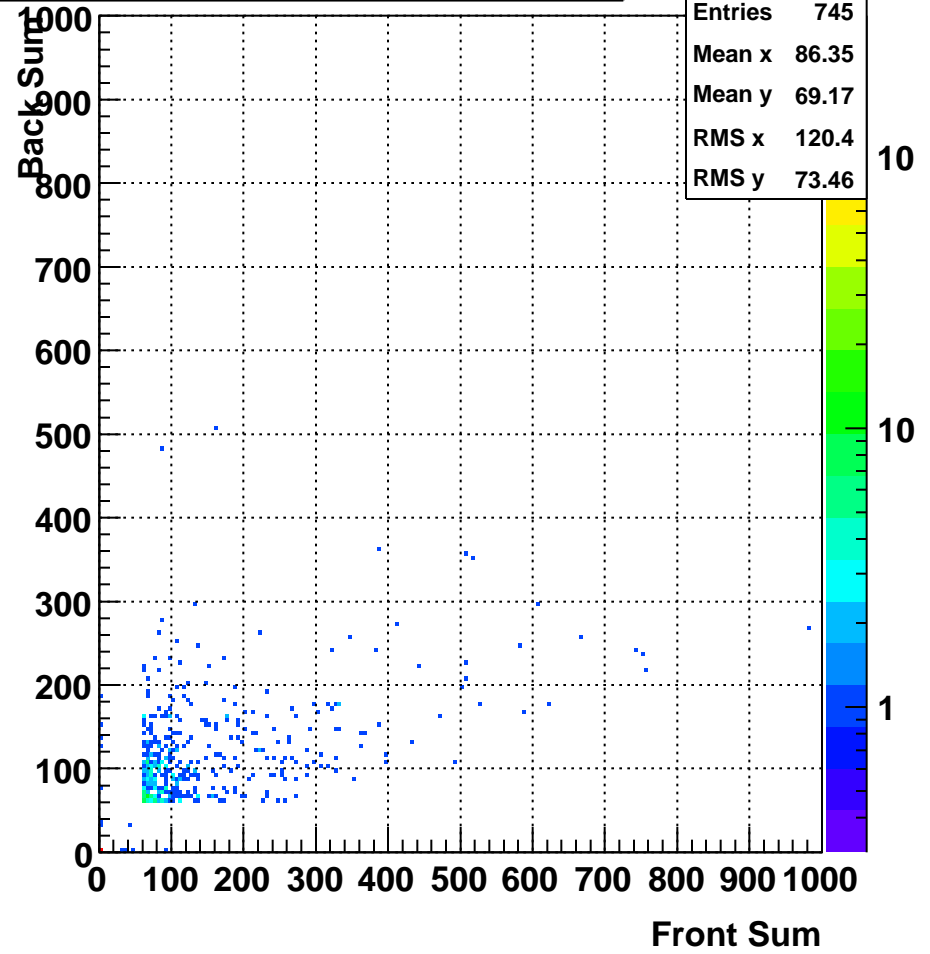
ZDC TDC vs. ADC (run 10109006)

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ZDC Front vs. Back ADC Sum, East

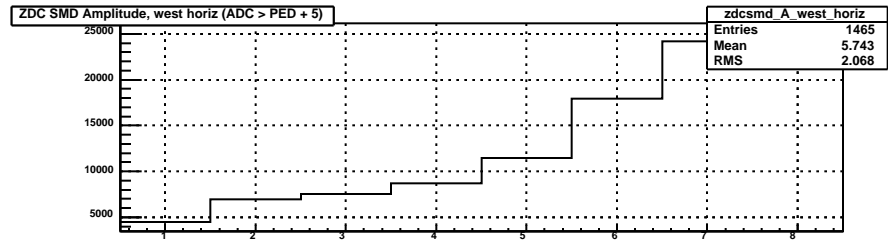
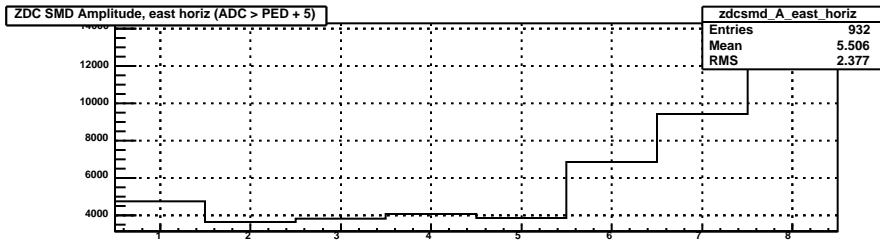
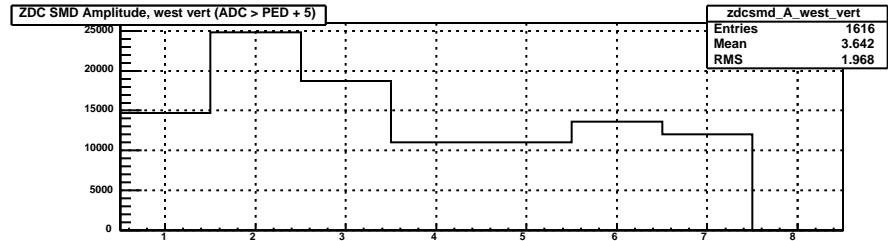
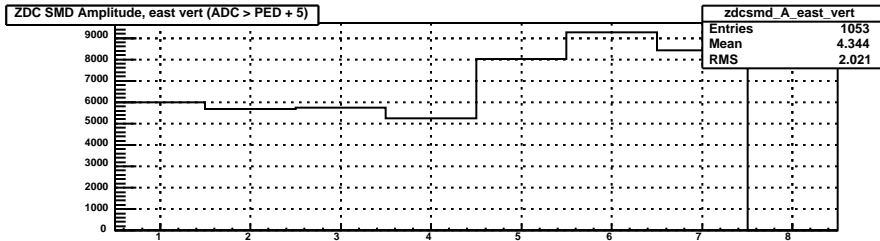
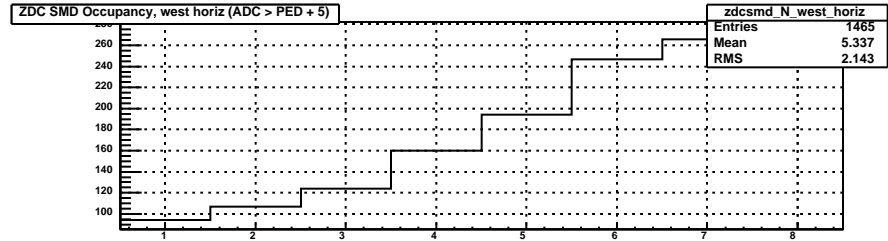
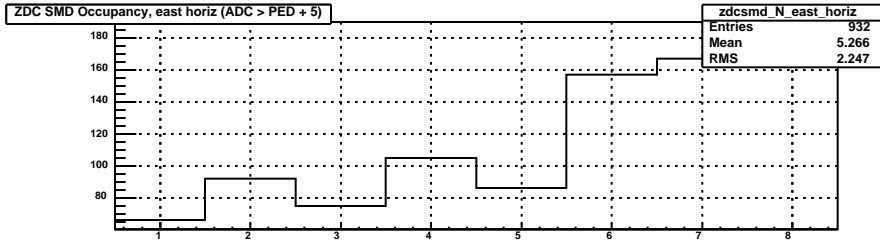
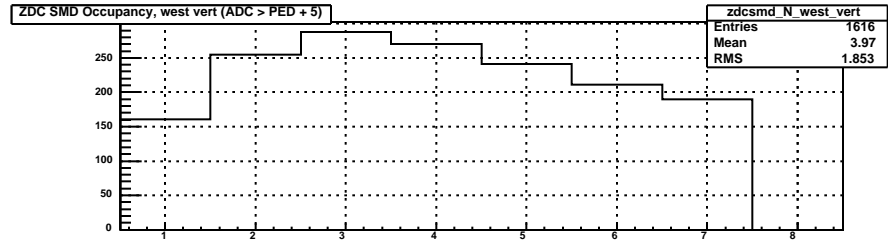
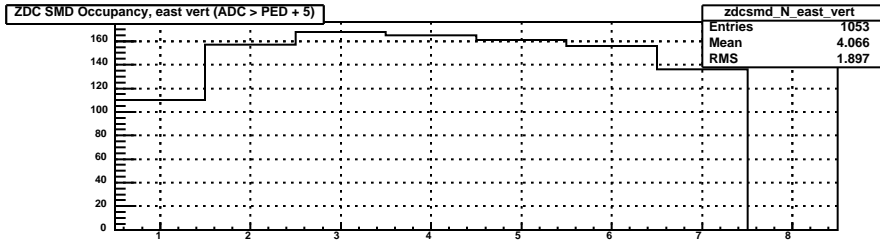


ZDC Front vs. Back ADC Sum, West



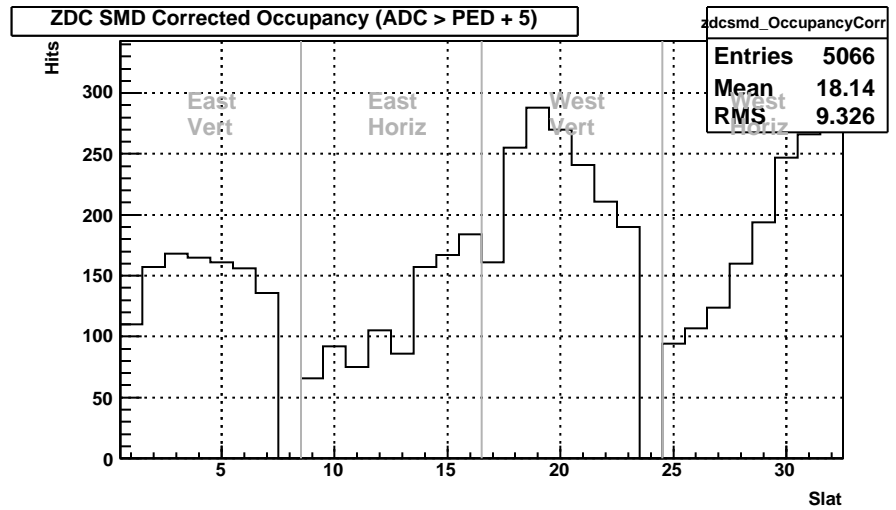
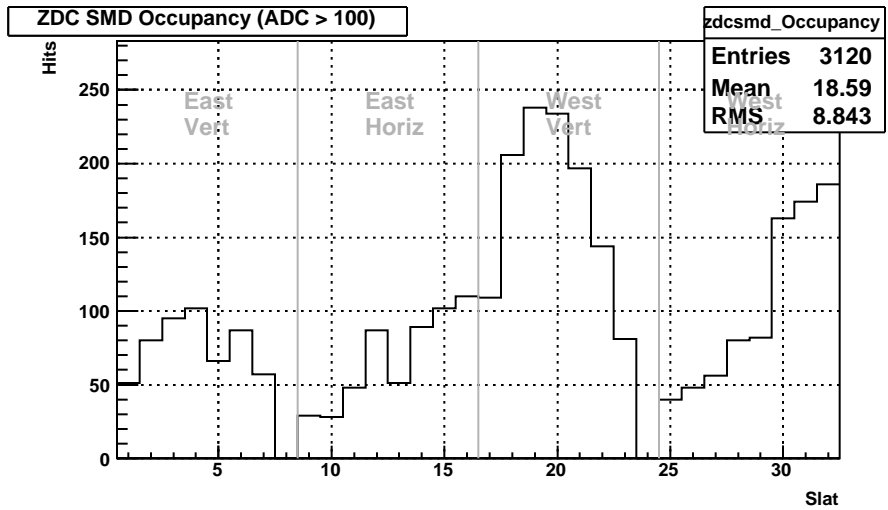
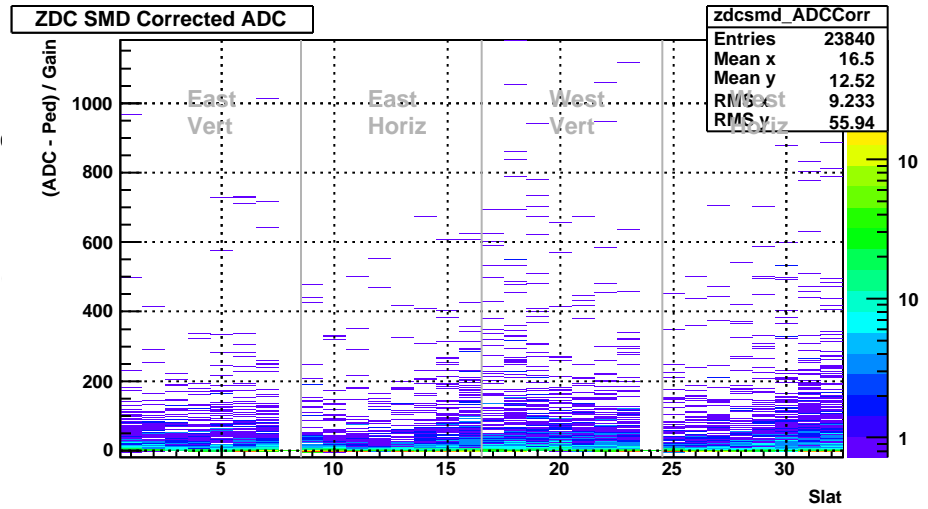
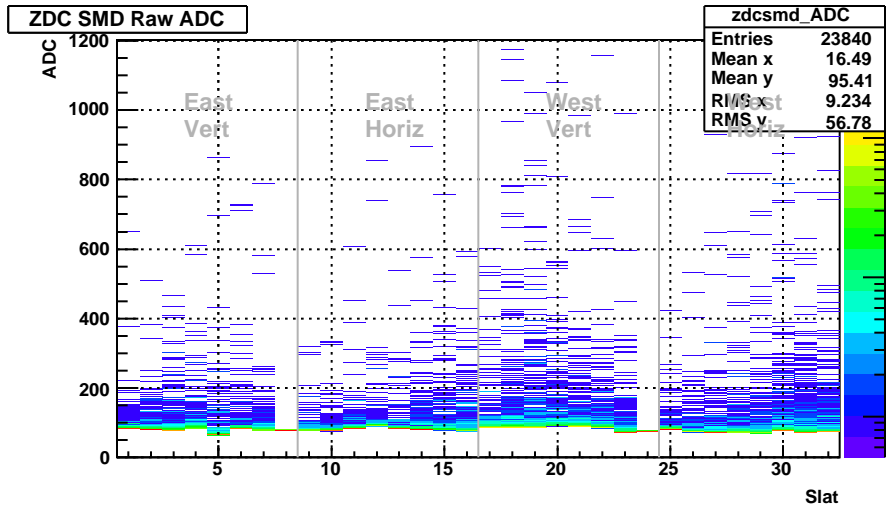
ZDC Front vs. Back Sum (run 10109006)

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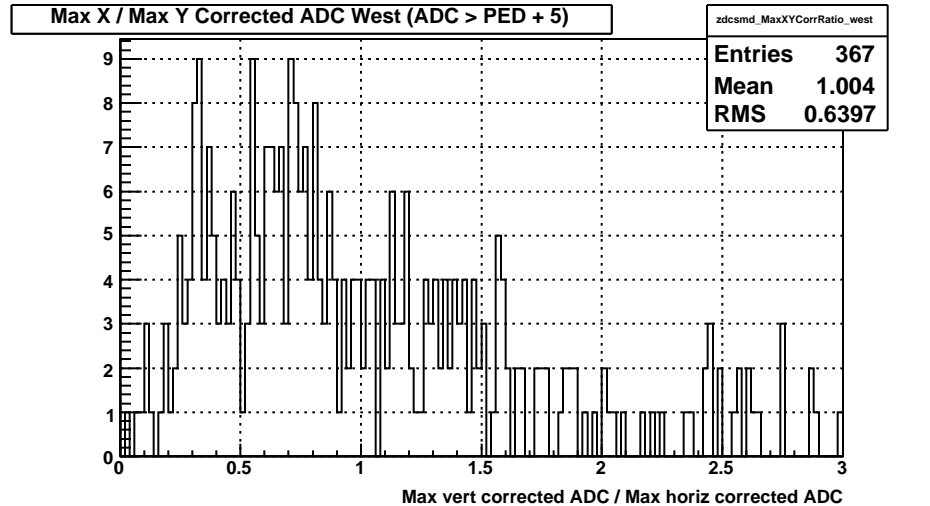
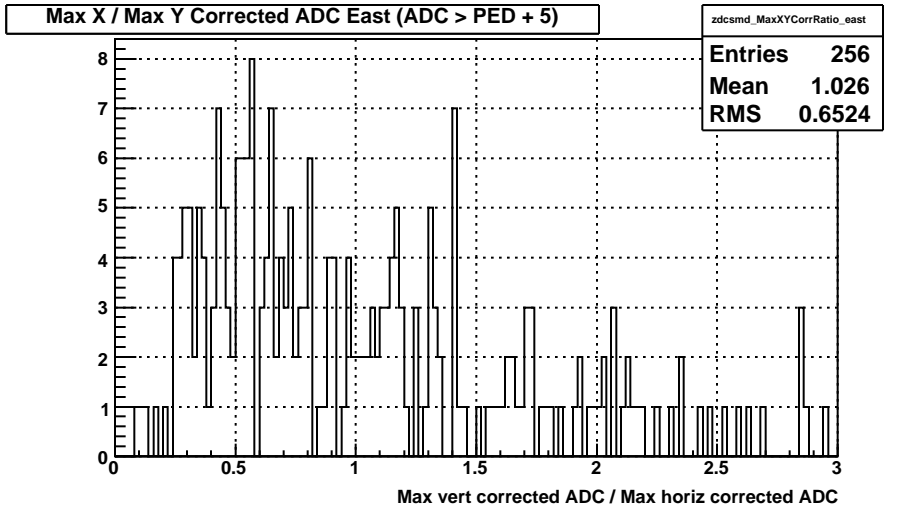
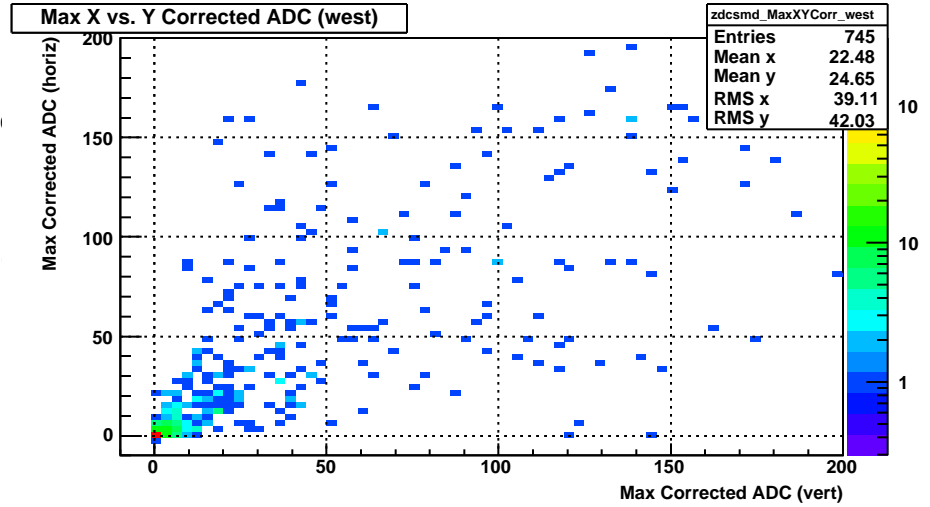
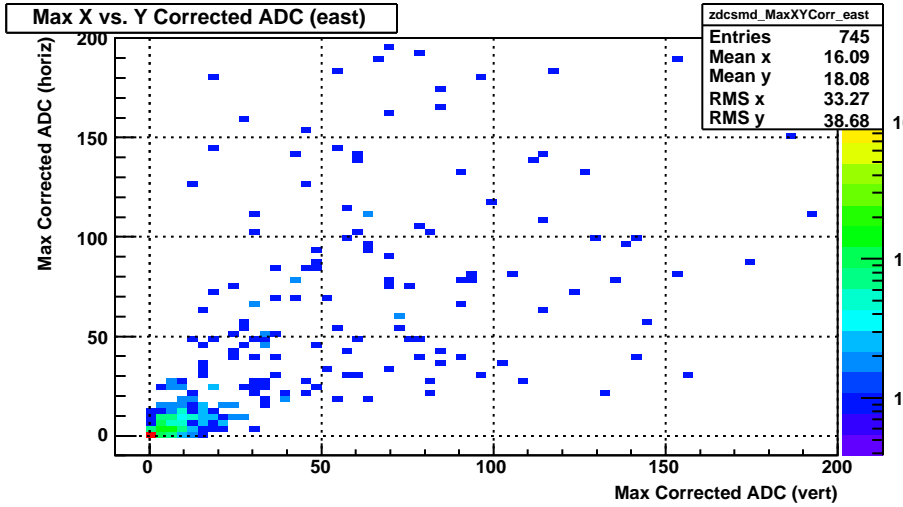
ZDC SMD (run 10109006)

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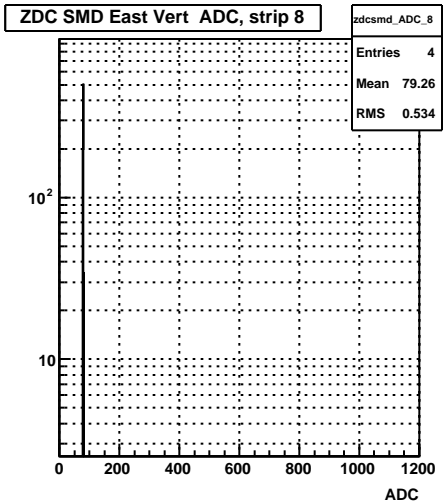
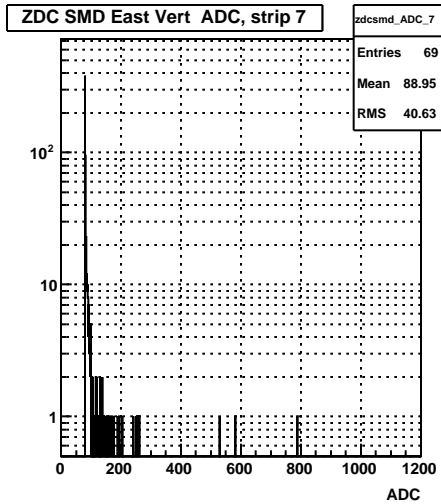
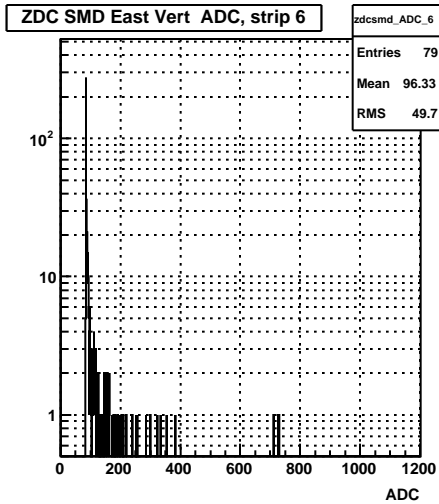
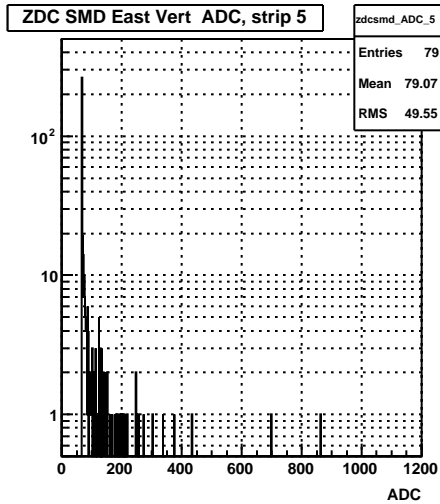
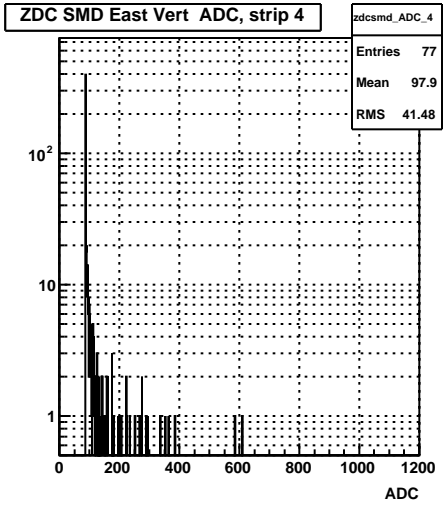
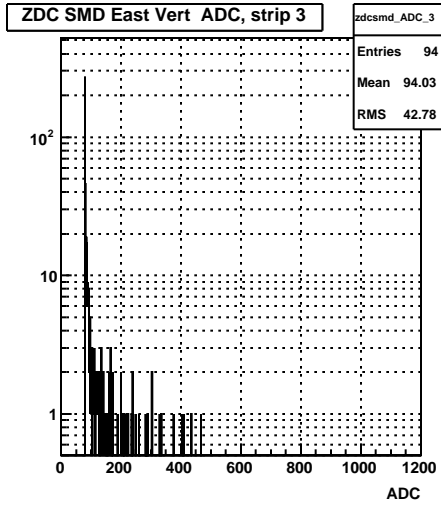
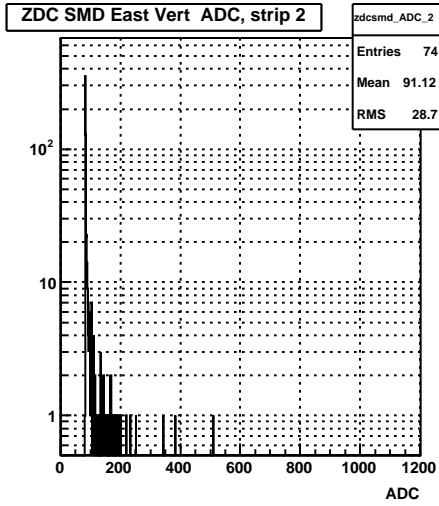
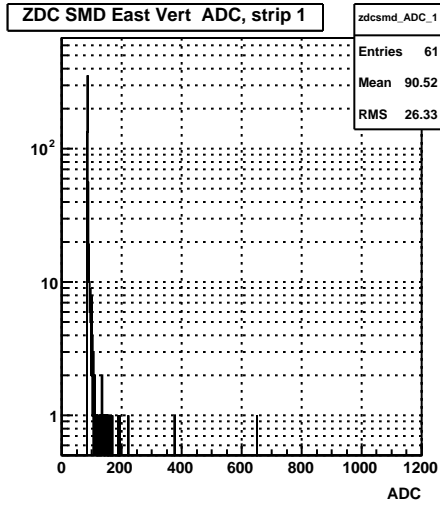
ZDC SMD Occupancy (run 10109006)

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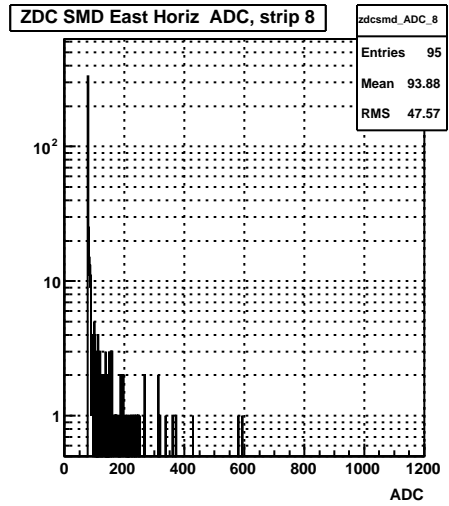
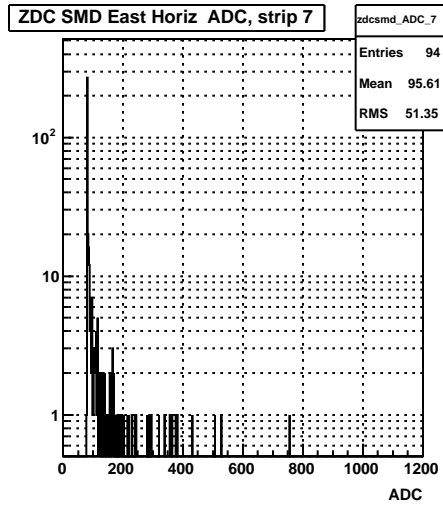
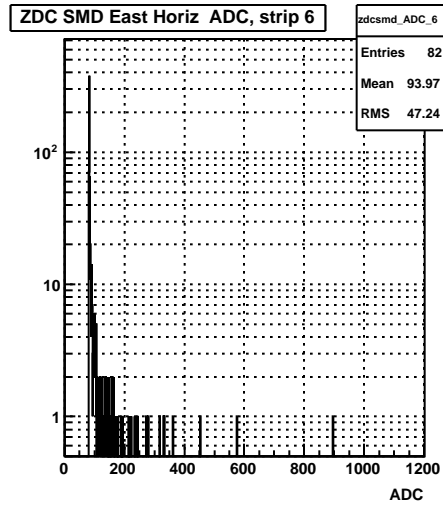
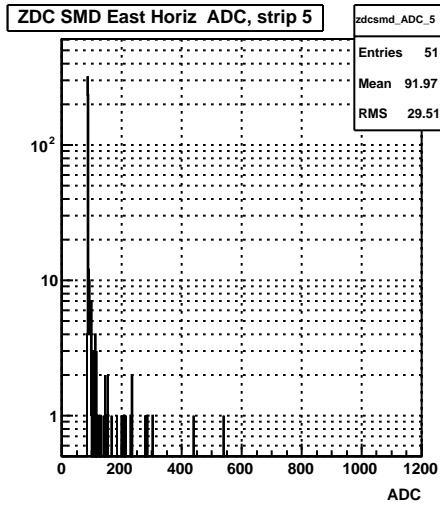
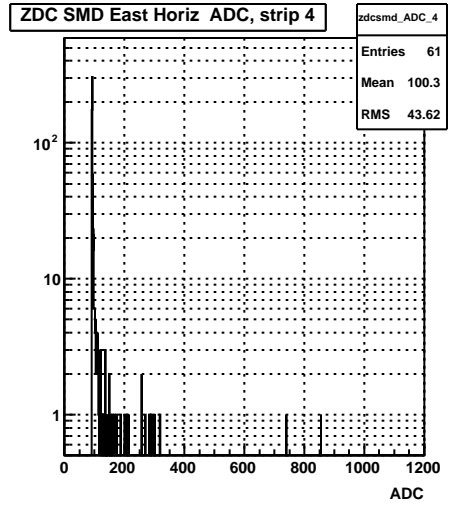
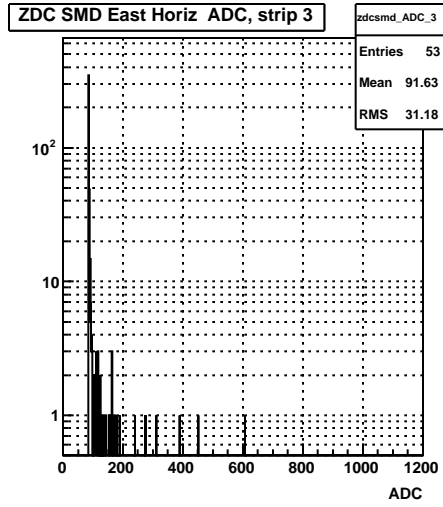
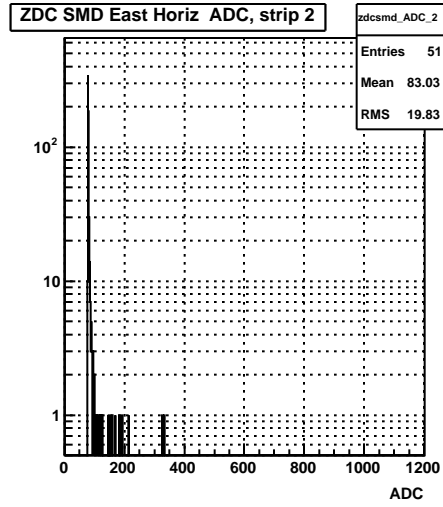
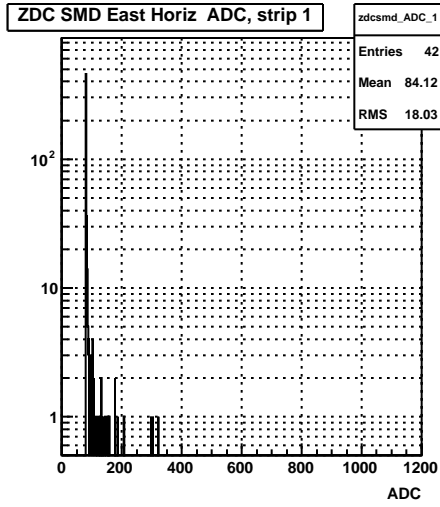
ZDC SMD Max XY (run 10109006)

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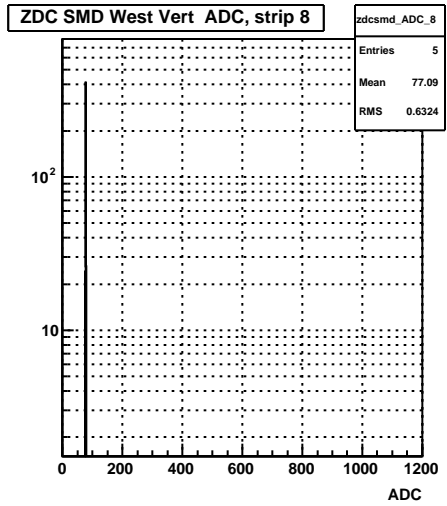
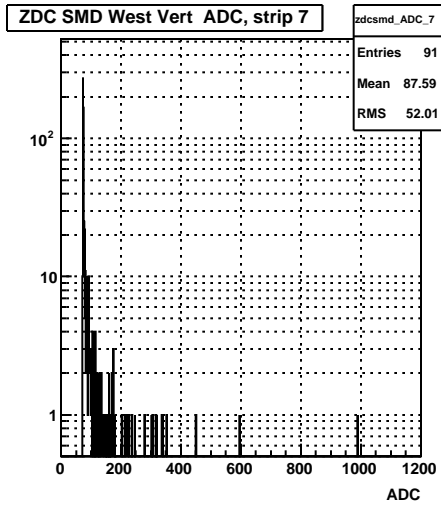
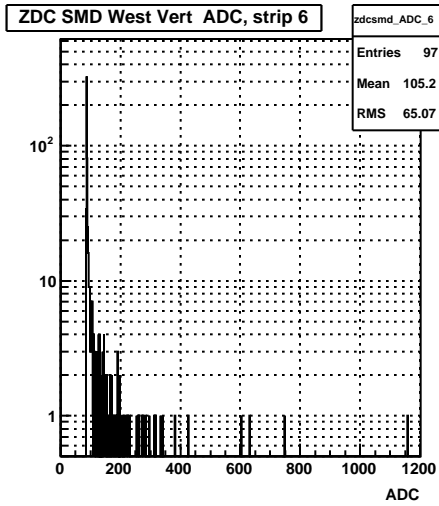
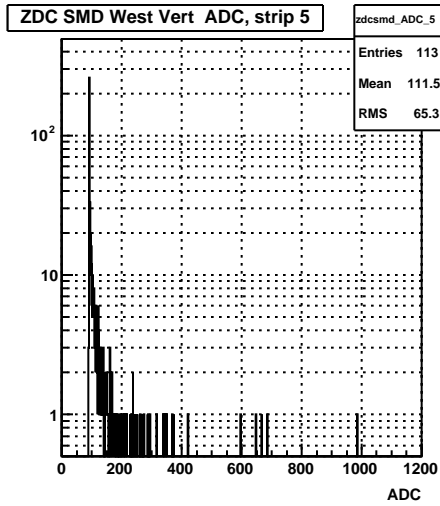
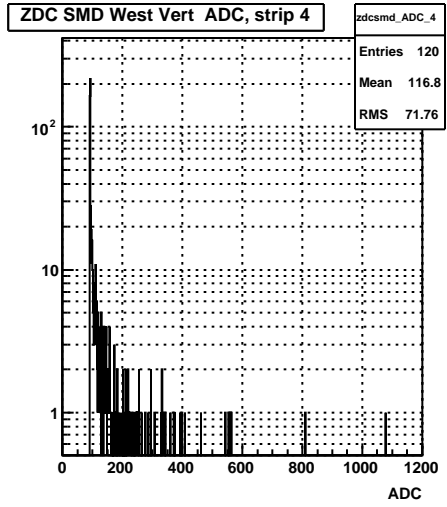
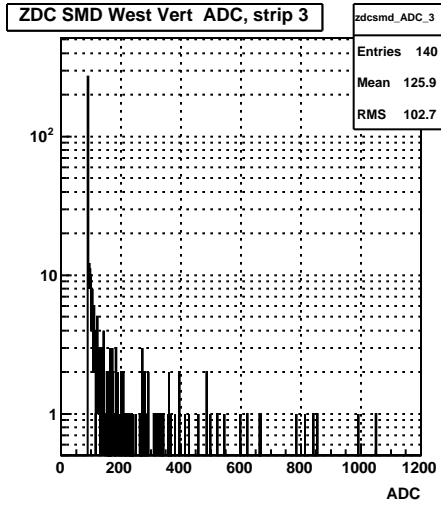
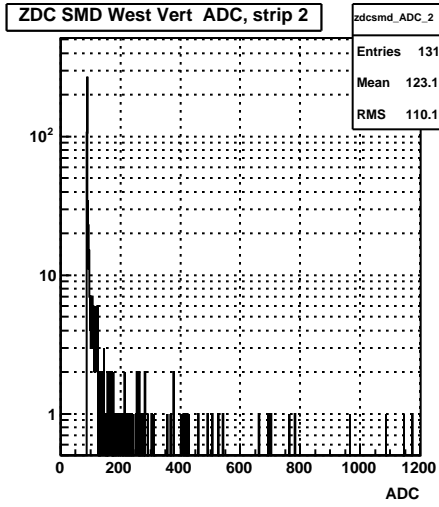
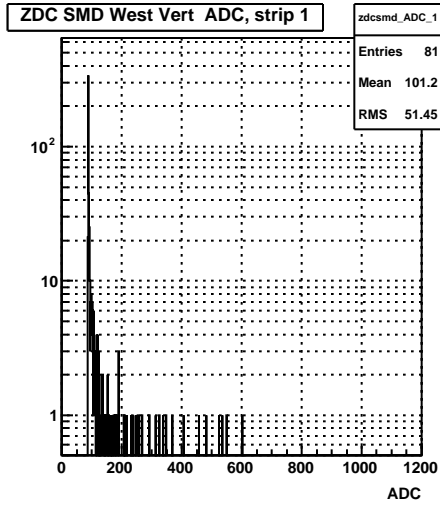
ZDC SMD ADC East Vert (run 10109006)

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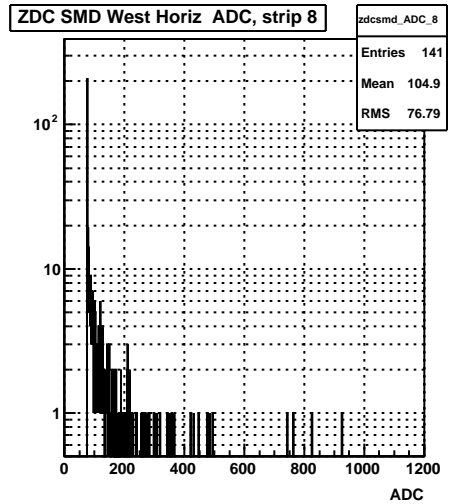
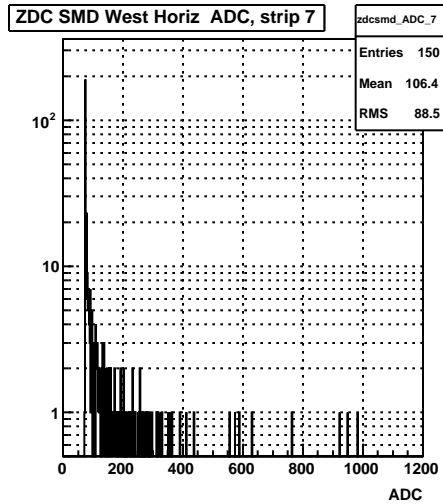
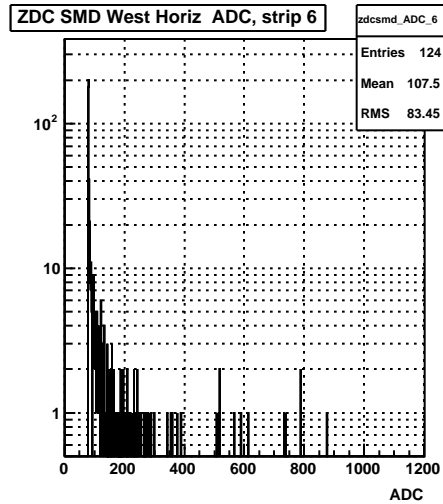
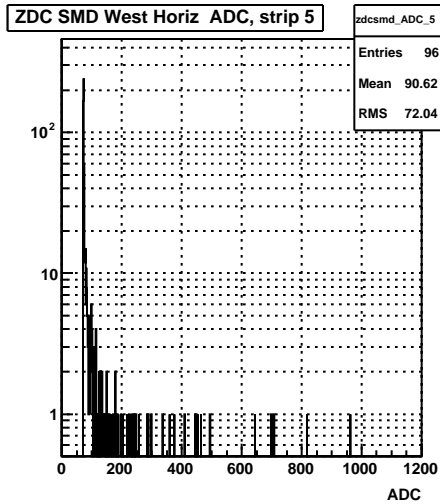
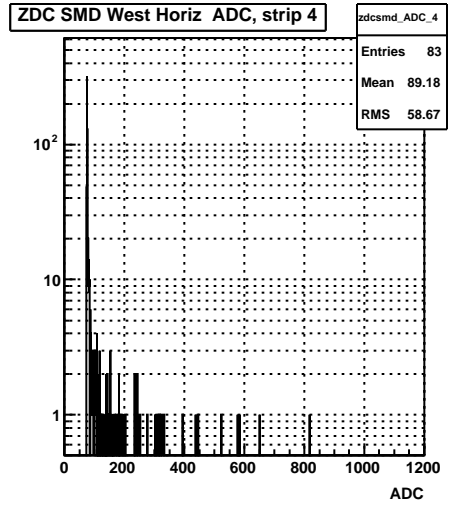
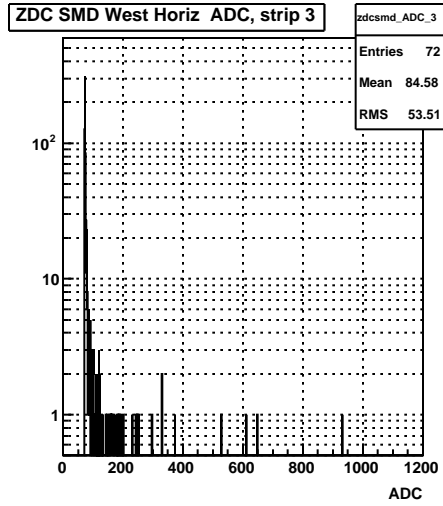
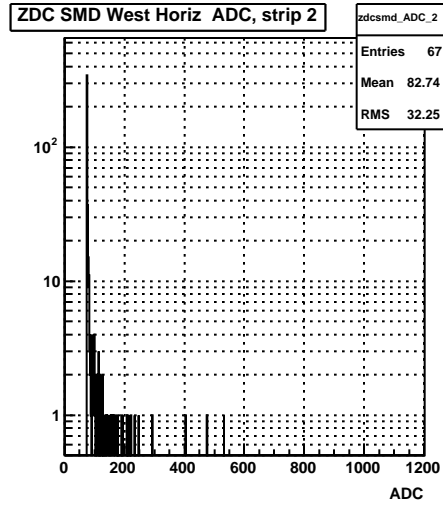
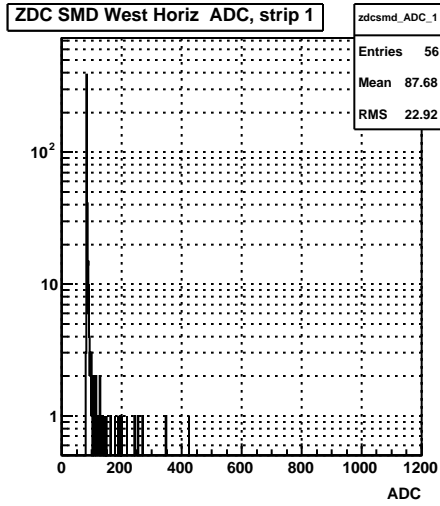
ZDC SMD ADC East Horiz (run 10109006)

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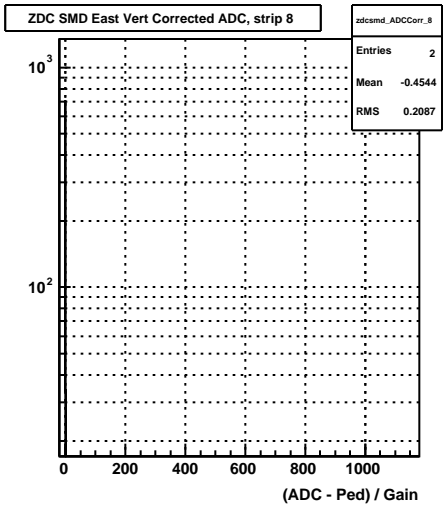
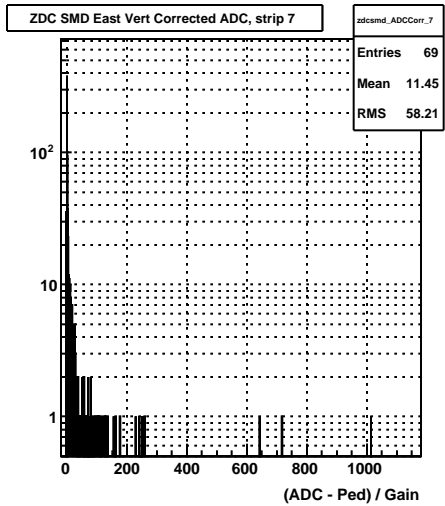
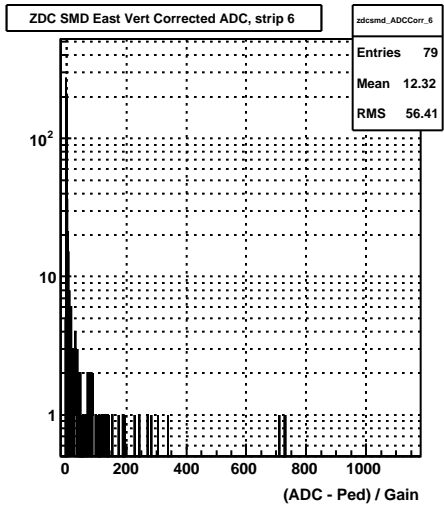
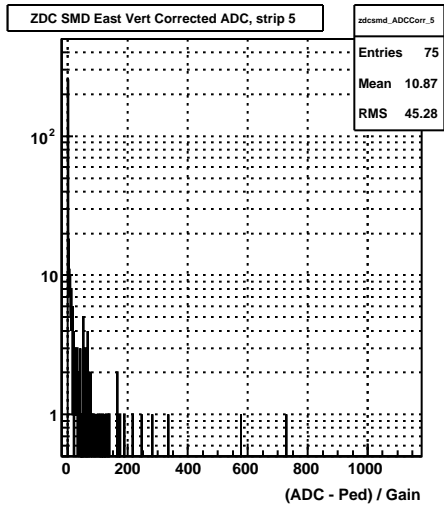
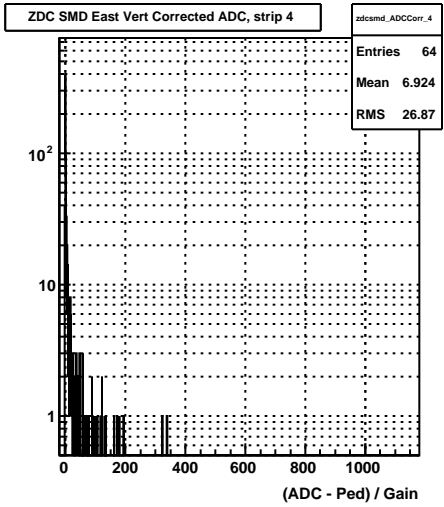
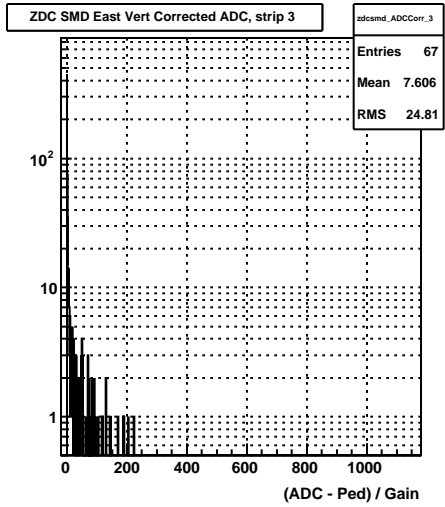
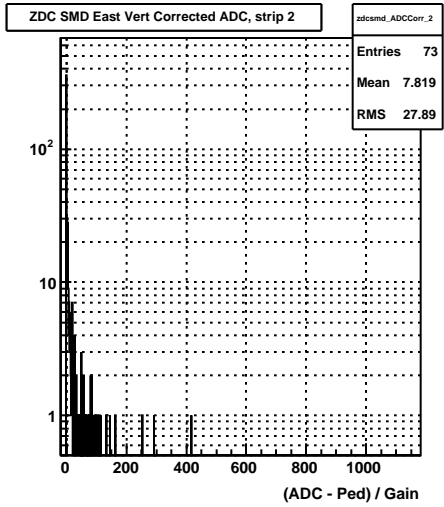
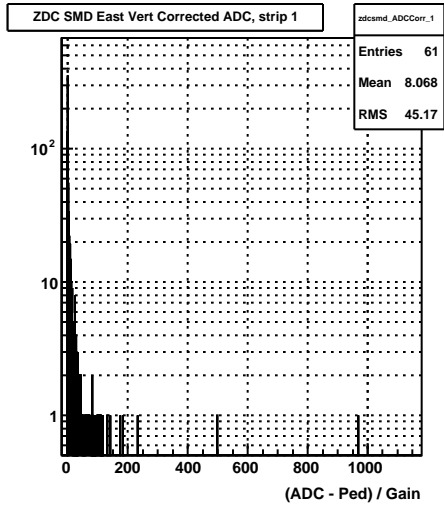
ZDC SMD ADC West Vert (run 10109006)

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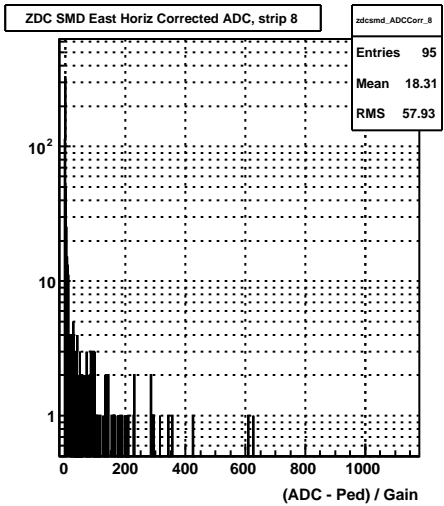
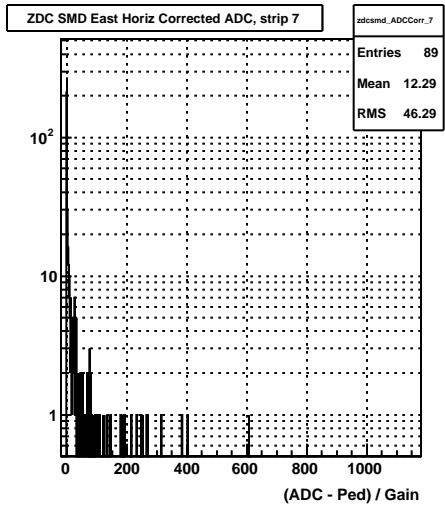
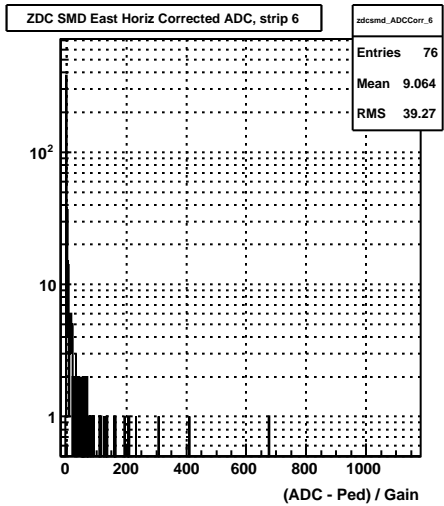
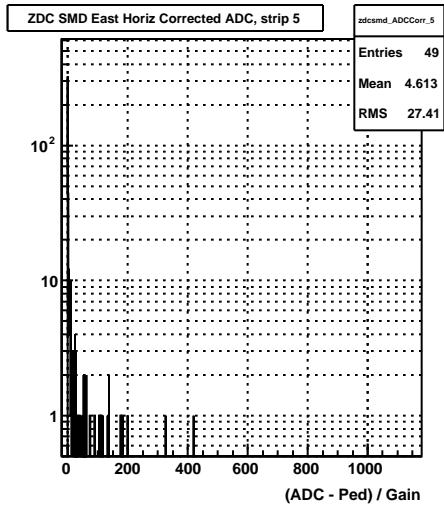
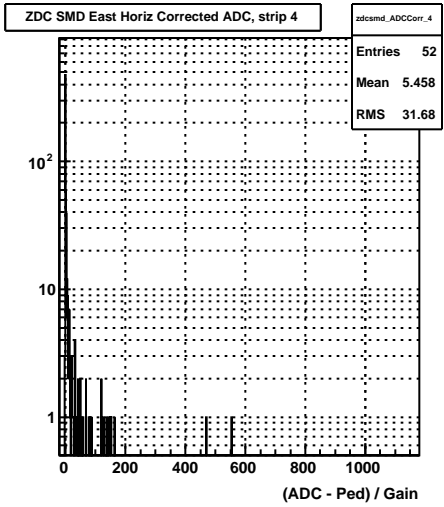
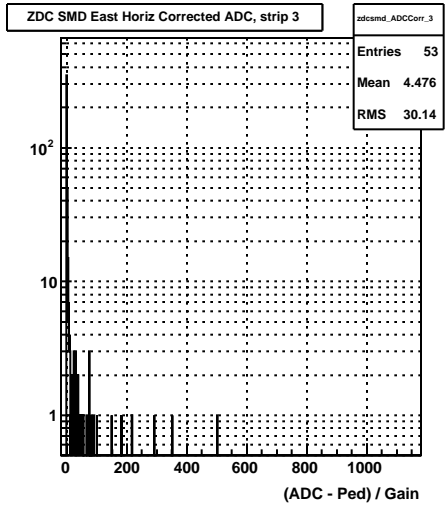
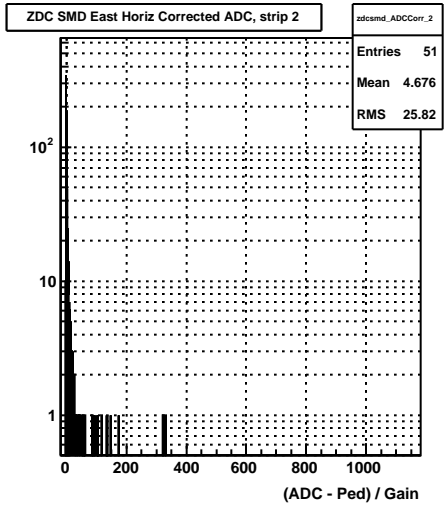
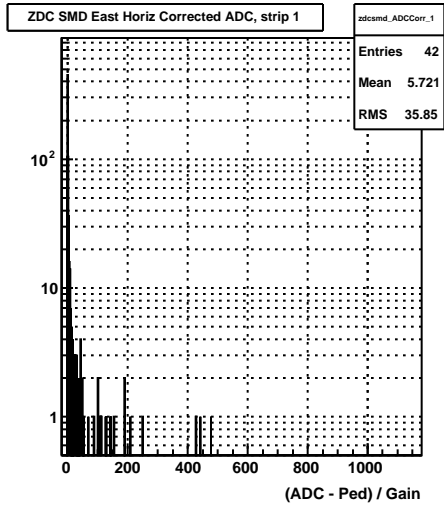
ZDC SMD ADC West Horiz (run 10109006)

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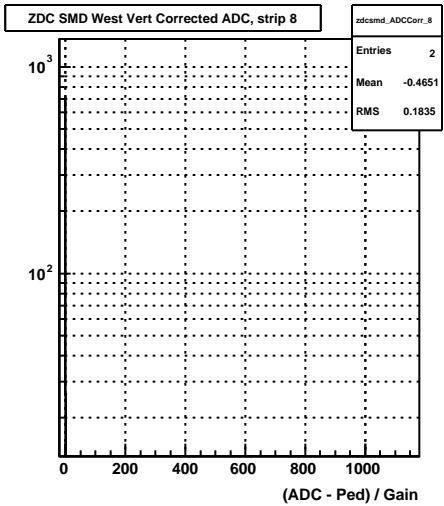
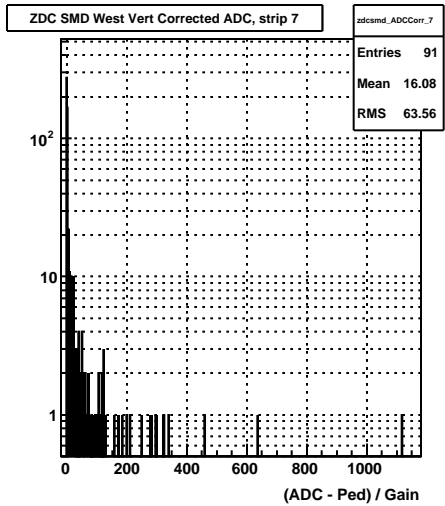
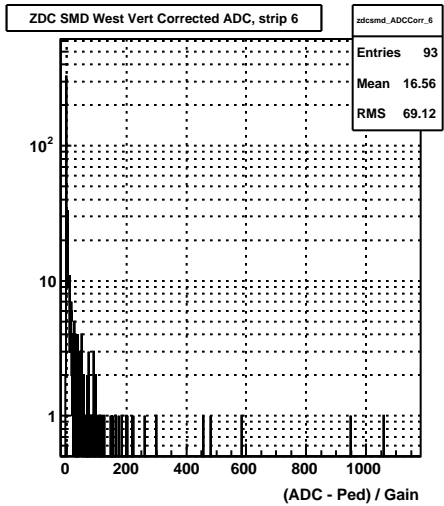
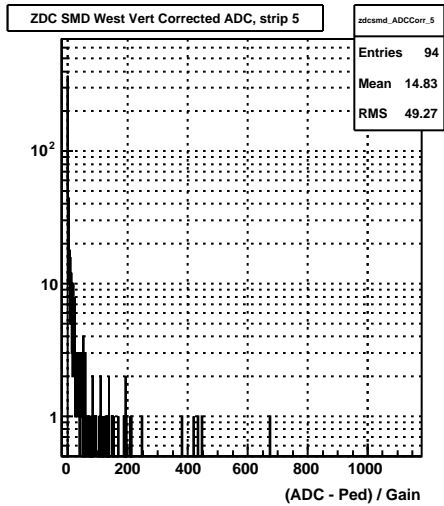
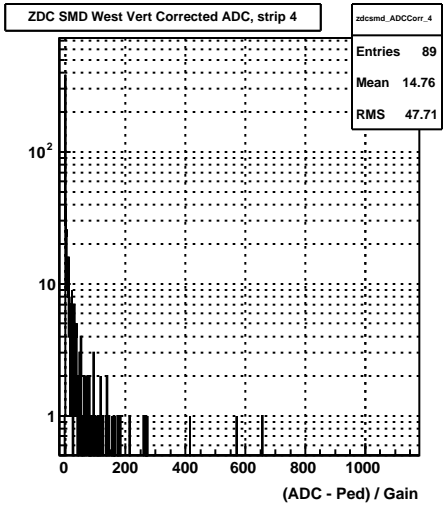
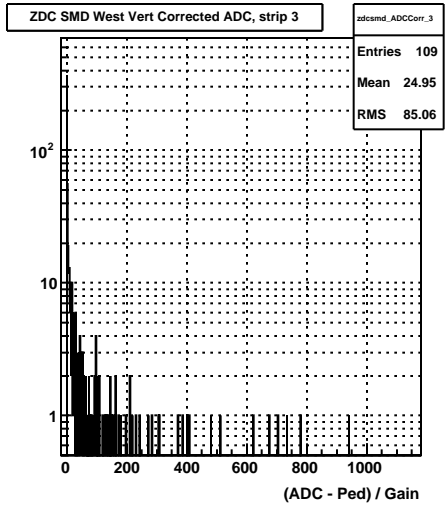
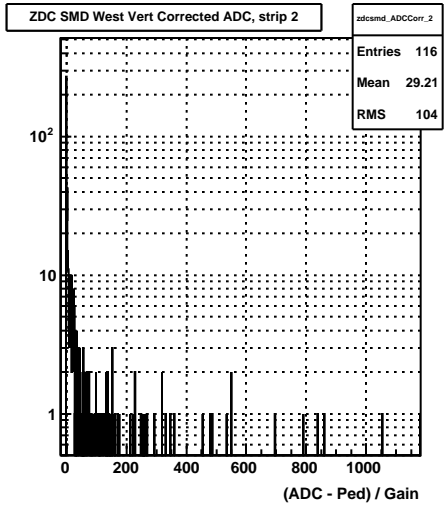
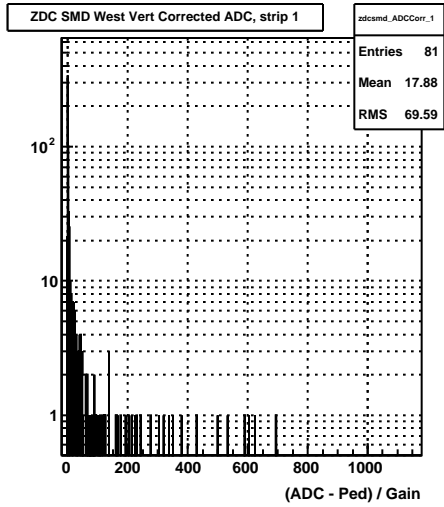
ZDC SMD ADC East Vert Corrected (run 10109006)

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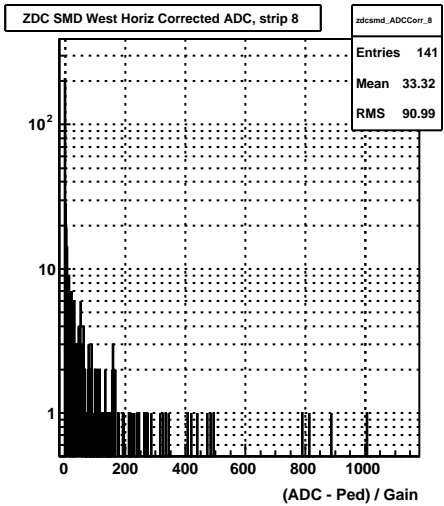
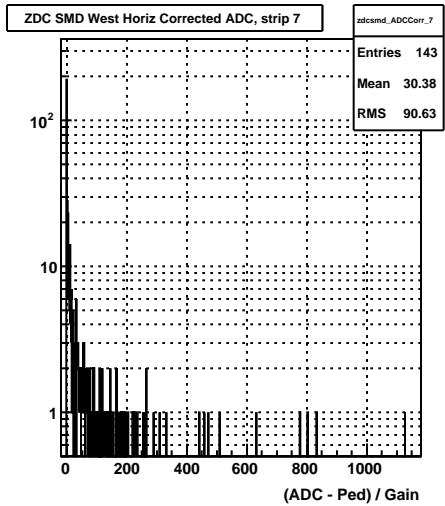
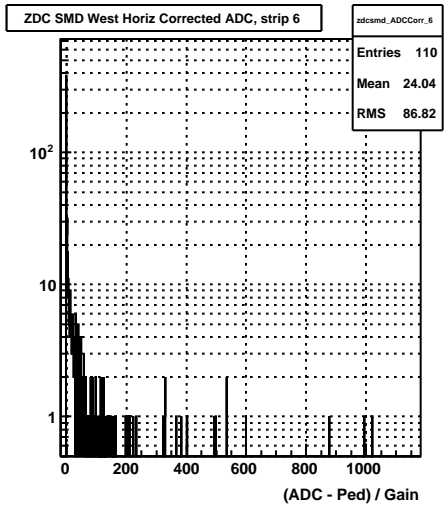
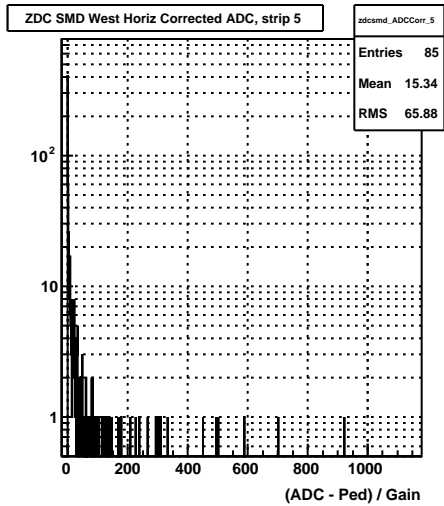
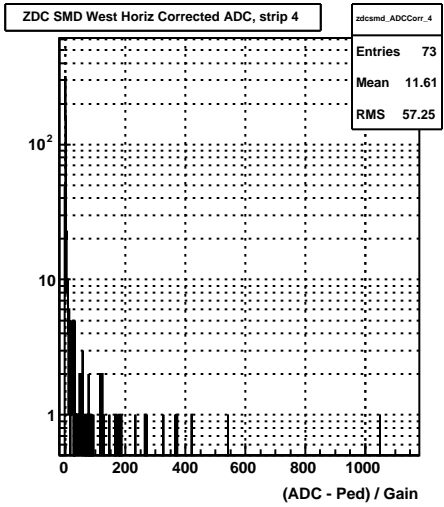
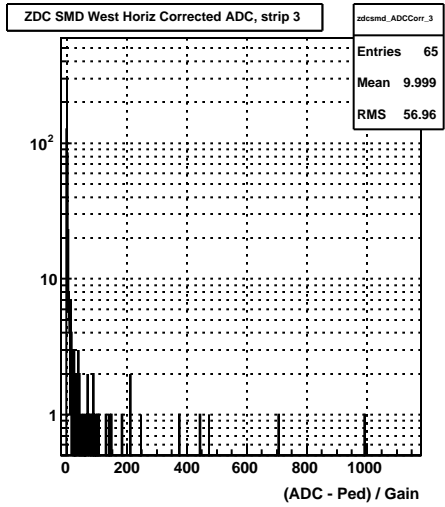
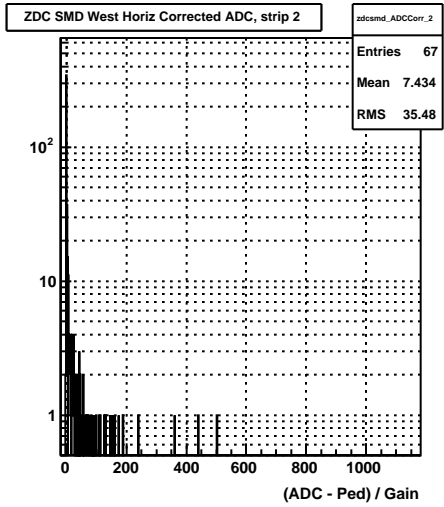
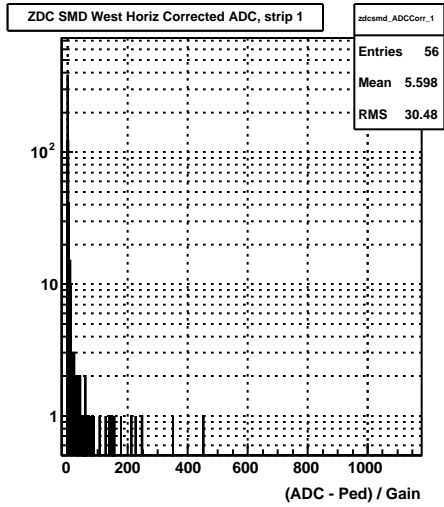
ZDC SMD ADC East Horiz Corrected (run 10109006)

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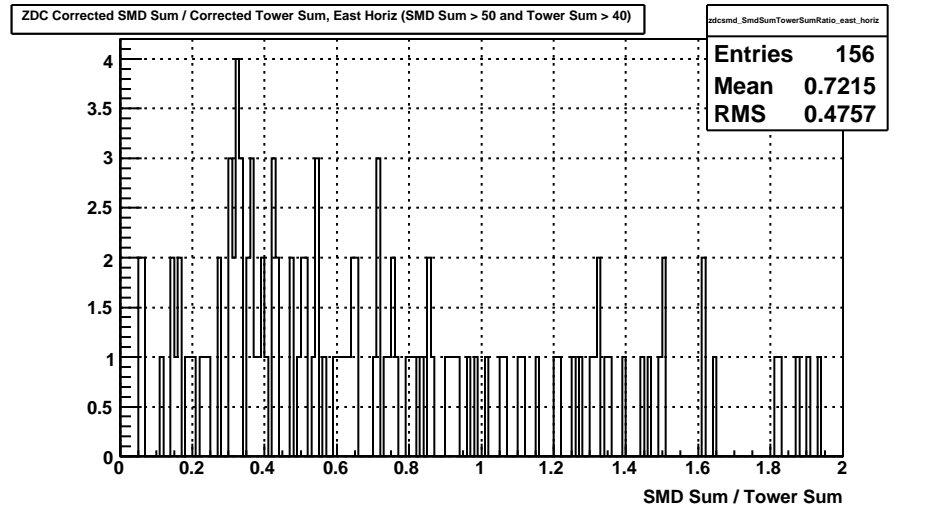
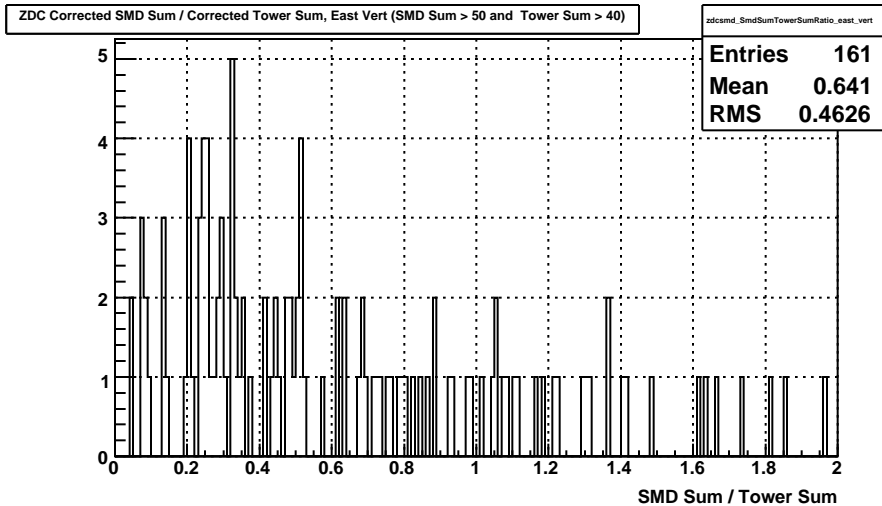
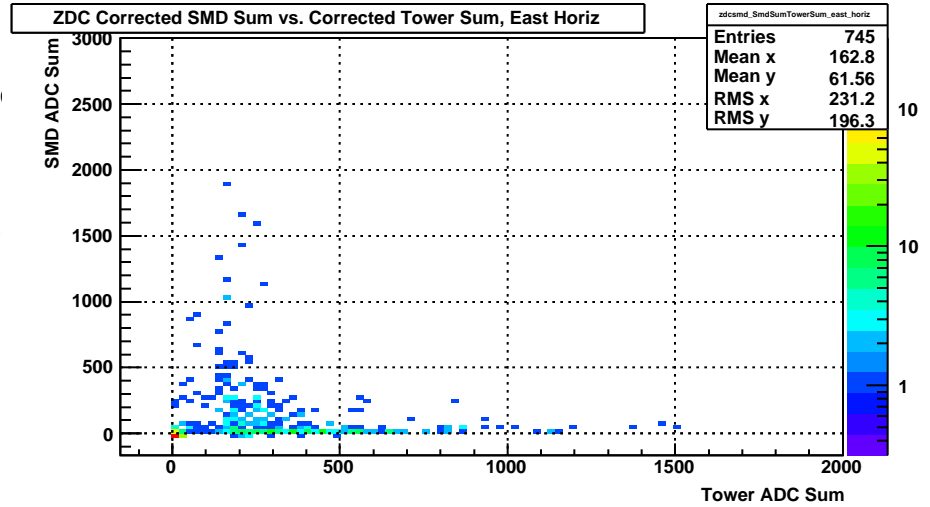
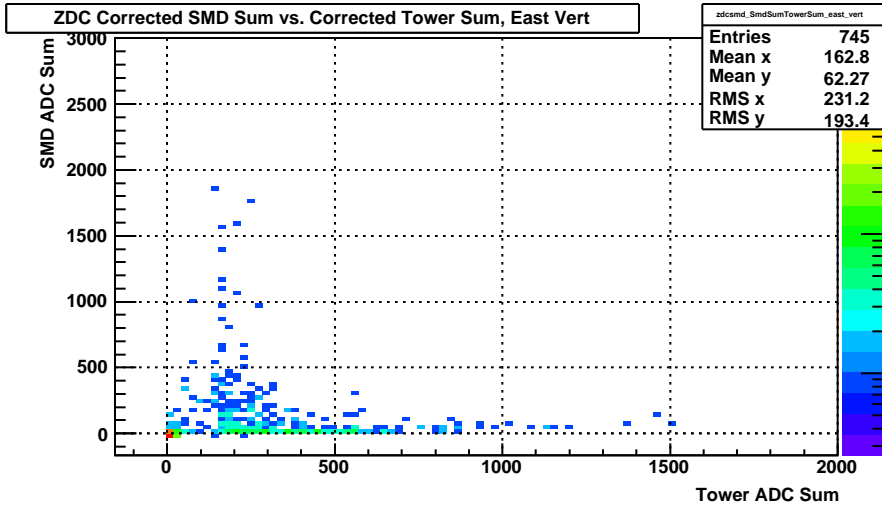
ZDC SMD ADC West Vert Corrected (run 10109006)

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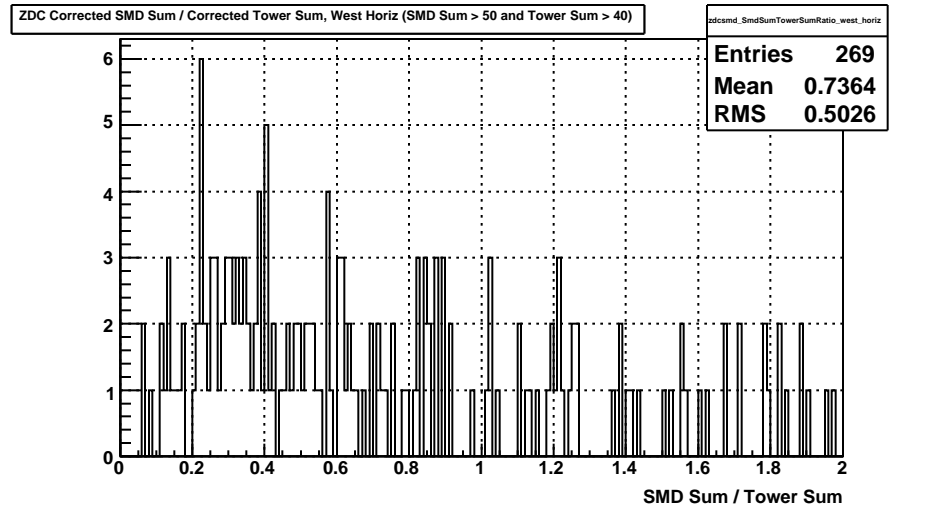
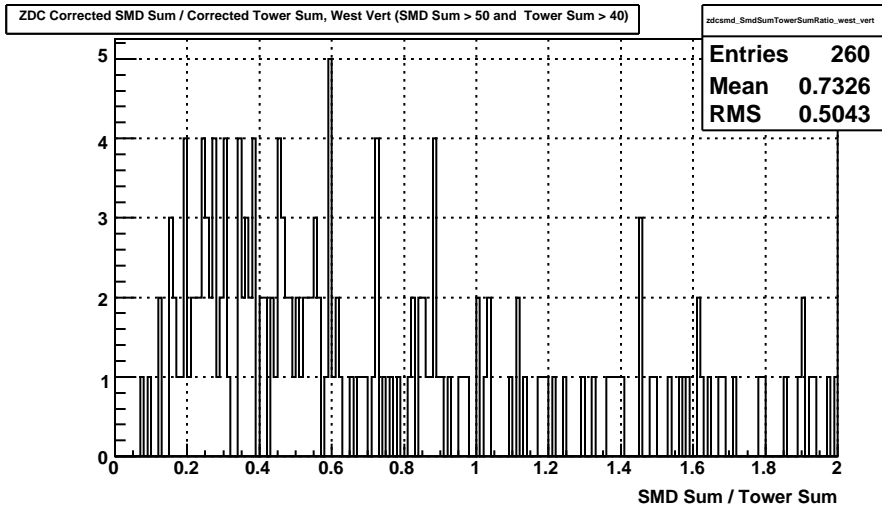
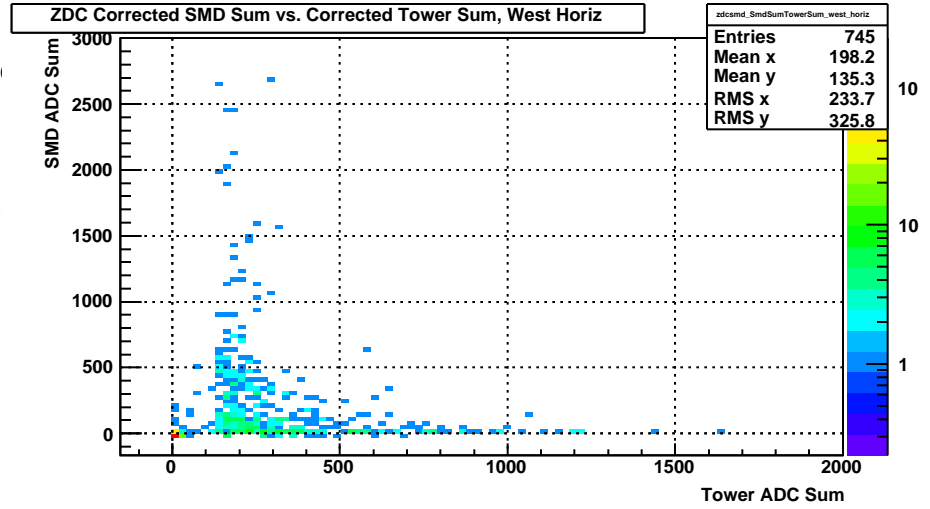
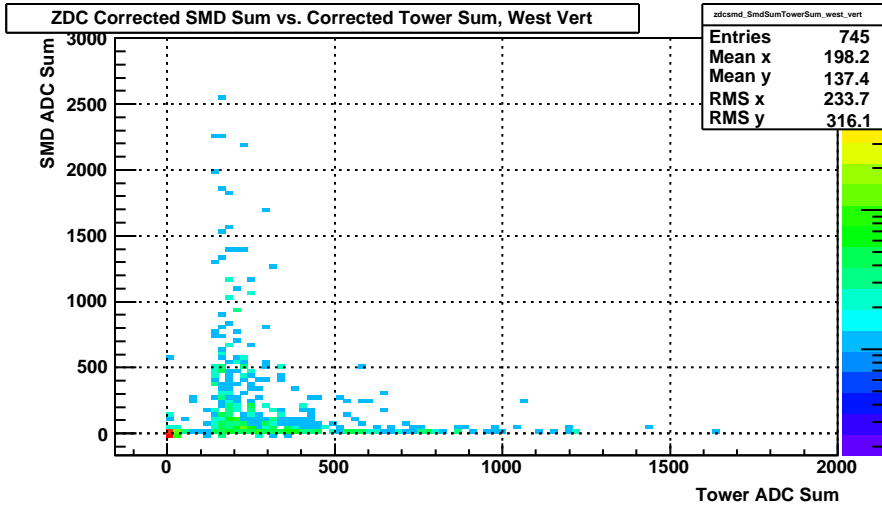
ZDC SMD ADC West Horiz Corrected (run 10109006)

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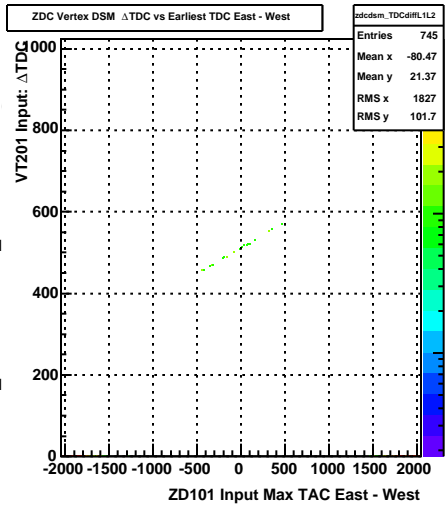
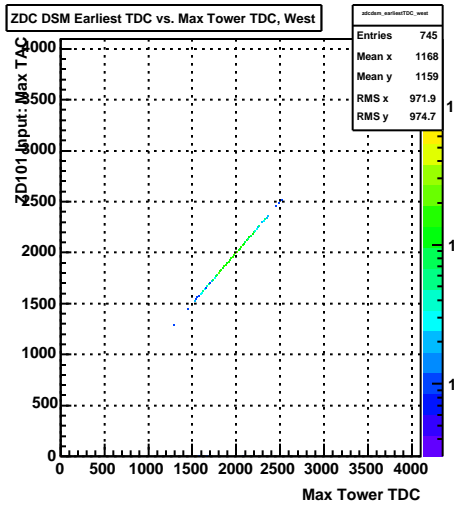
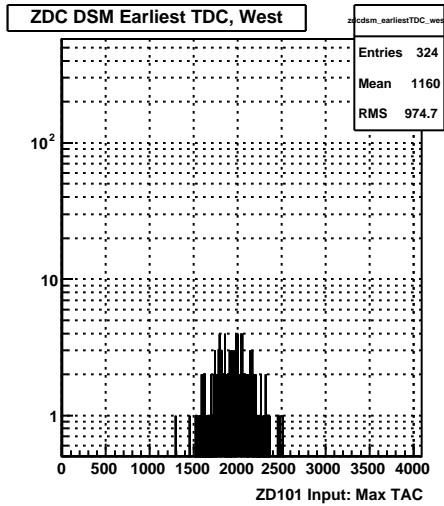
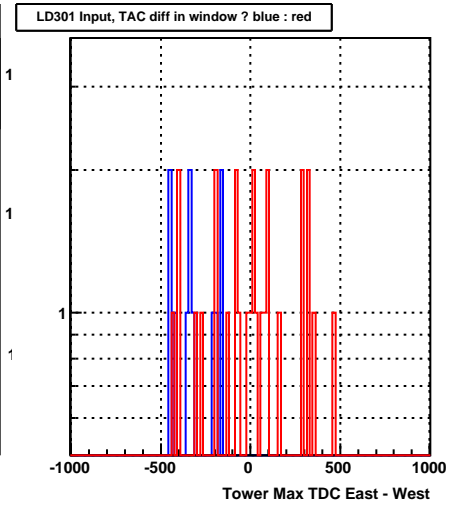
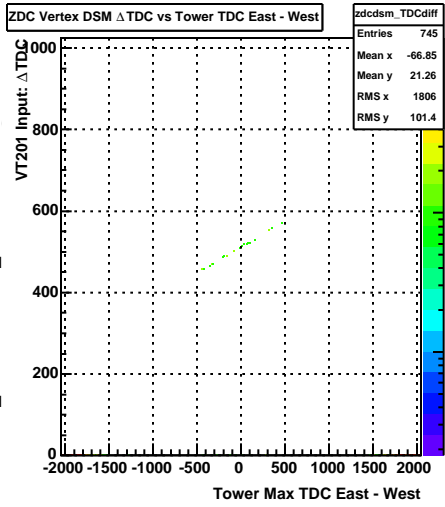
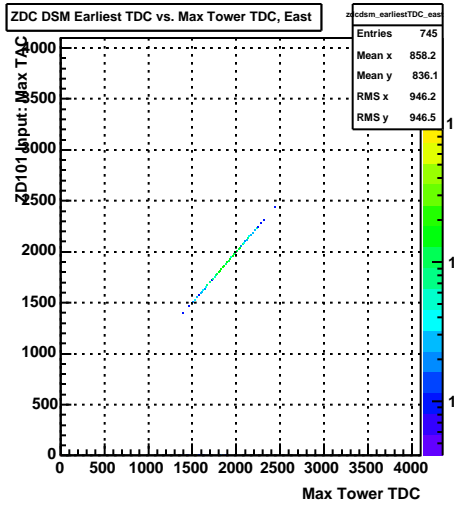
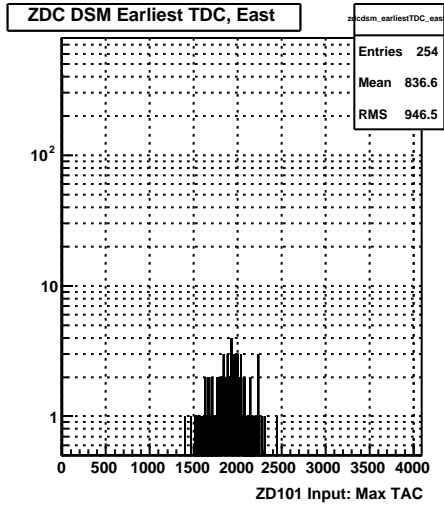
ZDC Corrected SMD Sum vs. Tower Sum East (run 10109006)

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ZDC Corrected SMD Sum vs. Tower Sum West (run 10109006)

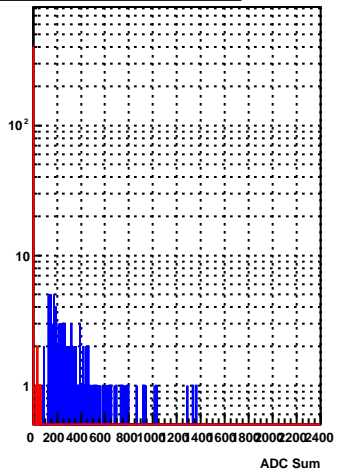
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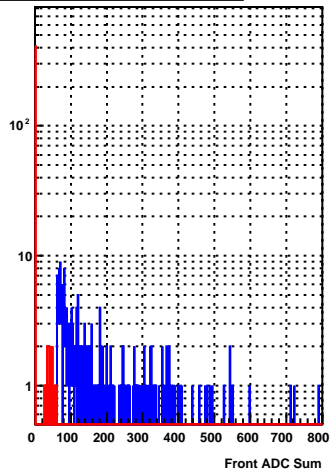
ZDC DSM Earliest TDC (run 10109006)

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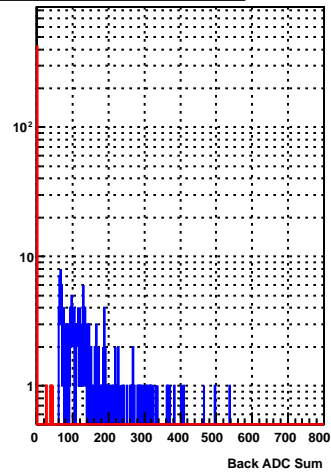
ZD101 Input, Sum > threshold ? blue : red, East



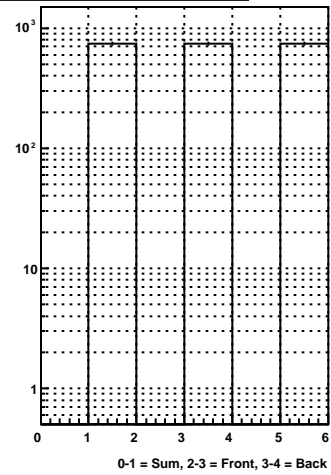
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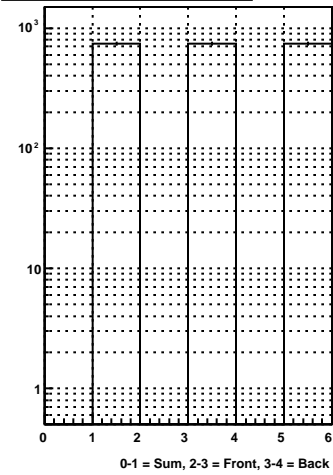
ZD101 Input, Back > threshold ? blue : red, East



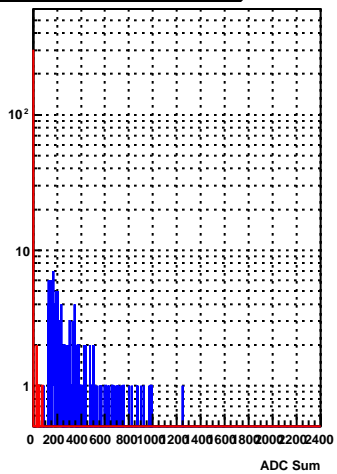
ADC > threshold ZD101==VT201?, East



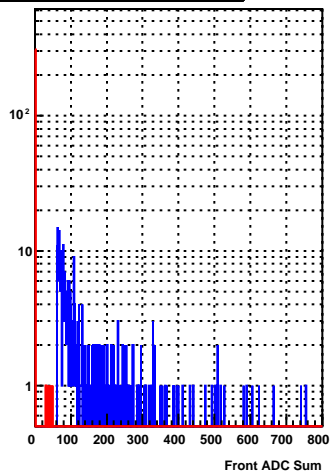
ADC > threshold VT201==LD301?, East



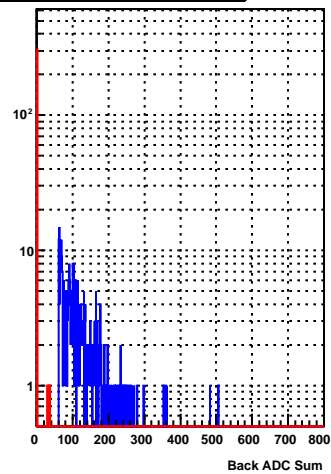
ZD101 Input, Sum > threshold ? blue : red, West



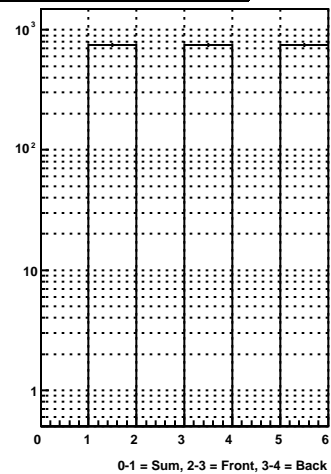
ZD101 Input, Front > threshold ? blue : red, West



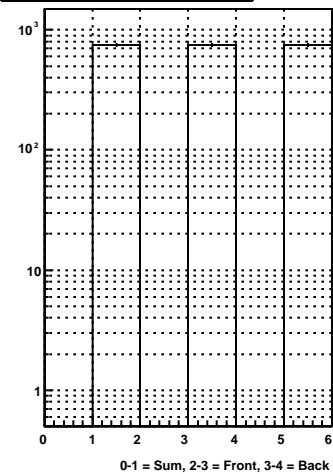
ZD101 Input, Back > threshold ? blue : red, West



ADC > threshold ZD101==VT201?, West



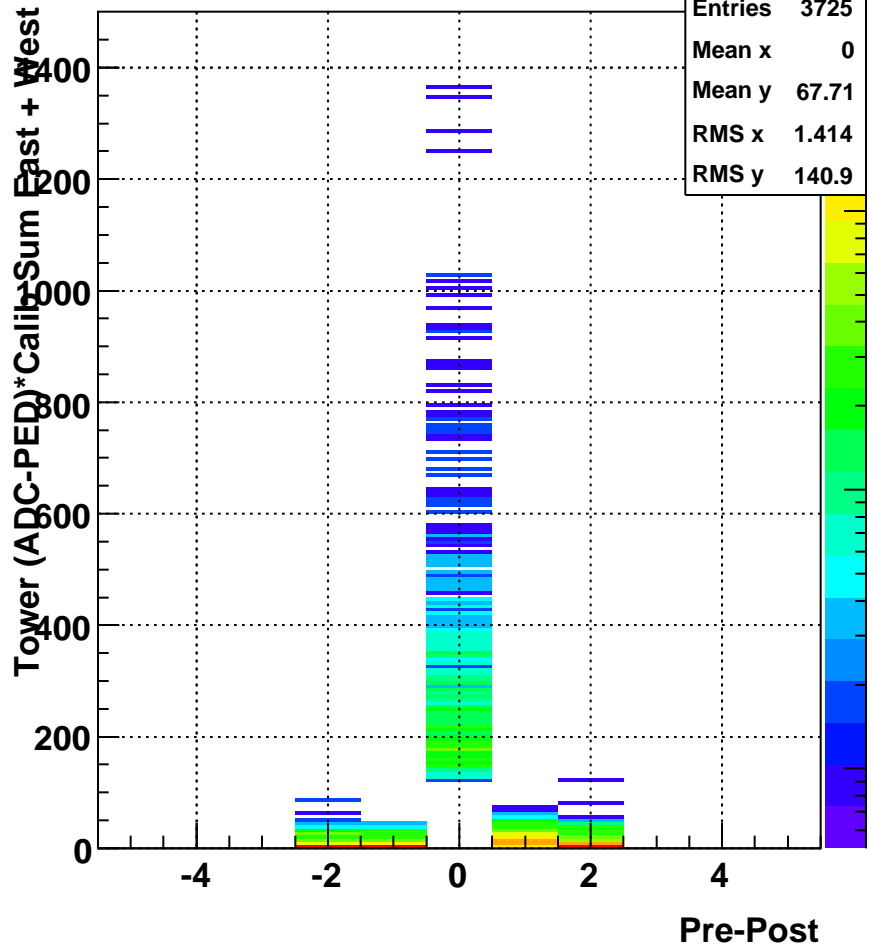
ADC > threshold VT201==LD301?, West



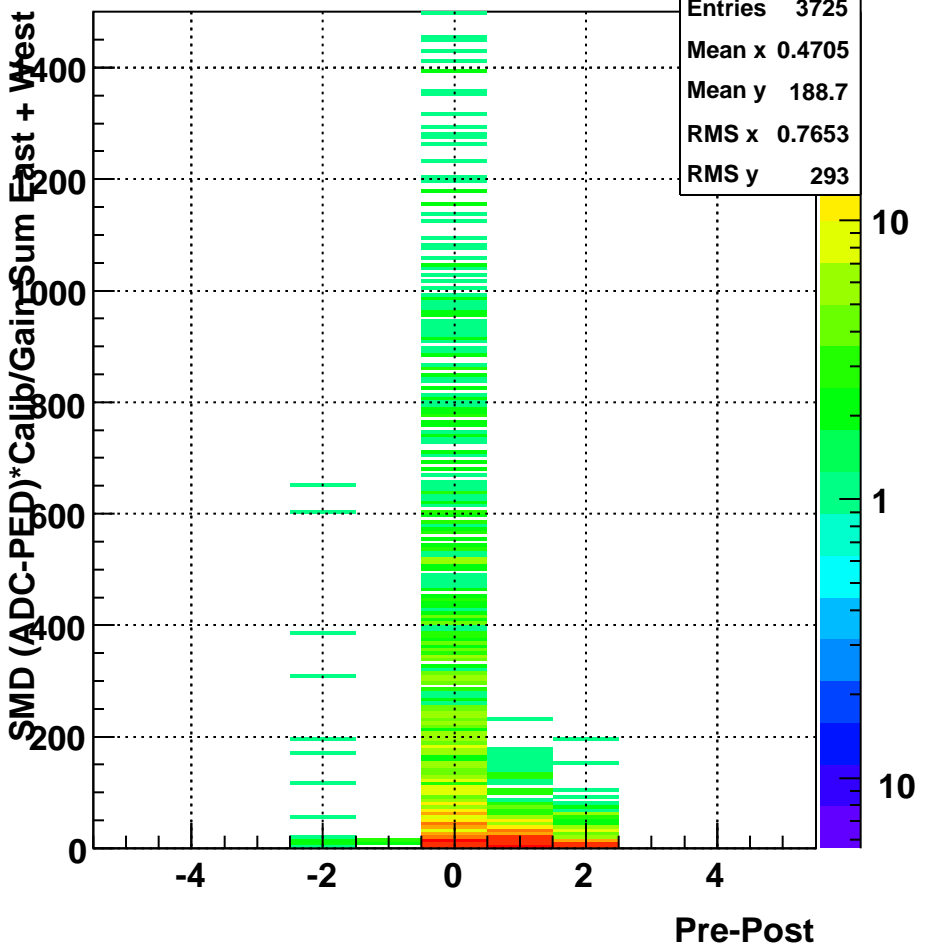
ZDC DSM ADC > threshold (run 10109006)

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ZDC Tower ADC Corr Sum vs. Pre-Post



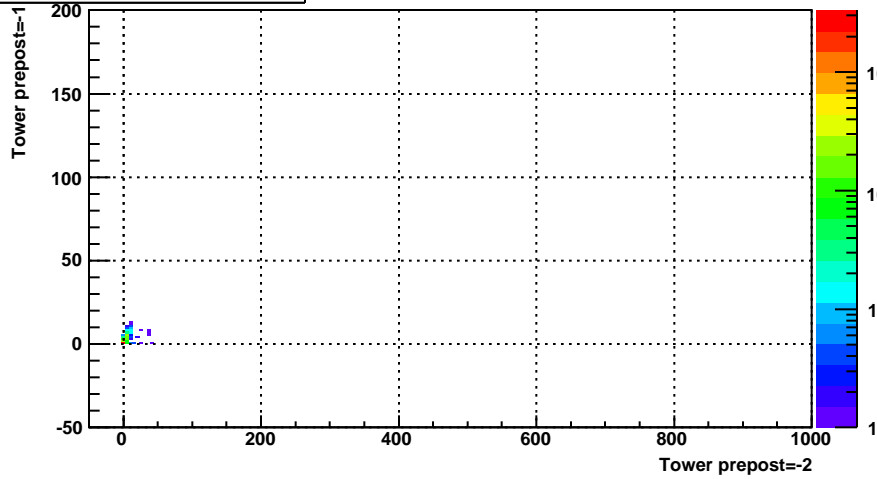
ZDC SMD ADC Corr Sum vs. Pre-Post



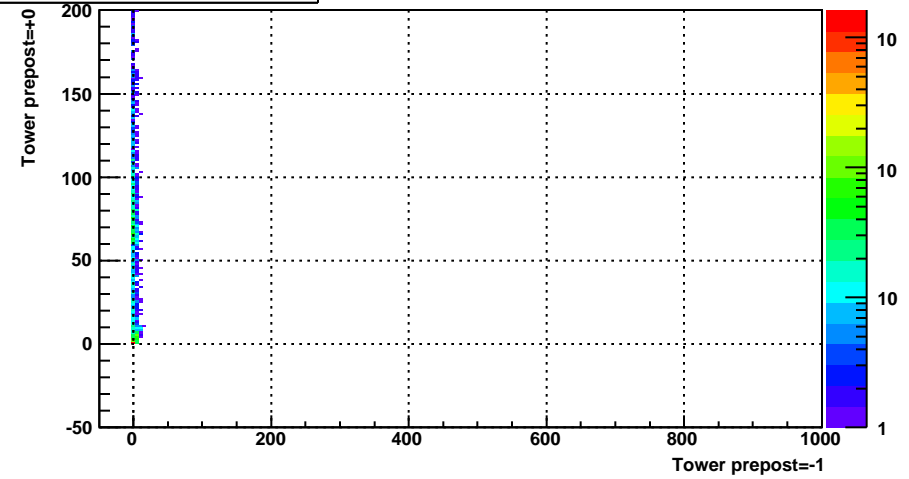
ZDC ADC Sum vs. Pre-Post (run 10109006)

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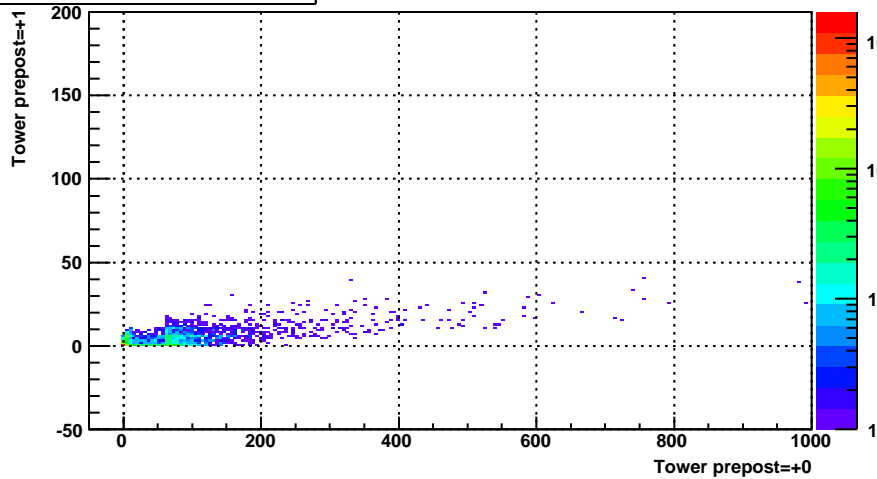
ZDC Tower PrePost -1 vs -2



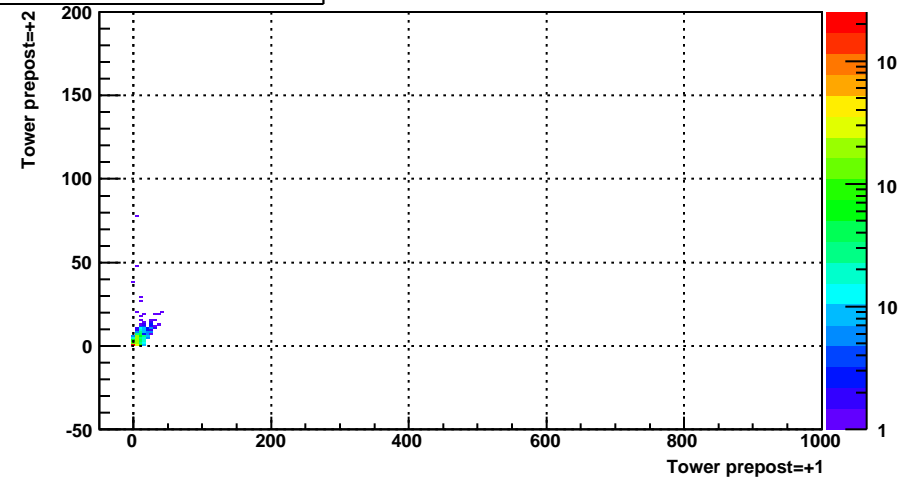
ZDC Tower PrePost +0 vs -1



ZDC Tower PrePost +1 vs +0



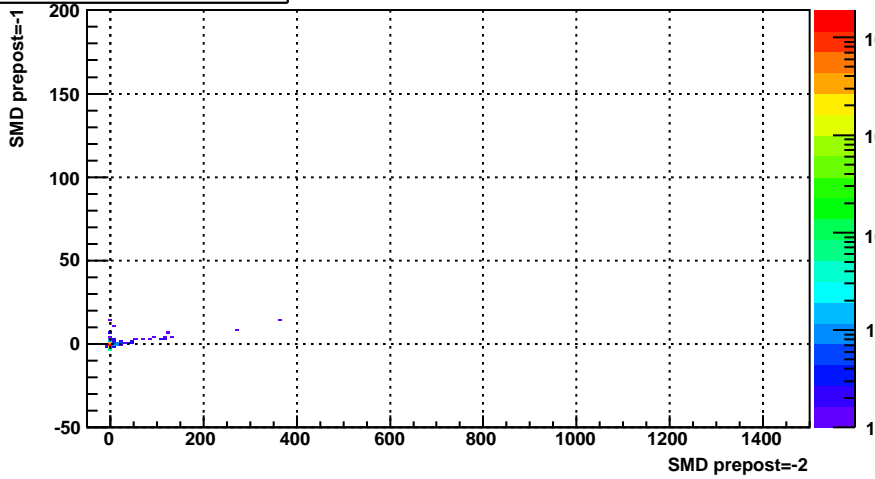
ZDC Tower PrePost +2 vs +1



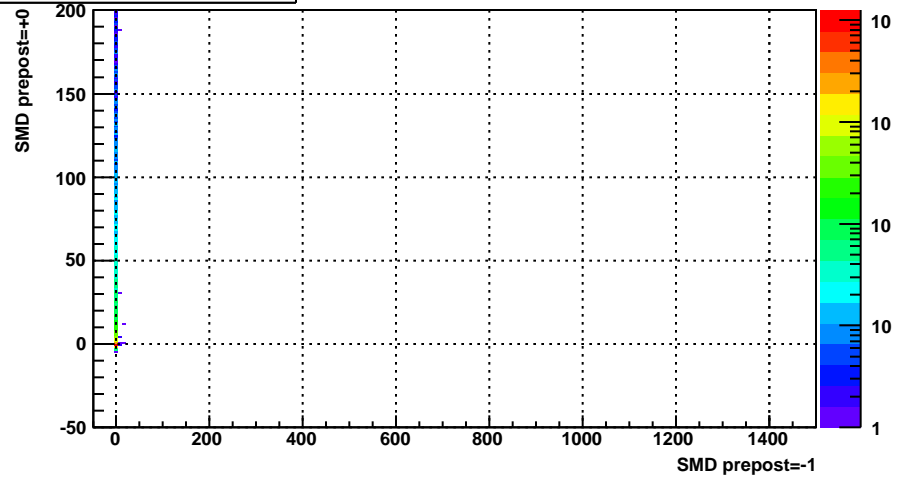
ZDC Tower ADC Pre-Post Correlation (run 10109006)

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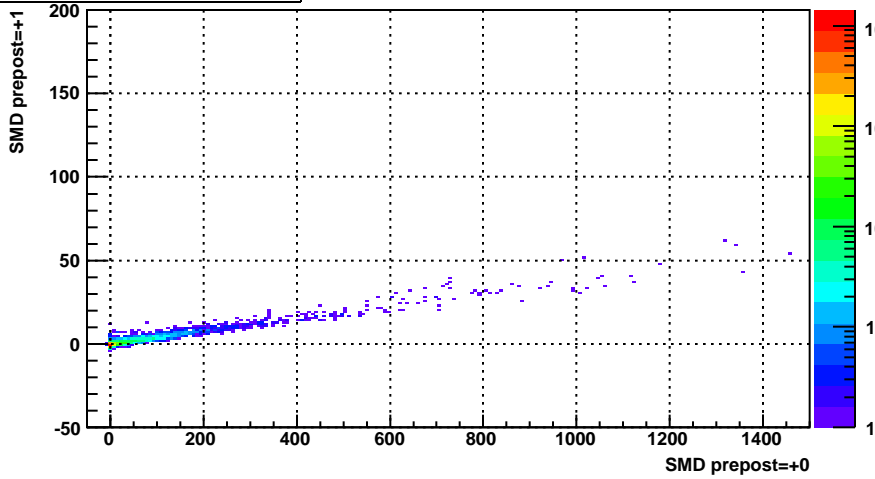
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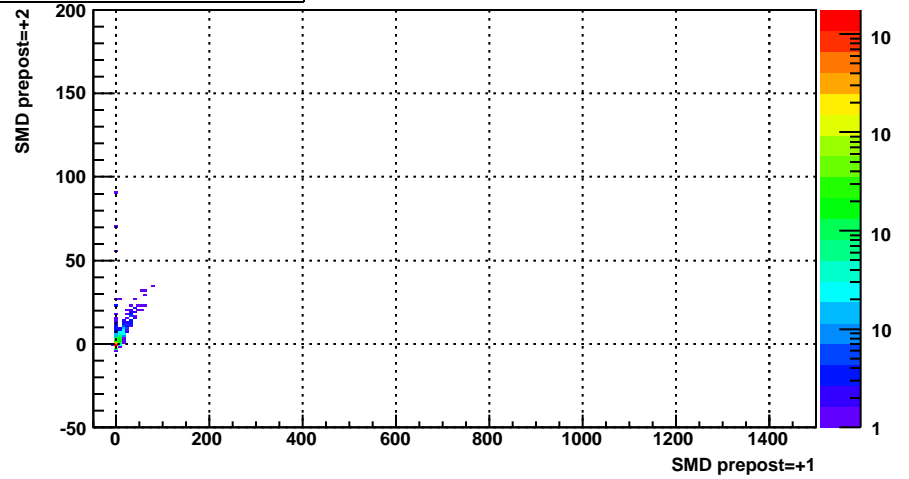
ZDC SMD PrePost +0 vs -1



ZDC SMD PrePost +1 vs +0

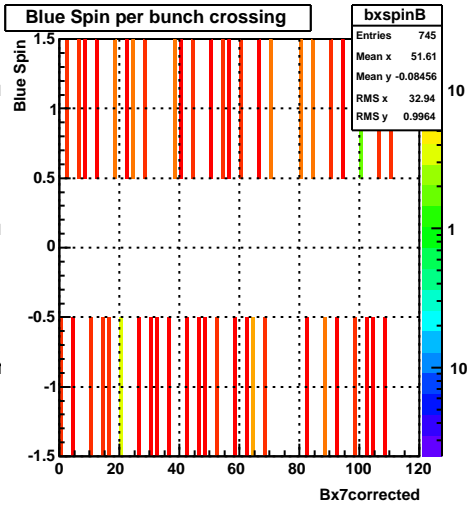
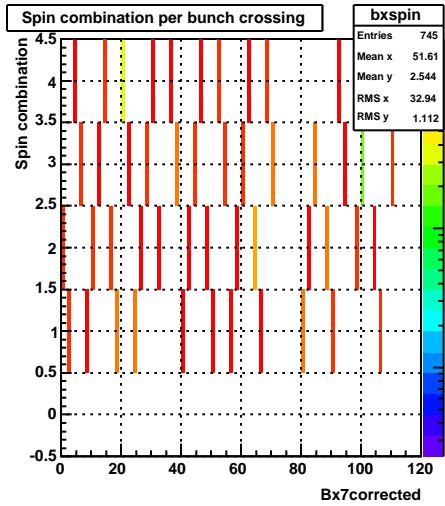
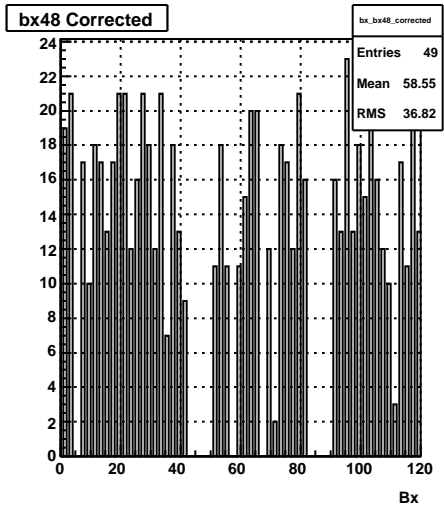
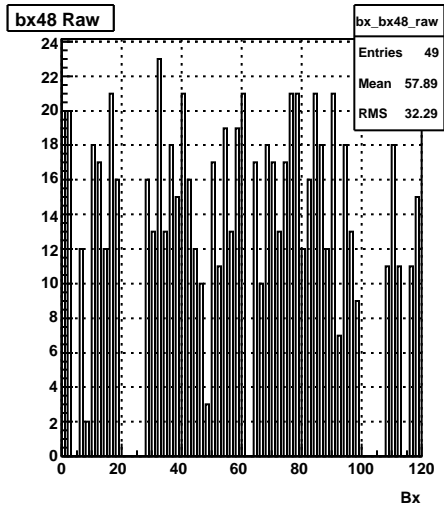
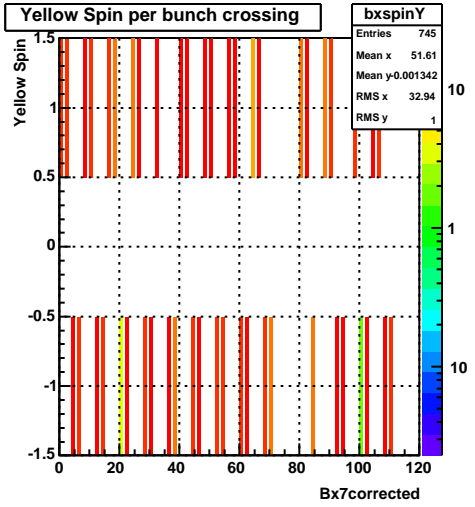
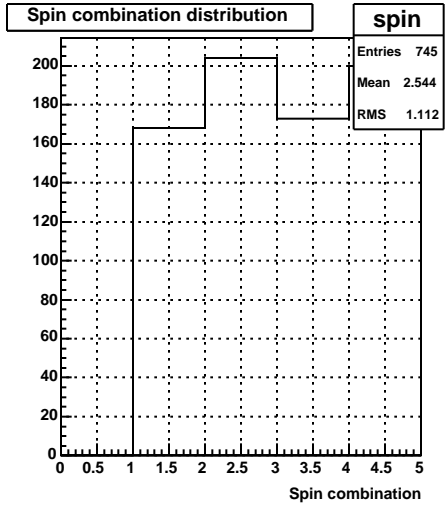
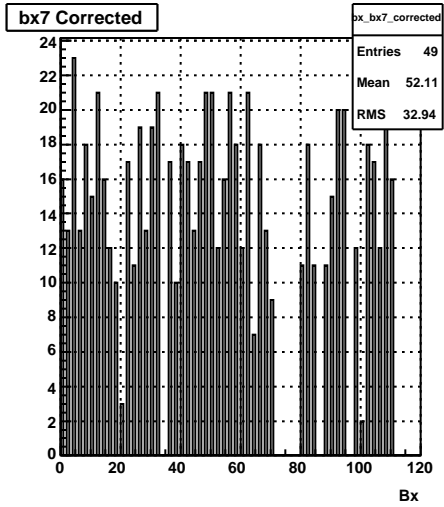
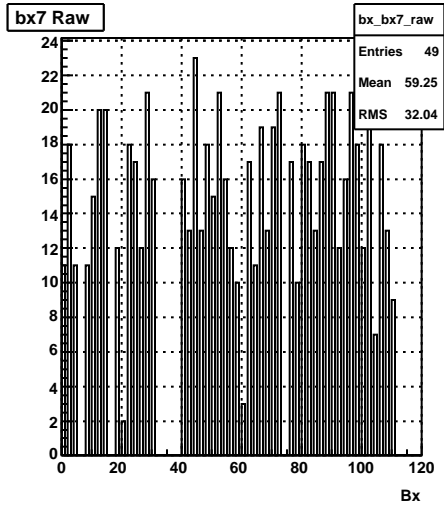


ZDC SMD PrePost +2 vs +1



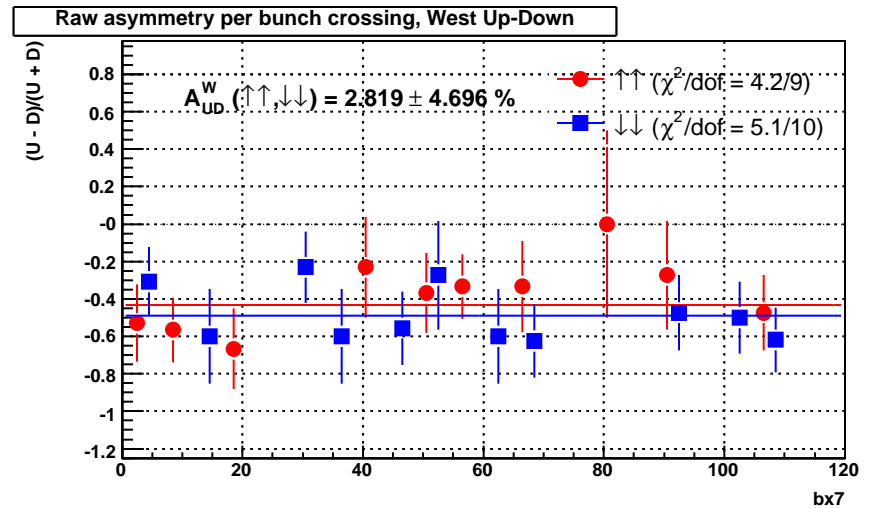
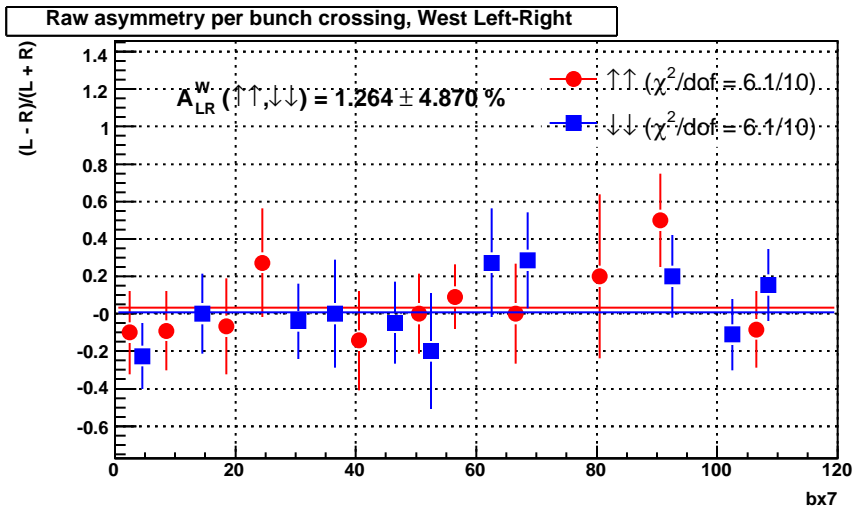
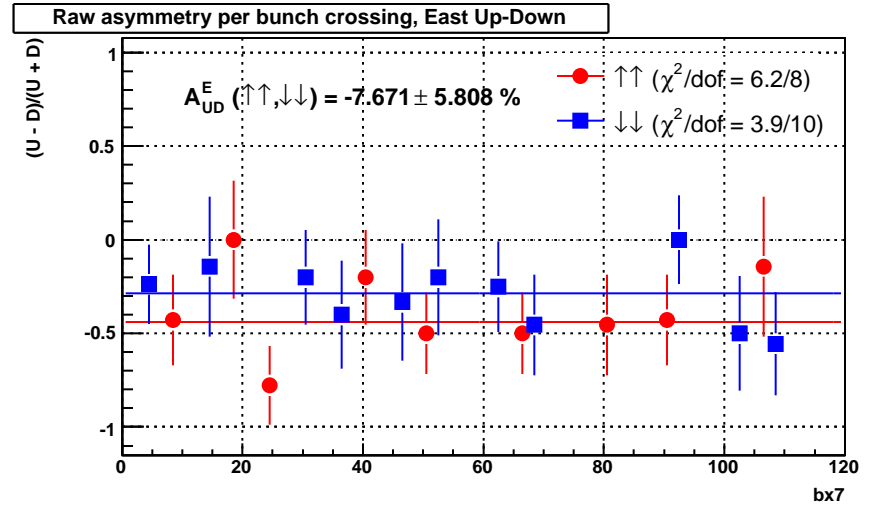
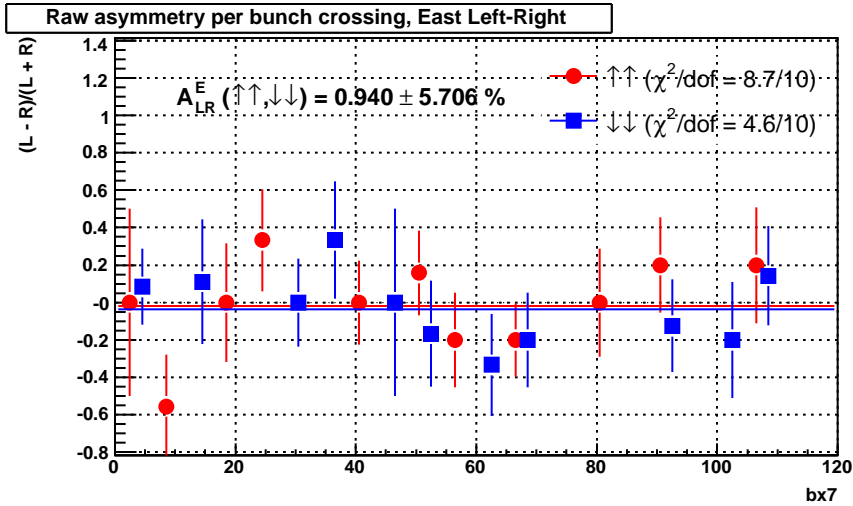
ZDC SMD ADC Pre-Post Correlation (run 10109006)

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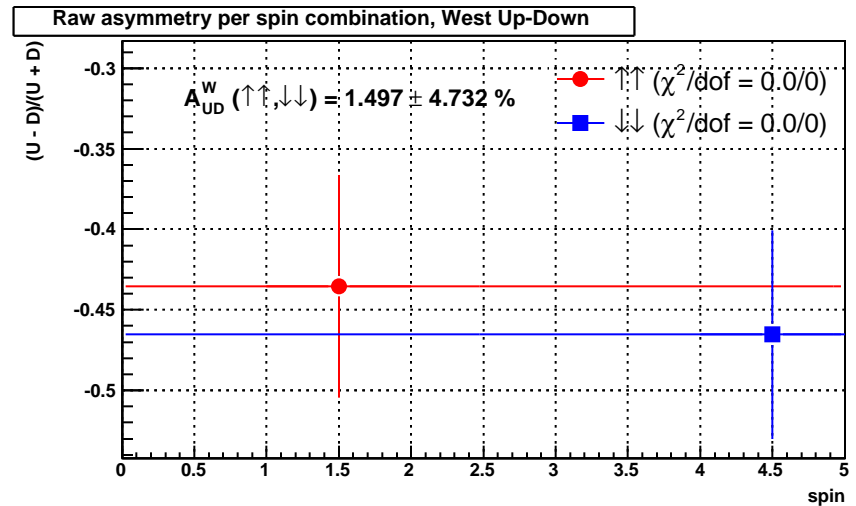
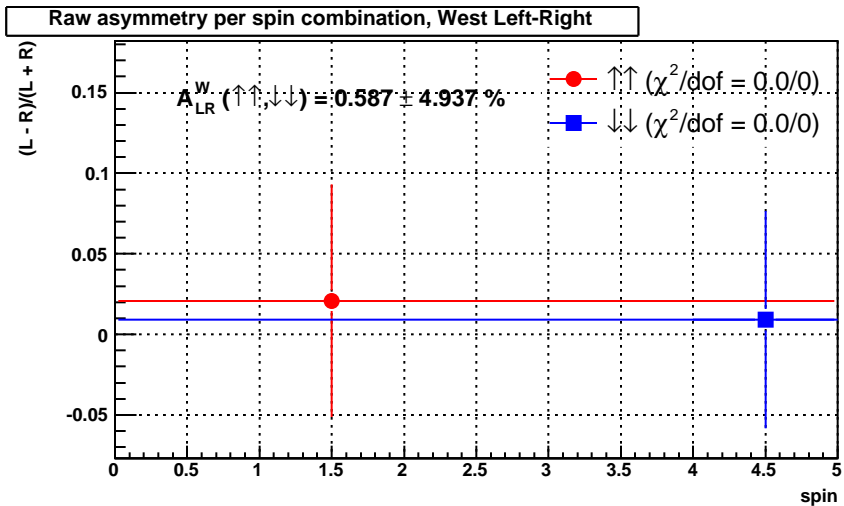
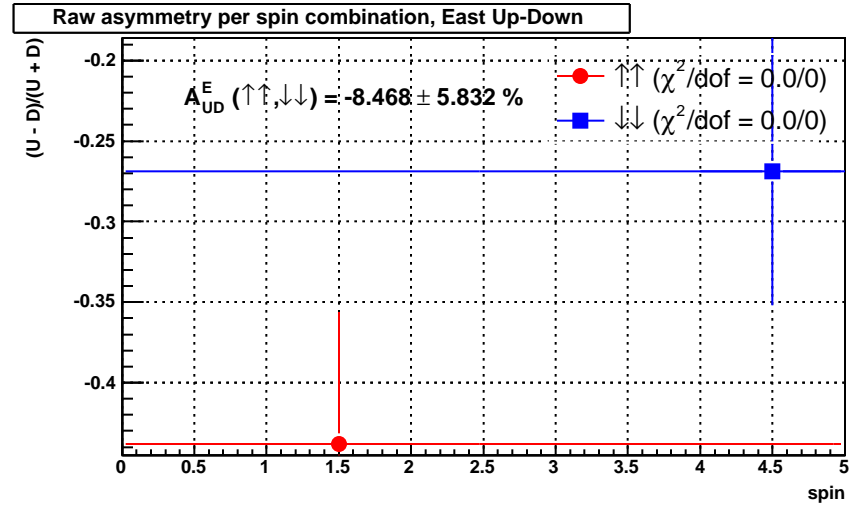
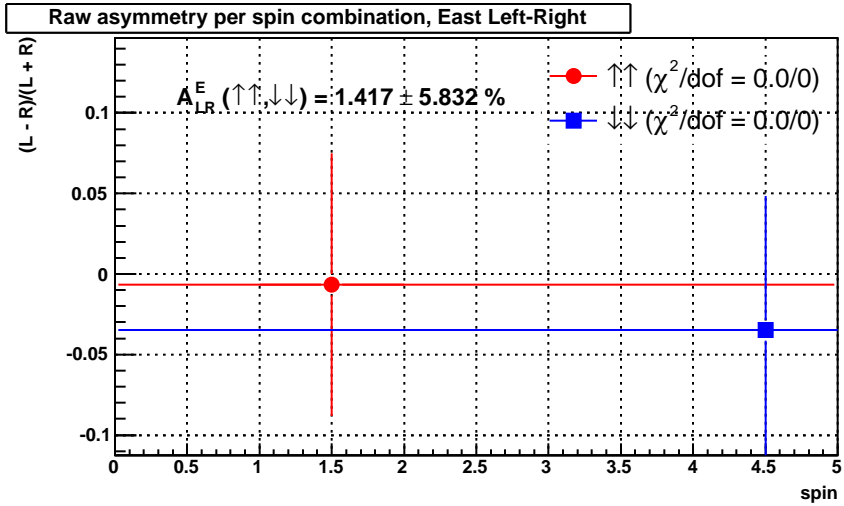


Bunch crossing distribution (run 10109006)

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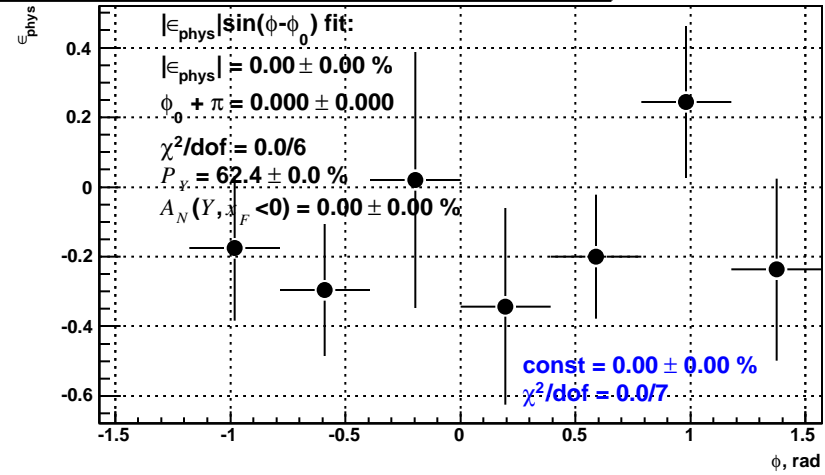
ZDC Raw Asymmetry per Bunch Crossing (run 10109006)



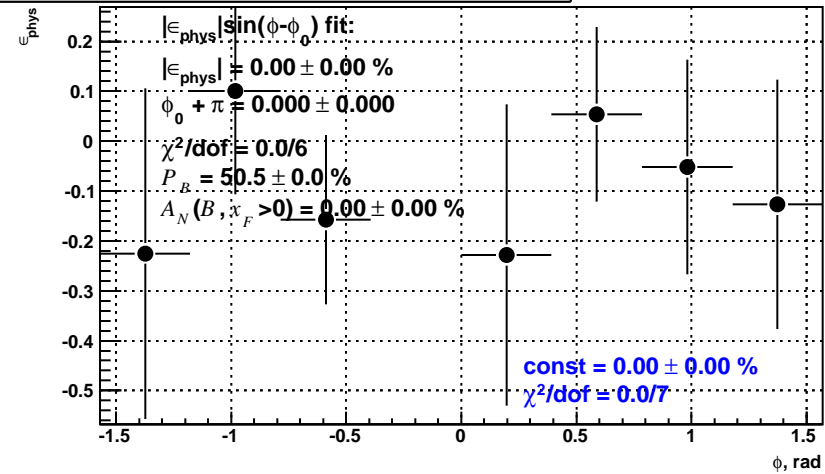
ZDC Raw Asymmetry per Spin Combination (run 10109006)

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Square root asymmetry vs. ϕ , Yellow Backward (West)



Square root asymmetry vs. ϕ , Blue Forward (West)



ZDC Single Spin Asymmetry (run 10109006)