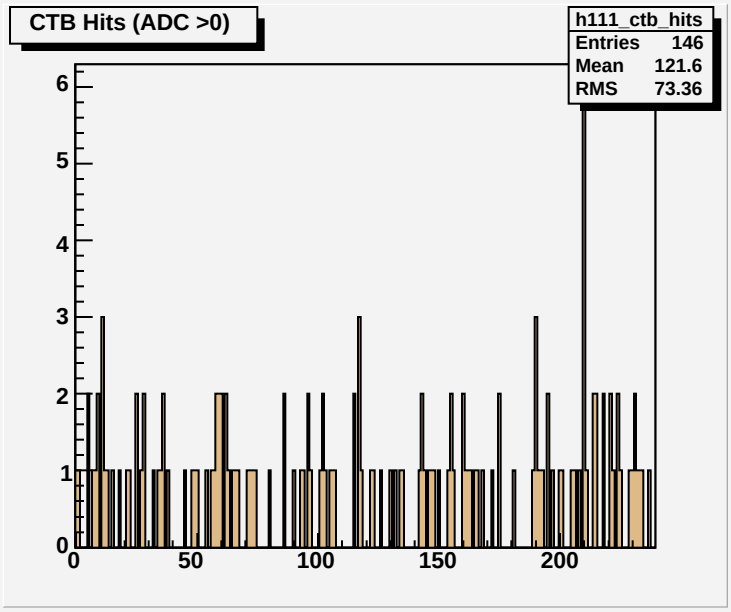
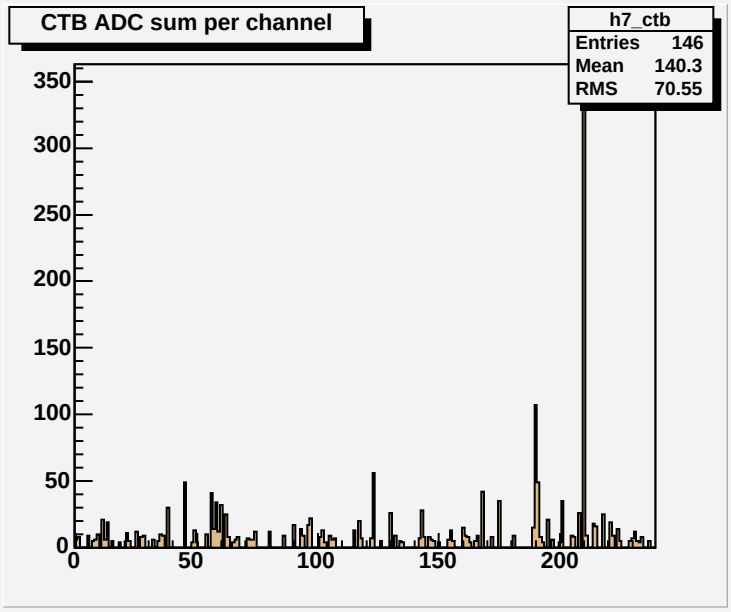
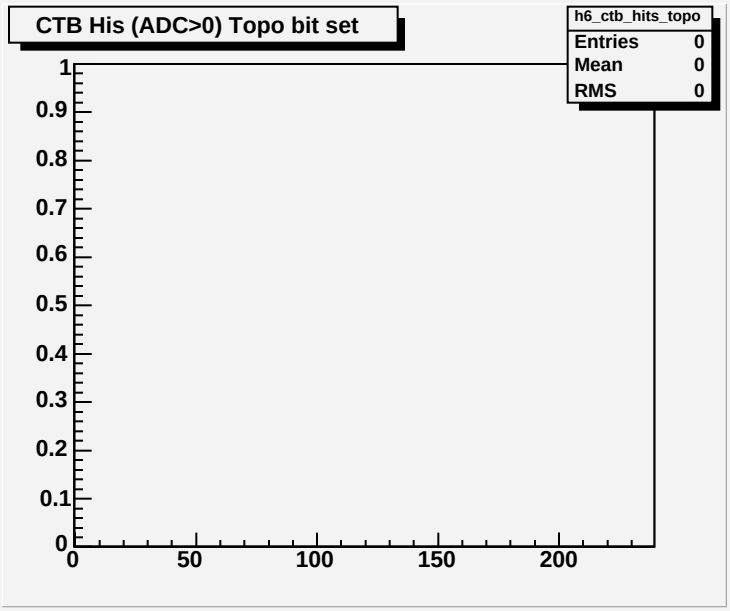
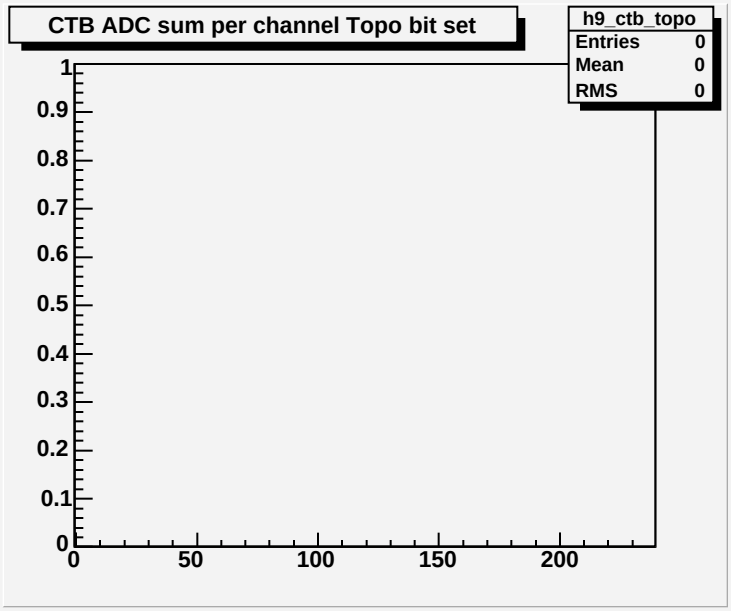
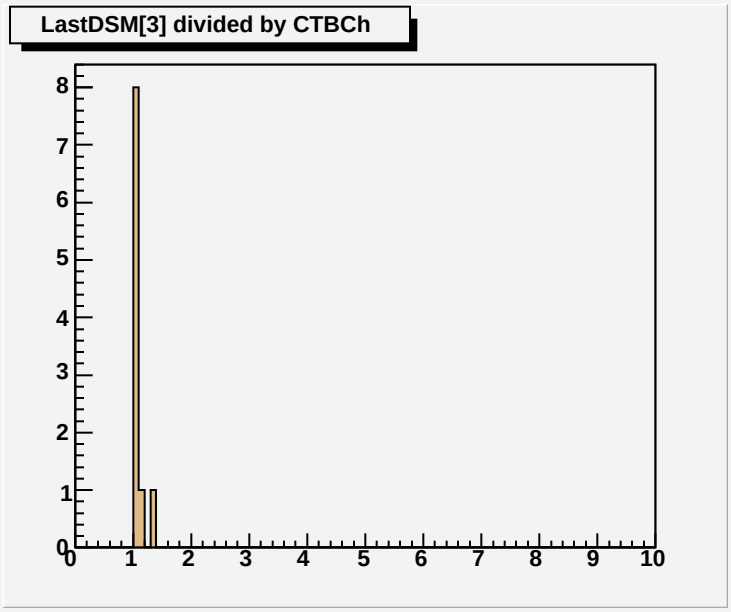
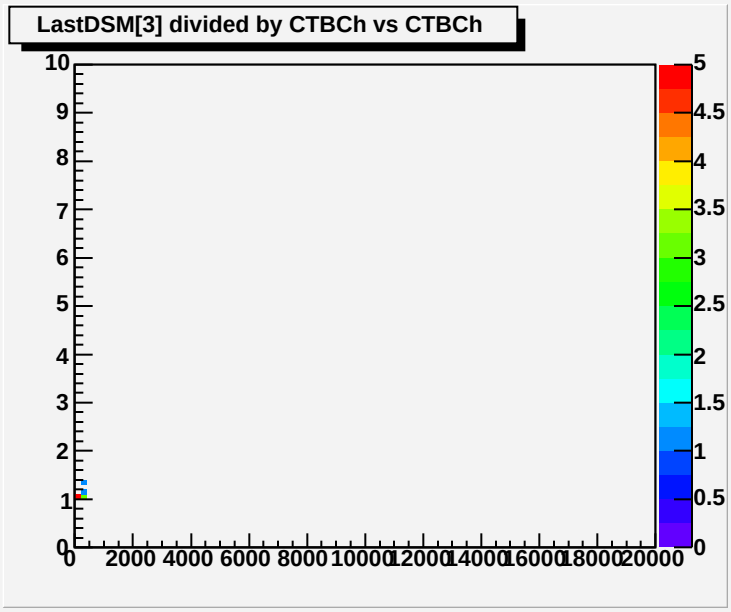

CTB Hits (ADC >0)
h111_ctb_hits
Entries 146
Mean 121.6
RMS 73.36

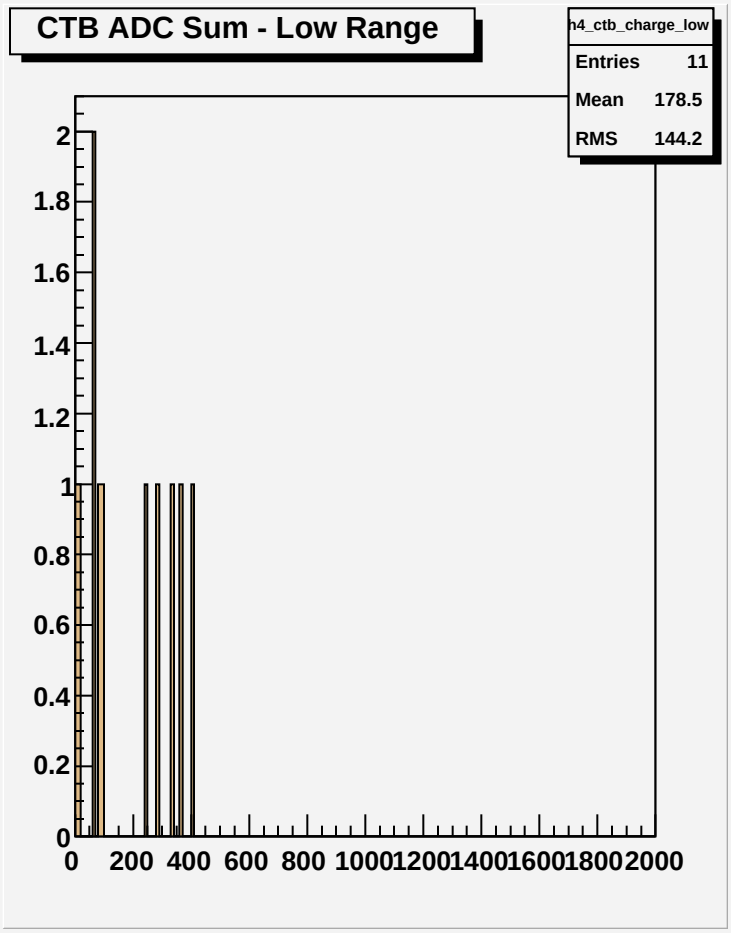

CTB ADC sum per channel
h7_ctb
Entries 146
Mean 140.3
RMS 70.55

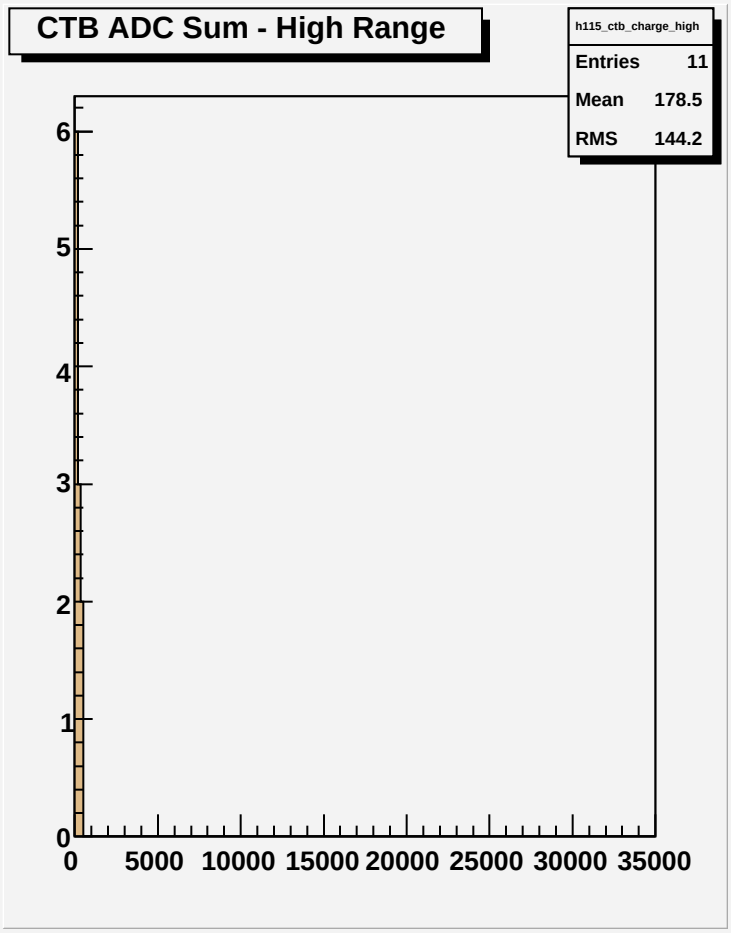

CTB His (ADC>0) Topo bit set
h6_ctb_hits_topo
Entries 0
Mean 0
RMS 0

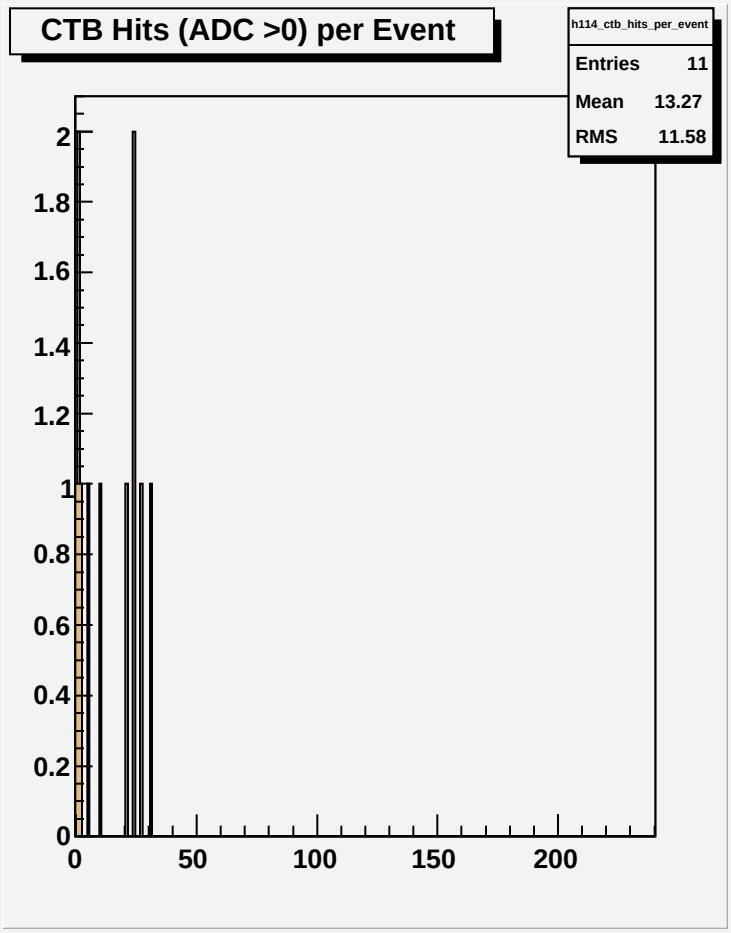

CTB ADC sum per channel Topo bit set
h9_ctb_topo
Entries 0
Mean 0
RMS 0

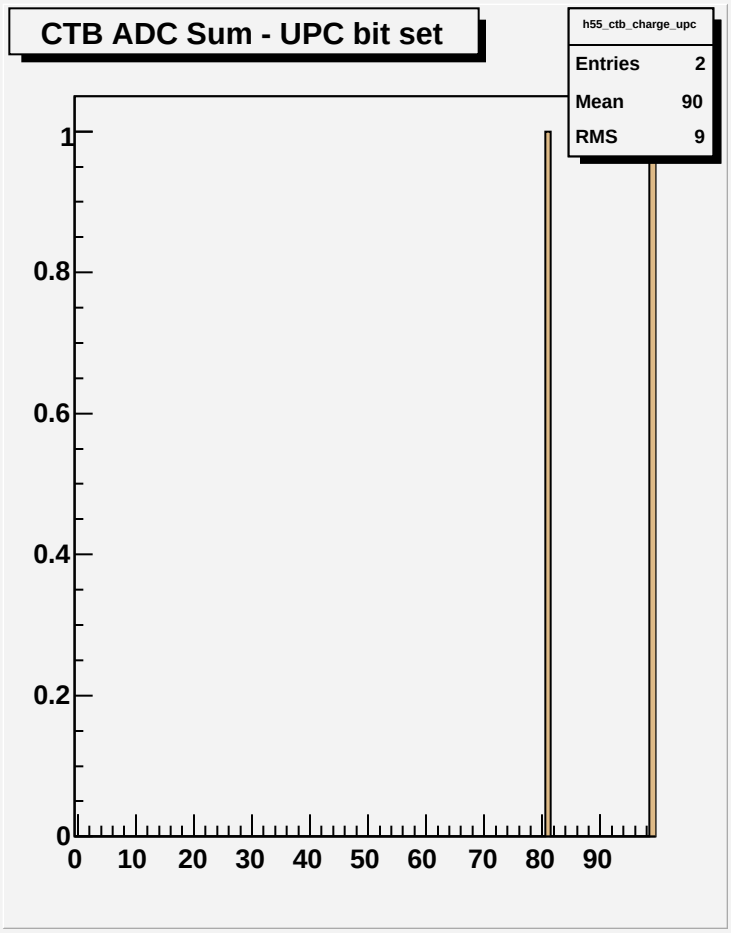

LastDSM[3] divided by CTBCh

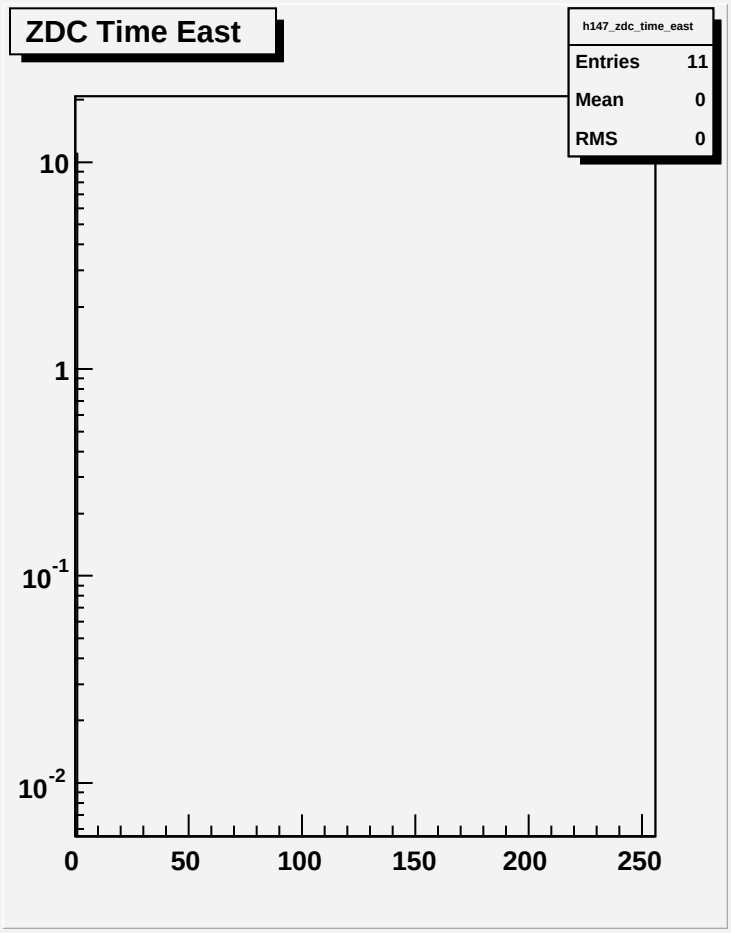

LastDSM[3] divided by CTBCh vs CTBCh

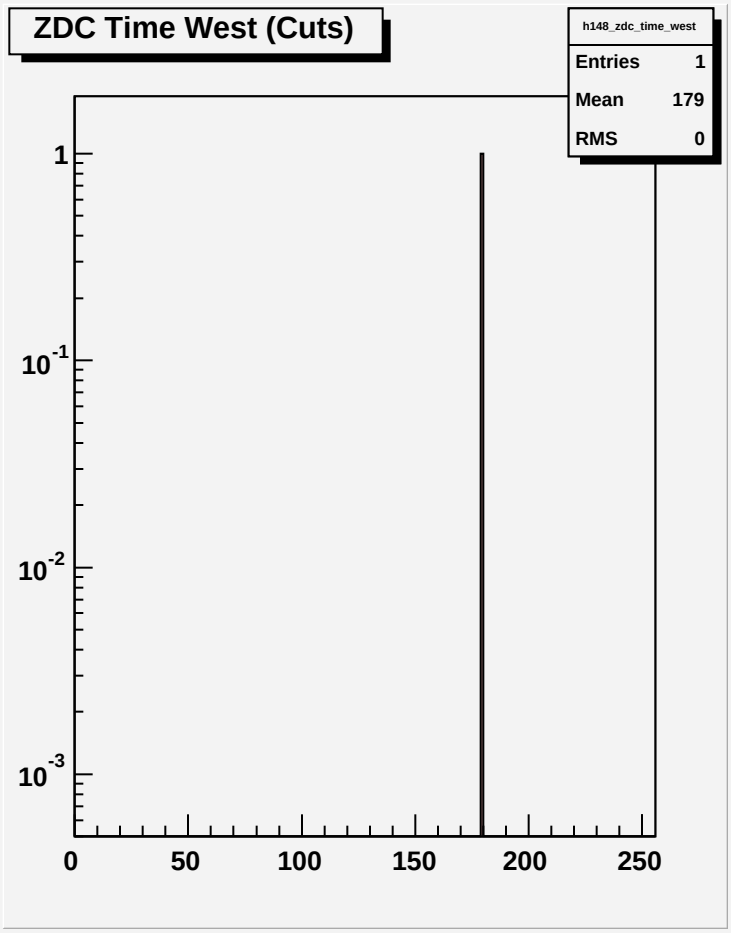

CTB ADC Sum - Low Range
h4_ctb_charge_low
Entries 11
Mean 178.5
RMS 144.2
0 0.2 0.4 0.6 0.8 1 1.2 1.4 1.6 1.8 2
0 200 400 600 800 1000 1200 1400 1600 1800 2000

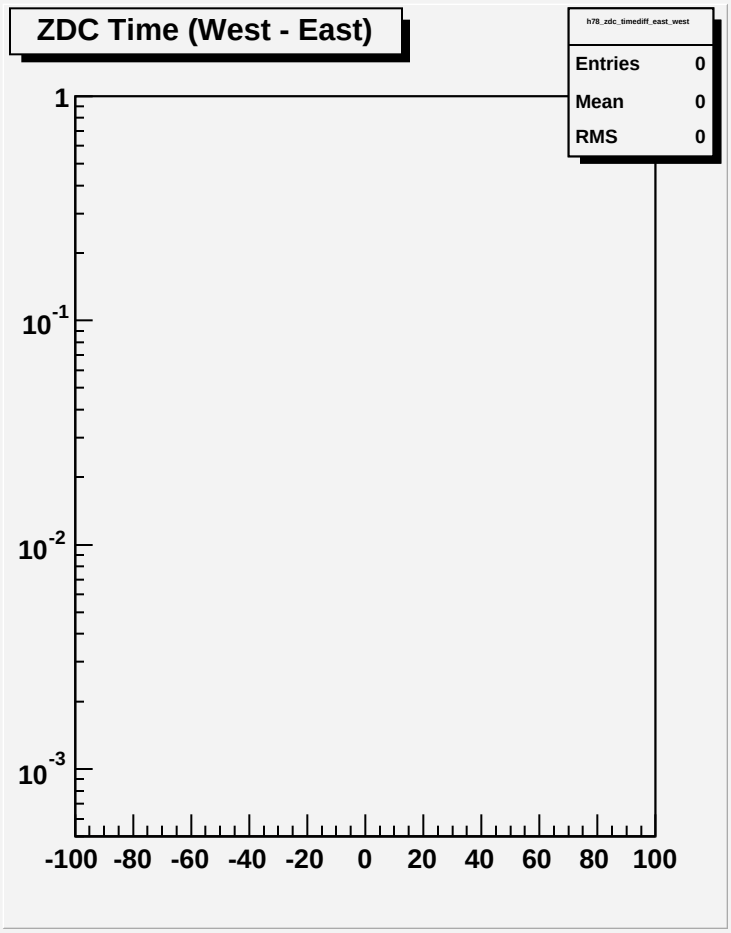

CTB ADC Sum - High Range
h115_ctb_charge_high
Entries 11
Mean 178.5
RMS 144.2
0 1 2 3 4 5 6
0 5000 10000 15000 20000 25000 30000 35000

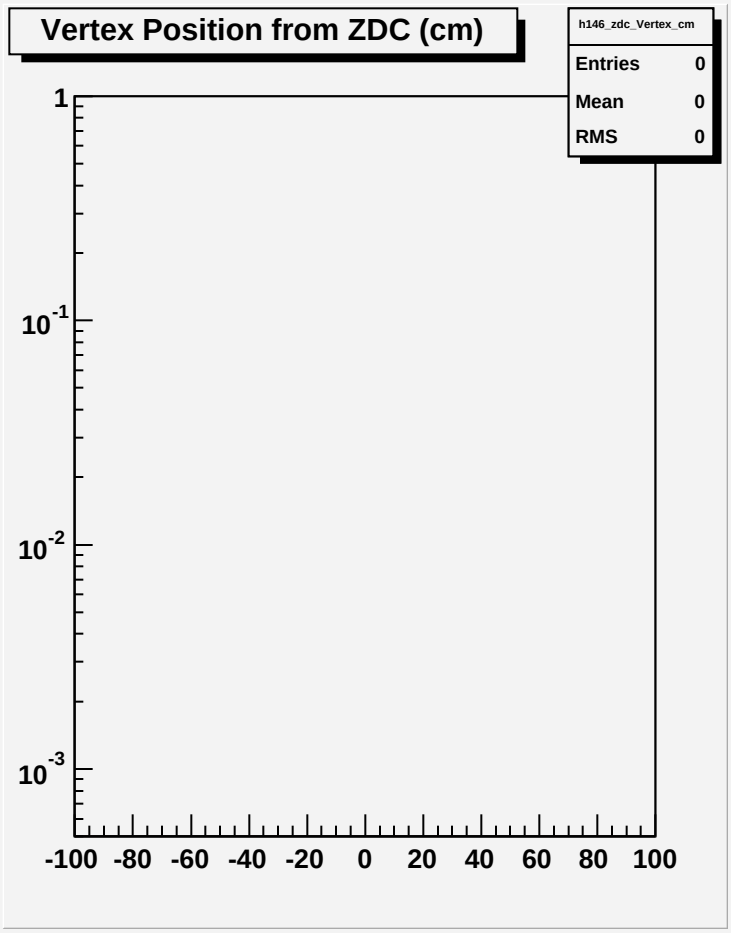

CTB Hits (ADC >0) per Event
h114_ctb_hits_per_event
Entries 11
Mean 13.27
RMS 11.58
0 0.2 0.4 0.6 0.8 1 1.2 1.4 1.6 1.8 2
0 50 100 150 200

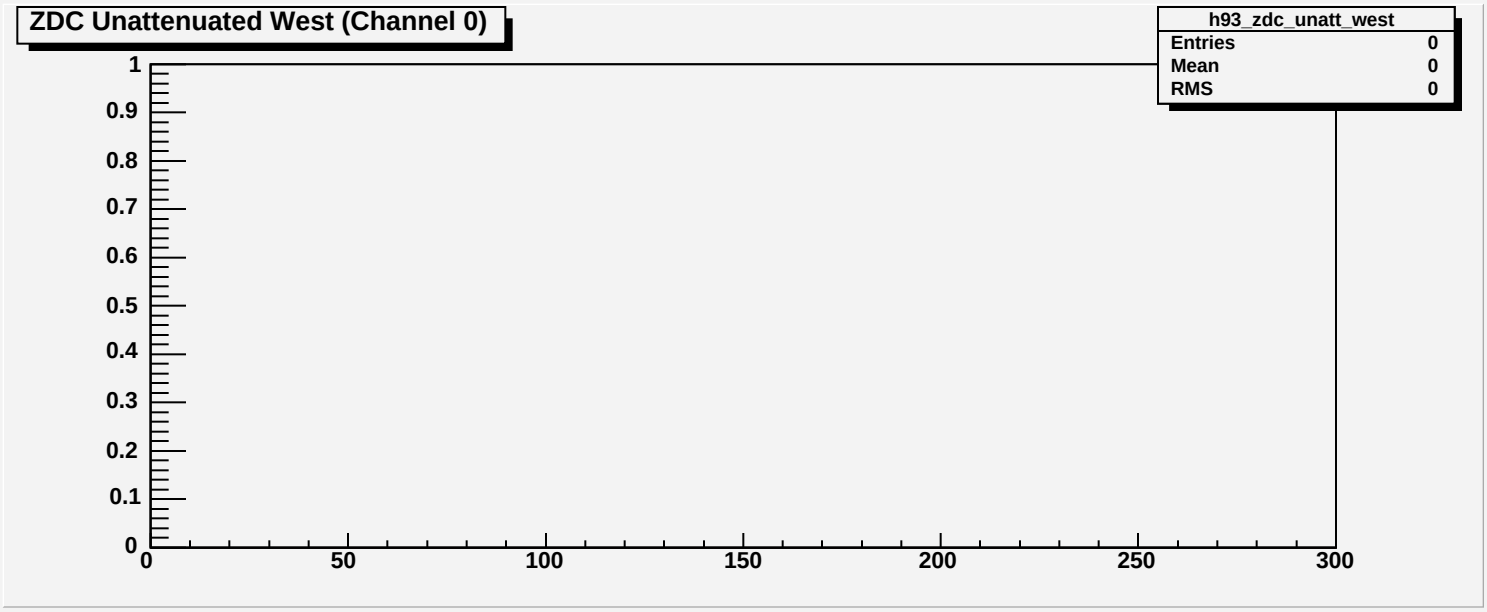

CTB ADC Sum - UPC bit set
h55_ctb_charge_upc
Entries 2
Mean 90
RMS 9
0 0.2 0.4 0.6 0.8 1
0 10 20 30 40 50 60 70 80 90

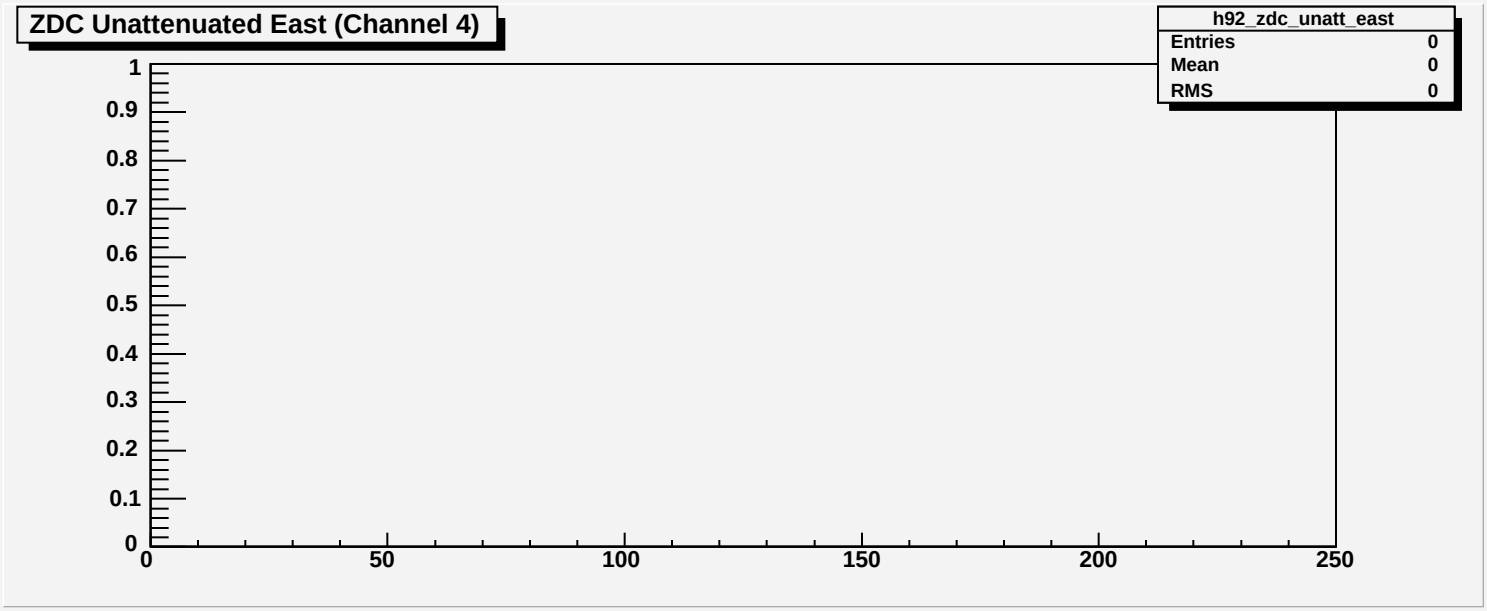

ZDC Time East
h147_zdc_time_east
Entries 11
Mean 0
RMS 0
10
1
10^-1
10^-2
0 50 100 150 200 250

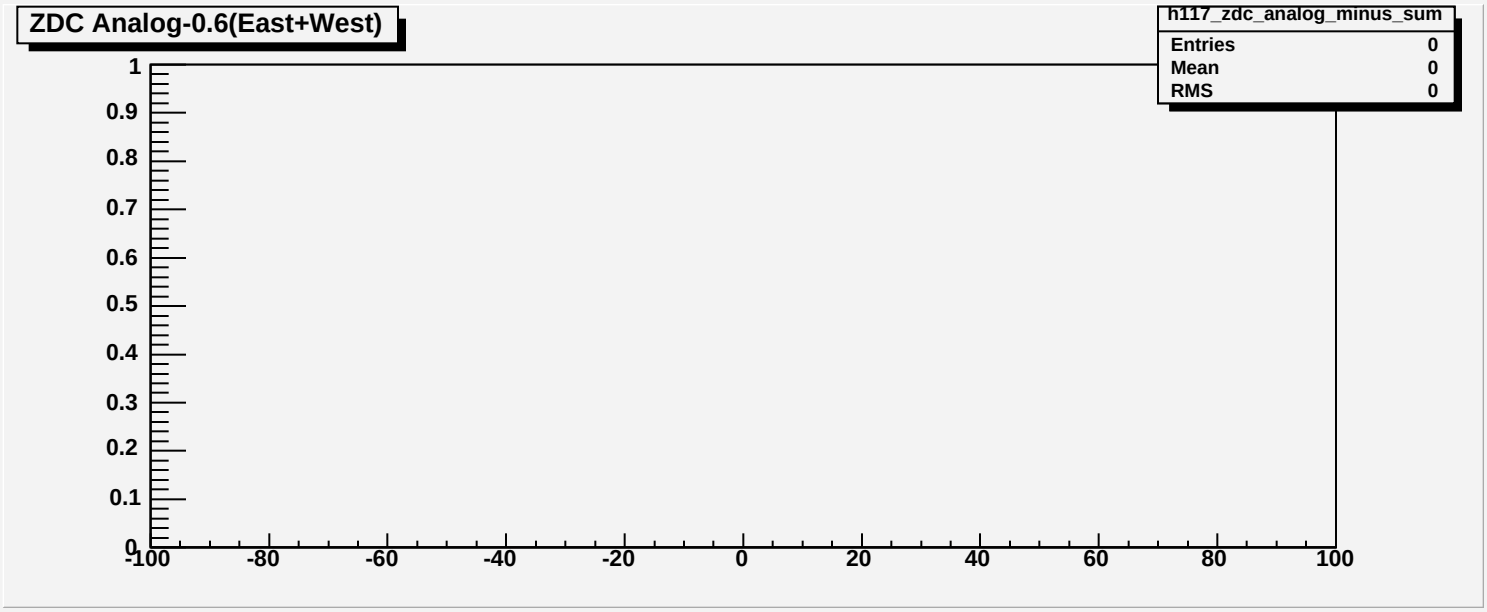

ZDC Time West (Cuts)
h148_zdc_time_west
Entries 1
Mean 179
RMS 0
1
10^-1
10^-2
10^-3
0 50 100 150 200 250

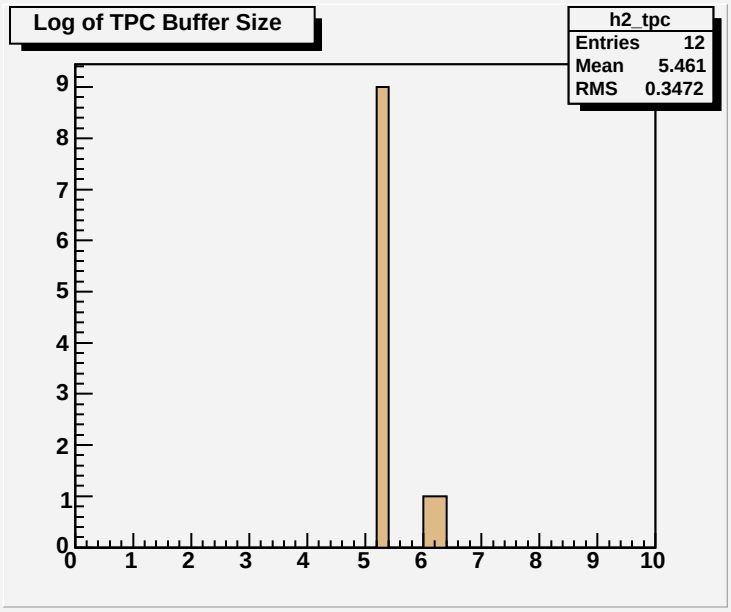

ZDC Time (West - East)
h78_zdc_timediff_east_west
Entries 0
Mean 0
RMS 0
1
10^-1
10^-2
10^-3
-100 -80 -60 -40 -20 0 20 40 60 80 100

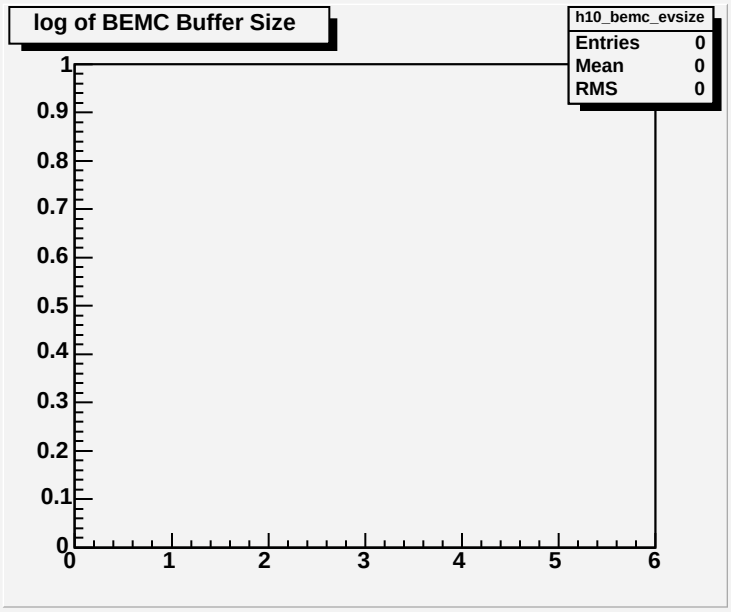

Vertex Position from ZDC (cm)
h146_zdc_Vertex_cm
Entries 0
Mean 0
RMS 0
1
10^-1
10^-2
10^-3
-100 -80 -60 -40 -20 0 20 40 60 80 100

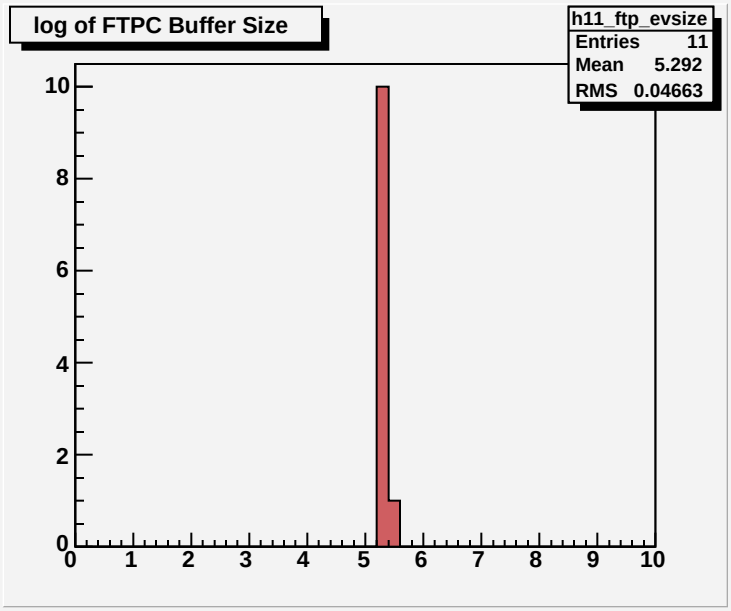

ZDC Unattenuated West (Channel 0)
h93_zdc_unatt_west
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
0
50
100
150
200
250
300

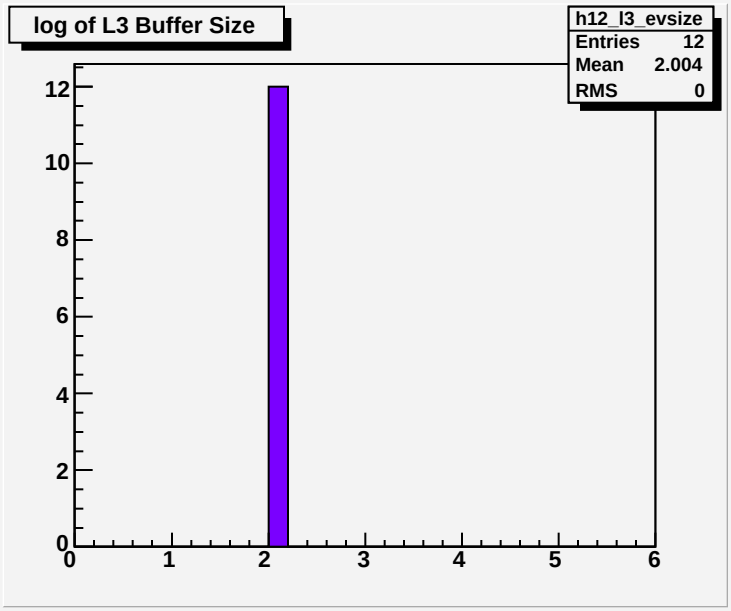

ZDC Unattenuated East (Channel 4)
h92_zdc_unatt_east
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
0
50
100
150
200
250

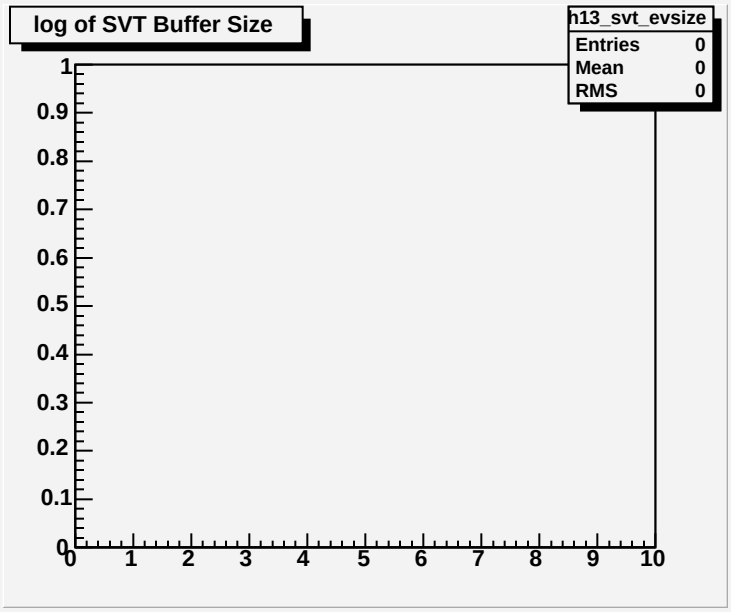

ZDC Analog-0.6(East+West)
h117_zdc_analog_minus_sum
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
-100
-80
-60
-40
-20
0
20
40
60
80
100

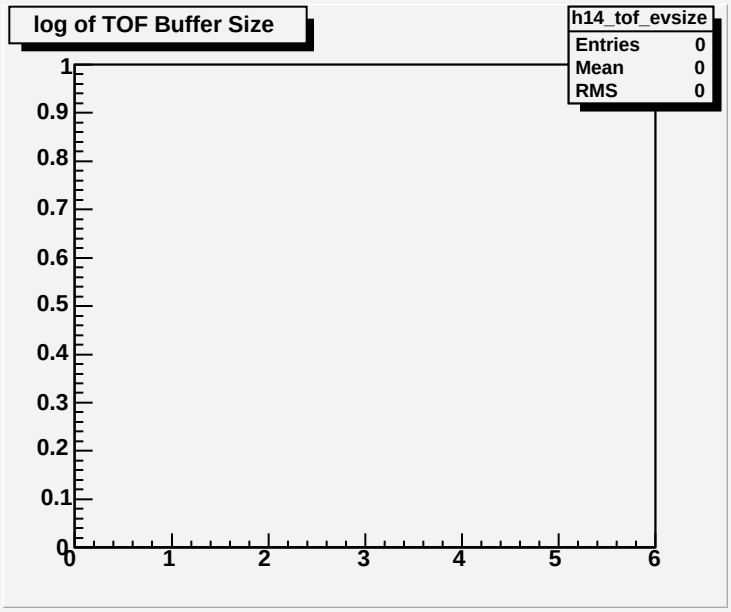

Log of TPC Buffer Size
h2_tpc
Entries 12
Mean 5.461
RMS 0.3472

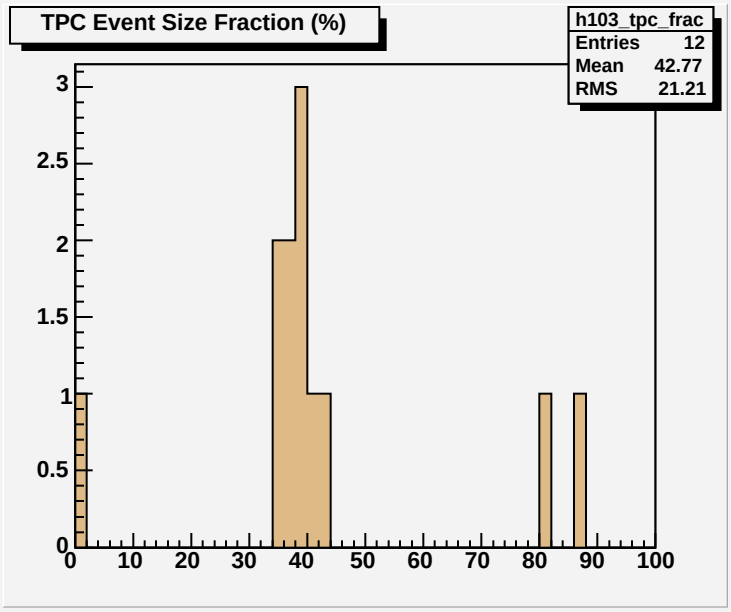

log of BEMC Buffer Size
h10_bemc_evsize
Entries 0
Mean 0
RMS 0

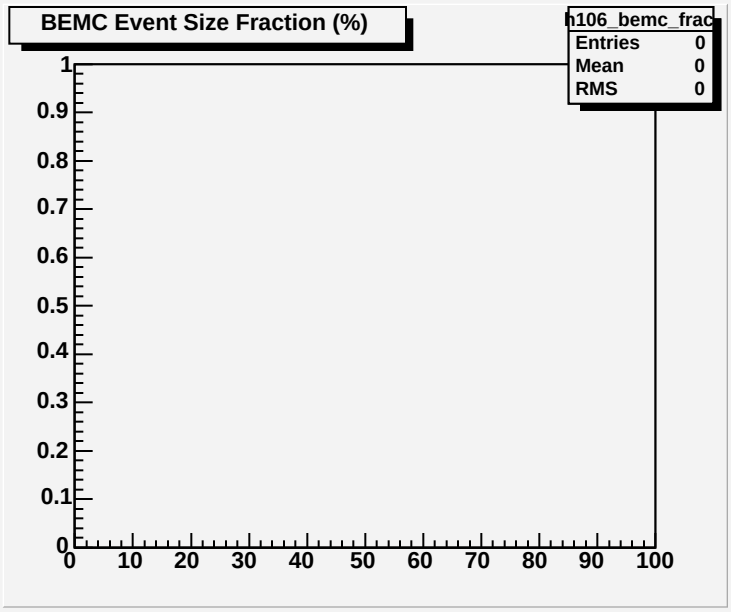

log of FTPC Buffer Size
h11_ftp_evsize
Entries 11
Mean 5.292
RMS 0.04663

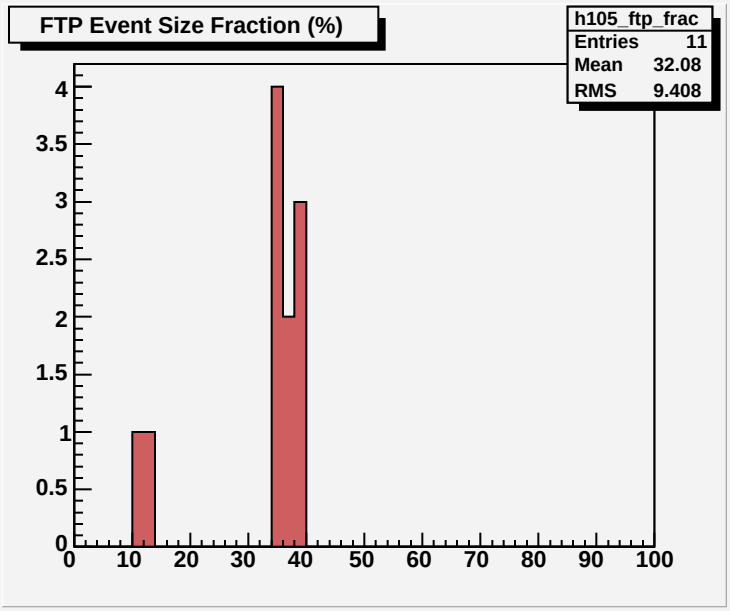

log of L3 Buffer Size
h12_l3_evsize
Entries 12
Mean 2.004
RMS 0

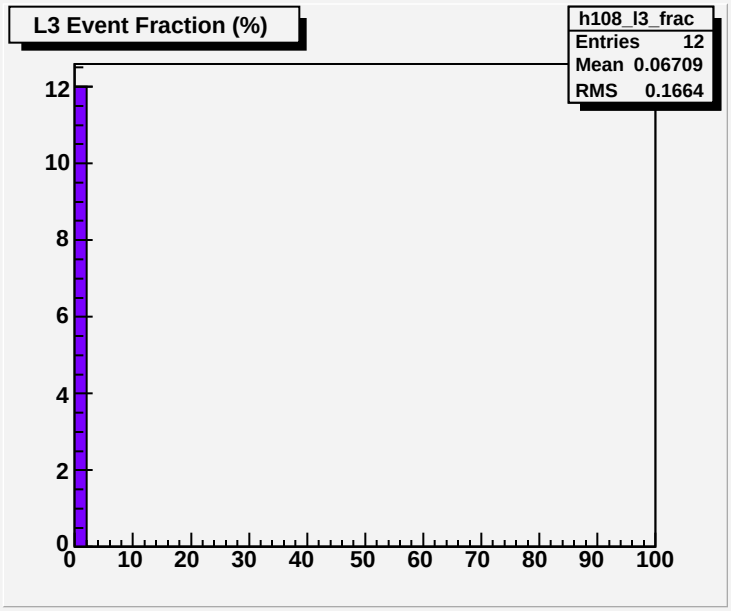

log of SVT Buffer Size
h13_svt_evsize
Entries 0
Mean 0
RMS 0

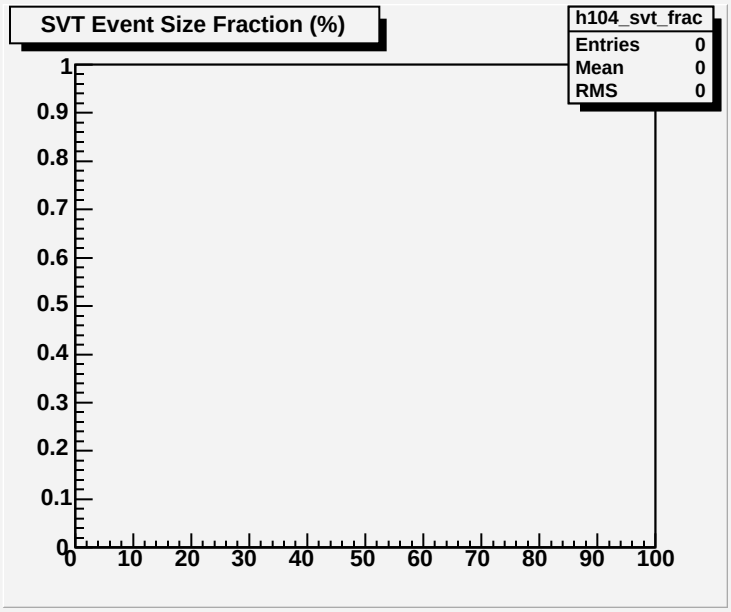

log of TOF Buffer Size
h14_tof_evsize
Entries 0
Mean 0
RMS 0

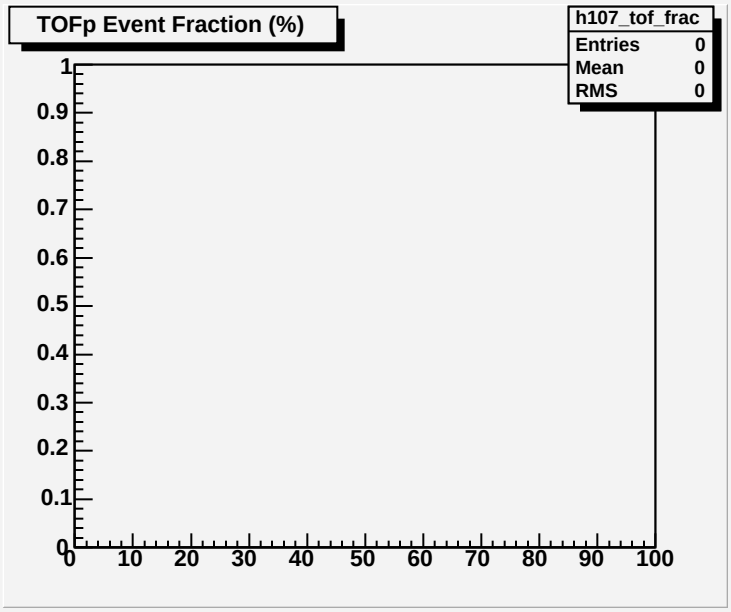

TPC Event Size Fraction (%)
h103_tpc_frac
Entries 12
Mean 42.77
RMS 21.21

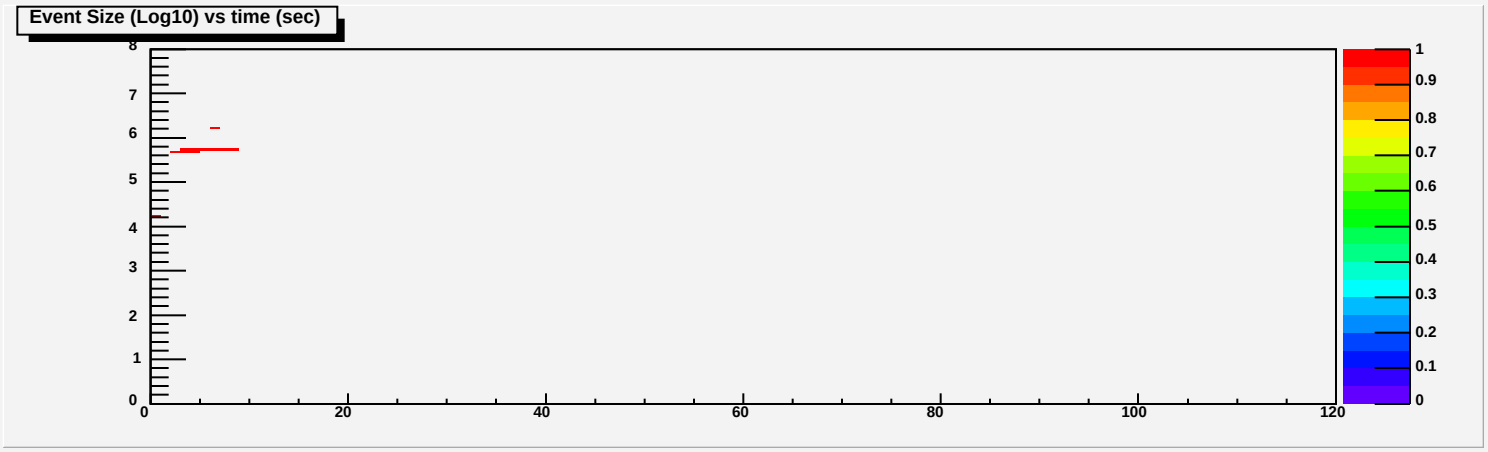

BEMC Event Size Fraction (%)
h106_bemc_frac
Entries 0
Mean 0
RMS 0

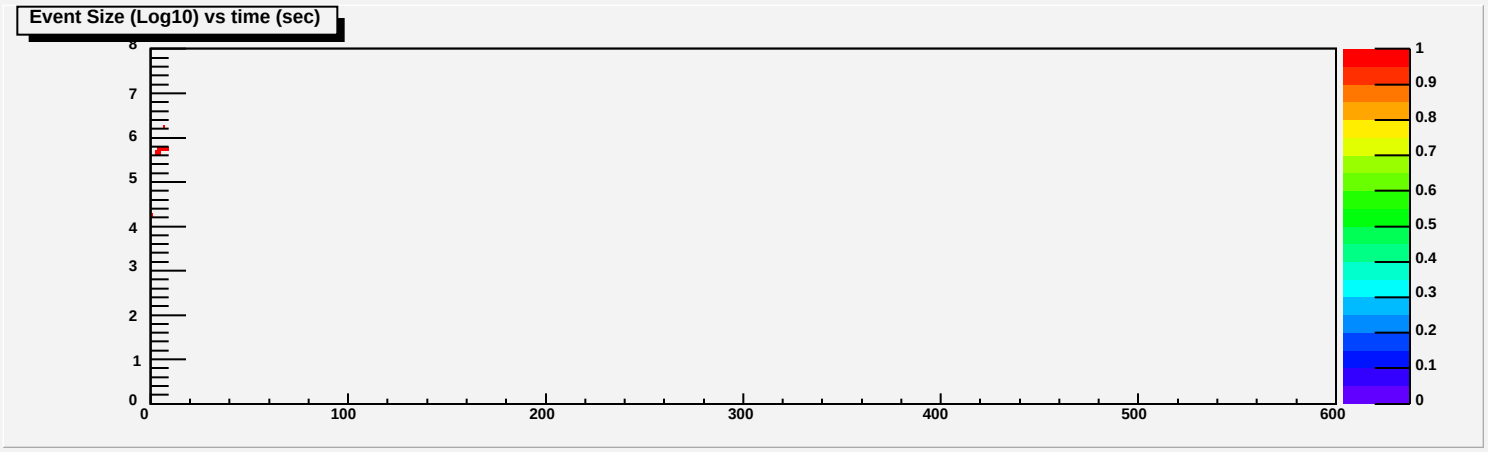

FTP Event Size Fraction (%)
h105_ftp_frac
Entries 11
Mean 32.08
RMS 9.408

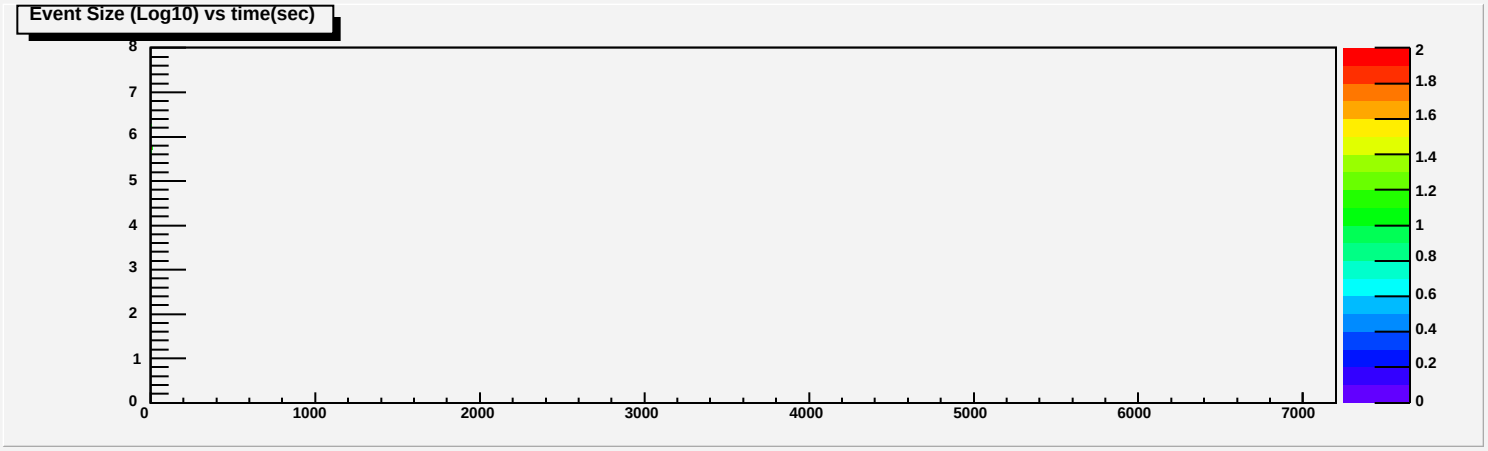

L3 Event Fraction (%)
h108_l3_frac
Entries 12
Mean 0.06709
RMS 0.1664

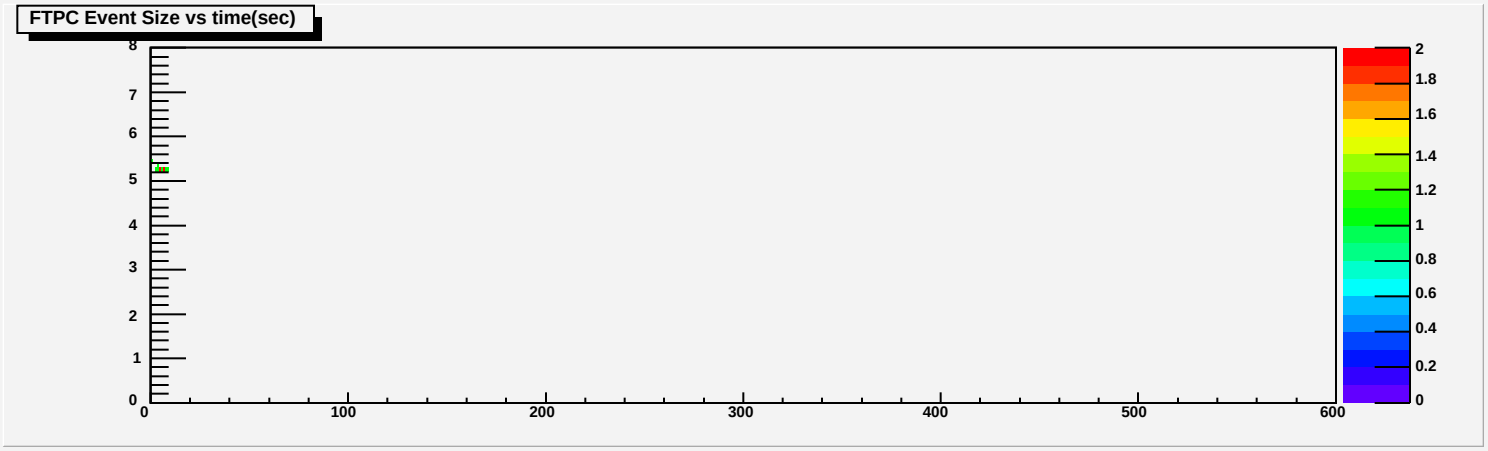

SVT Event Size Fraction (%)
h104_svt_frac
Entries 0
Mean 0
RMS 0

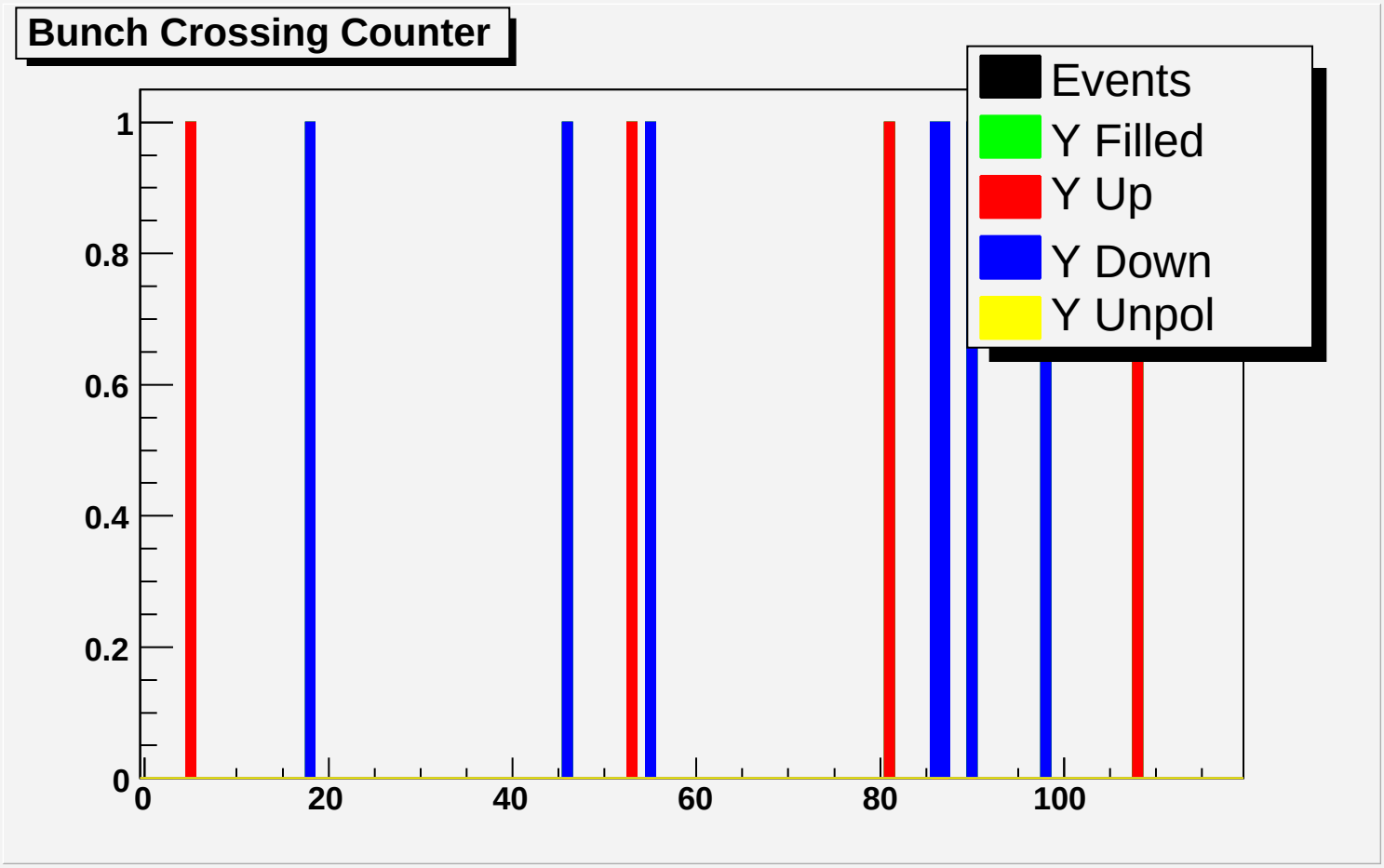

TOFp Event Fraction (%)
h107_tof_frac
Entries 0
Mean 0
RMS 0

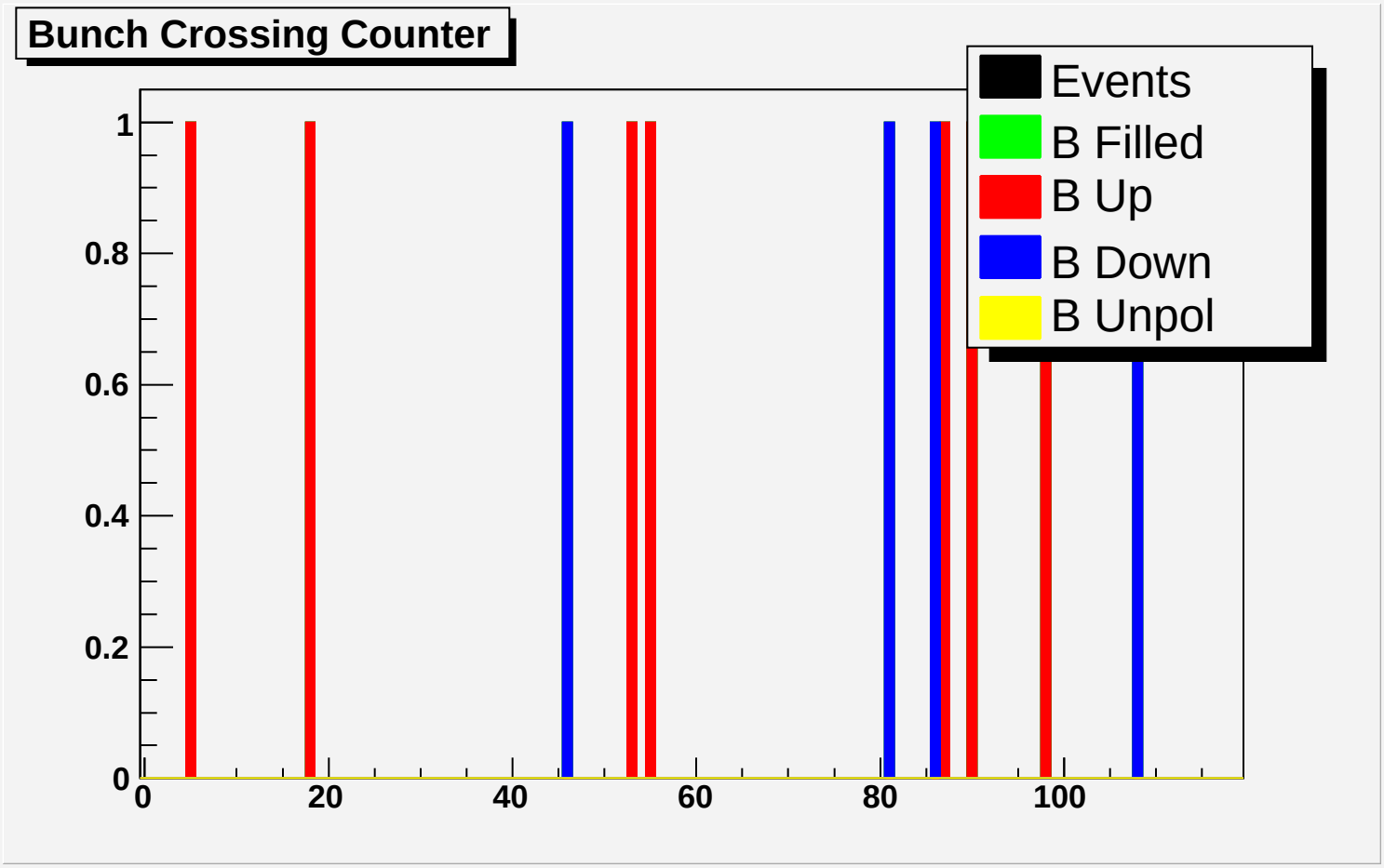

Event Size (Log10) vs time (sec)

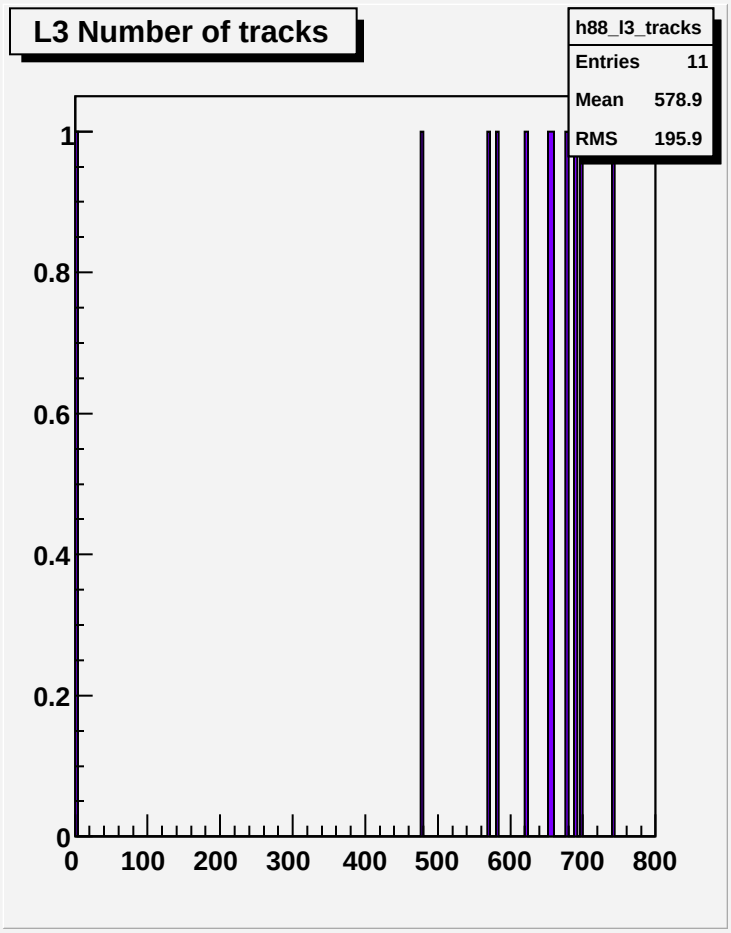

Event Size (Log10) vs time (sec)

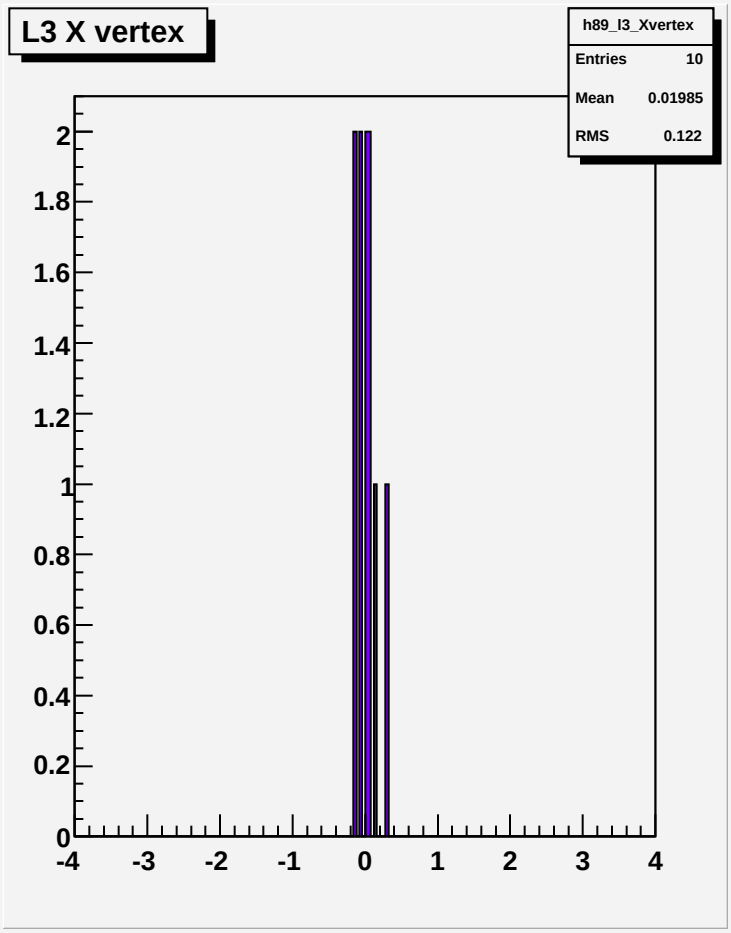

Event Size (Log10) vs time(sec)

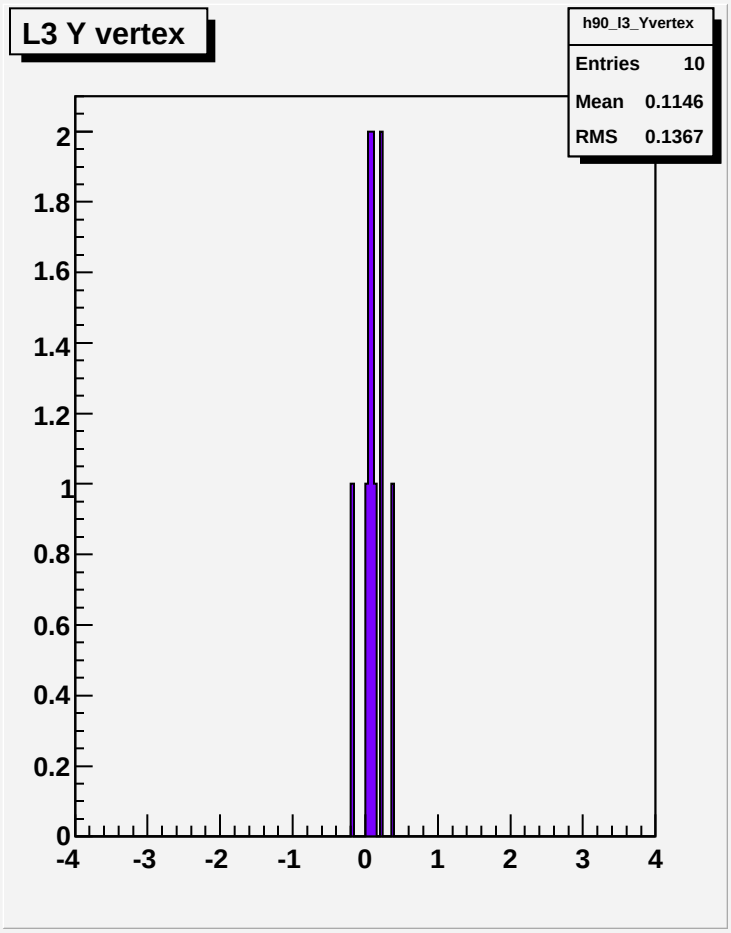

FTPC Event Size vs time(sec)

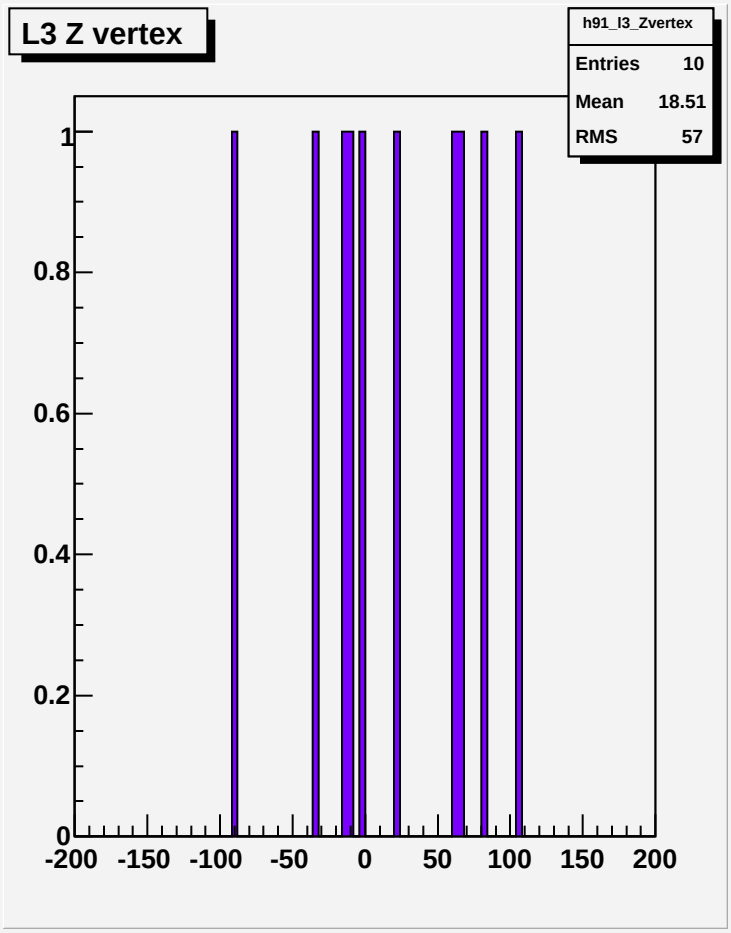

Bunch Crossing Counter
Events
Y Filled
Y Up
Y Down
Y Unpol
1
0.8
0.6
0.4
0.2
0
0
20
40
60
80
100

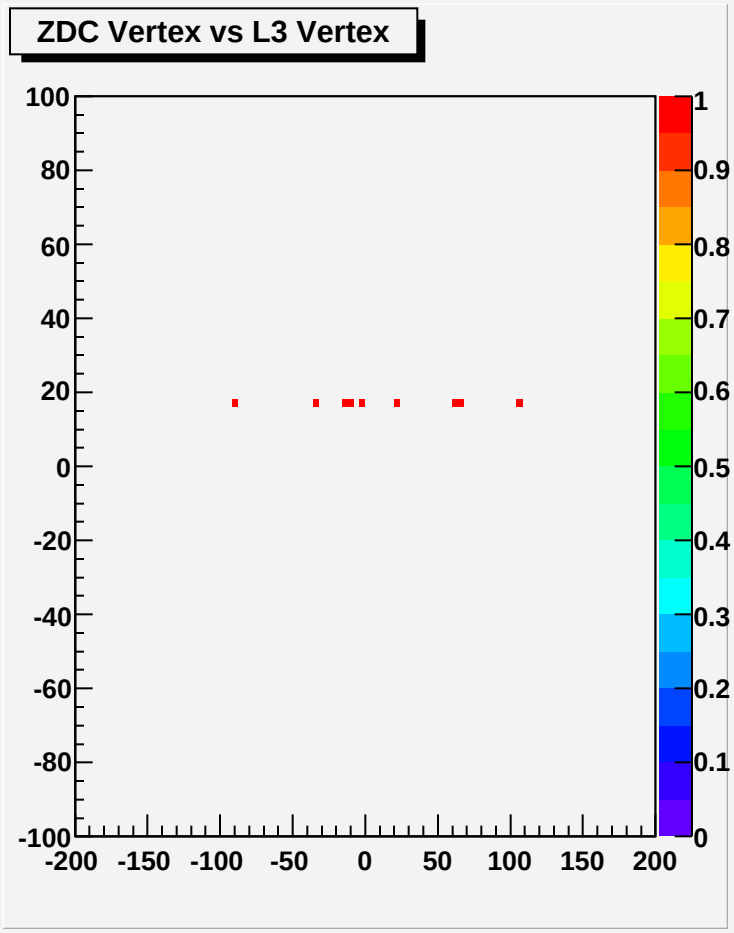

Bunch Crossing Counter
Events
B Filled
B Up
B Down
B Unpol
1
0.8
0.6
0.4
0.2
0
0
20
40
60
80
100

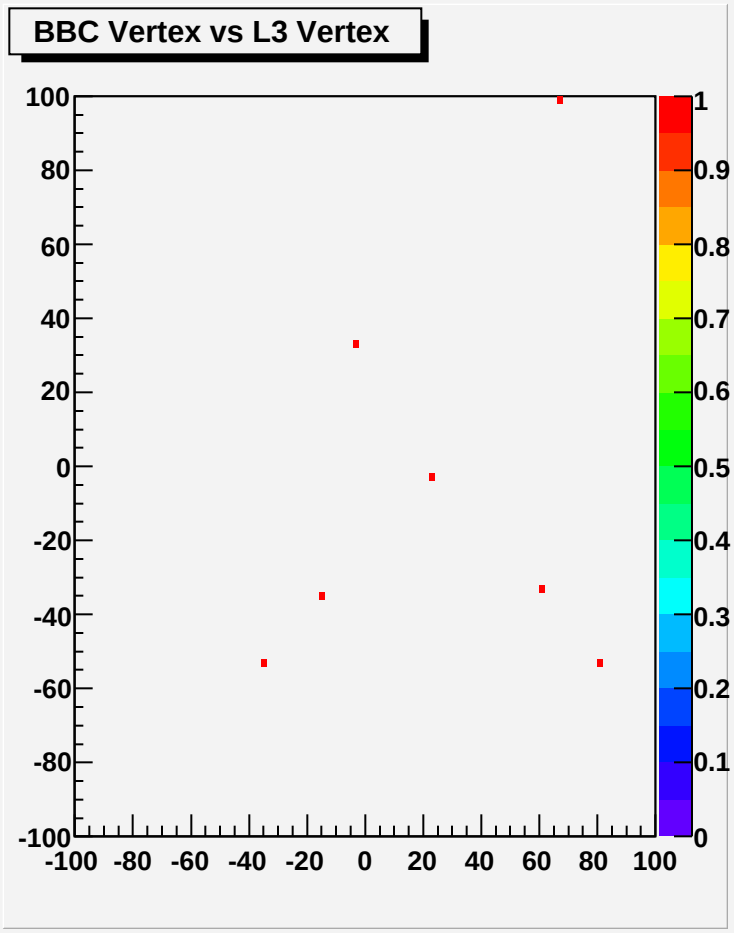

L3 Number of tracks
h88_l3_tracks
Entries 11
Mean 578.9
RMS 195.9
0
0.2
0.4
0.6
0.8
1
0 100 200 300 400 500 600 700 800

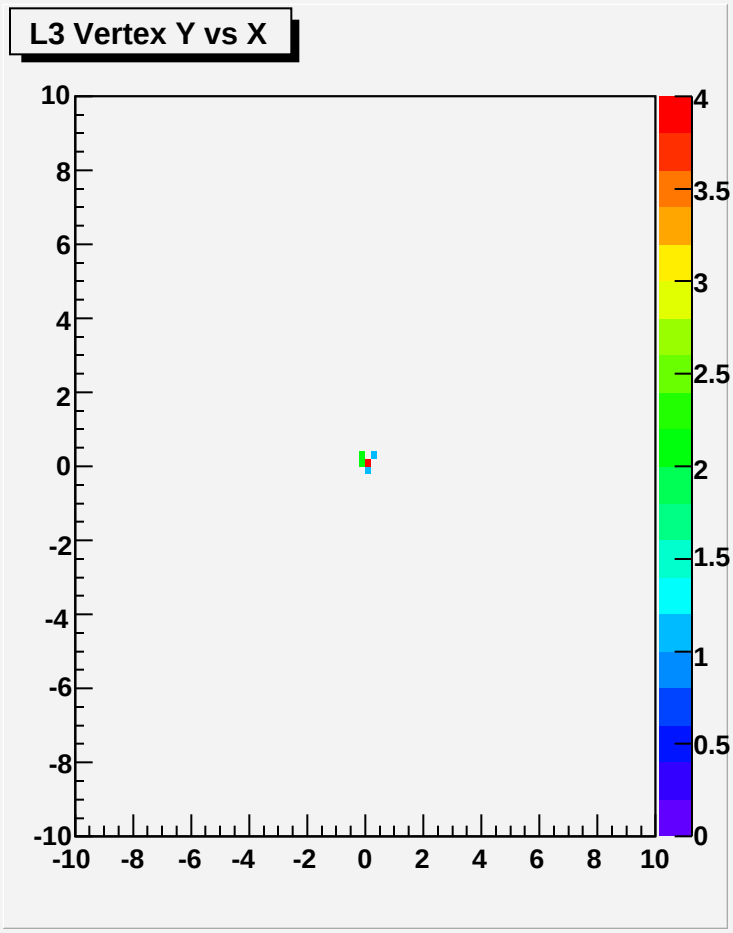

L3 X vertex
h89_l3_Xvertex
Entries 10
Mean 0.01985
RMS 0.122
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
-4 -3 -2 -1 0 1 2 3 4

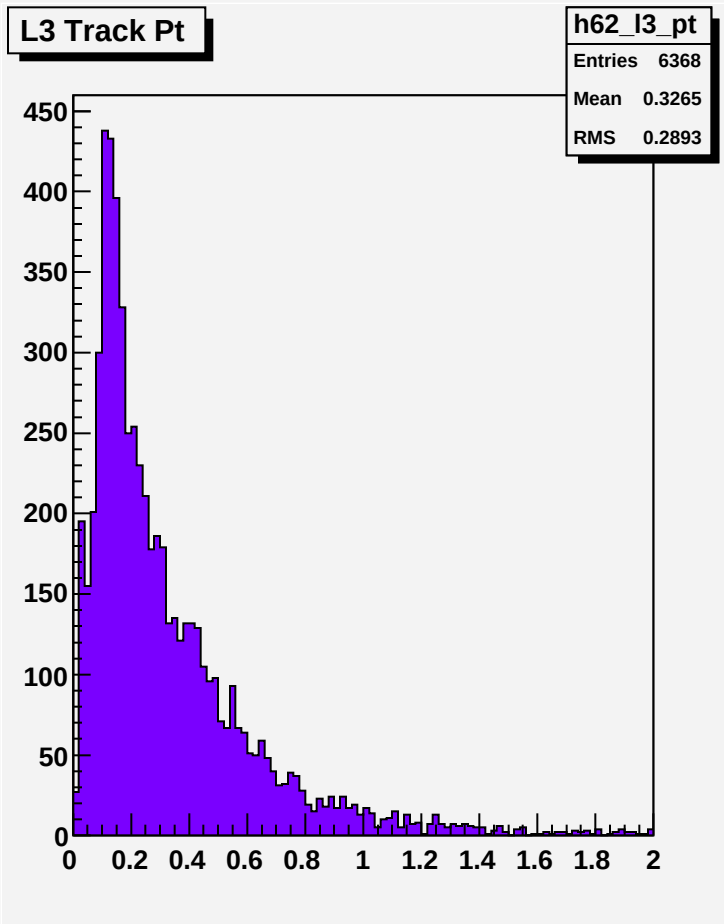

L3 Y vertex
h90_l3_Yvertex
Entries 10
Mean 0.1146
RMS 0.1367
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
-4 -3 -2 -1 0 1 2 3 4

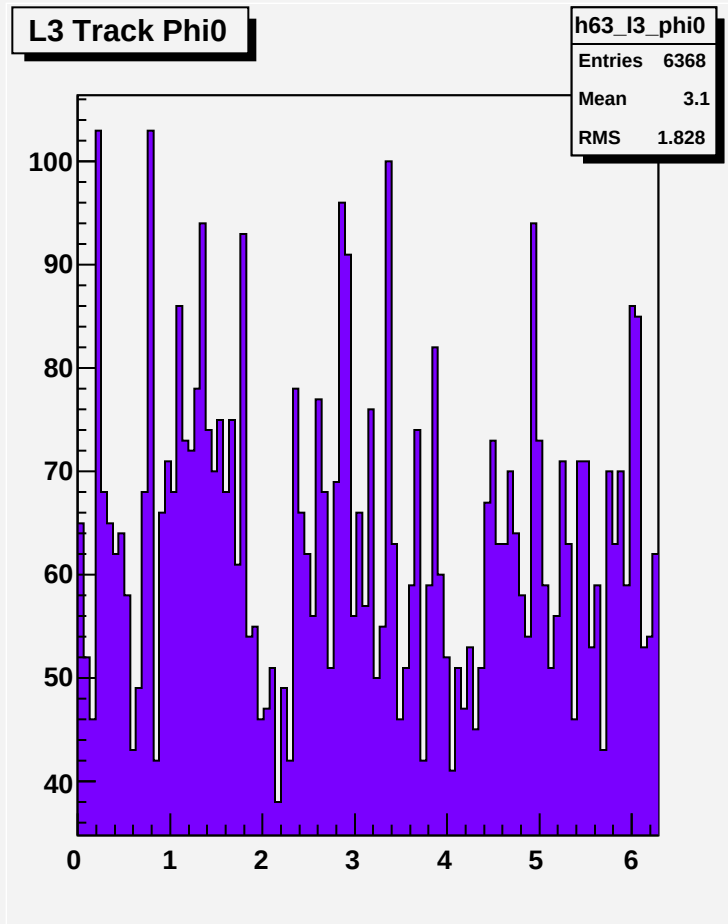

L3 Z vertex
h91_l3_Zvertex
Entries 10
Mean 18.51
RMS 57
0
0.2
0.4
0.6
0.8
1
-200 -150 -100 -50 0 50 100 150 200

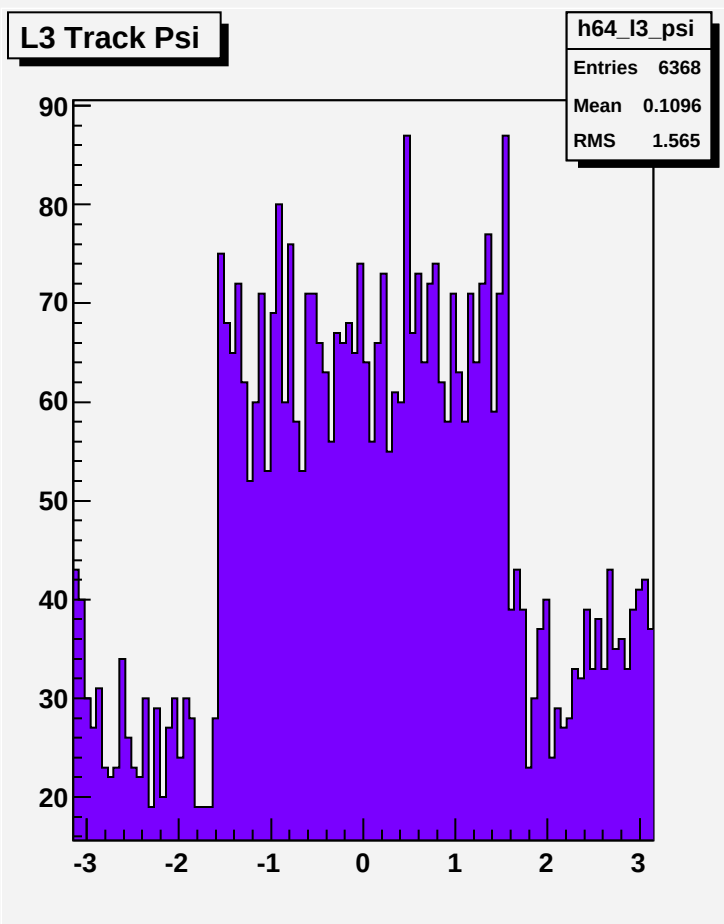

ZDC Vertex vs L3 Vertex
100
80
60
40
20
0
-20
-40
-60
-80
-100
-200 -150 -100 -50 0 50 100 150 200
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0

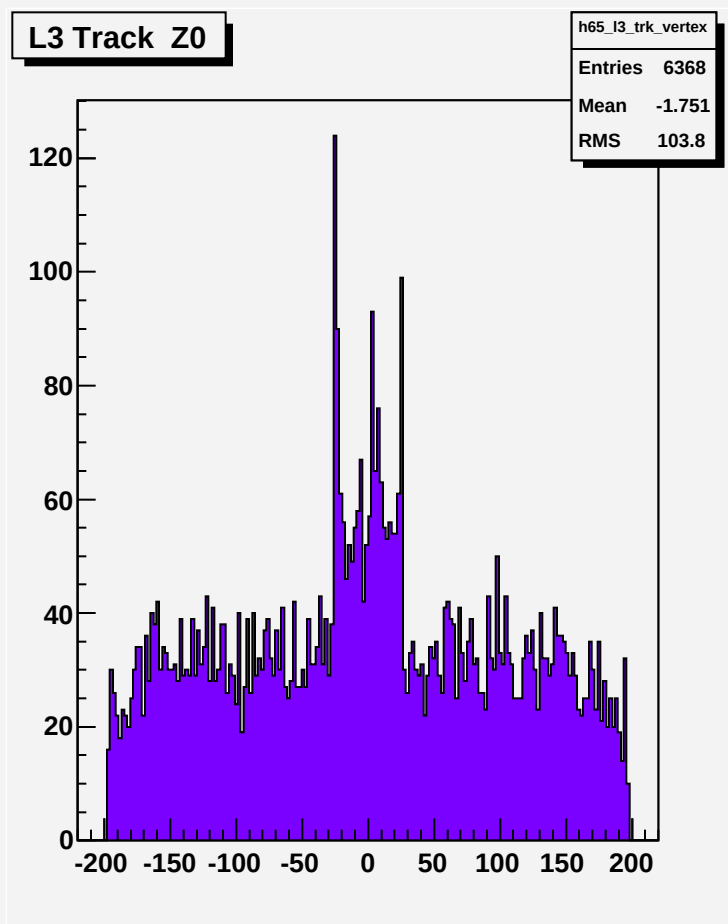

BBC Vertex vs L3 Vertex
100
80
60
40
20
0
-20
-40
-60
-80
-100
-100 -80 -60 -40 -20 0 20 40 60 80 100
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0

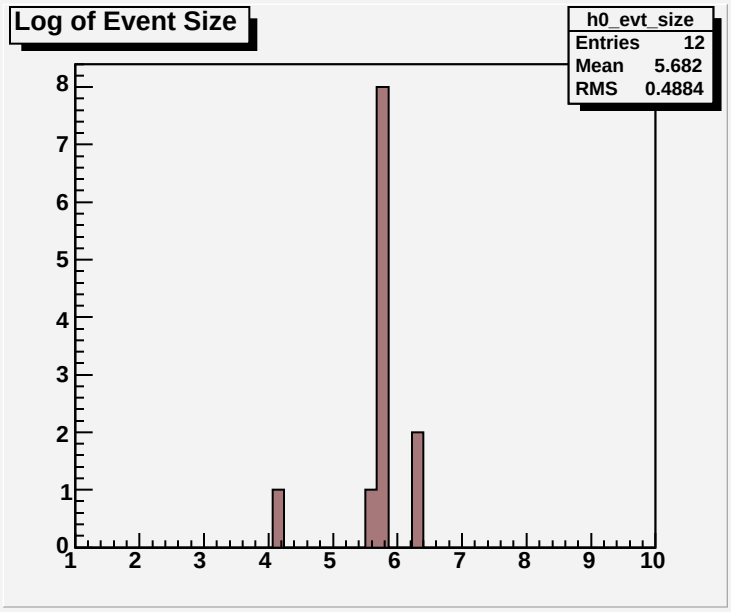

L3 Vertex Y vs X
10
8
6
4
2
0
-2
-4
-6
-8
-10
-10 -8 -6 -4 -2 0 2 4 6 8 10
4
3.5
3
2.5
2
1.5
1
0.5
0

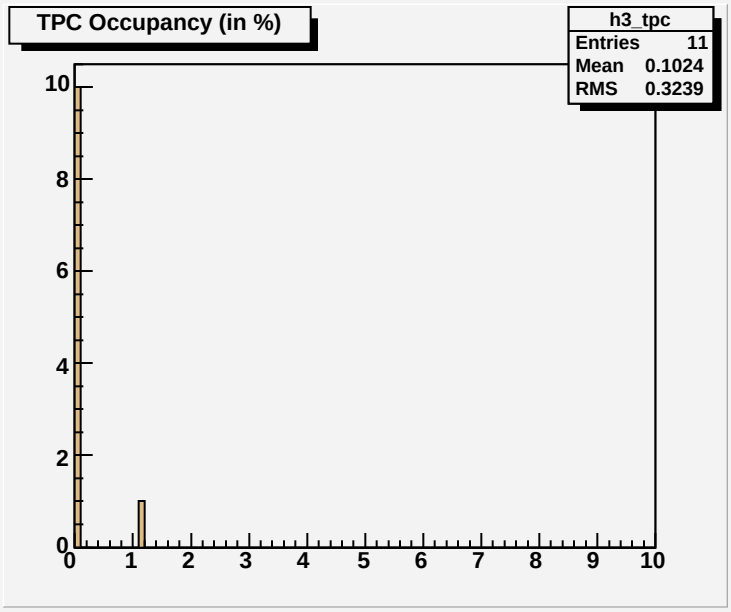

L3 Track Pt
h62_l3_pt
Entries 6368
Mean 0.3265
RMS 0.2893
450
400
350
300
250
200
150
100
50
0
0 0.2 0.4 0.6 0.8 1 1.2 1.4 1.6 1.8 2

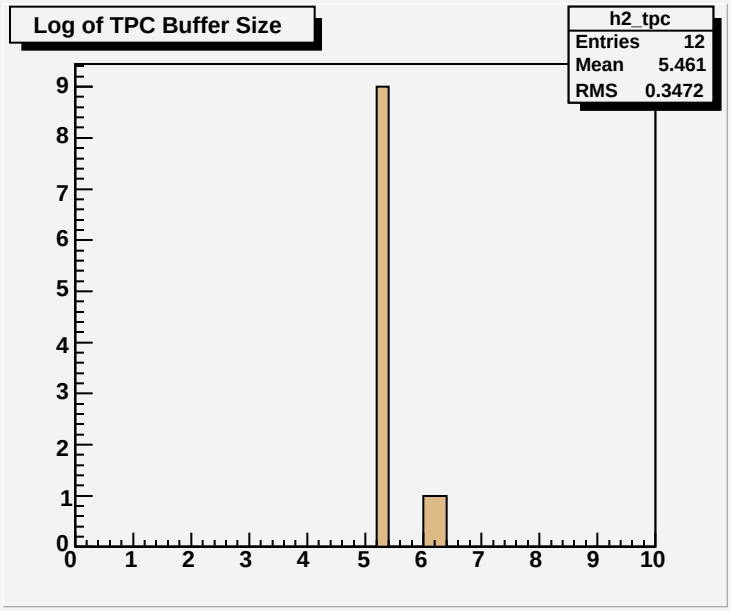

L3 Track Phi0
h63_l3_phi0
Entries 6368
Mean 3.1
RMS 1.828
100
90
80
70
60
50
40
0 1 2 3 4 5 6

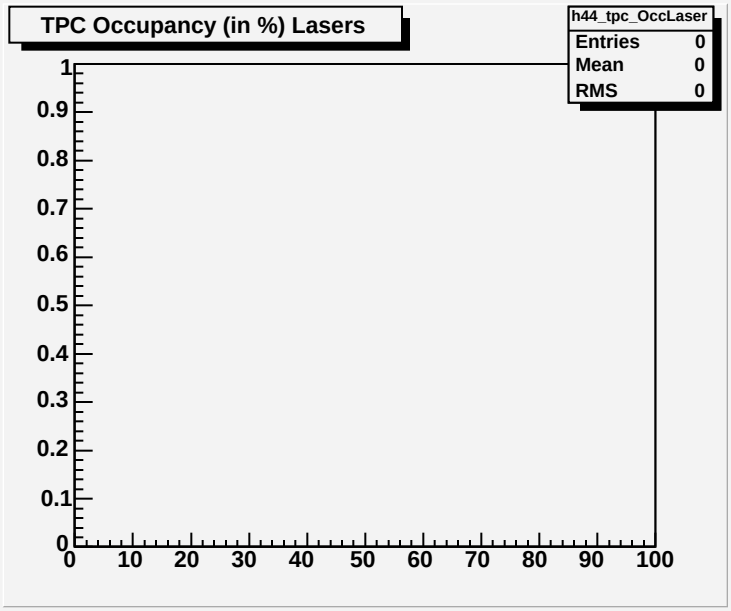

L3 Track Psi
h64_l3_psi
Entries 6368
Mean 0.1096
RMS 1.565
90
80
70
60
50
40
30
20
-3 -2 -1 0 1 2 3

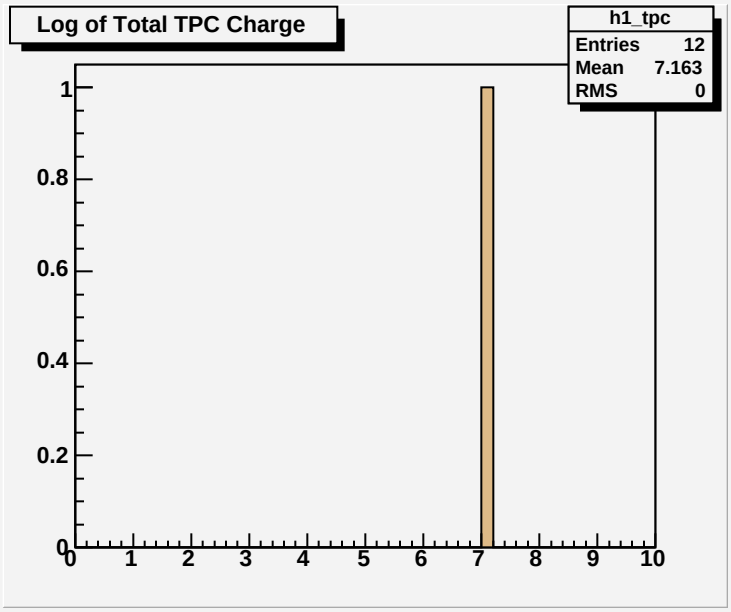

L3 Track Z0
h65_l3_trk_vertex
Entries 6368
Mean -1.751
RMS 103.8
120
100
80
60
40
20
0
-200 -150 -100 -50 0 50 100 150 200

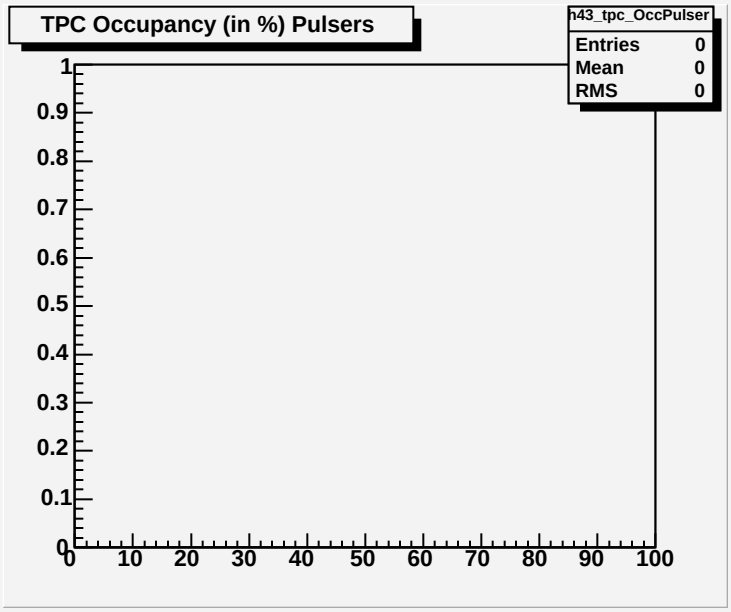

Log of Event Size
h0_evt_size
Entries 12
Mean 5.682
RMS 0.4884

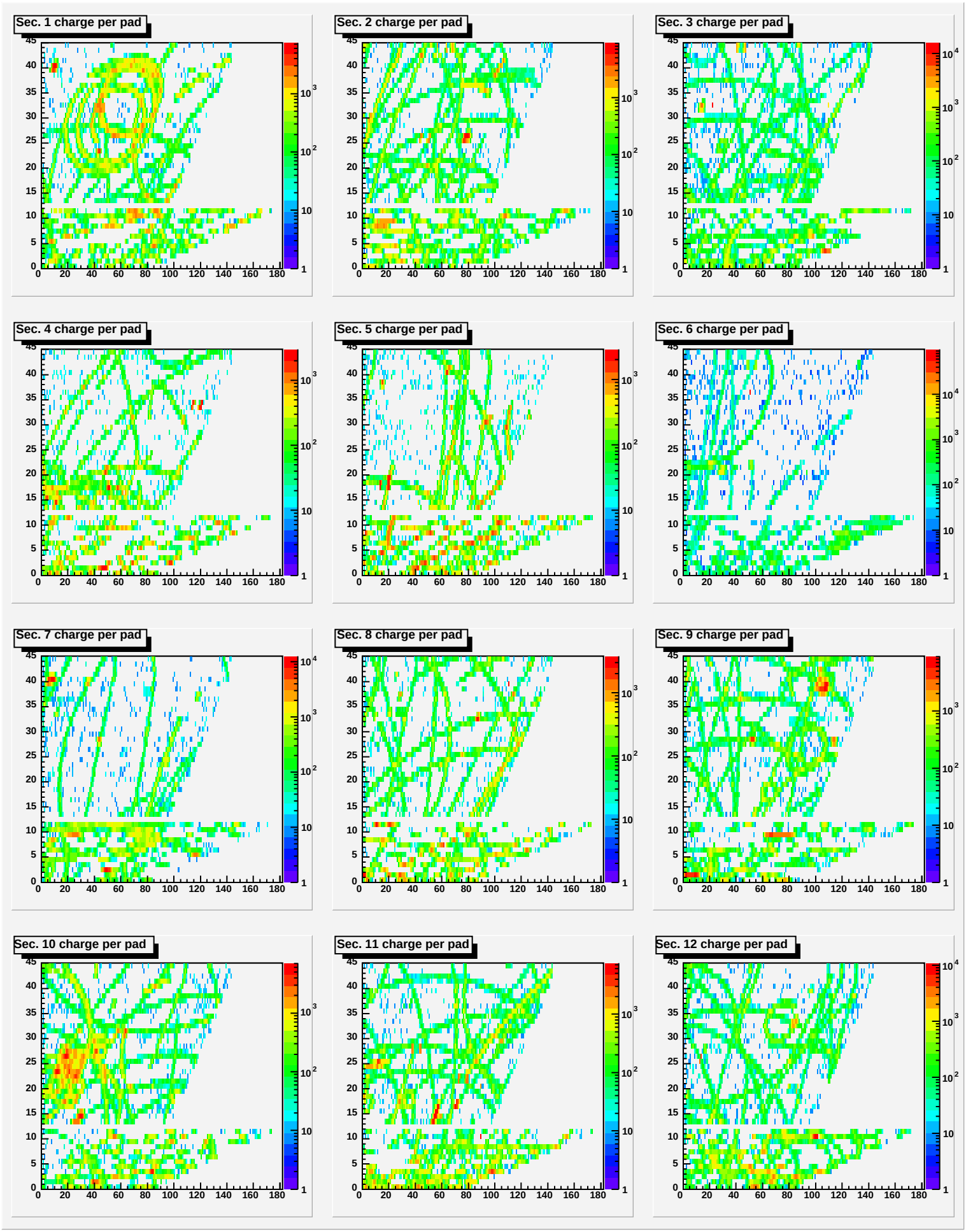

TPC Occupancy (in %)
h3_tpc
Entries 11
Mean 0.1024
RMS 0.3239

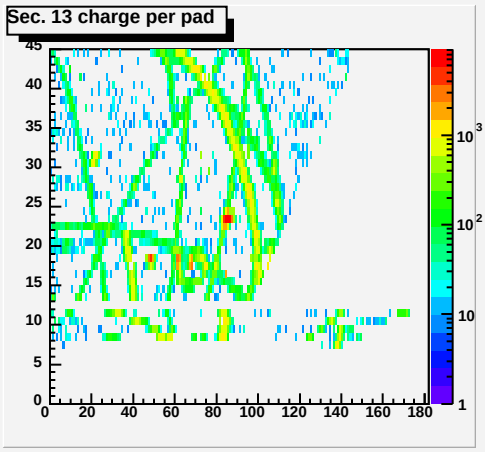

Log of TPC Buffer Size
h2_tpc
Entries 12
Mean 5.461
RMS 0.3472

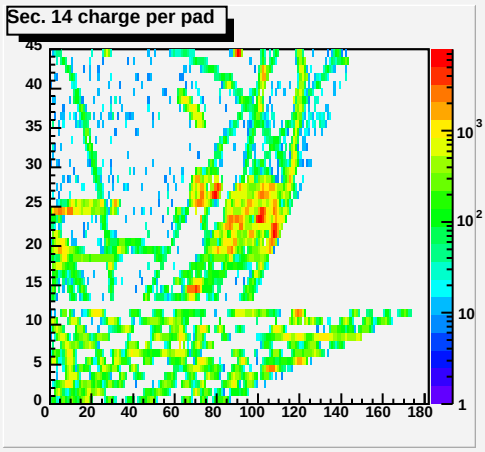

TPC Occupancy (in %) Lasers
h44_tpc_OccLaser
Entries 0
Mean 0
RMS 0

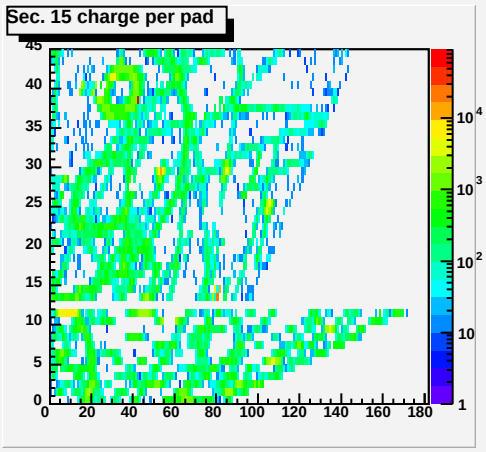

Log of Total TPC Charge
h1_tpc
Entries 12
Mean 7.163
RMS 0

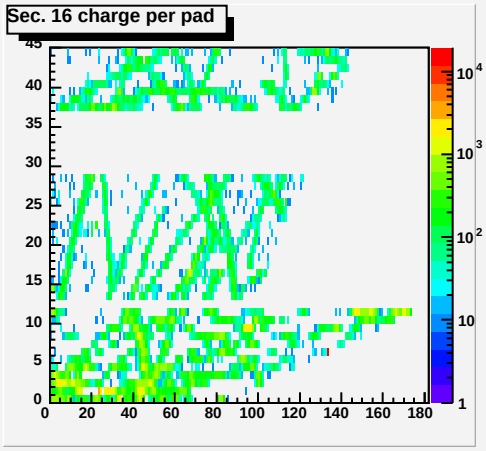

TPC Occupancy (in %) Pulsers
h43_tpc_OccPulser
Entries 0
Mean 0
RMS 0

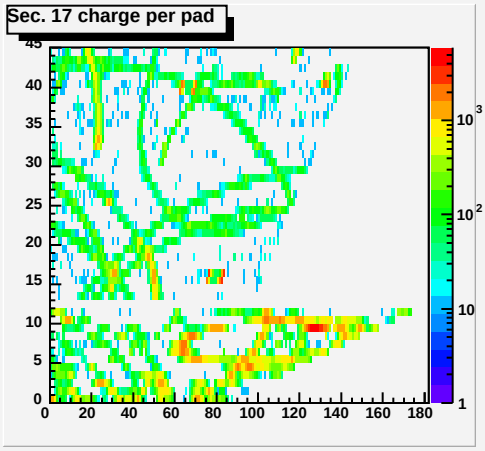

Sec. 1 charge per pad
Sec. 2 charge per pad
Sec. 3 charge per pad
Sec. 4 charge per pad
Sec. 5 charge per pad
Sec. 6 charge per pad
Sec. 7 charge per pad
Sec. 8 charge per pad
Sec. 9 charge per pad
Sec. 10 charge per pad
Sec. 11 charge per pad
Sec. 12 charge per pad

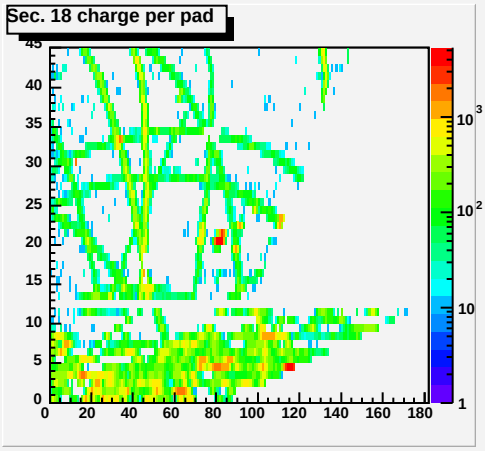

Sec. 13 charge per pad

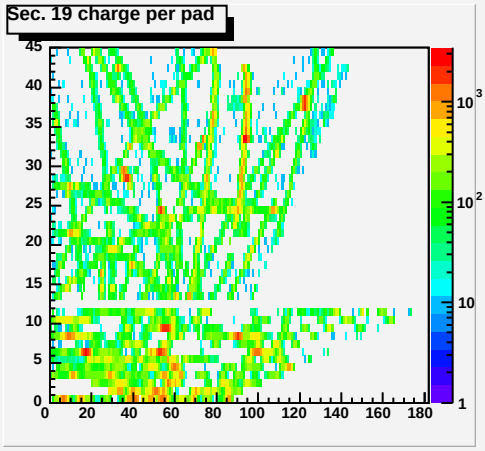

Sec. 14 charge per pad

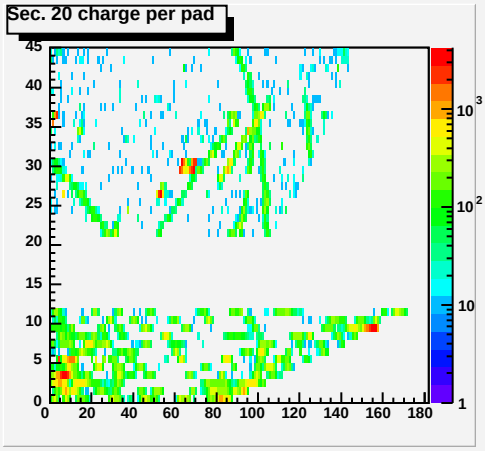

Sec. 15 charge per pad

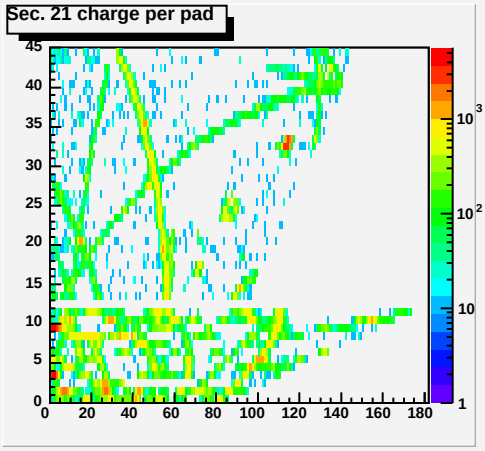

Sec. 16 charge per pad

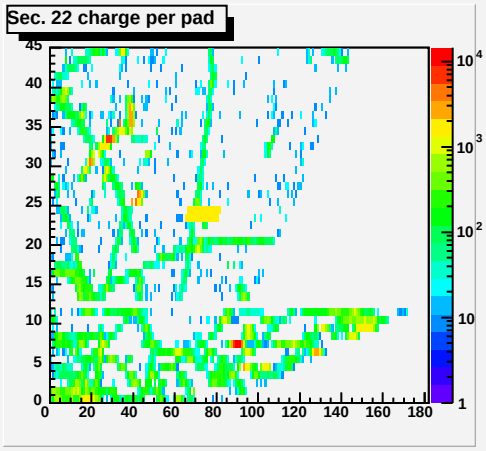

Sec. 17 charge per pad

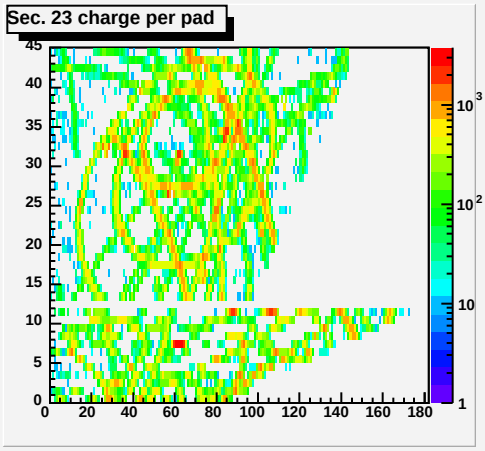

Sec. 18 charge per pad

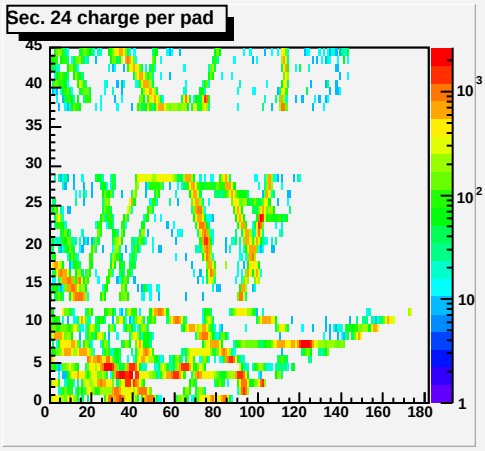

Sec. 19 charge per pad

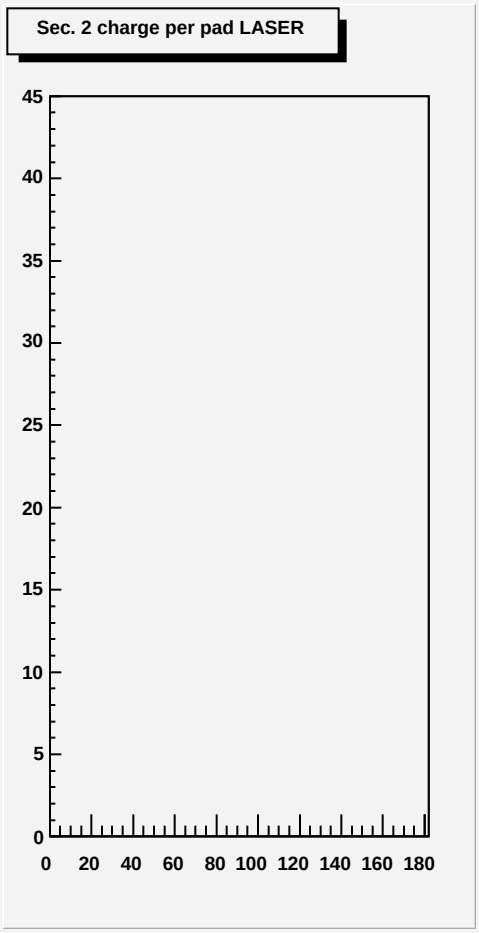

Sec. 20 charge per pad

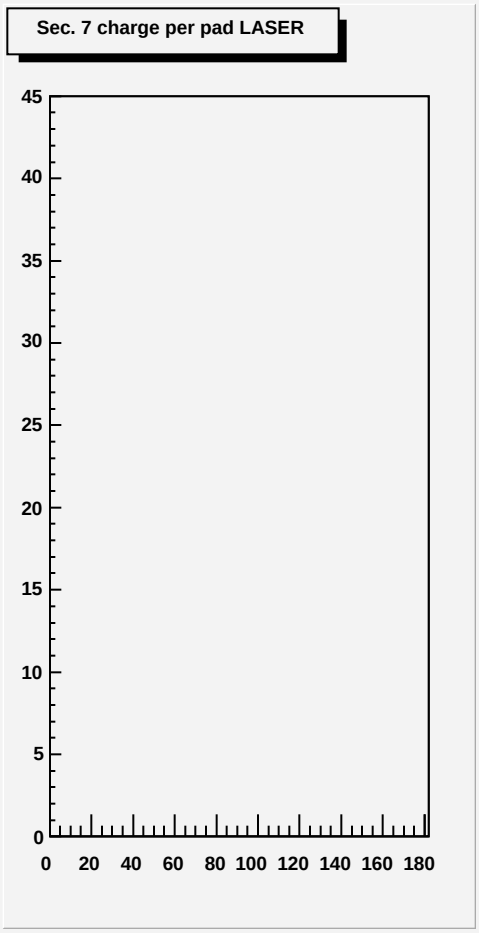

Sec. 21 charge per pad

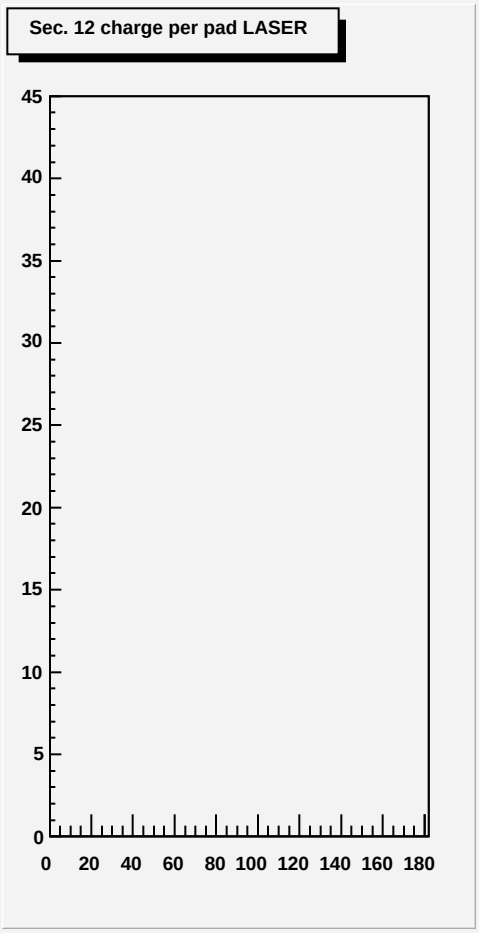

Sec. 22 charge per pad

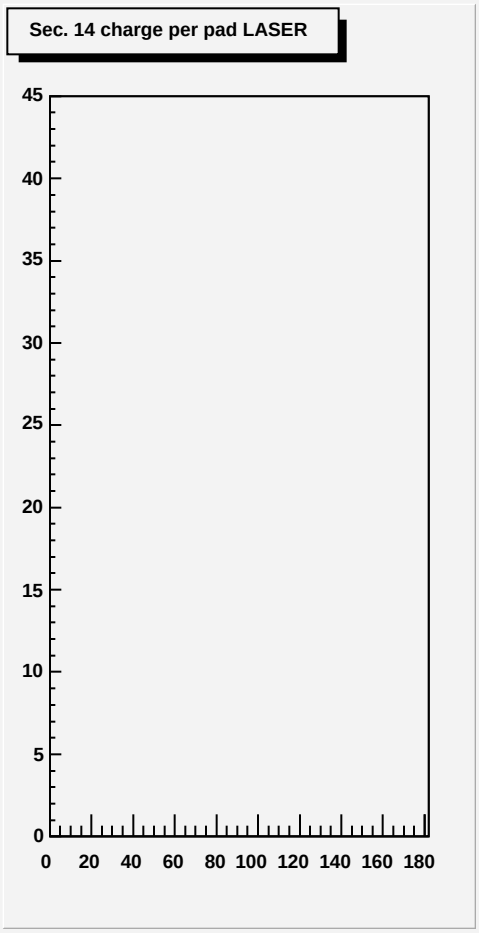

Sec. 23 charge per pad

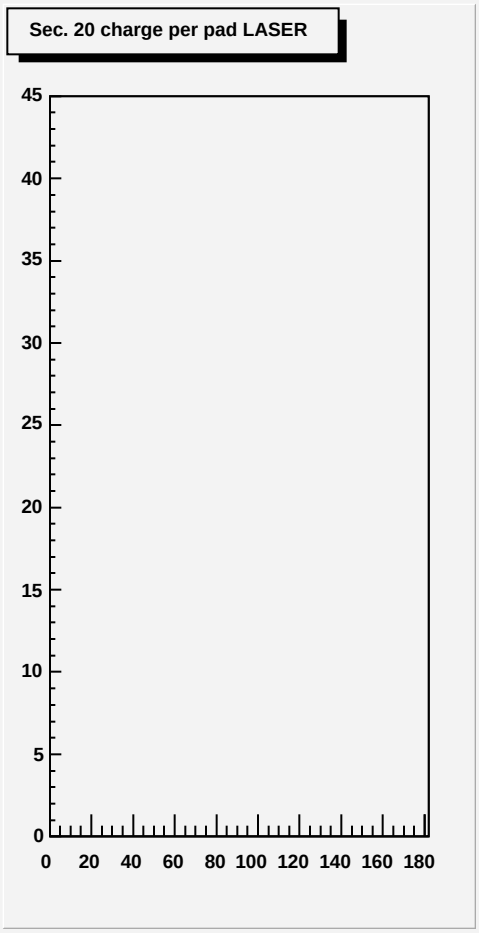

Sec. 24 charge per pad

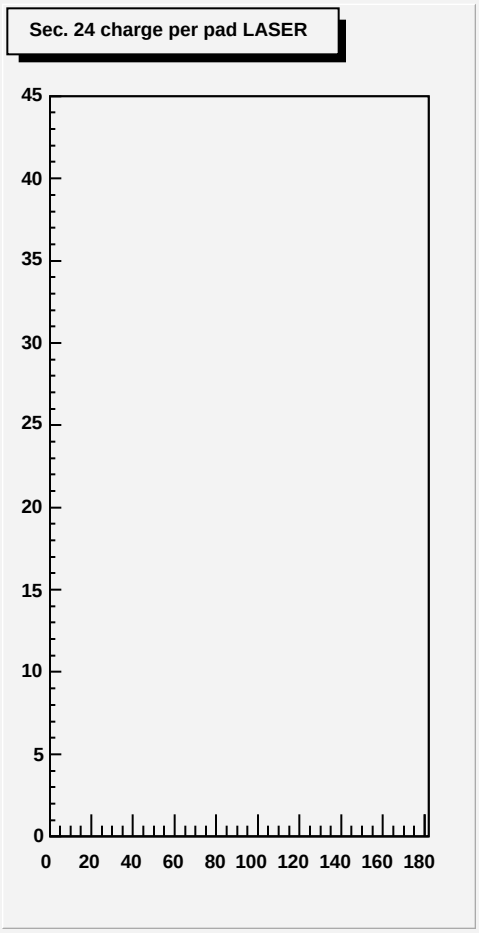

Sec. 2 charge per pad LASER
45
40
35
30
25
20
15
10
5
0
0 20 40 60 80 100 120 140 160 180

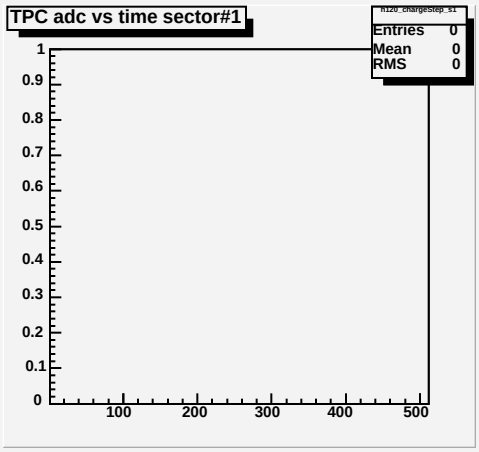

Sec. 7 charge per pad LASER
45
40
35
30
25
20
15
10
5
0
0 20 40 60 80 100 120 140 160 180

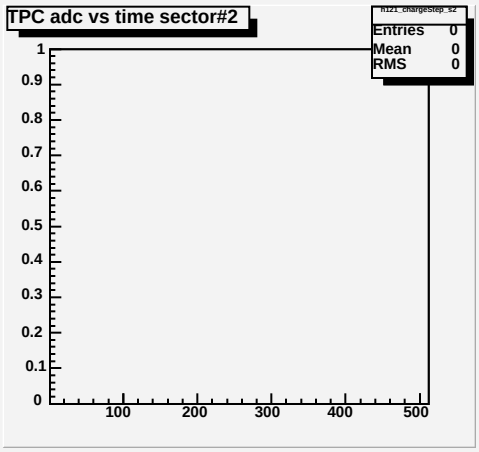

Sec. 12 charge per pad LASER
45
40
35
30
25
20
15
10
5
0
0 20 40 60 80 100 120 140 160 180

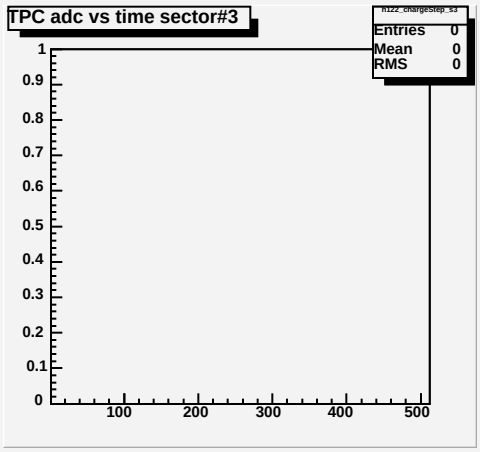

Sec. 14 charge per pad LASER
45
40
35
30
25
20
15
10
5
0
0 20 40 60 80 100 120 140 160 180

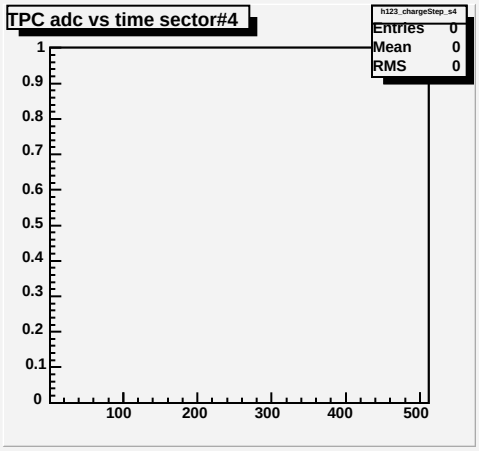

Sec. 20 charge per pad LASER
45
40
35
30
25
20
15
10
5
0
0 20 40 60 80 100 120 140 160 180

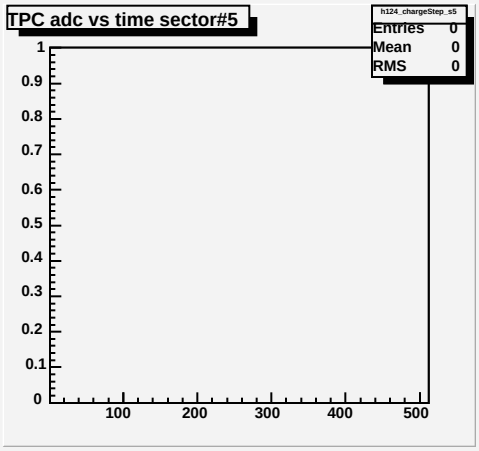

Sec. 24 charge per pad LASER
45
40
35
30
25
20
15
10
5
0
0 20 40 60 80 100 120 140 160 180

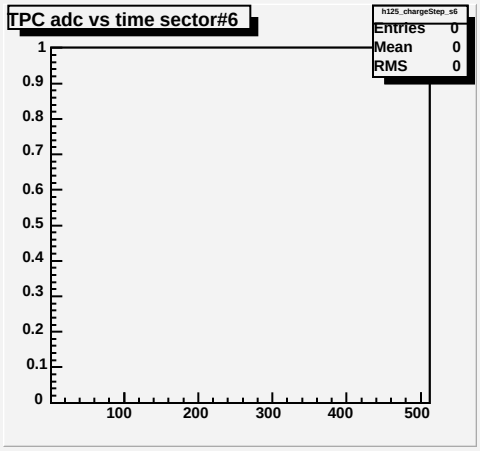

TPC adc vs time sector#1
h120_chargeStep_s1
Entries 0
Mean 0
RMS 0

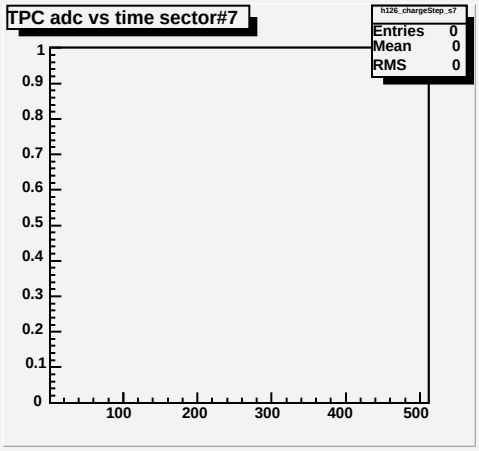

TPC adc vs time sector#2
h121_chargeStep_s2
Entries 0
Mean 0
RMS 0

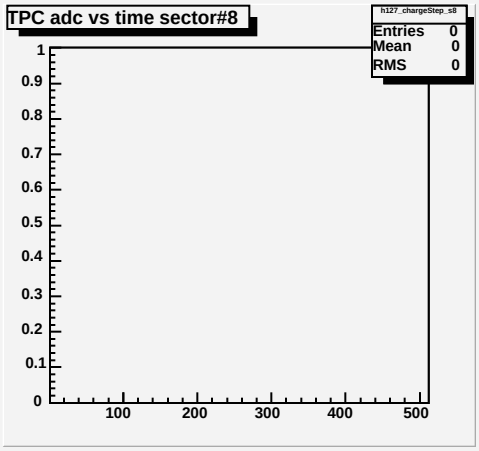

TPC adc vs time sector#3
h122_chargeStep_s3
Entries 0
Mean 0
RMS 0

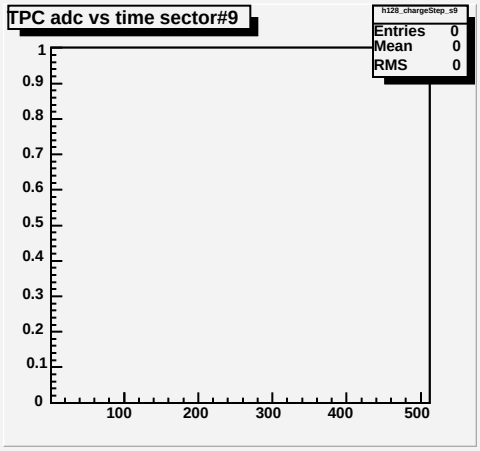

TPC adc vs time sector#4
h123_chargeStep_s4
Entries 0
Mean 0
RMS 0

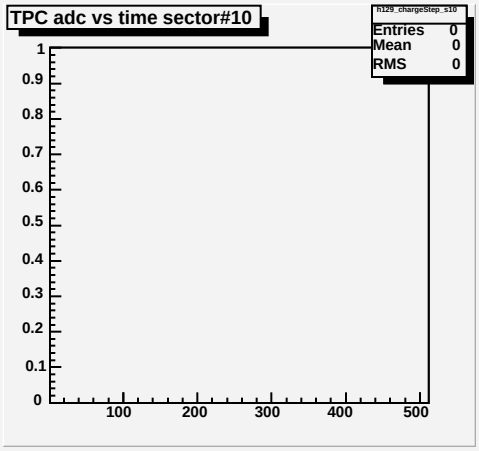

TPC adc vs time sector#5
h124_chargeStep_s5
Entries 0
Mean 0
RMS 0

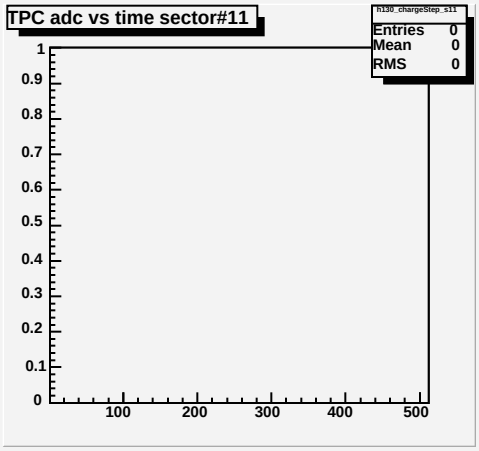

TPC adc vs time sector#6
h125_chargeStep_s6
Entries 0
Mean 0
RMS 0

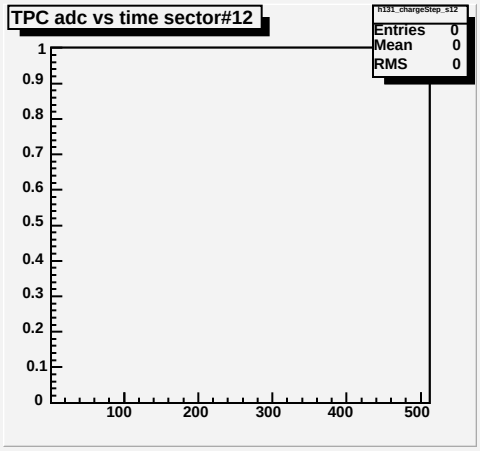

TPC adc vs time sector#7
h126_chargeStep_s7
Entries 0
Mean 0
RMS 0

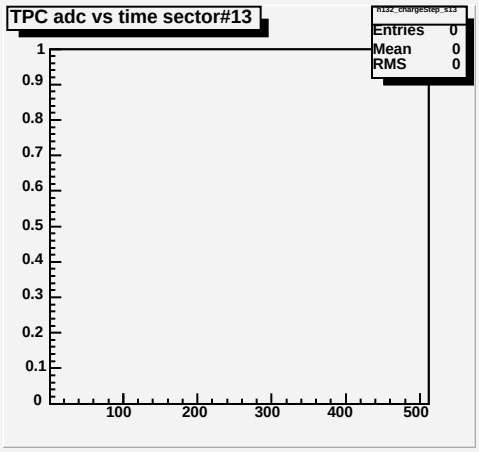

TPC adc vs time sector#8
h127_chargeStep_s8
Entries 0
Mean 0
RMS 0

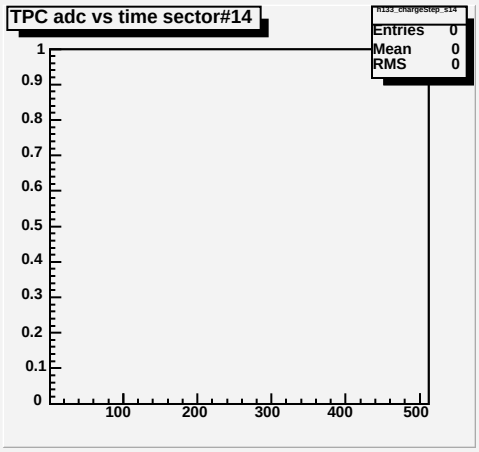

TPC adc vs time sector#9
h128_chargeStep_s9
Entries 0
Mean 0
RMS 0

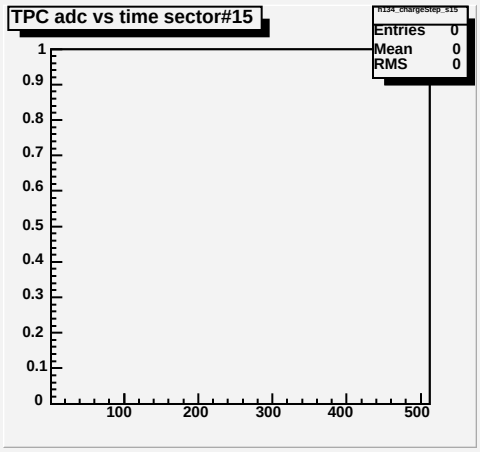

TPC adc vs time sector#10
h129_chargeStep_s10
Entries 0
Mean 0
RMS 0

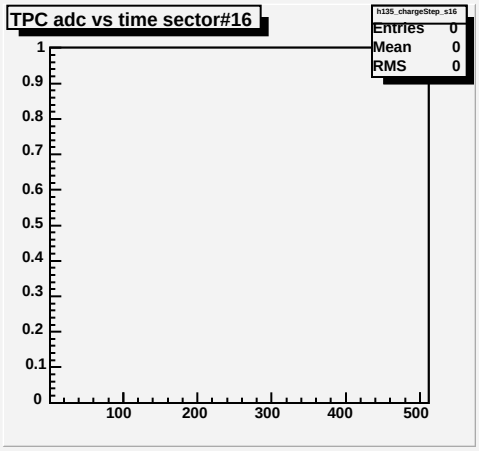

TPC adc vs time sector#11
h130_chargeStep_s11
Entries 0
Mean 0
RMS 0

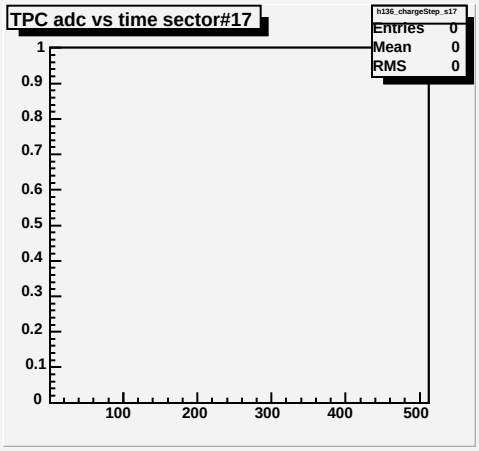

TPC adc vs time sector#12
h131_chargeStep_s12
Entries 0
Mean 0
RMS 0

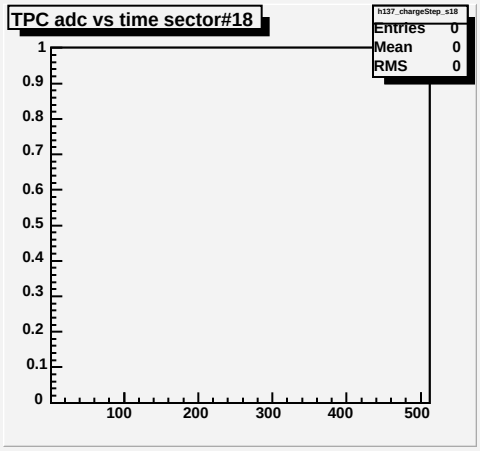

TPC adc vs time sector#13
h132_chargeStep_s13
Entries 0
Mean 0
RMS 0

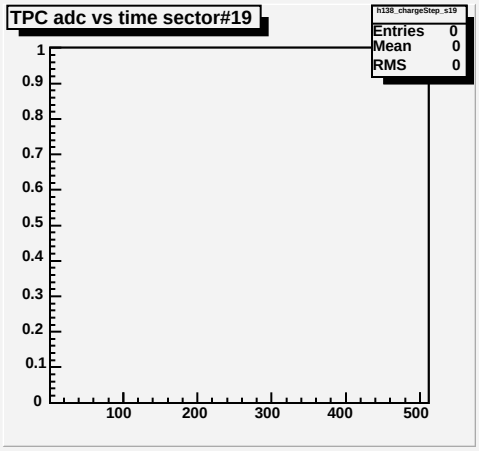

TPC adc vs time sector#14
h133_chargeStep_s14
Entries 0
Mean 0
RMS 0

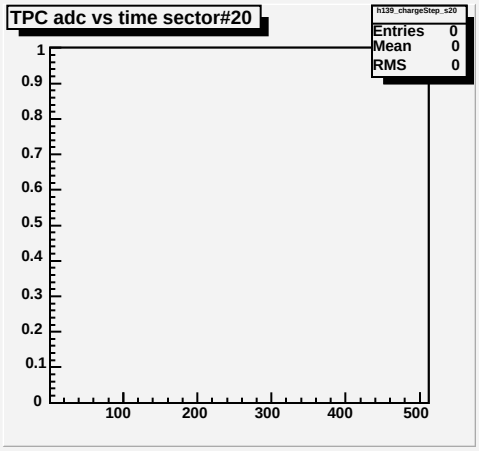

TPC adc vs time sector#15
h134_chargeStep_s15
Entries 0
Mean 0
RMS 0

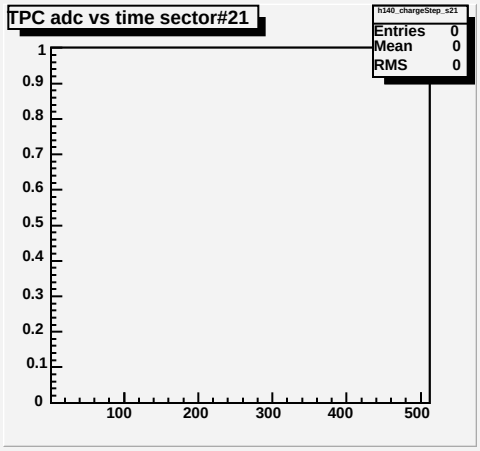

TPC adc vs time sector#16
h135_chargeStep_s16
Entries 0
Mean 0
RMS 0

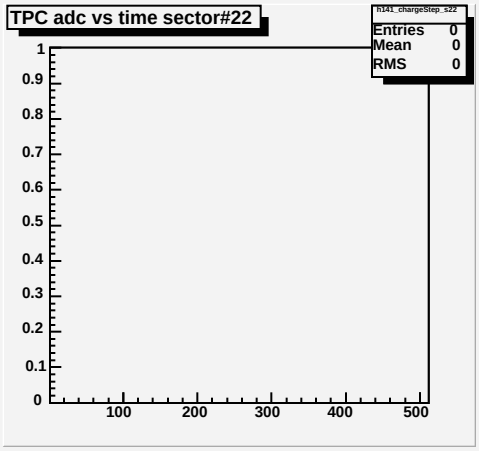

TPC adc vs time sector#17
h136_chargeStep_s17
Entries 0
Mean 0
RMS 0

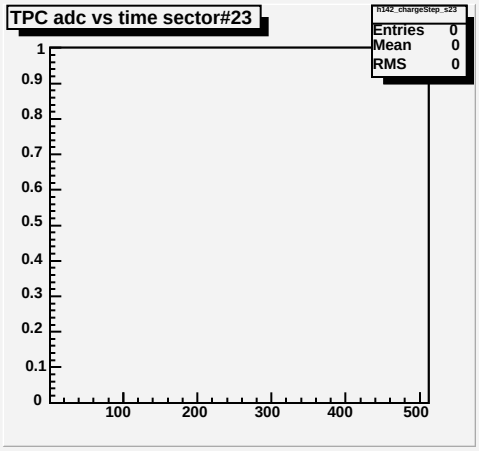

TPC adc vs time sector#18
h137_chargeStep_s18
Entries 0
Mean 0
RMS 0

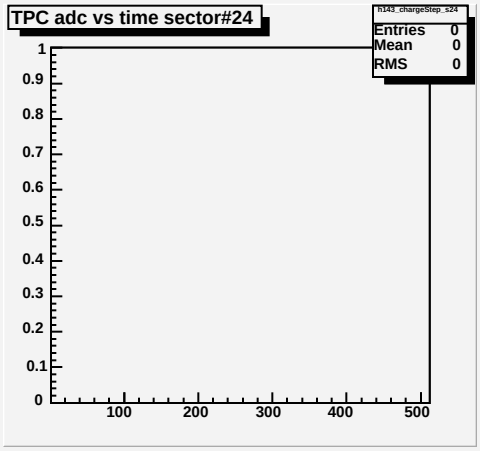

TPC adc vs time sector#19
h138_chargeStep_s19
Entries 0
Mean 0
RMS 0

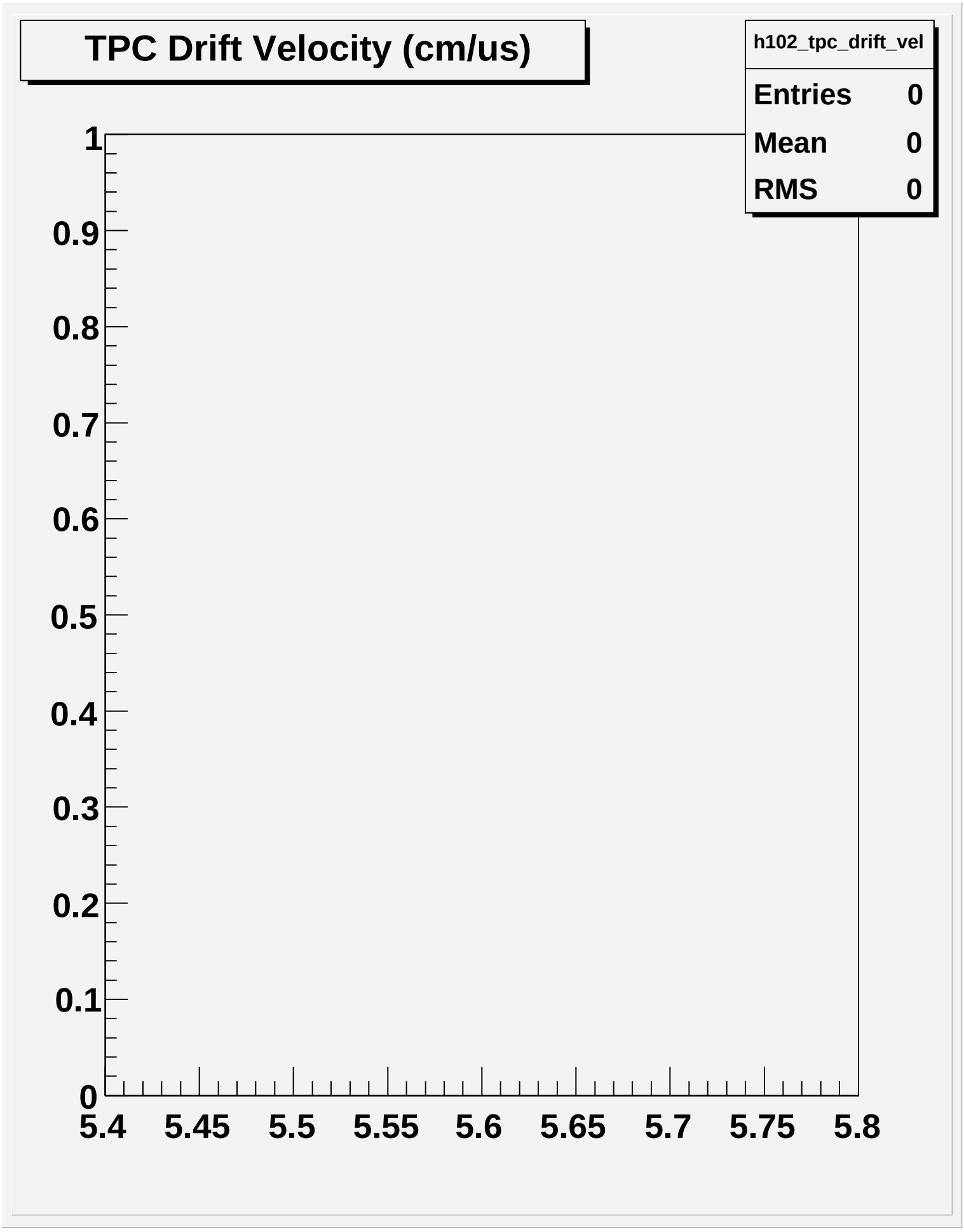

TPC adc vs time sector#20
h139_chargeStep_s20
Entries 0
Mean 0
RMS 0

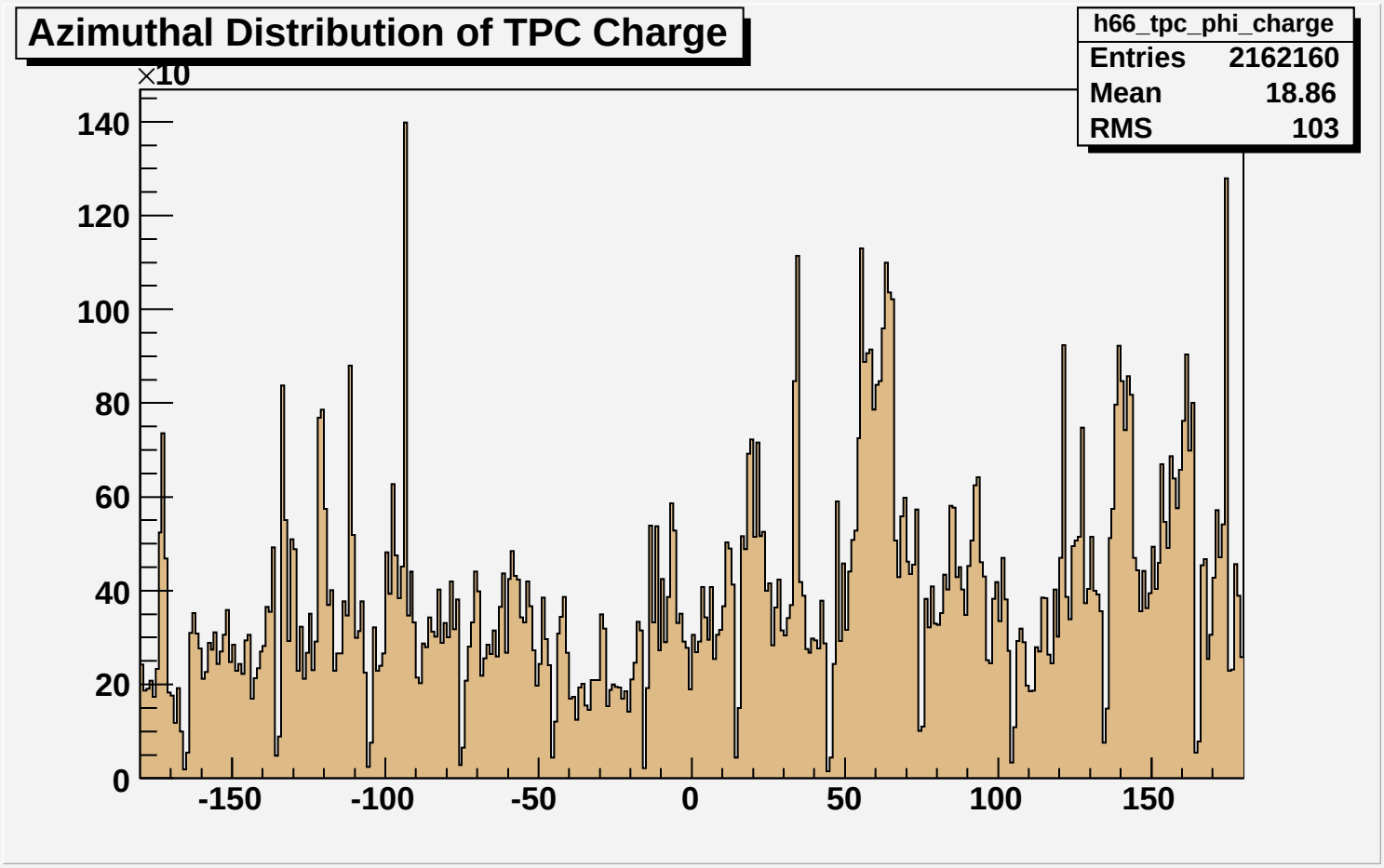

TPC adc vs time sector#21
h140_chargeStep_s21
Entries 0
Mean 0
RMS 0

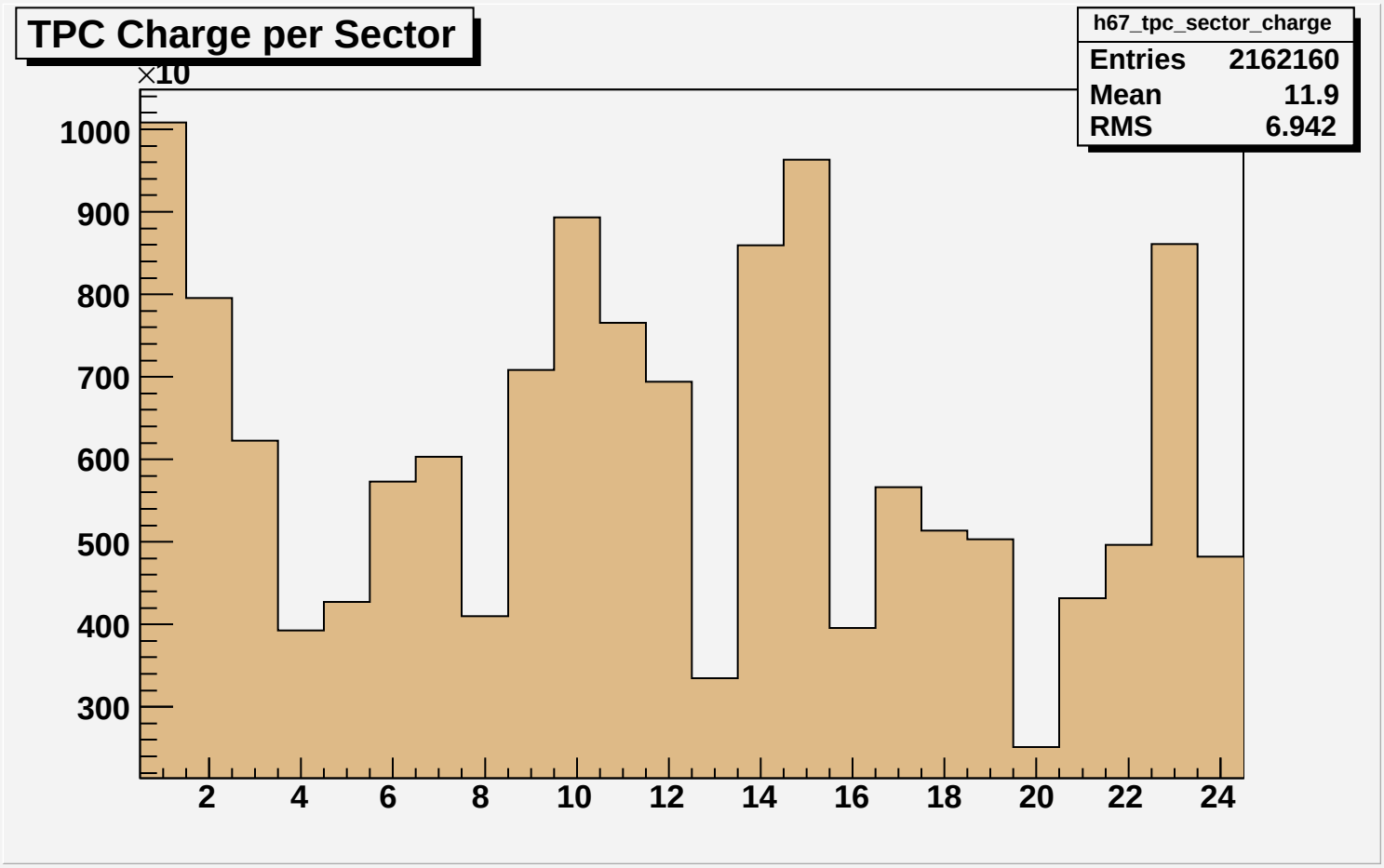

TPC adc vs time sector#22
h141_chargeStep_s22
Entries 0
Mean 0
RMS 0

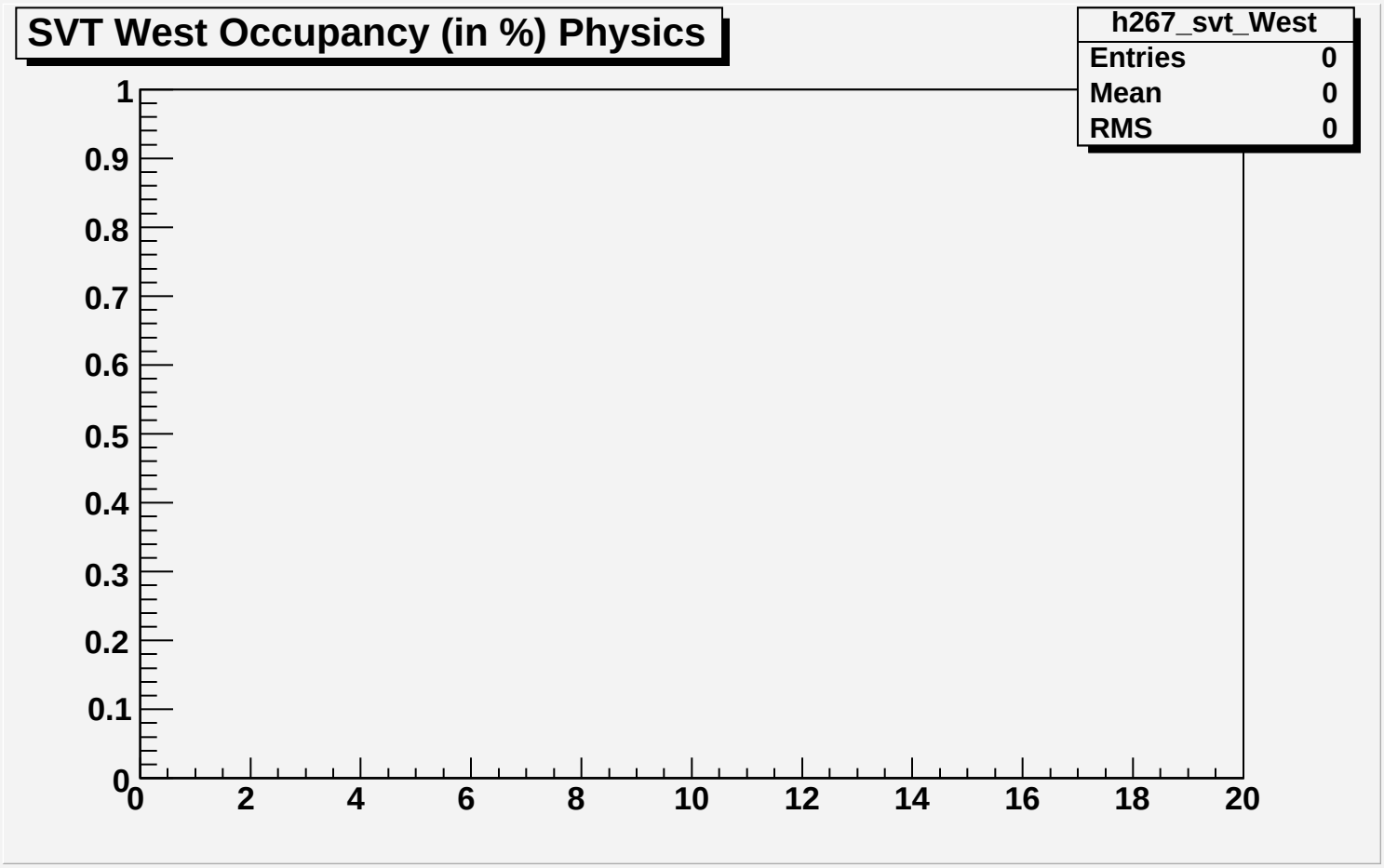

TPC adc vs time sector#23
h142_chargeStep_s23
Entries 0
Mean 0
RMS 0

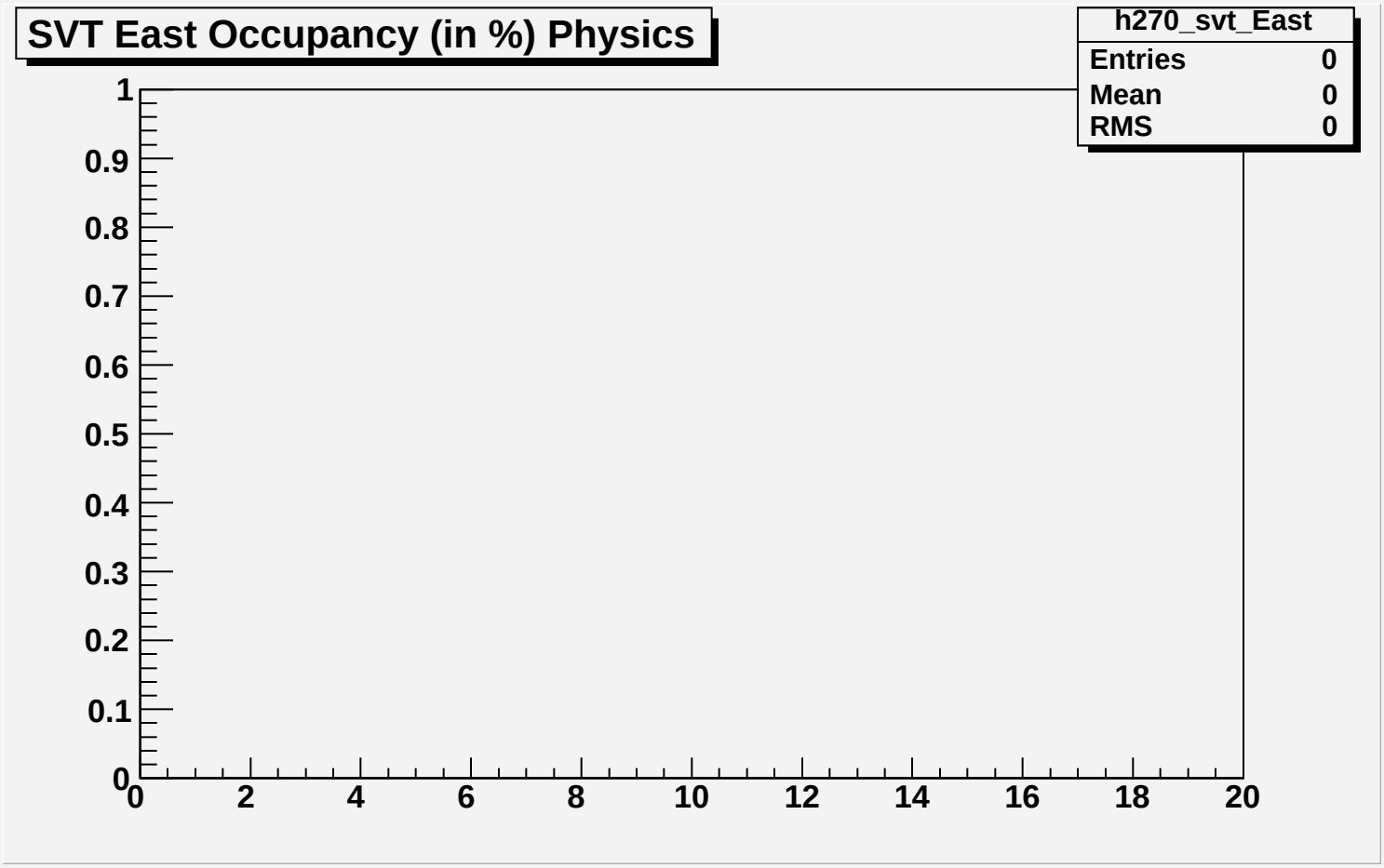

TPC adc vs time sector#24
h143_chargeStep_s24
Entries 0
Mean 0
RMS 0

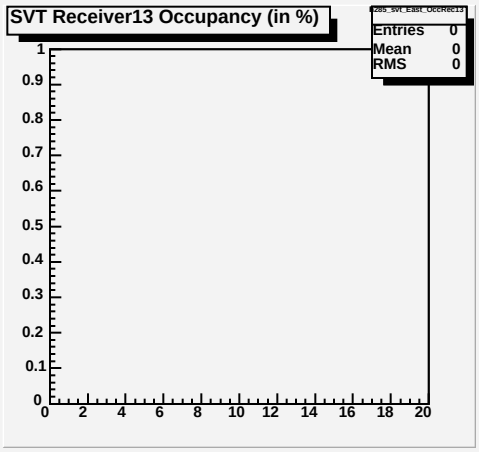

TPC Drift Velocity (cm/us)
h102_tpc_drift_vel
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
5.4
5.45
5.5
5.55
5.6
5.65
5.7
5.75
5.8

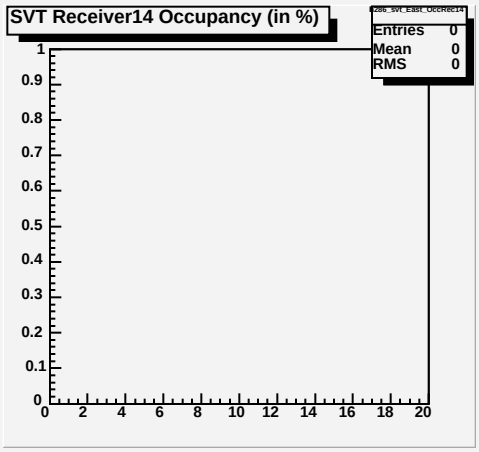

Azimuthal Distribution of TPC Charge
h66_tpc_phi_charge
Entries 2162160
Mean 18.86
RMS 103
×10
140
120
100
80
60
40
20
0
-150
-100
-50
0
50
100
150

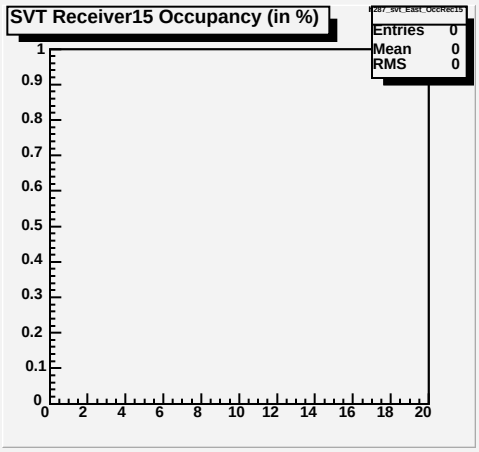

TPC Charge per Sector
h67_tpc_sector_charge
Entries 2162160
Mean 11.9
RMS 6.942
×10
1000
900
800
700
600
500
400
300
2
4
6
8
10
12
14
16
18
20
22
24

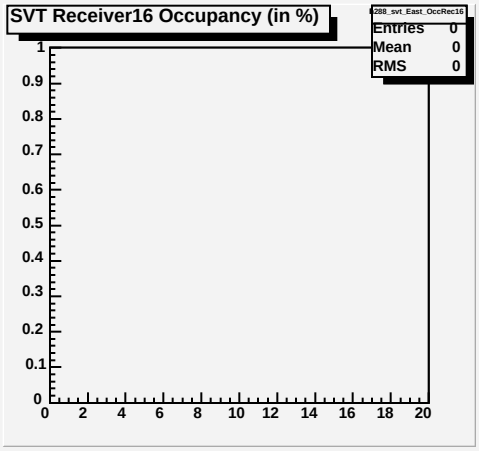

SVT West Occupancy (in %) Physics
h267_svt_West
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
0 2 4 6 8 10 12 14 16 18 20

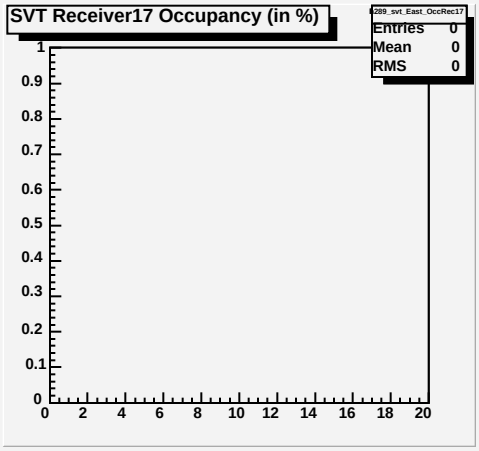

SVT East Occupancy (in %) Physics
h270_svt_East
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
0 2 4 6 8 10 12 14 16 18 20

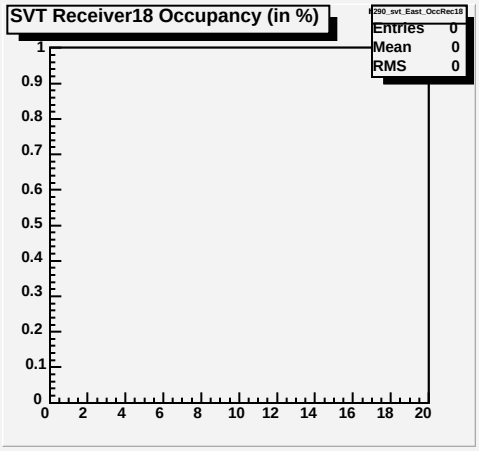

SVT Receiver13 Occupancy (in %)
h285_svt_East_OccRec13
Entries 0
Mean 0
RMS 0

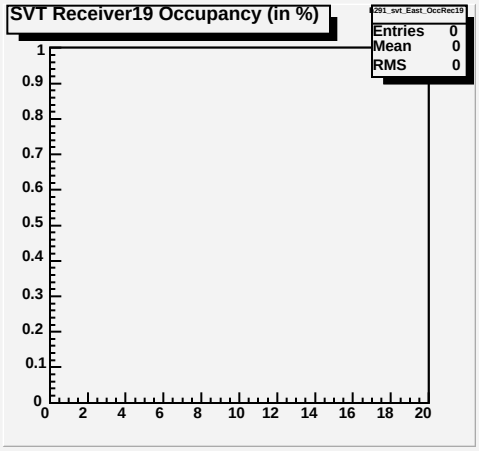

SVT Receiver14 Occupancy (in %)
h286_svt_East_OccRec14
Entries 0
Mean 0
RMS 0

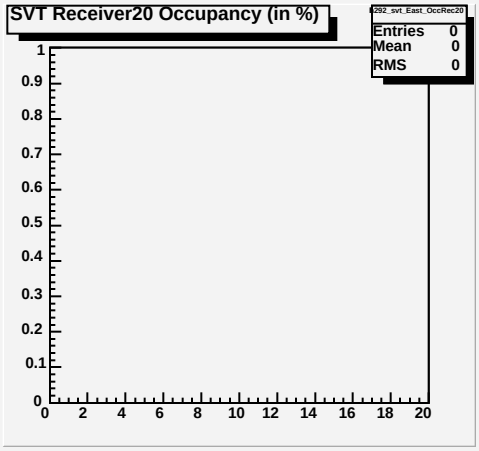

SVT Receiver15 Occupancy (in %)
h287_svt_East_OccRec15
Entries 0
Mean 0
RMS 0

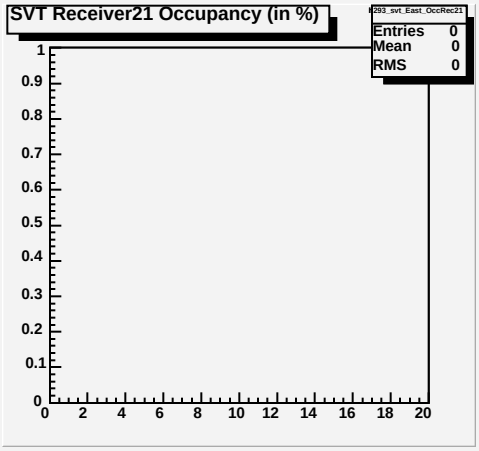

SVT Receiver16 Occupancy (in %)
h288_svt_East_OccRec16
Entries 0
Mean 0
RMS 0

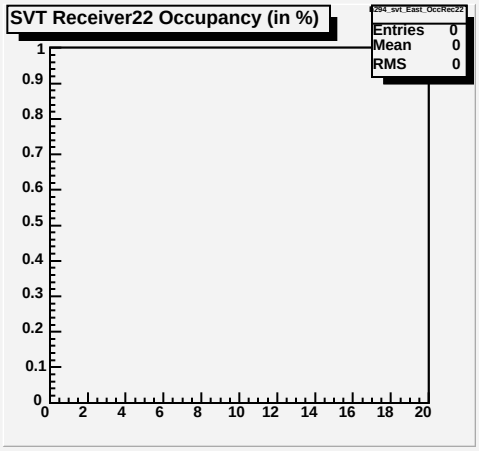

SVT Receiver17 Occupancy (in %)
h289_svt_East_OccRec17
Entries 0
Mean 0
RMS 0

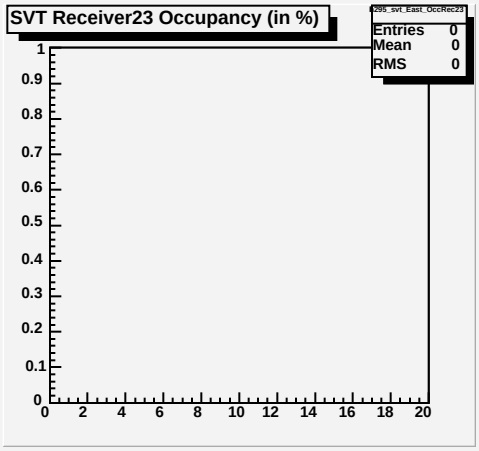

SVT Receiver18 Occupancy (in %)
h290_svt_East_OccRec18
Entries 0
Mean 0
RMS 0

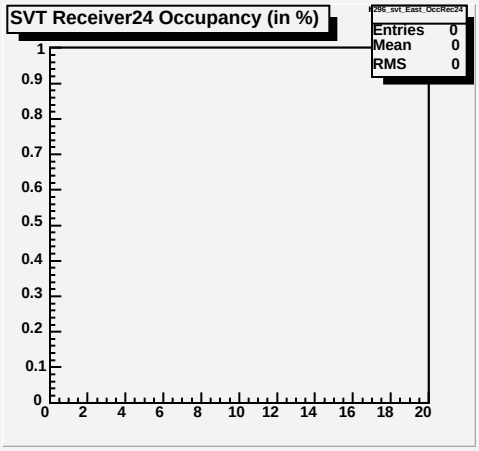

SVT Receiver19 Occupancy (in %)
h291_svt_East_OccRec19
Entries 0
Mean 0
RMS 0

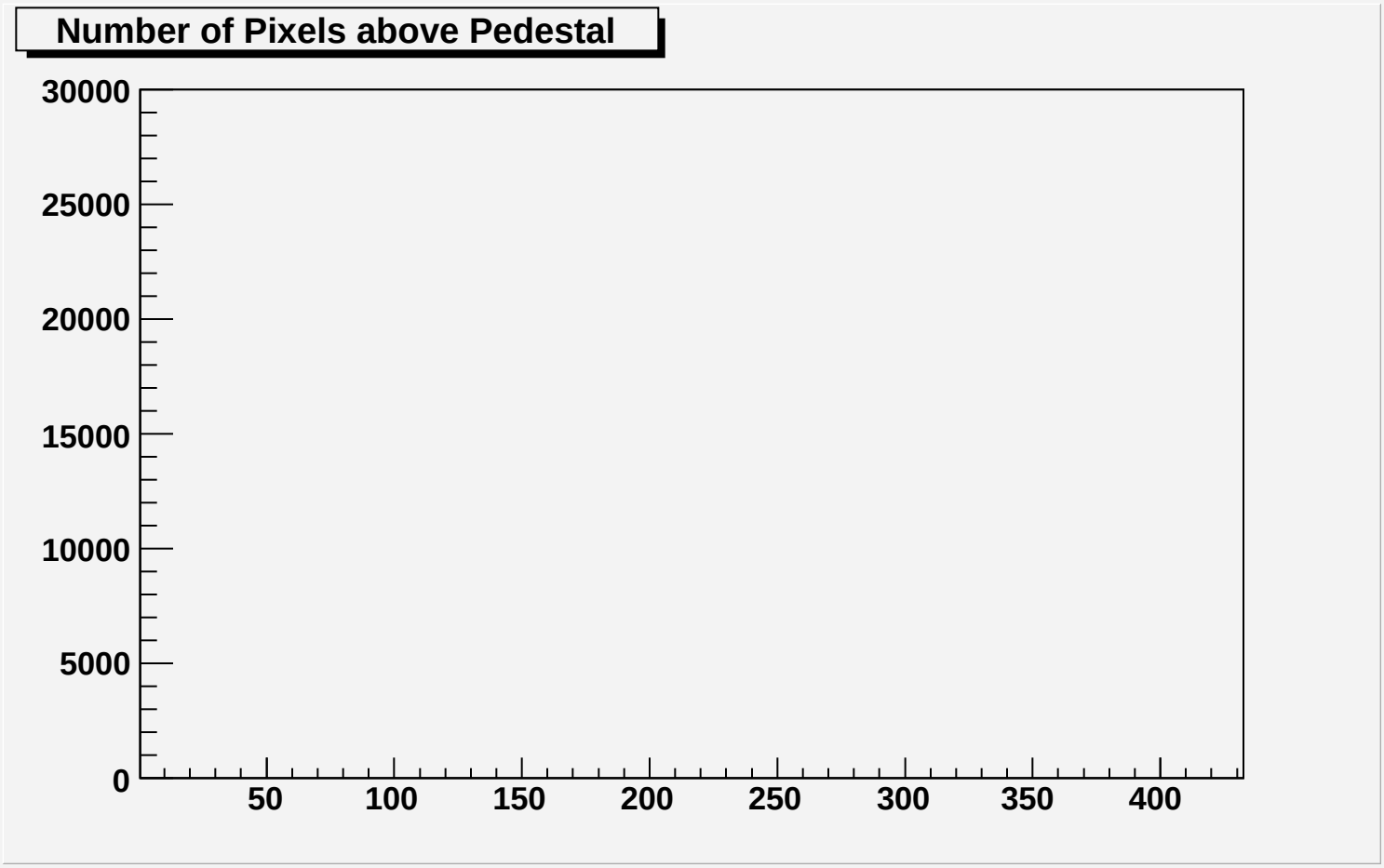

SVT Receiver20 Occupancy (in %)
h292_svt_East_OccRec20
Entries 0
Mean 0
RMS 0

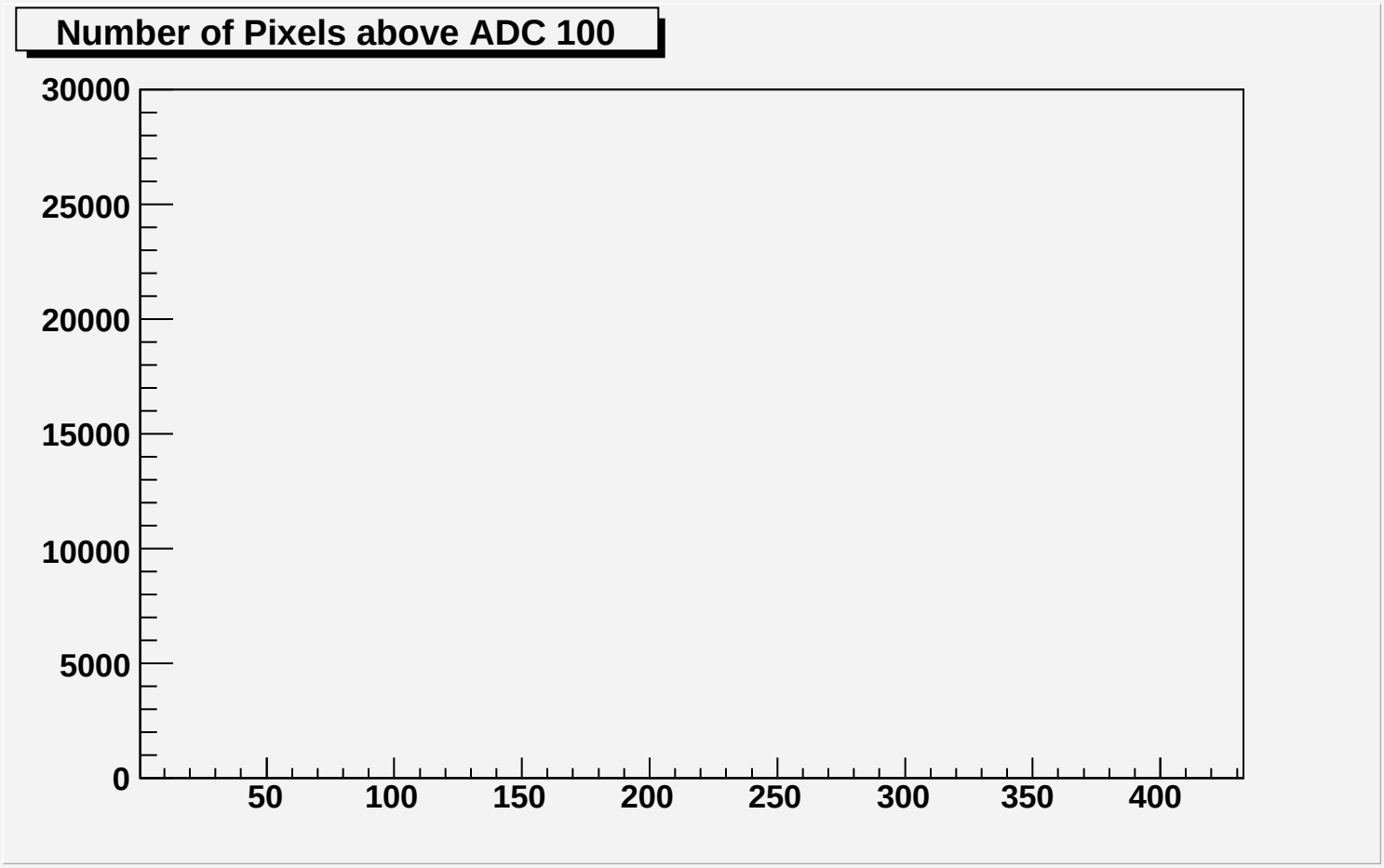

SVT Receiver21 Occupancy (in %)
h293_svt_East_OccRec21
Entries 0
Mean 0
RMS 0

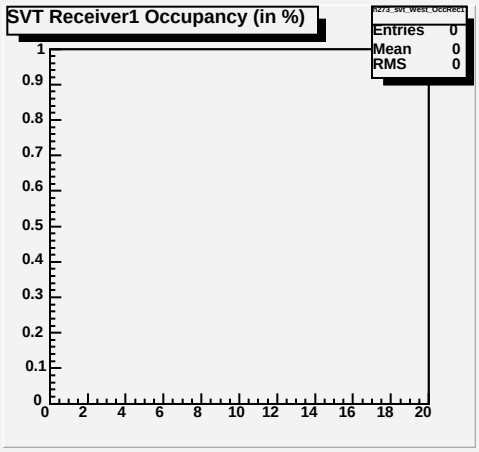

SVT Receiver22 Occupancy (in %)
h294_svt_East_OccRec22
Entries 0
Mean 0
RMS 0

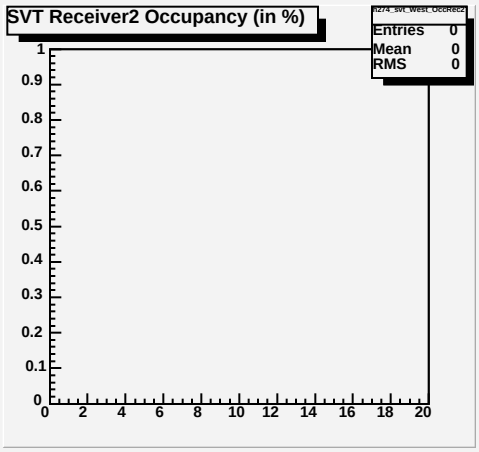

SVT Receiver23 Occupancy (in %)
h295_svt_East_OccRec23
Entries 0
Mean 0
RMS 0

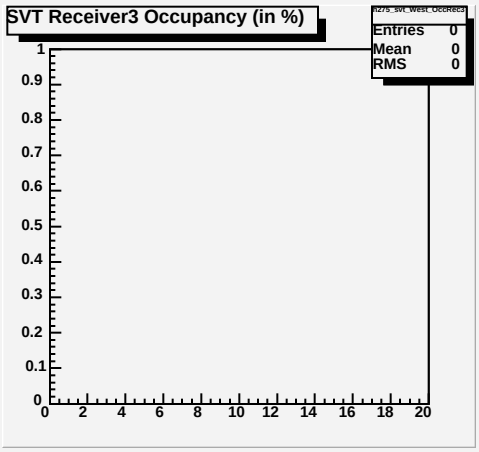

SVT Receiver24 Occupancy (in %)
h296_svt_East_OccRec24
Entries 0
Mean 0
RMS 0

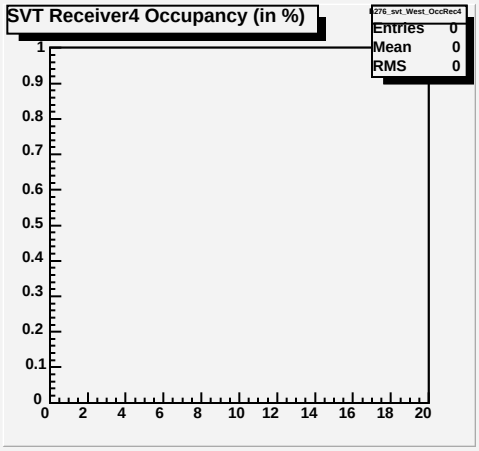

Number of Pixels above Pedestal
30000
25000
20000
15000
10000
5000
0
50
100
150
200
250
300
350
400

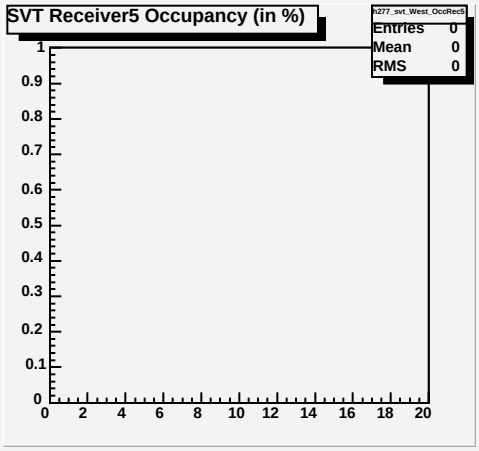

Number of Pixels above ADC 100
30000
25000
20000
15000
10000
5000
0
50
100
150
200
250
300
350
400

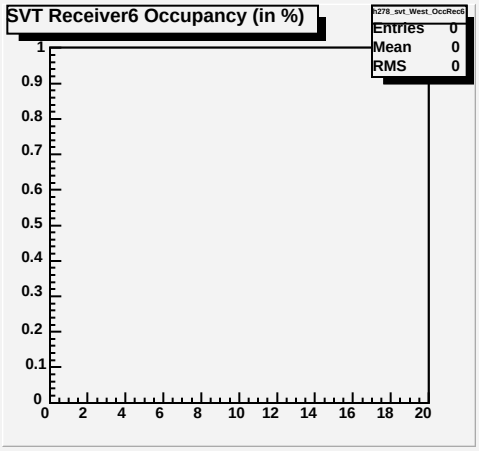

SVT Receiver1 Occupancy (in %)
h273_svt_West_OccRec1
Entries 0
Mean 0
RMS 0

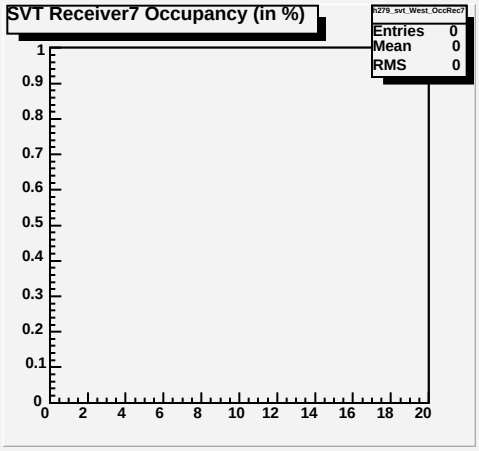

SVT Receiver2 Occupancy (in %)
h274_svt_West_OccRec2
Entries 0
Mean 0
RMS 0

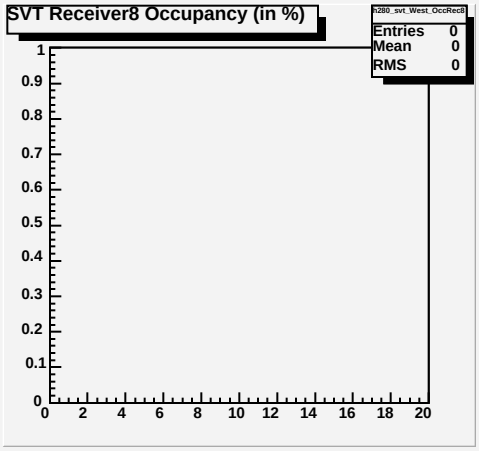

SVT Receiver3 Occupancy (in %)
h275_svt_West_OccRec3
Entries 0
Mean 0
RMS 0

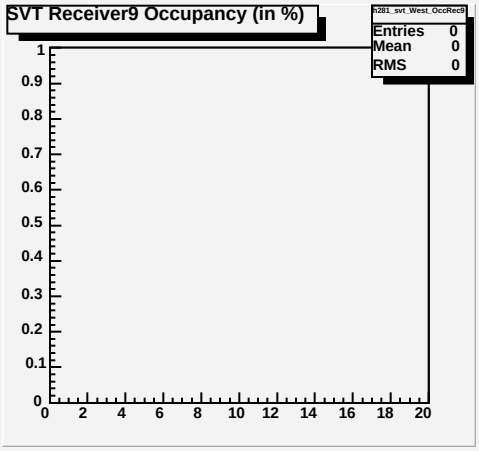

SVT Receiver4 Occupancy (in %)
h276_svt_West_OccRec4
Entries 0
Mean 0
RMS 0

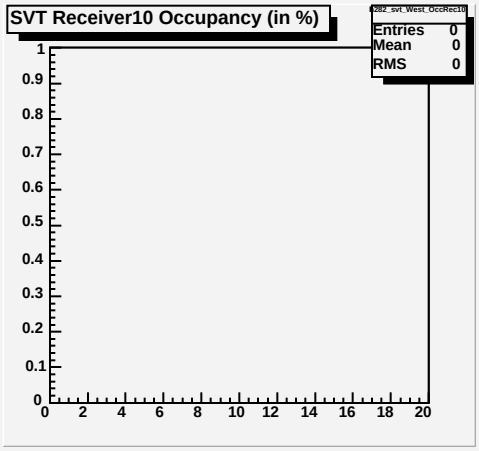

SVT Receiver5 Occupancy (in %)
h277_svt_West_OccRec5
Entries 0
Mean 0
RMS 0

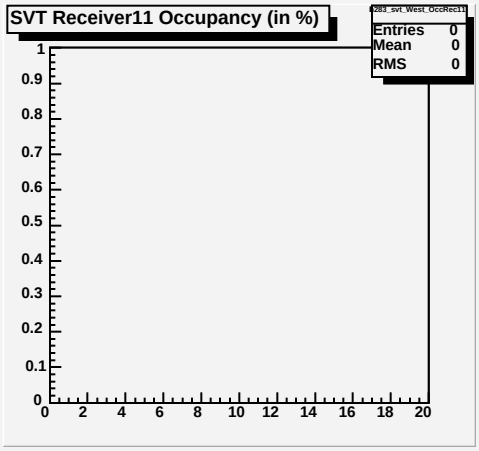

SVT Receiver6 Occupancy (in %)
h278_svt_West_OccRec6
Entries 0
Mean 0
RMS 0

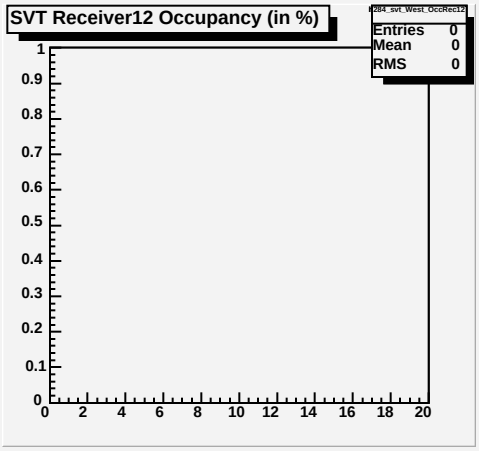

SVT Receiver7 Occupancy (in %)
h279_svt_West_OccRec7
Entries 0
Mean 0
RMS 0

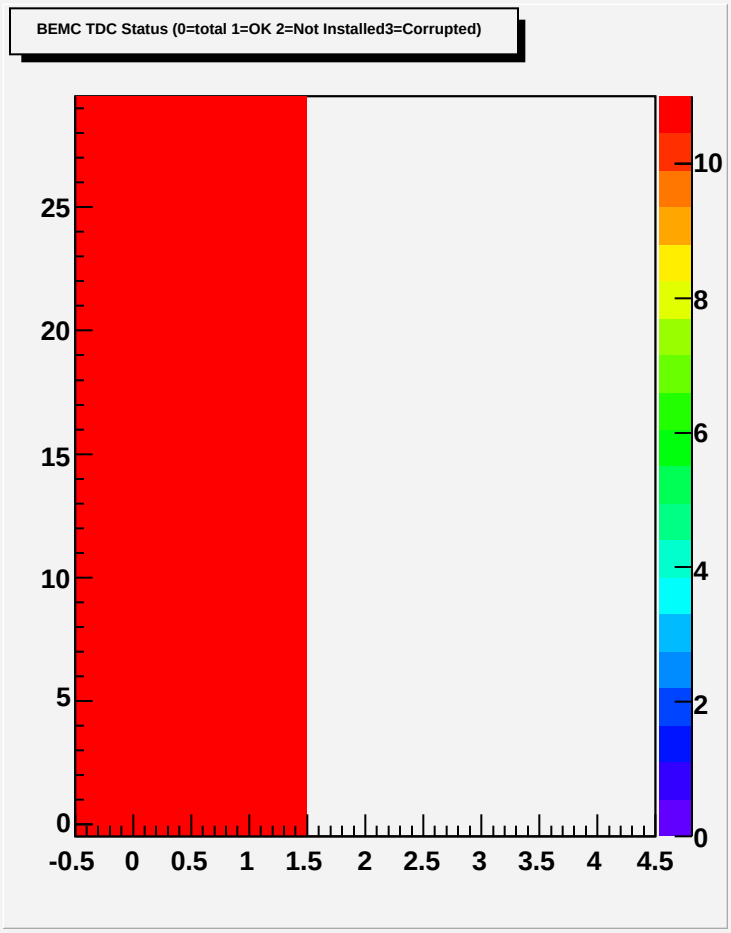

SVT Receiver8 Occupancy (in %)
h280_svt_West_OccRec8
Entries 0
Mean 0
RMS 0

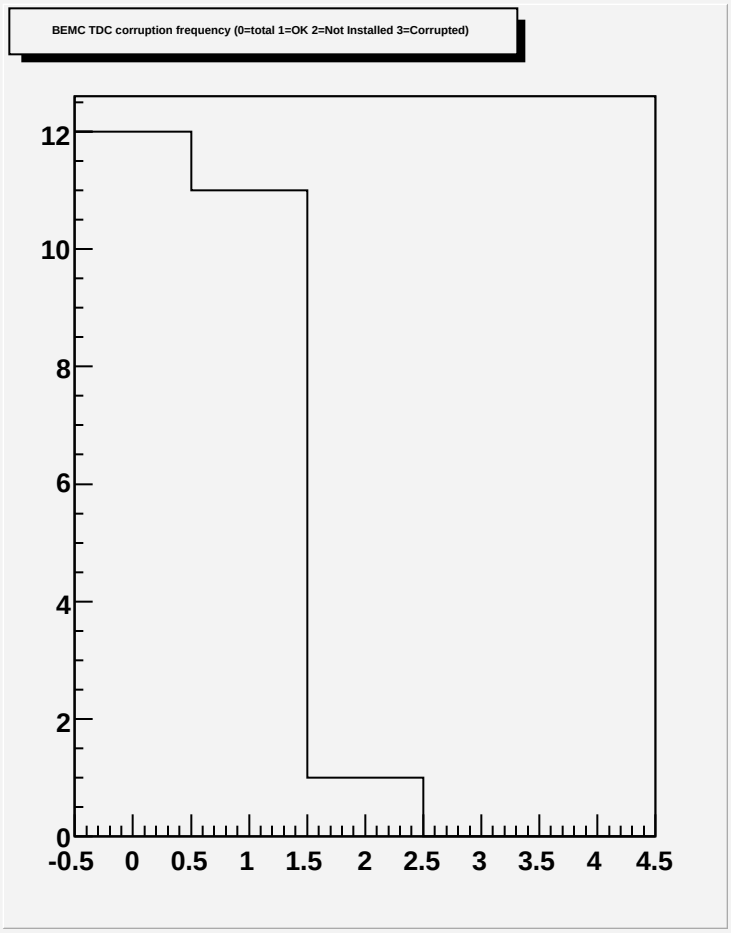

SVT Receiver9 Occupancy (in %)
h281_svt_West_OccRec9
Entries 0
Mean 0
RMS 0

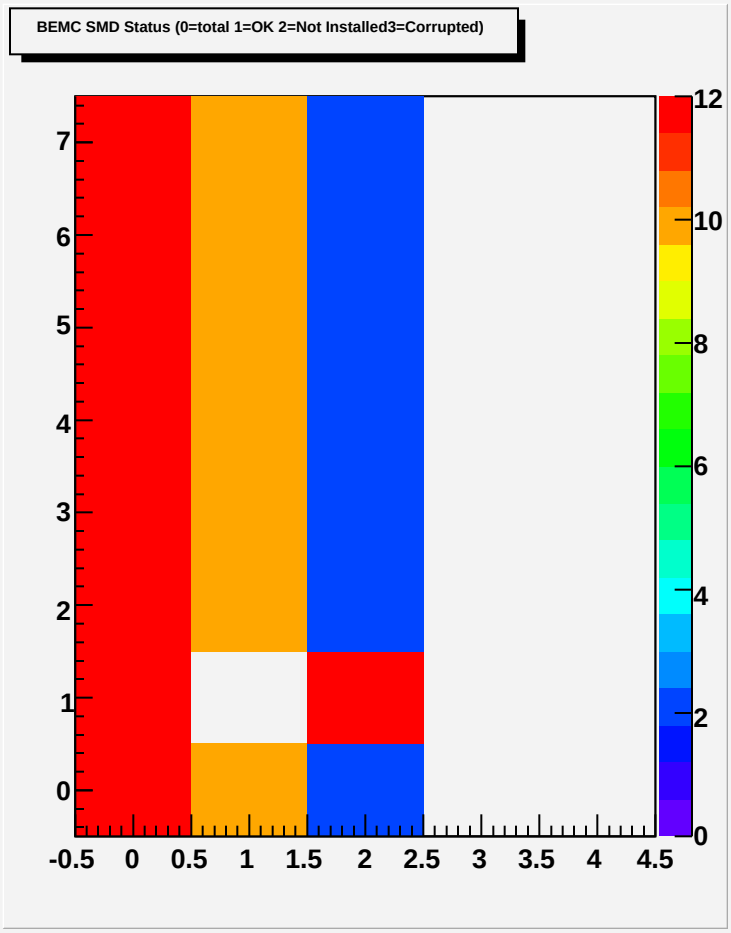

SVT Receiver10 Occupancy (in %)
h282_svt_West_OccRec10
Entries 0
Mean 0
RMS 0

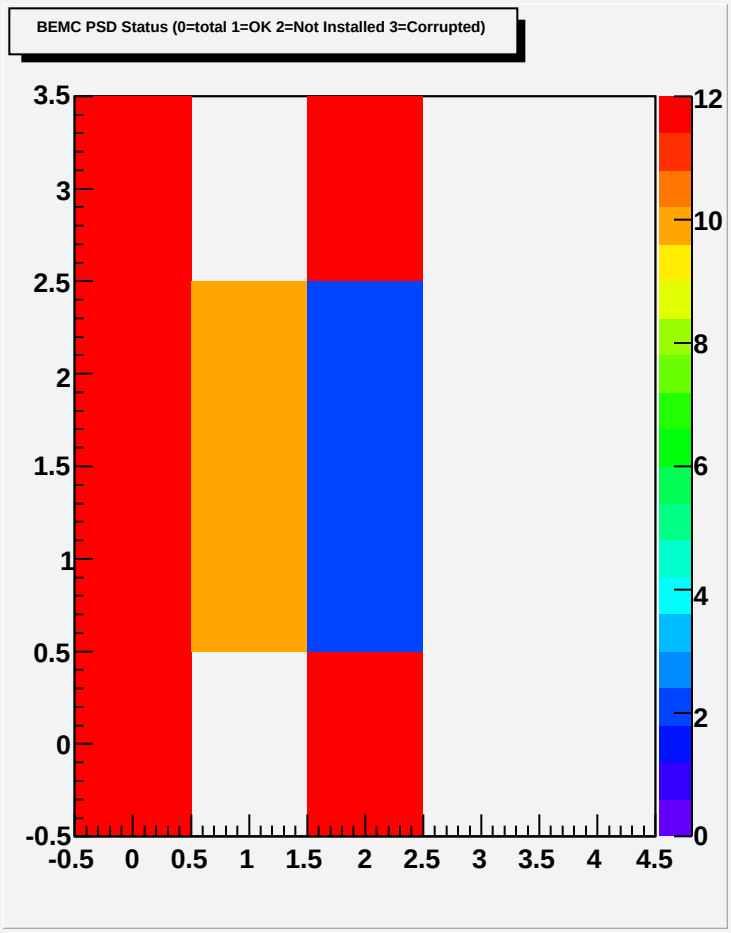

SVT Receiver11 Occupancy (in %)
h283_svt_West_OccRec11
Entries 0
Mean 0
RMS 0

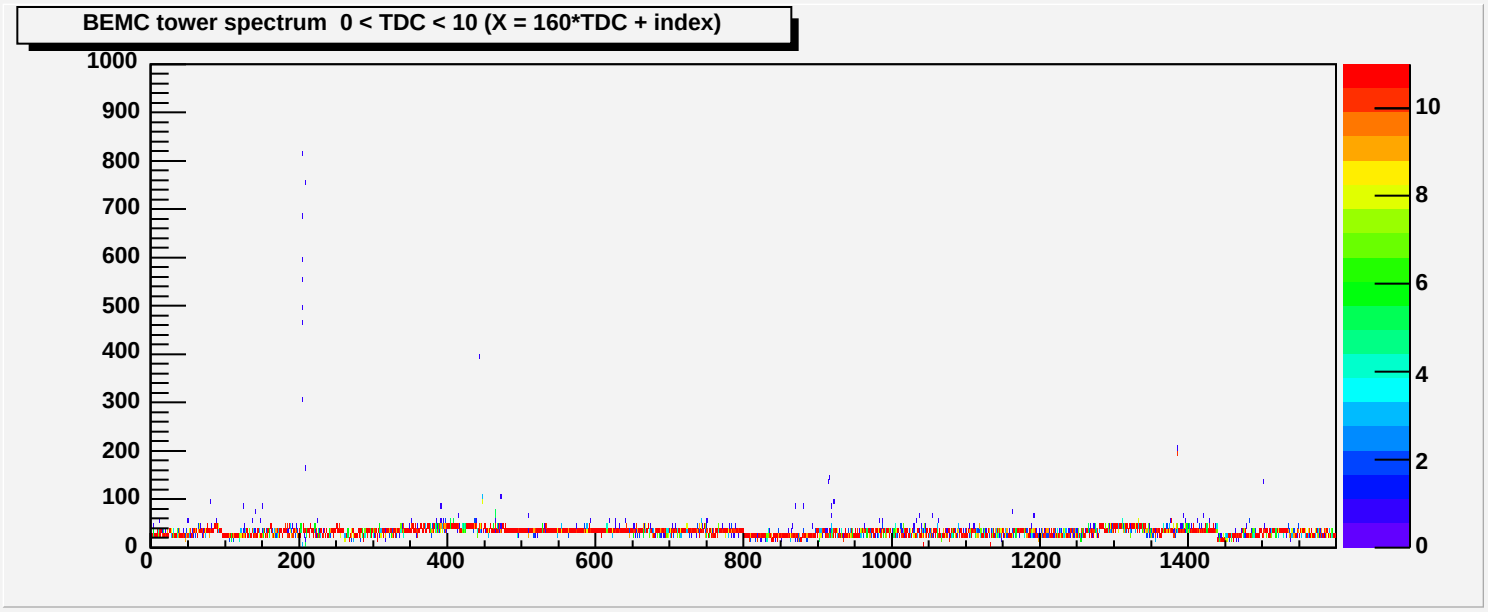

SVT Receiver12 Occupancy (in %)
h284_svt_West_OccRec12
Entries 0
Mean 0
RMS 0

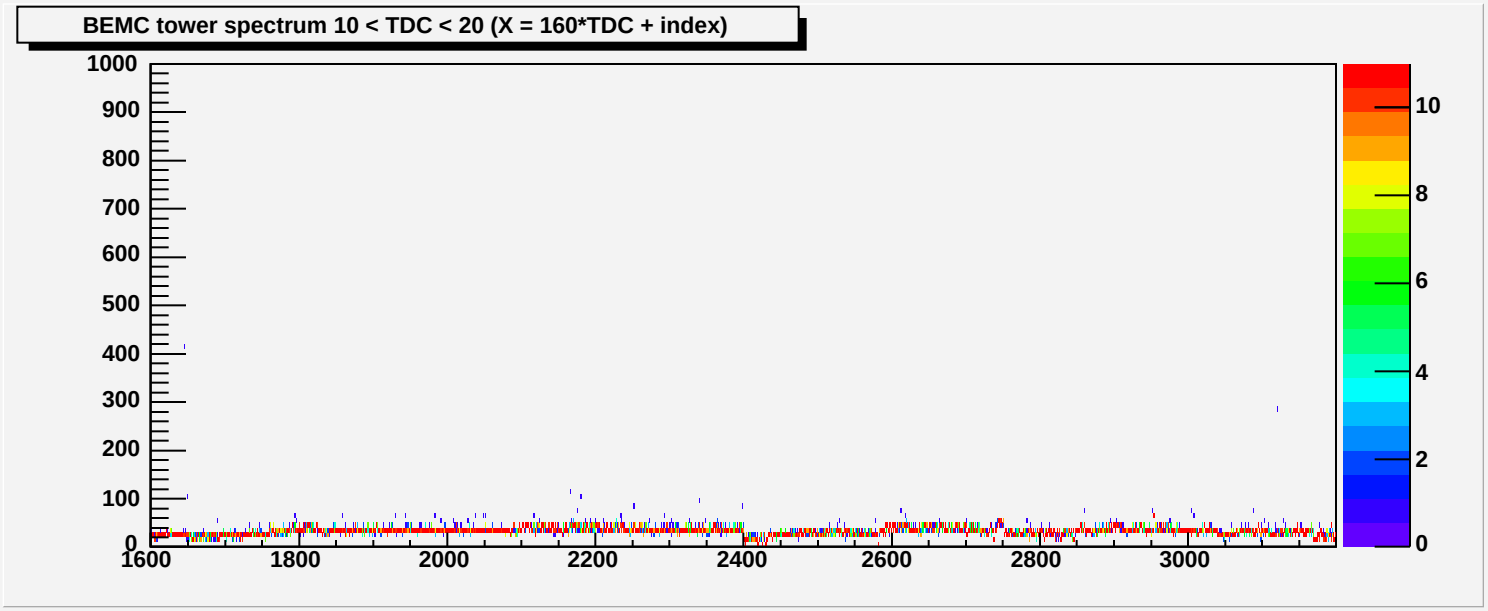

BEMC TDC Status (0=total 1=OK 2=Not Installed3=Corrupted)

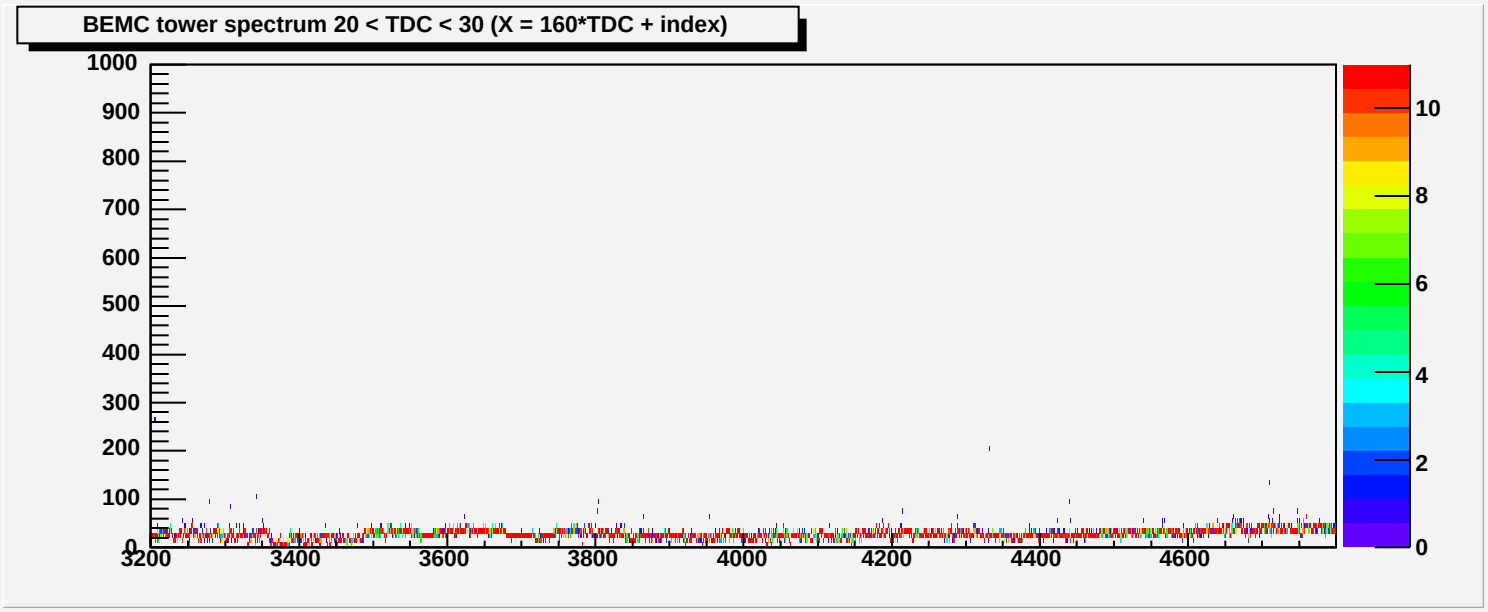

BEMC TDC corruption frequency (0=total 1=OK 2=Not Installed 3=Corrupted)

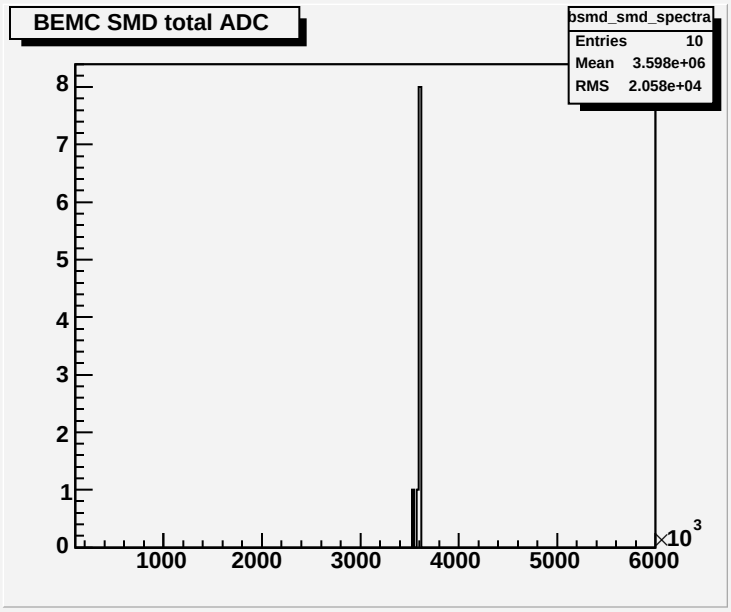

BEMC SMD Status (0=total 1=OK 2=Not Installed3=Corrupted)

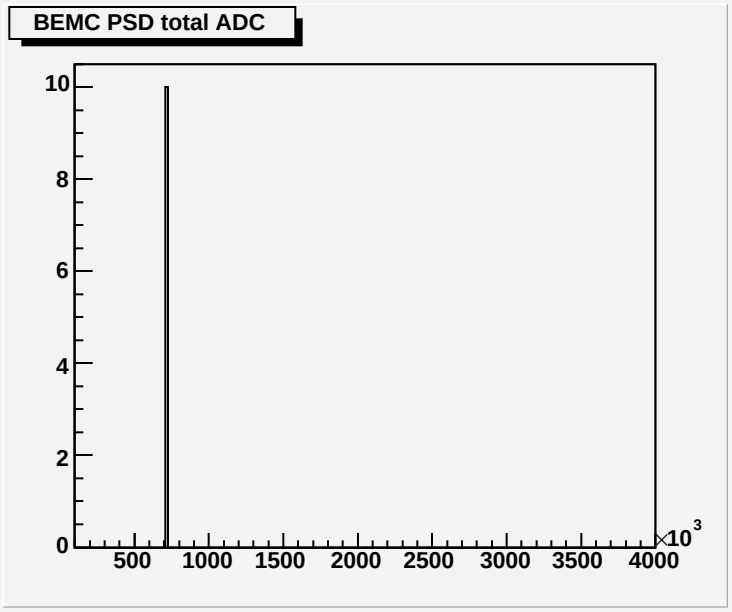

BEMC PSD Status (0=total 1=OK 2=Not Installed 3=Corrupted)

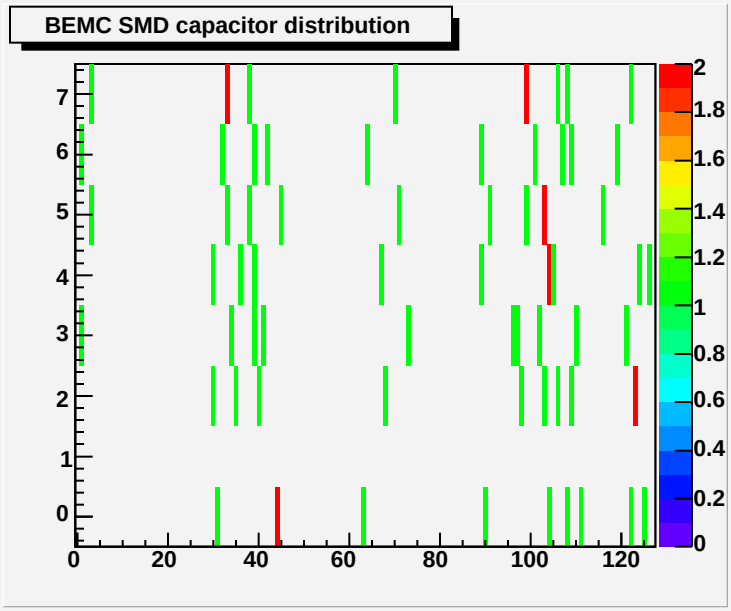

BEMC tower spectrum 0 < TDC < 10 (X = 160*TDC + index)
1000
900
800
700
600
500
400
300
200
100
0
0
200
400
600
800
1000
1200
1400
10
8
6
4
2
0

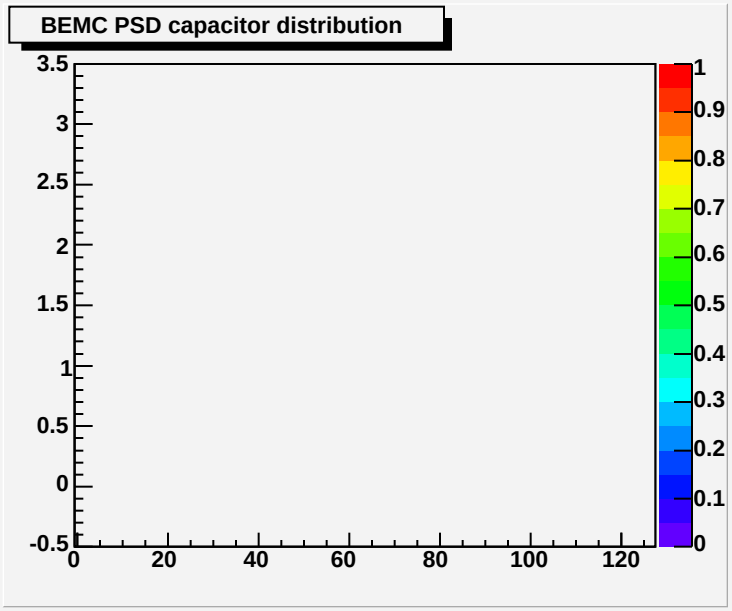

BEMC tower spectrum 10 < TDC < 20 (X = 160*TDC + index)
1000
900
800
700
600
500
400
300
200
100
0
1600
1800
2000
2200
2400
2600
2800
3000
10
8
6
4
2
0

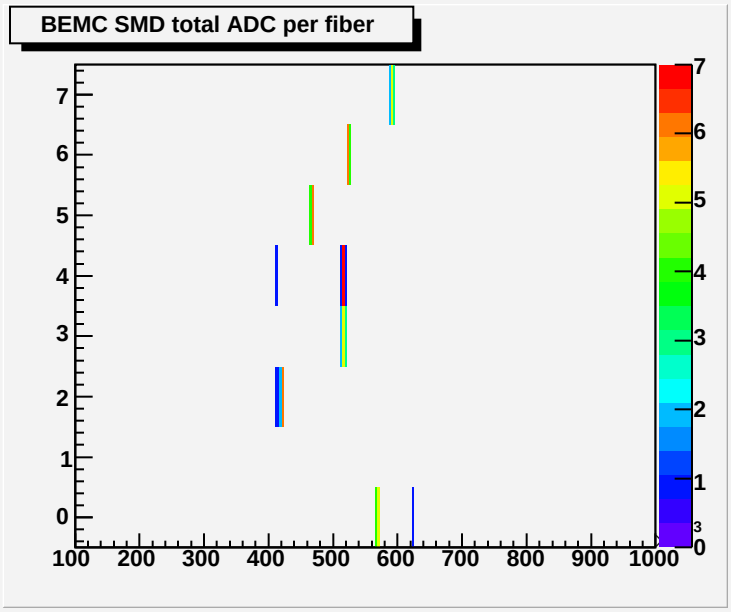

BEMC tower spectrum 20 < TDC < 30 (X = 160*TDC + index)
1000
900
800
700
600
500
400
300
200
100
0
3200
3400
3600
3800
4000
4200
4400
4600
10
8
6
4
2
0

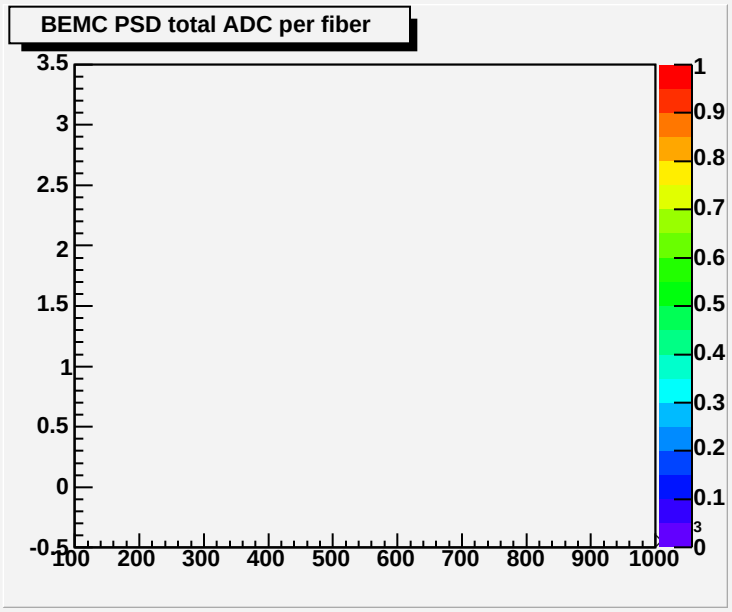

BEMC SMD total ADC
bsmd_smd_spectra
Entries 10
Mean 3.598e+06
RMS 2.058e+04
×10^3

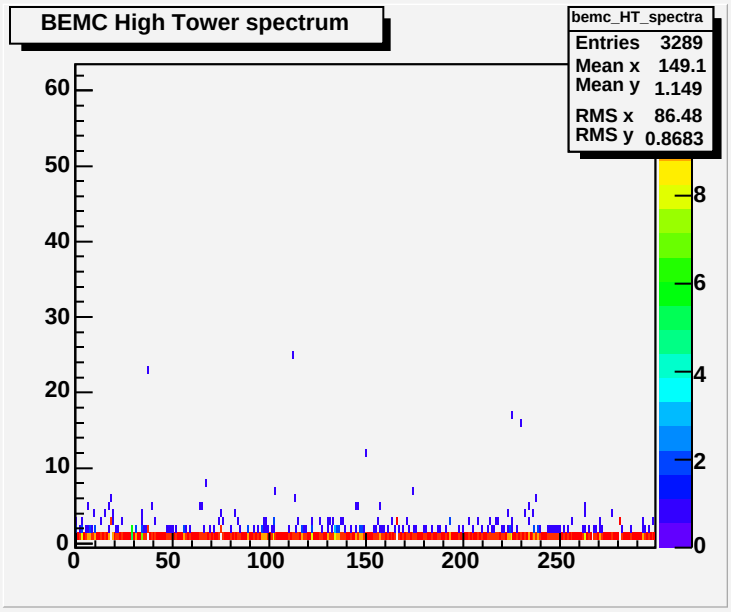

BEMC PSD total ADC
×10^3

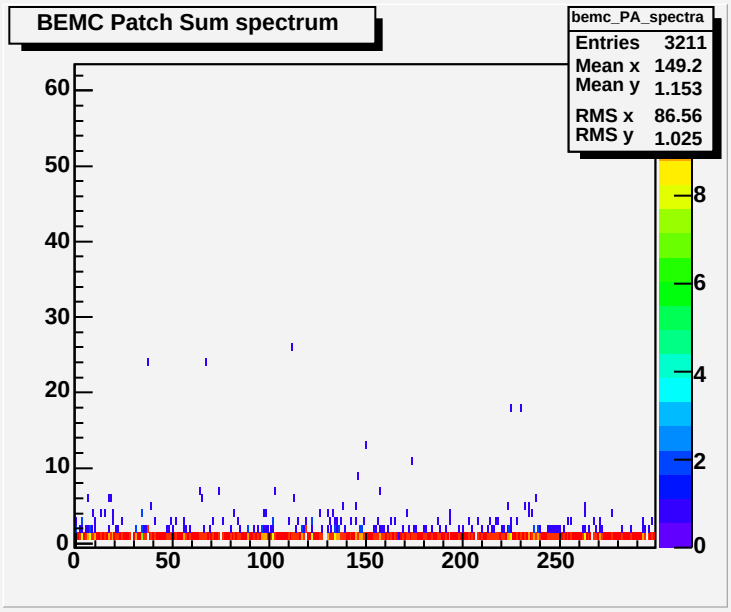

BEMC SMD capacitor distribution

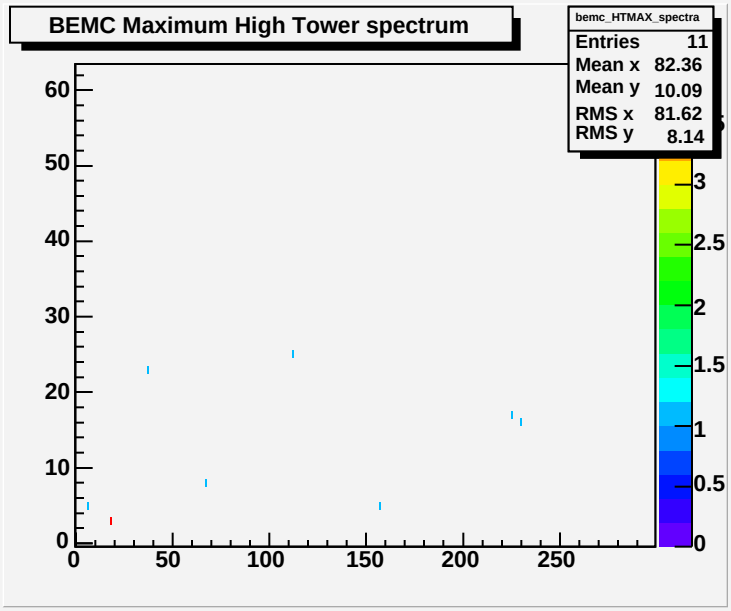

BEMC PSD capacitor distribution

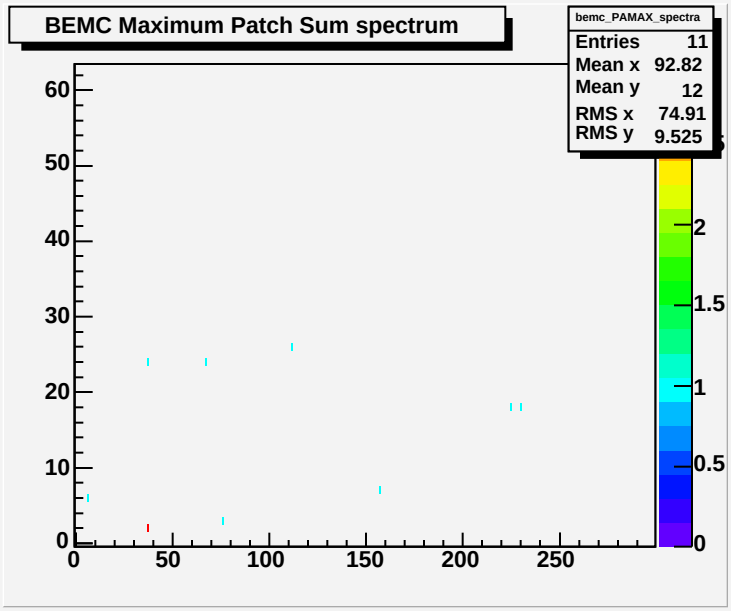

BEMC SMD total ADC per fiber

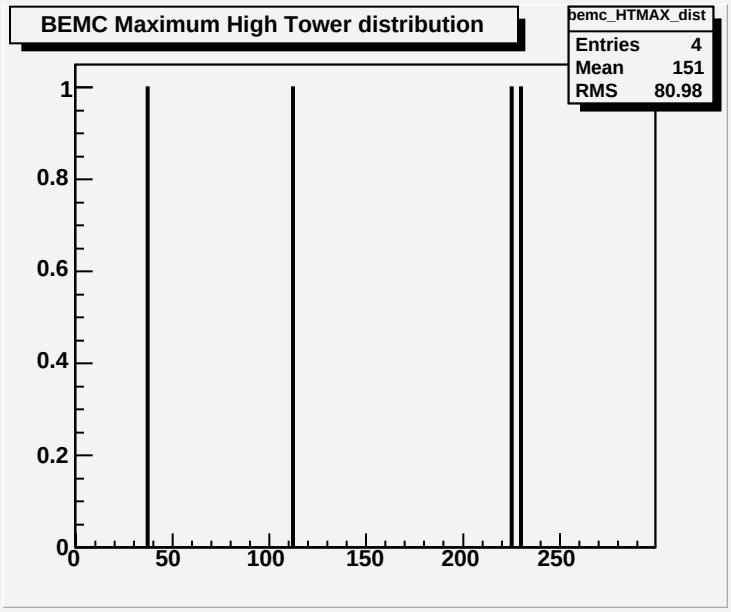

BEMC PSD total ADC per fiber

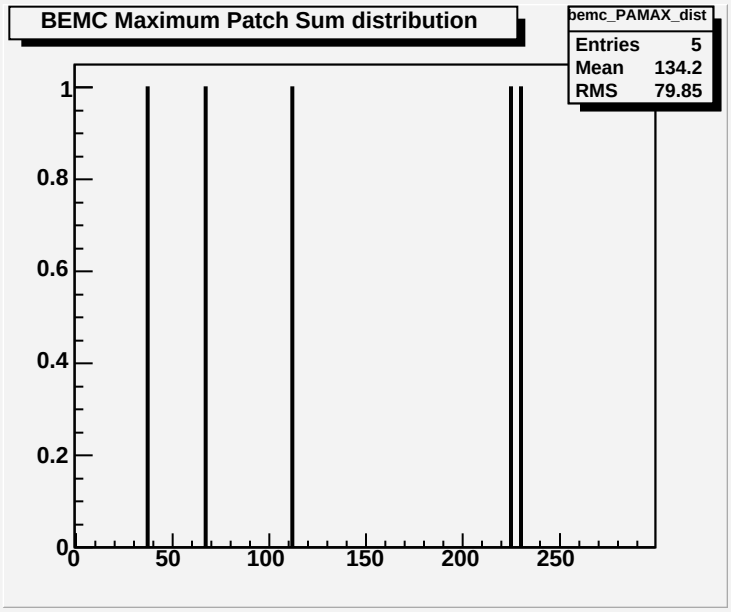

BEMC High Tower spectrum
bemc_HT_spectra
Entries 3289
Mean x 149.1
Mean y 1.149
RMS x 86.48
RMS y 0.8683

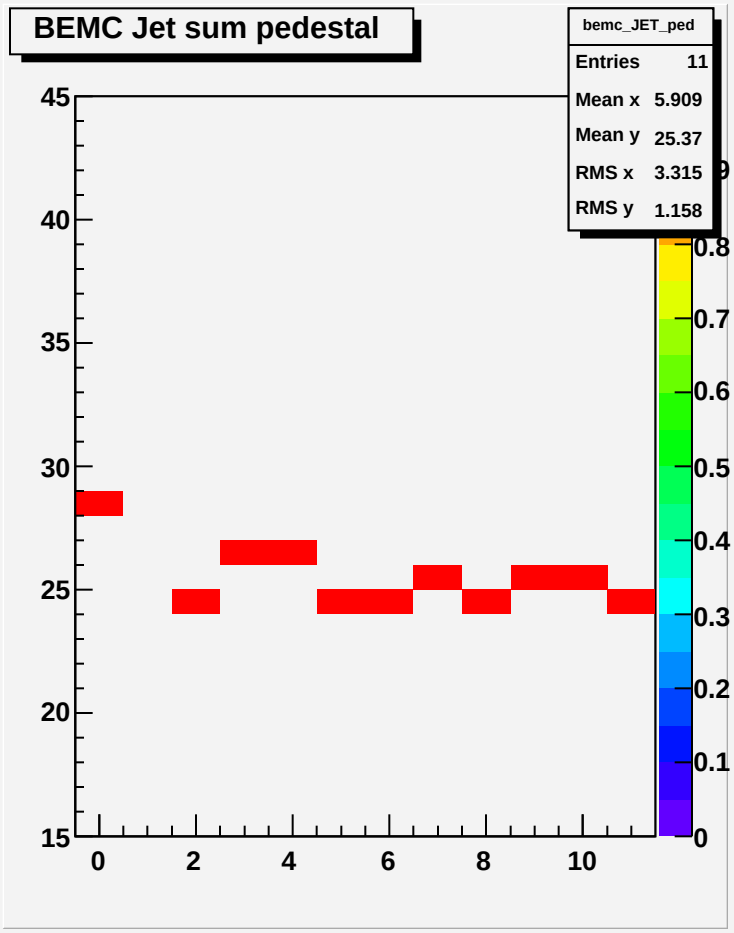

BEMC Patch Sum spectrum
bemc_PA_spectra
Entries 3211
Mean x 149.2
Mean y 1.153
RMS x 86.56
RMS y 1.025

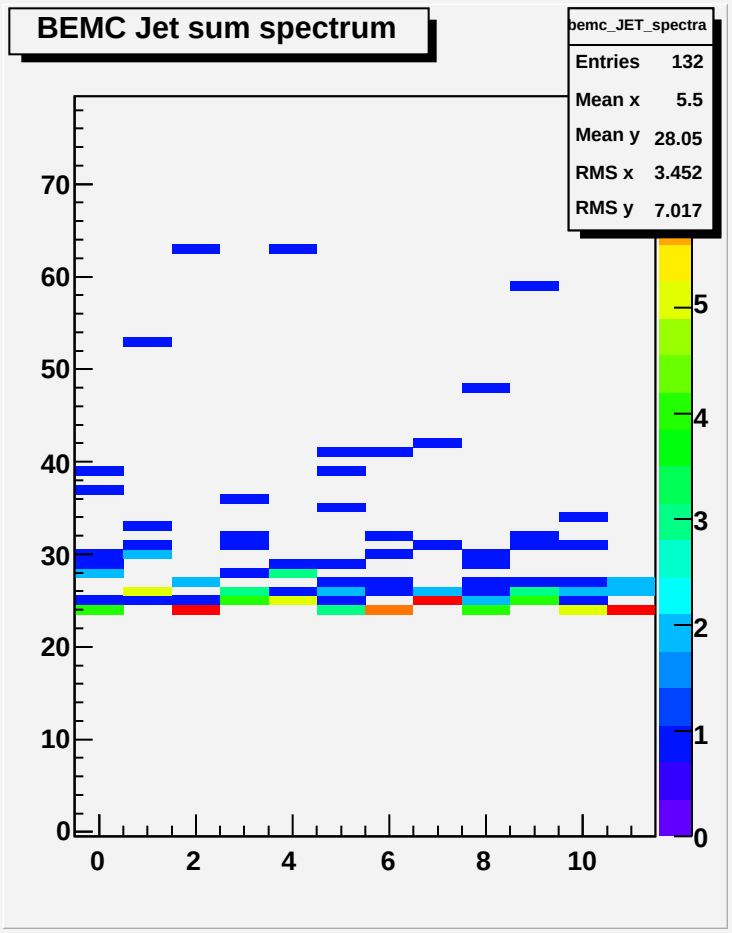

BEMC Maximum High Tower spectrum
bemc_HTMAX_spectra
Entries 11
Mean x 82.36
Mean y 10.09
RMS x 81.62
RMS y 8.14

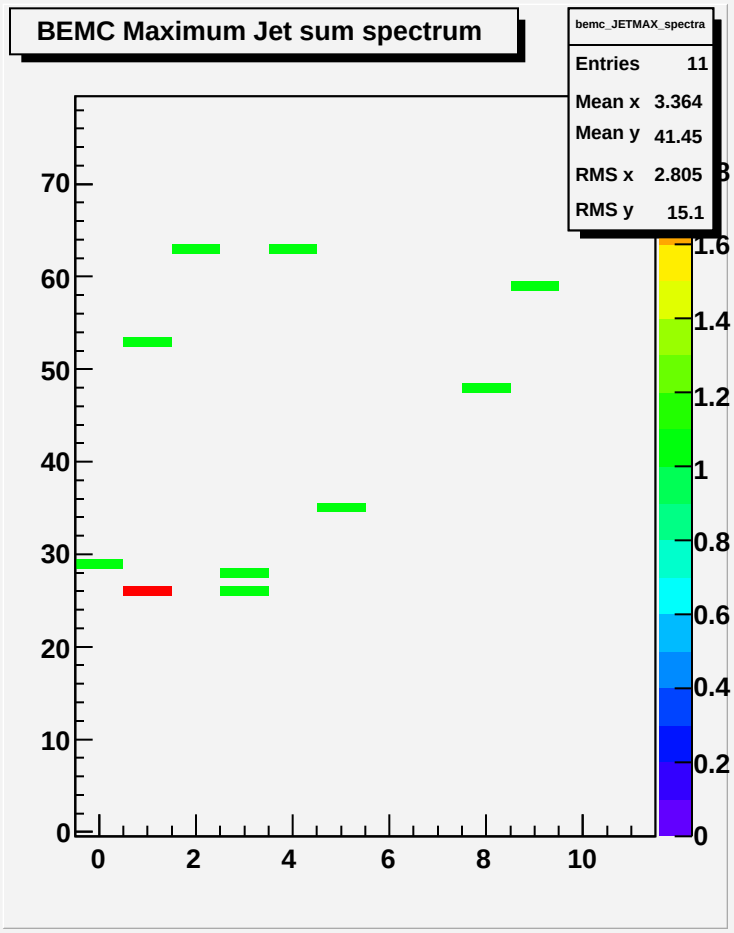

BEMC Maximum Patch Sum spectrum
bemc_PAMAX_spectra
Entries 11
Mean x 92.82
Mean y 12
RMS x 74.91
RMS y 9.525

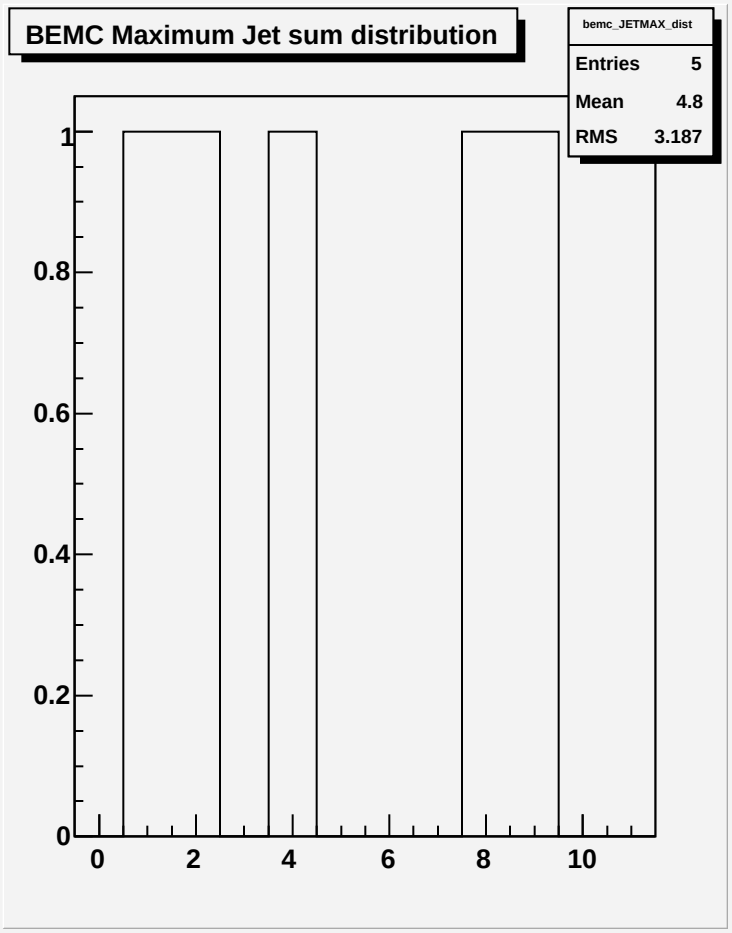

BEMC Maximum High Tower distribution
bemc_HTMAX_dist
Entries 4
Mean 151
RMS 80.98

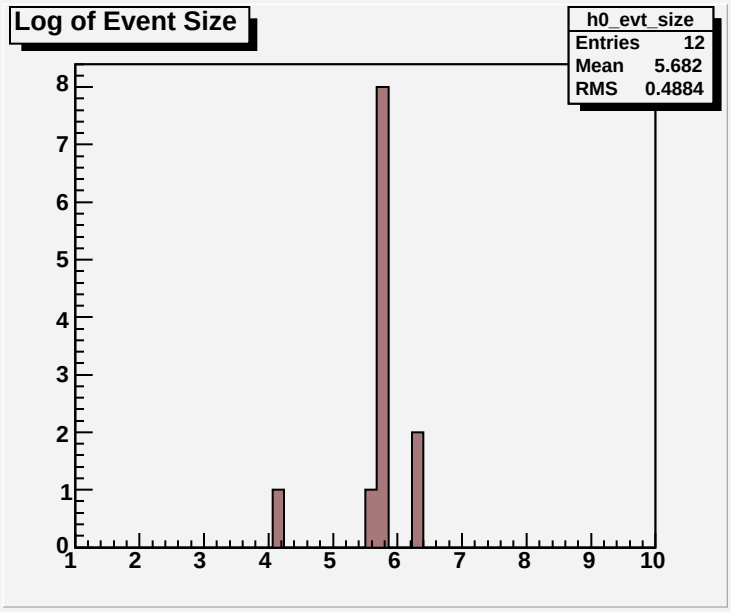

BEMC Maximum Patch Sum distribution
bemc_PAMAX_dist
Entries 5
Mean 134.2
RMS 79.85

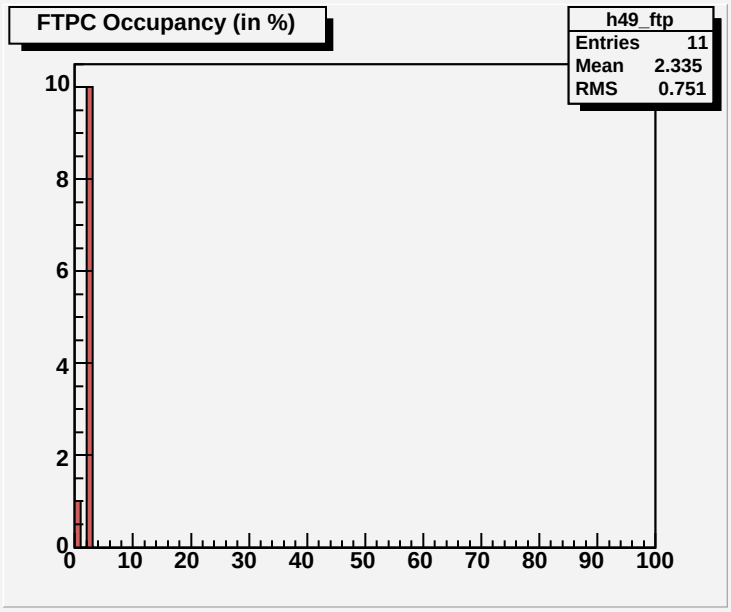

BEMC Jet sum pedestal
bemc_JET_ped
Entries 11
Mean x 5.909
Mean y 25.37
RMS x 3.315
RMS y 1.158

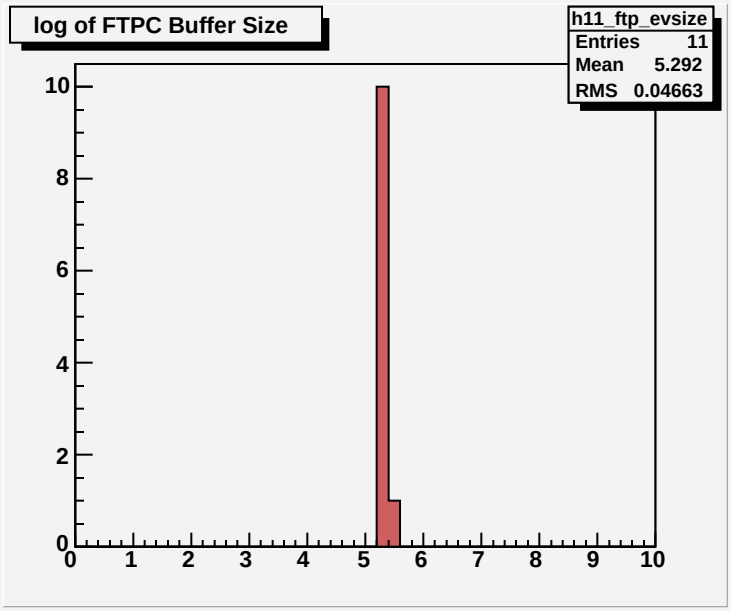

BEMC Jet sum spectrum
bemc_JET_spectra
Entries 132
Mean x 5.5
Mean y 28.05
RMS x 3.452
RMS y 7.017

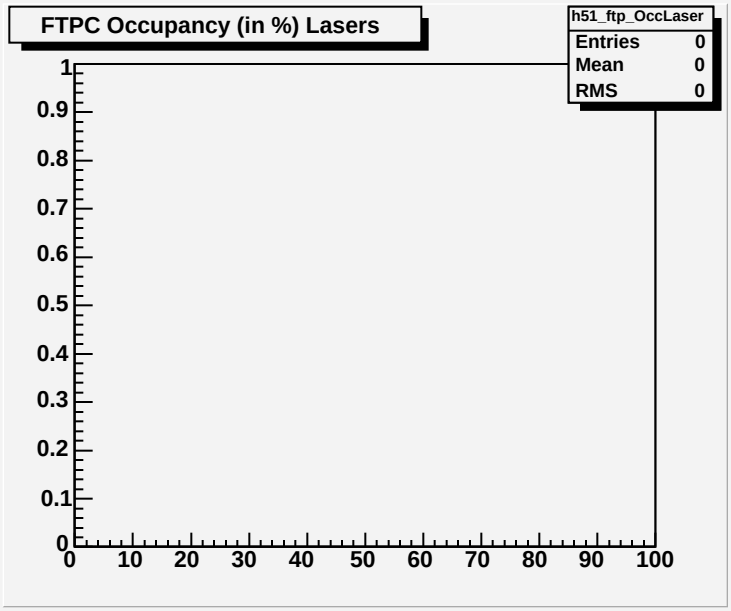

BEMC Maximum Jet sum spectrum
bemc_JETMAX_spectra
Entries 11
Mean x 3.364
Mean y 41.45
RMS x 2.805
RMS y 15.1

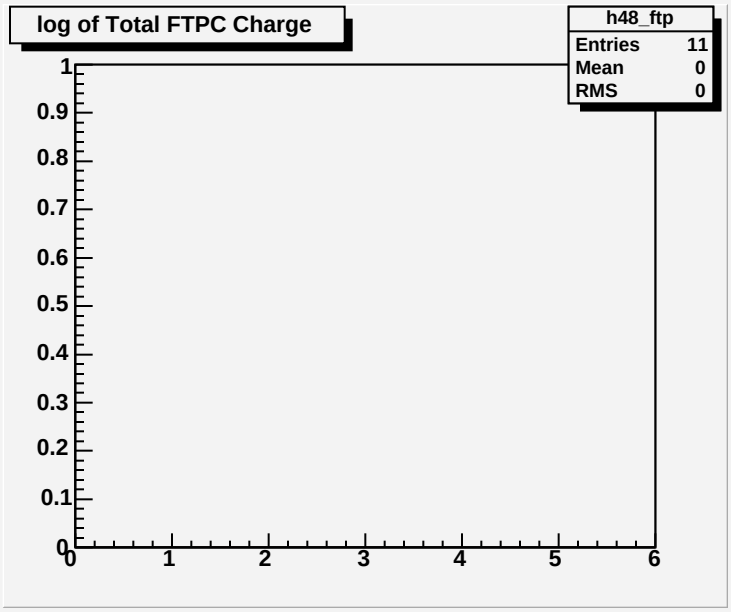

BEMC Maximum Jet sum distribution
bemc_JETMAX_dist
Entries 5
Mean 4.8
RMS 3.187

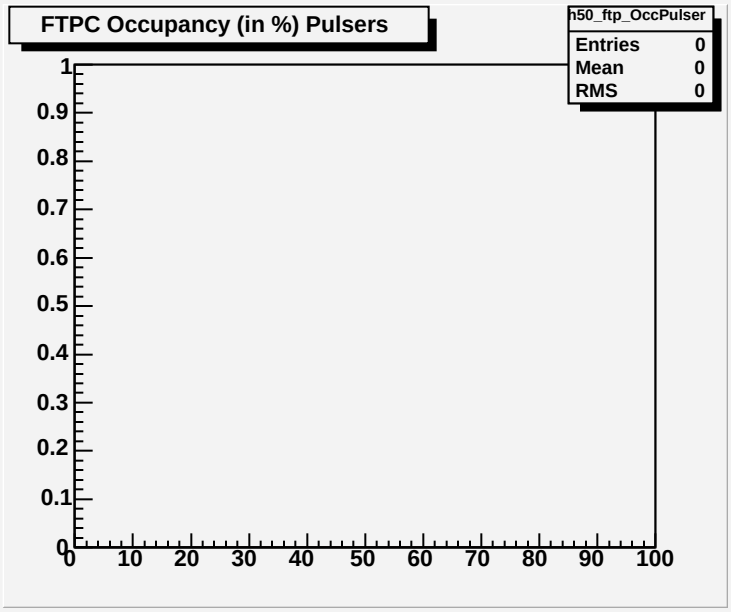

Log of Event Size
h0_evt_size
Entries 12
Mean 5.682
RMS 0.4884

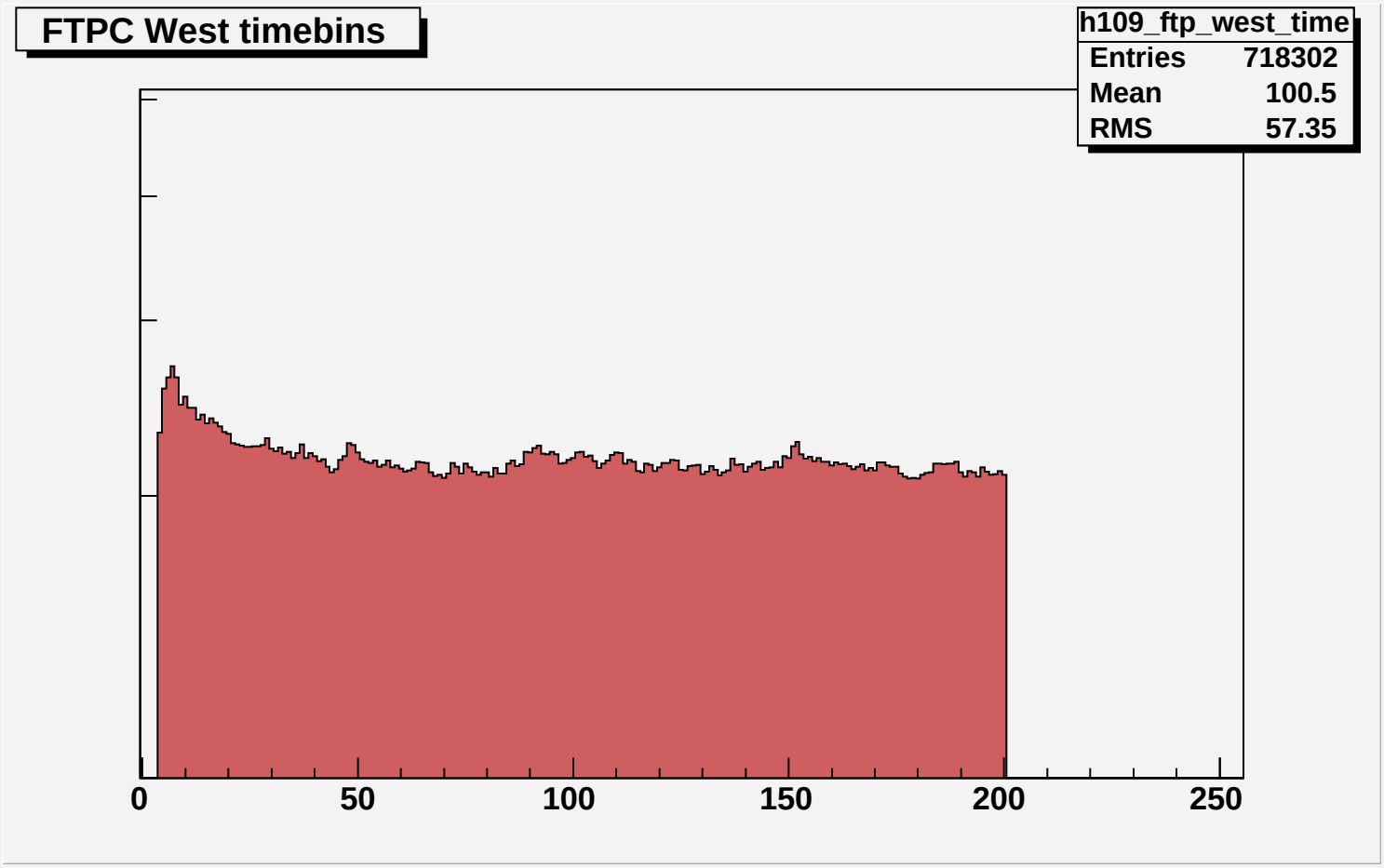

FTPC Occupancy (in %)
h49_ftp
Entries 11
Mean 2.335
RMS 0.751

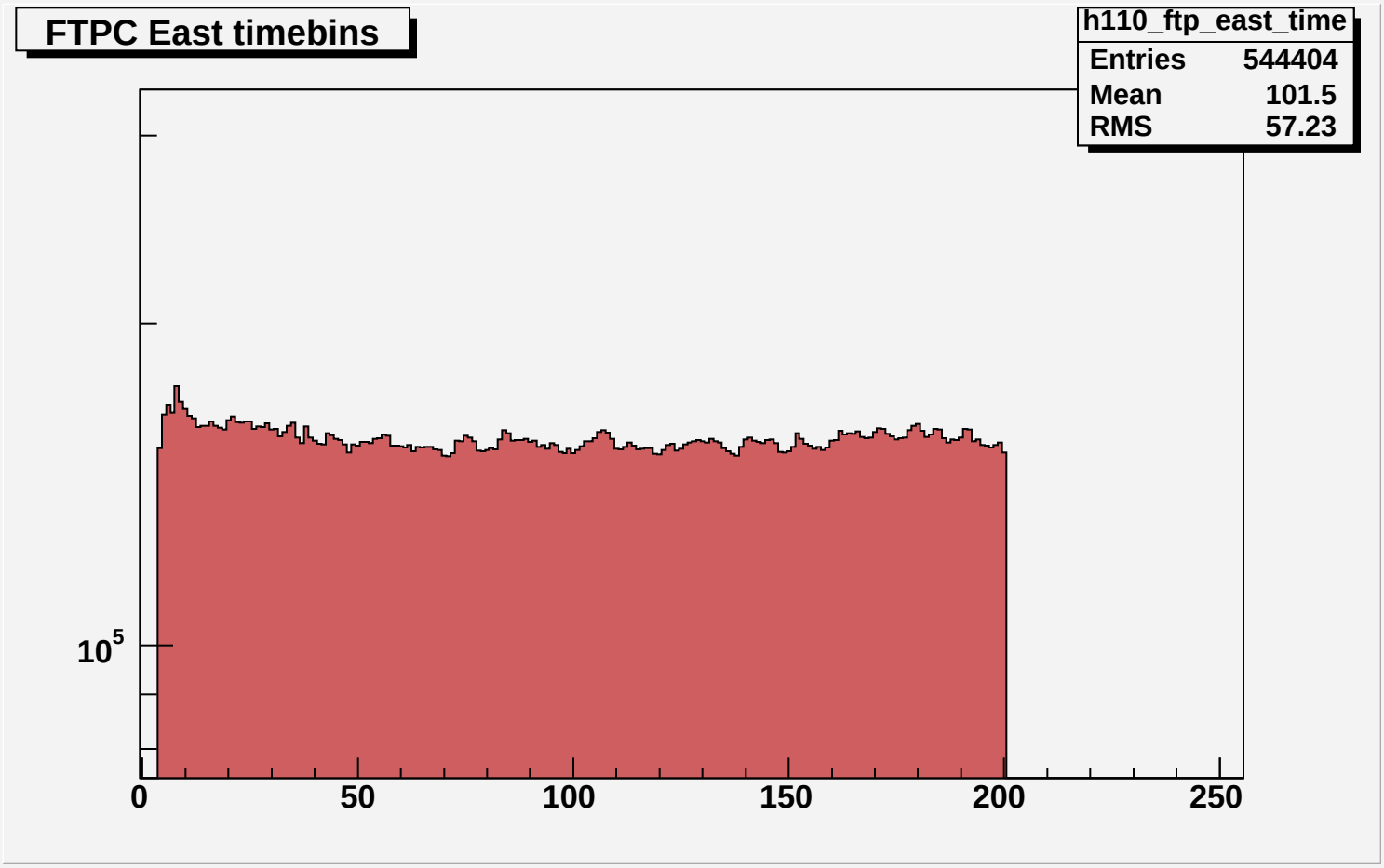

log of FTPC Buffer Size
h11_ftp_evsize
Entries 11
Mean 5.292
RMS 0.04663

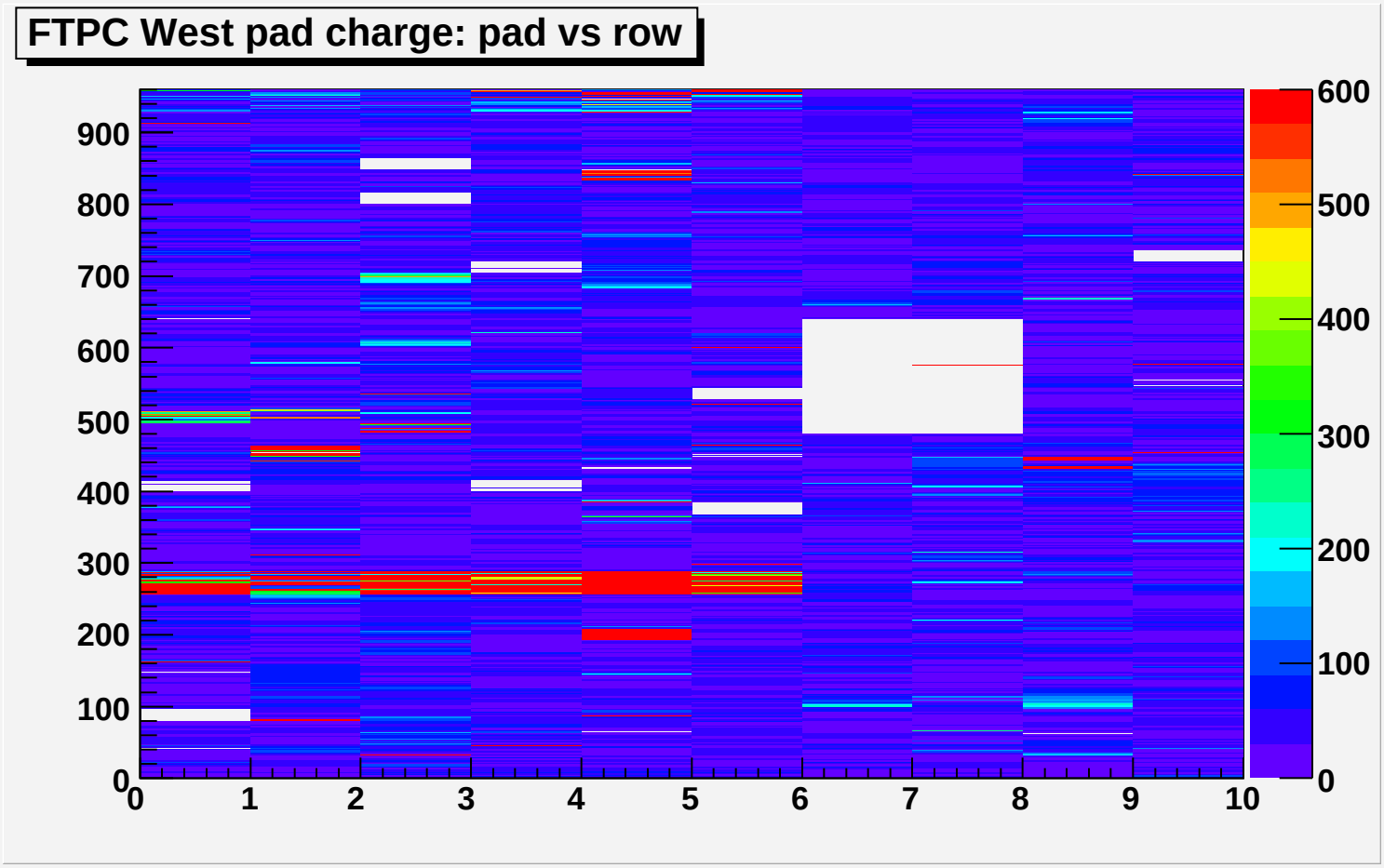

FTPC Occupancy (in %) Lasers
h51_ftp_OccLaser
Entries 0
Mean 0
RMS 0

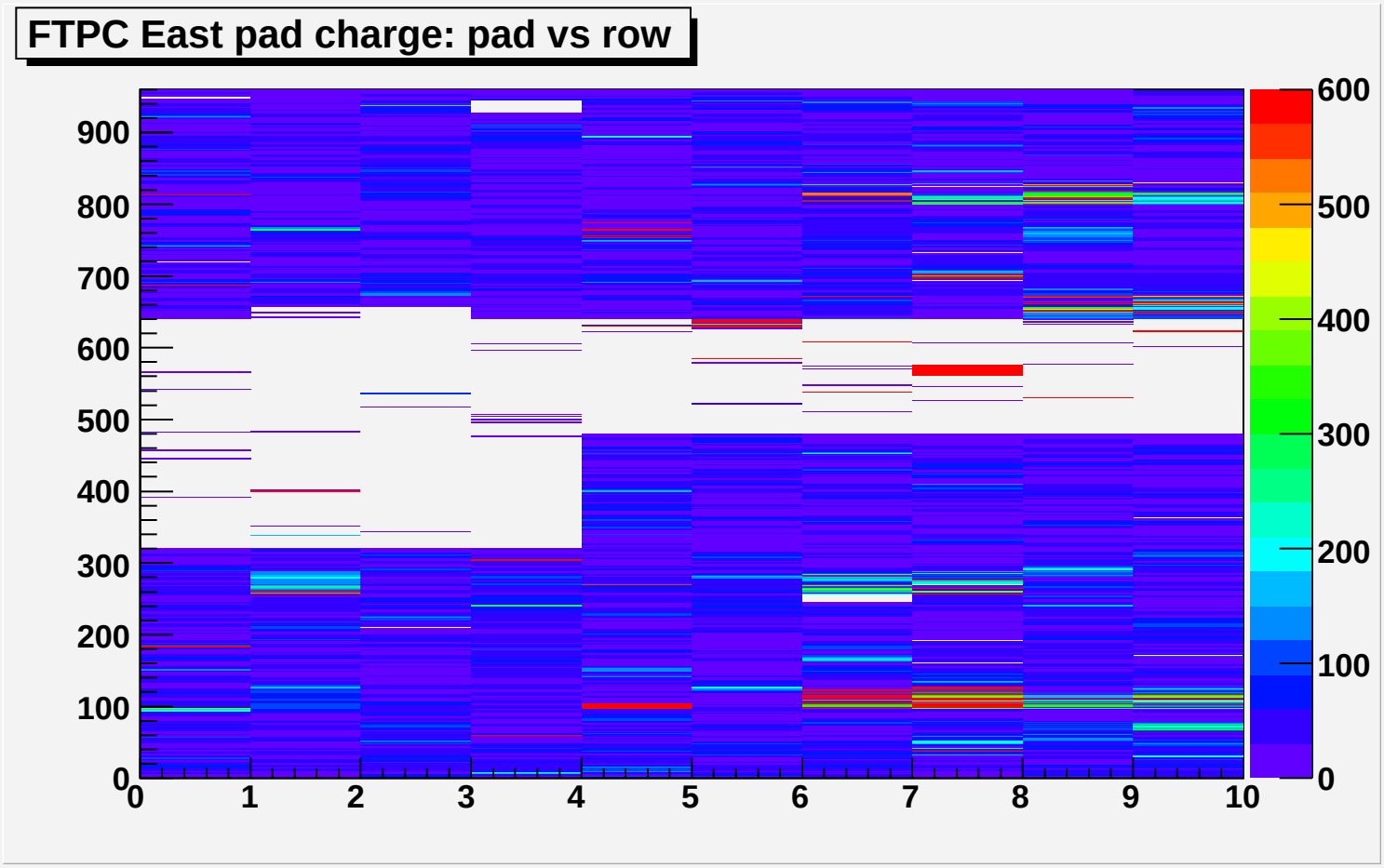

log of Total FTPC Charge
h48_ftp
Entries 11
Mean 0
RMS 0

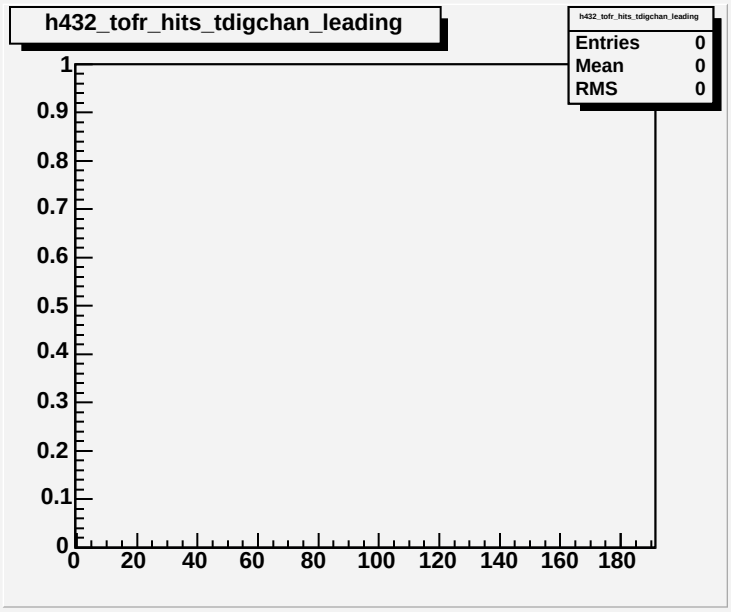

FTPC Occupancy (in %) Pulsers
h50_ftp_OccPulser
Entries 0
Mean 0
RMS 0

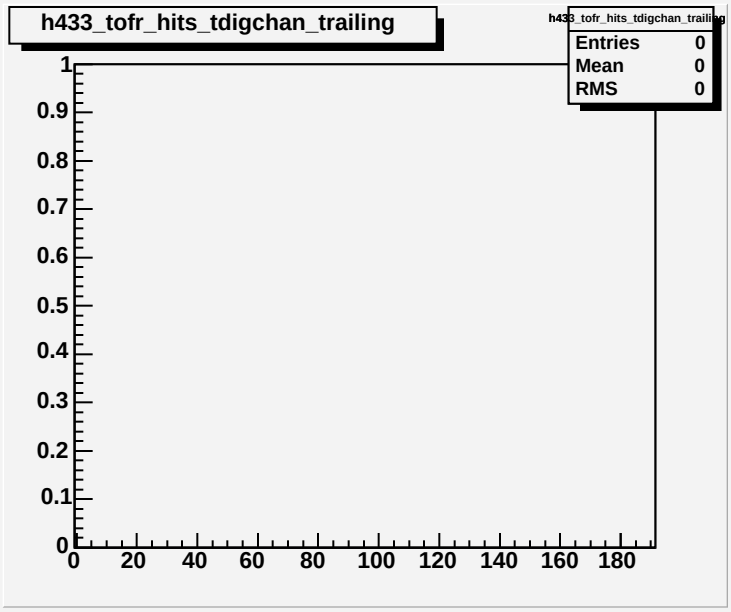

FTPC West timebins
h109_ftp_west_time
Entries 718302
Mean 100.5
RMS 57.35
0
50
100
150
200
250


FTPC East timebins
h110_ftp_east_time
Entries 544404
Mean 101.5
RMS 57.23
10^5
0
50
100
150
200
250


FTPC West pad charge: pad vs row
0
100
200
300
400
500
600
700
800
900
0
1
2
3
4
5
6
7
8
9
10
0
100
200
300
400
500
600


FTPC East pad charge: pad vs row
0
100
200
300
400
500
600
700
800
900
0
1
2
3
4
5
6
7
8
9
10
0
100
200
300
400
500
600


h432_tofr_hits_tdigchan_leading
h432_tofr_hits_tdigchan_leading
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
0 20 40 60 80 100 120 140 160 180


h433_tofr_hits_tdigchan_trailing
h433_tofr_hits_tdigchan_trailing
Entries 0
Mean 0
RMS 0
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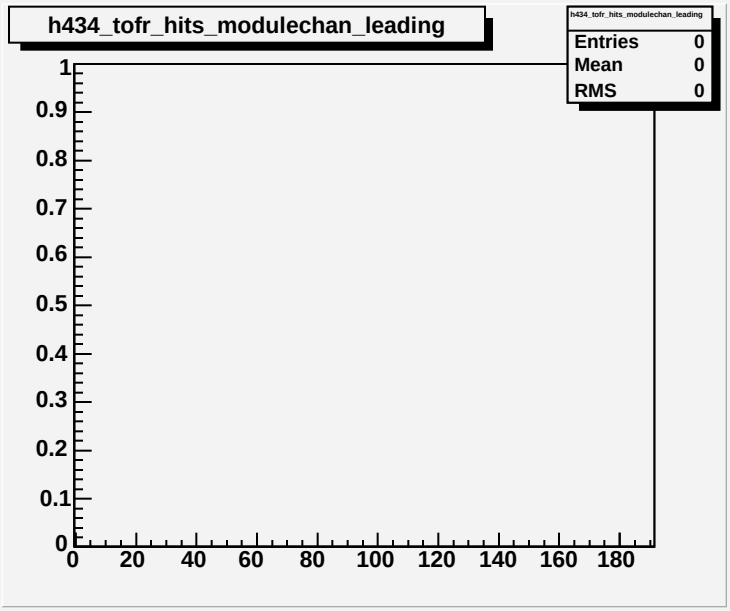
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
0 20 40 60 80 100 120 140 160 180


h434_tofr_hits_modulechan_leading
h434_tofr_hits_modulechan_leading
Entries 0
Mean 0
RMS 0
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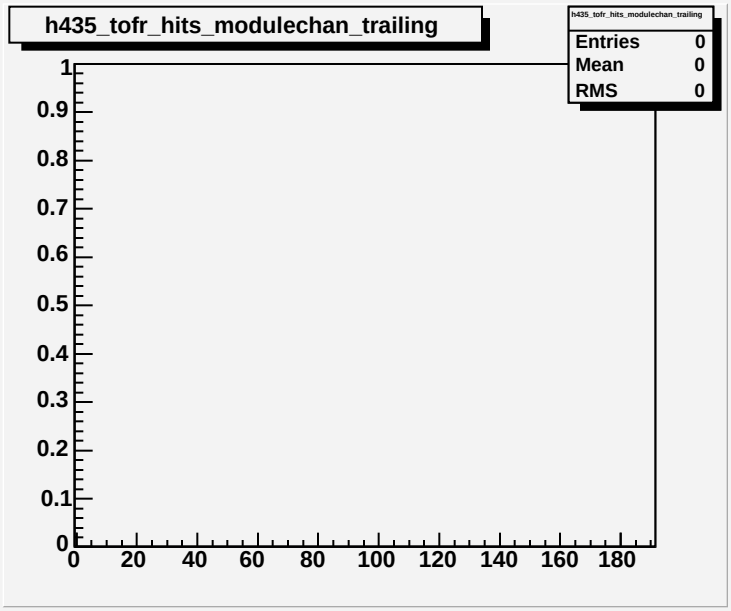
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
0 20 40 60 80 100 120 140 160 180


h435_tofr_hits_modulechan_trailing
h435_tofr_hits_modulechan_trailing
Entries 0
Mean 0
RMS 0
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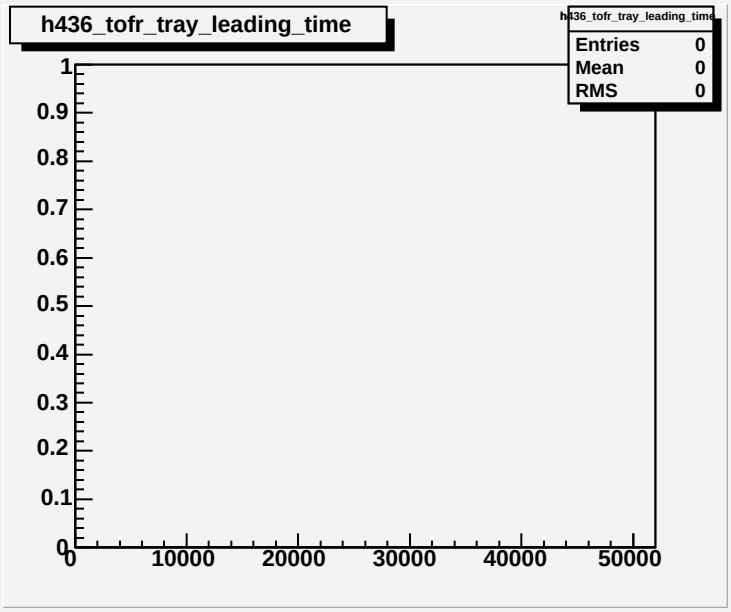
0.6
0.5
0.4
0.3
0.2
0.1
0
0 20 40 60 80 100 120 140 160 180


h436_tofr_tray_leading_time
h436_tofr_tray_leading_time
Entries 0
Mean 0
RMS 0
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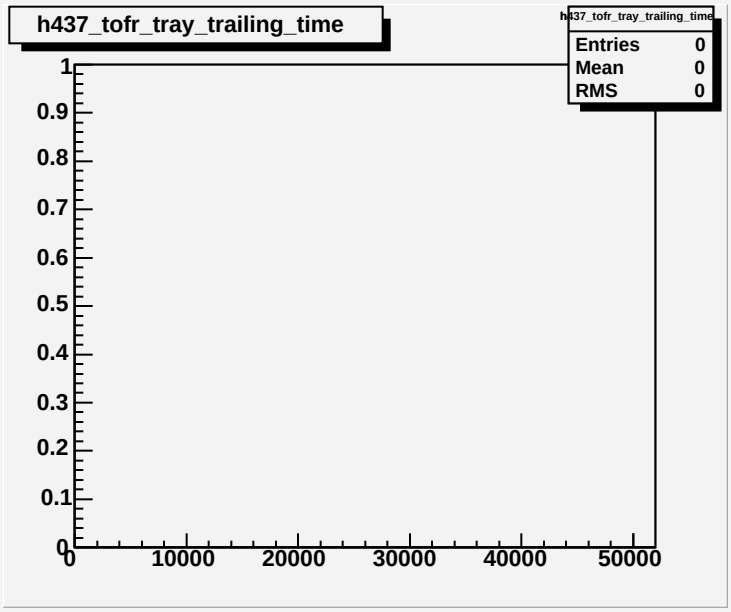
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
0 10000 20000 30000 40000 50000


h437_tofr_tray_trailing_time
h437_tofr_tray_trailing_time
Entries 0
Mean 0
RMS 0
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0.6
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0.2
0.1
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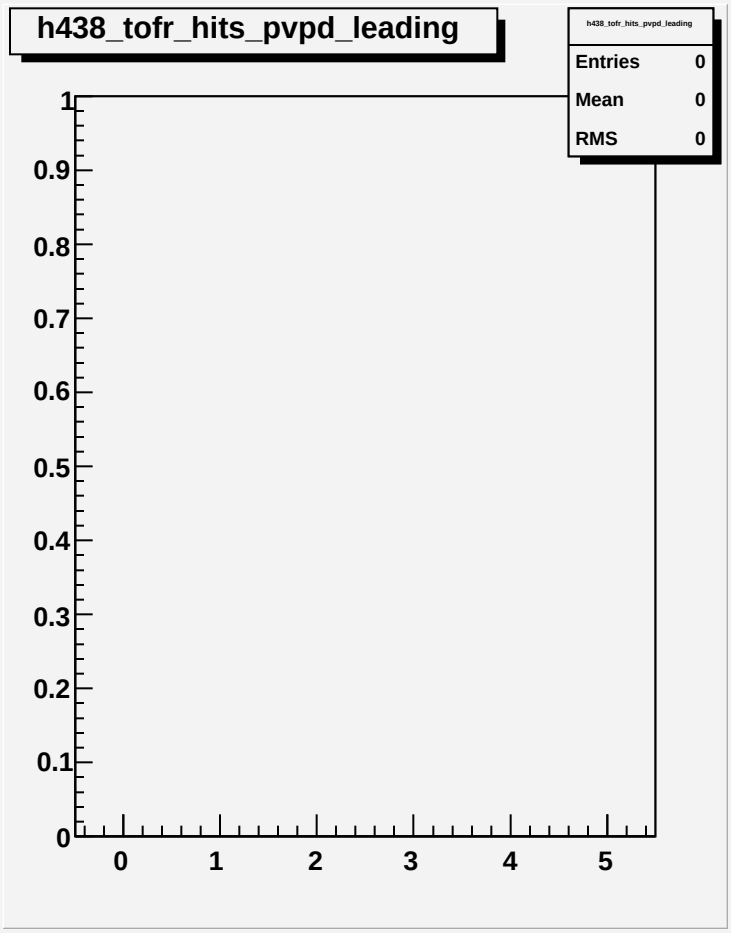
0 10000 20000 30000 40000 50000


h438_tofr_hits_pvpd_leading
h438_tofr_hits_pvpd_leading
Entries 0
Mean 0
RMS 0
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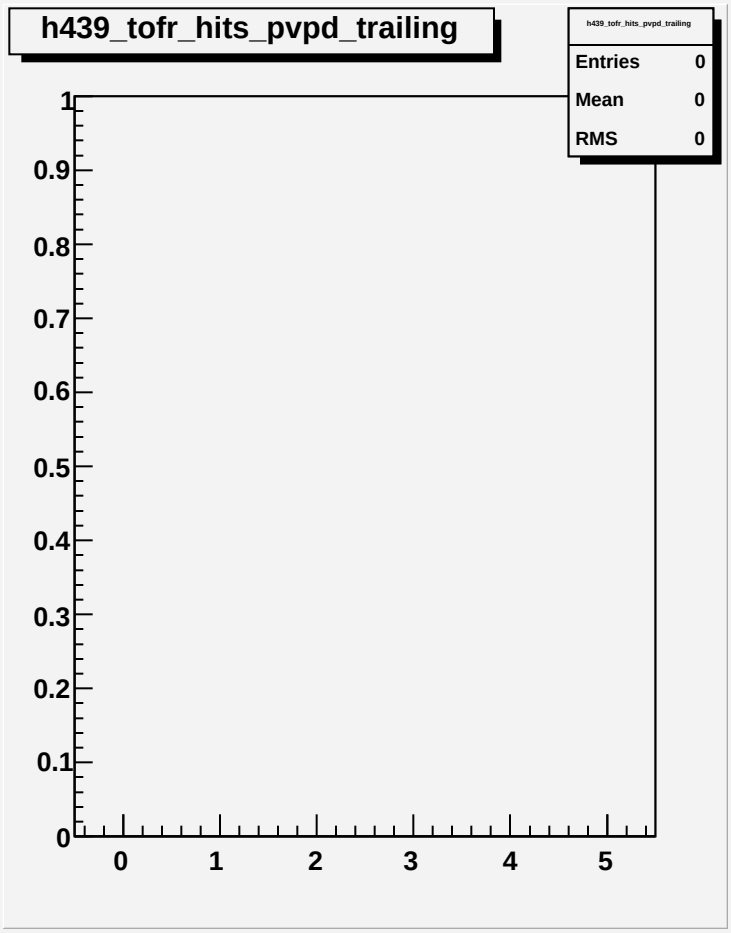
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
0 1 2 3 4 5


h439_tofr_hits_pvpd_trailing
h439_tofr_hits_pvpd_trailing
Entries 0
Mean 0
RMS 0
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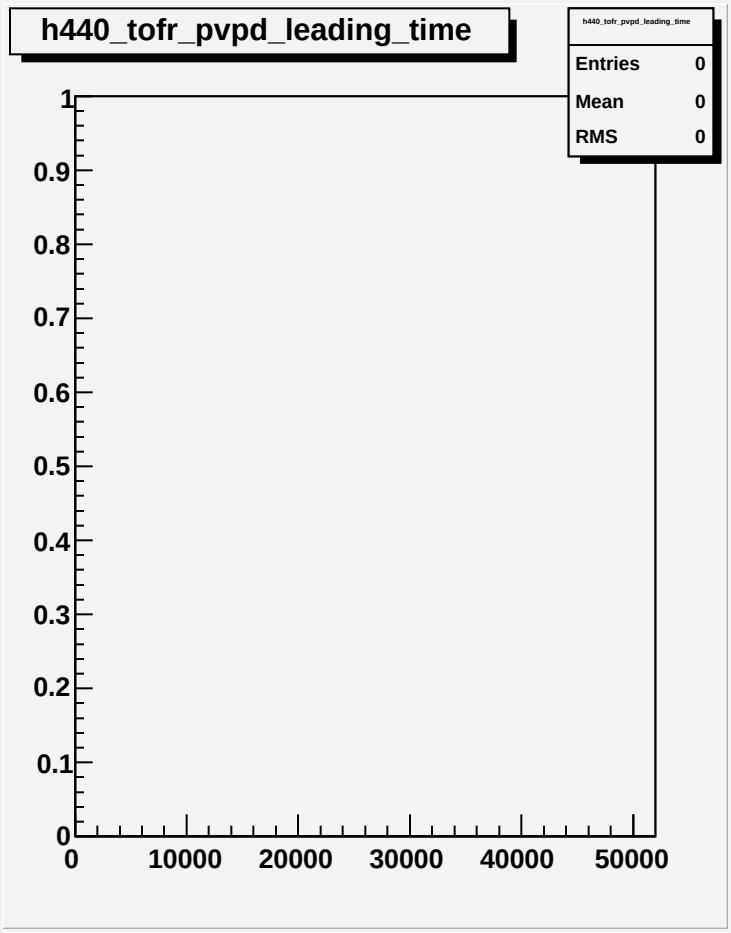
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
0 1 2 3 4 5


h440_tofr_pvpd_leading_time
h440_tofr_pvpd_leading_time
Entries 0
Mean 0
RMS 0
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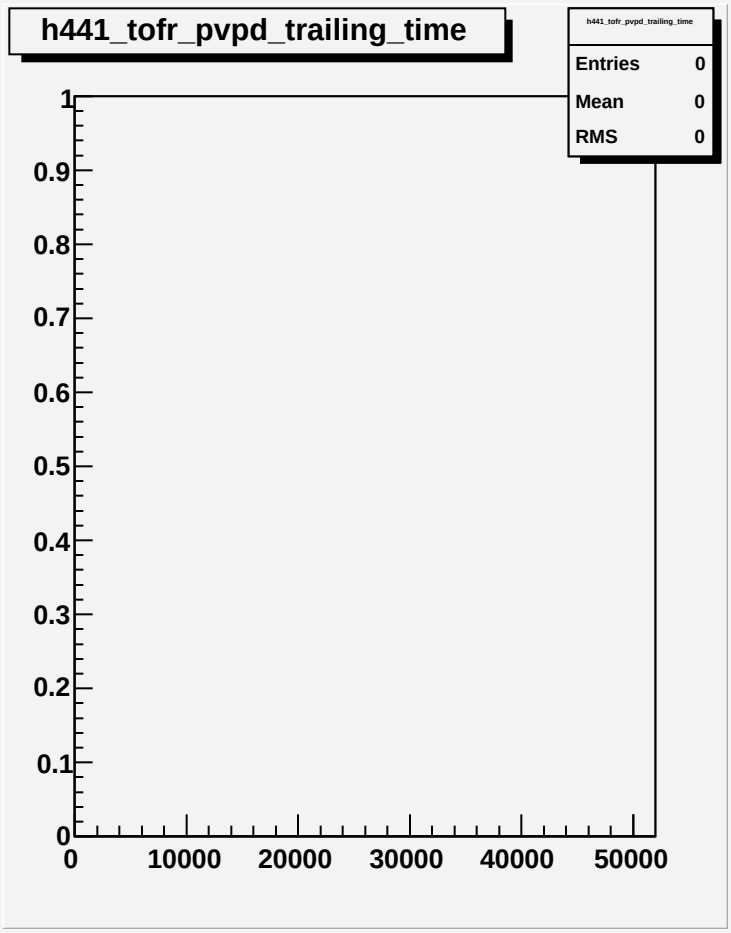
0.5
0.4
0.3
0.2
0.1
0
0 10000 20000 30000 40000 50000


h441_tofr_pvpd_trailing_time
h441_tofr_pvpd_trailing_time
Entries 0
Mean 0
RMS 0
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0.6
0.5
0.4
0.3
0.2
0.1
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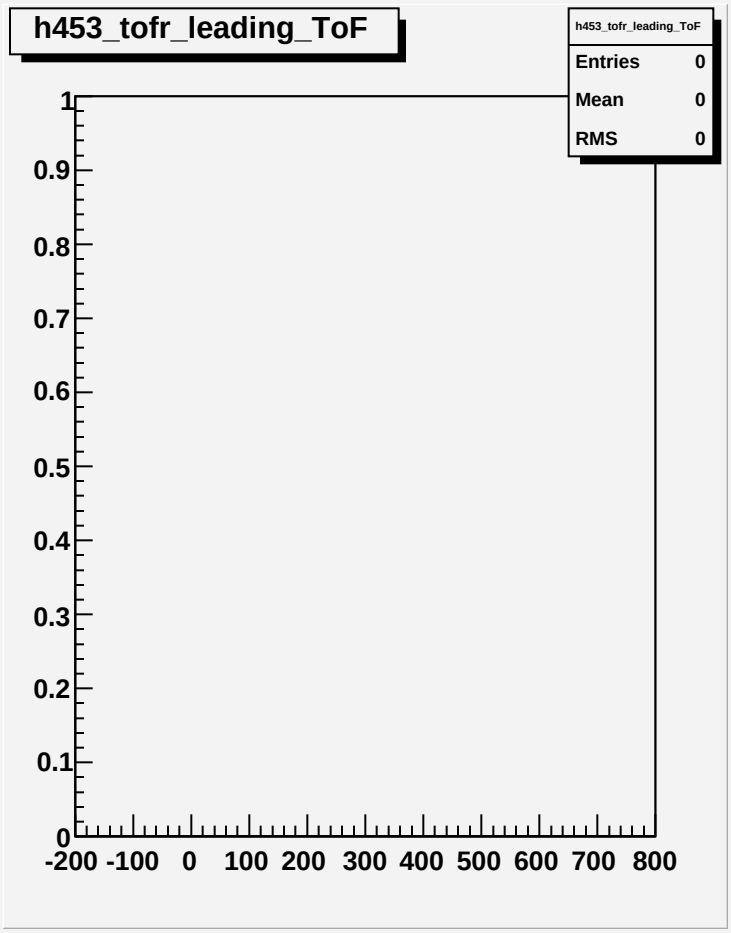
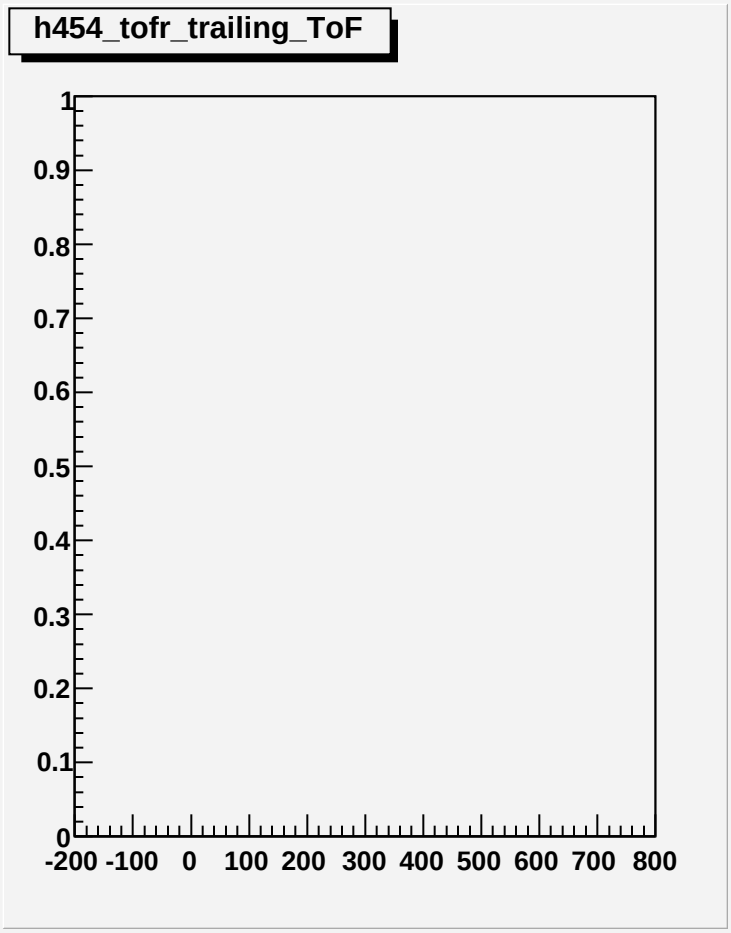
0 10000 20000 30000 40000 50000


h453_tofr_leading_ToF
h453_tofr_leading_ToF
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
-200 -100 0 100 200 300 400 500 600 700 800


h454_tofr_trailing_ToF
1
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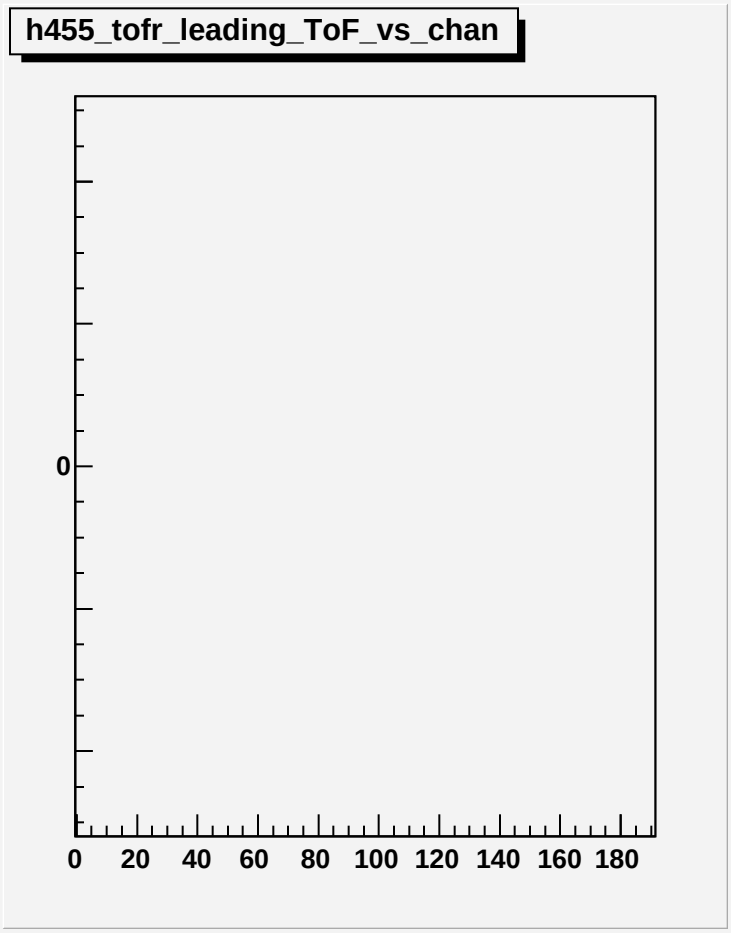
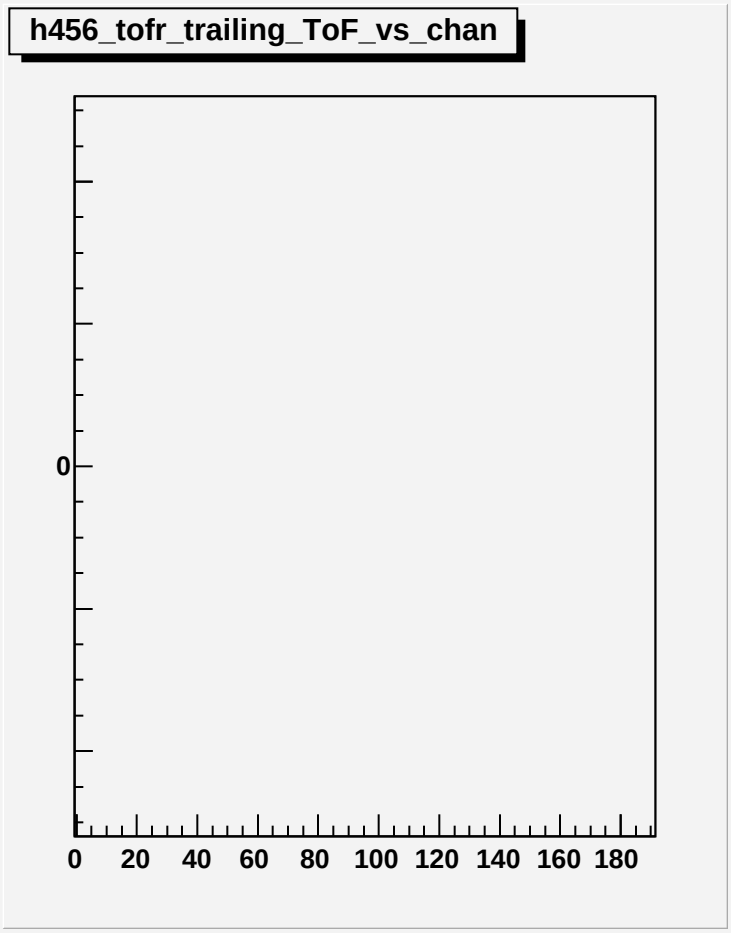
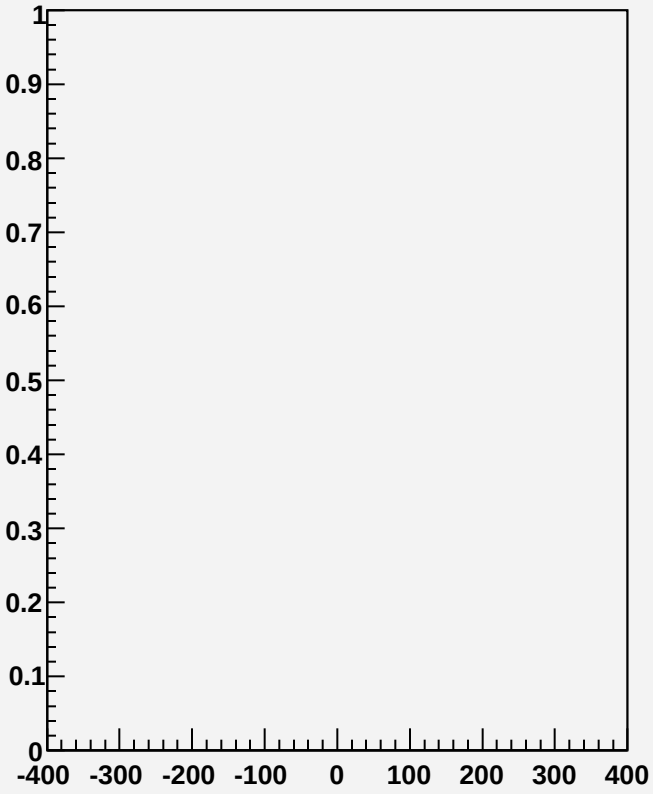
0.6
0.5
0.4
0.3
0.2
0.1
0
-200 -100 0 100 200 300 400 500 600 700 800

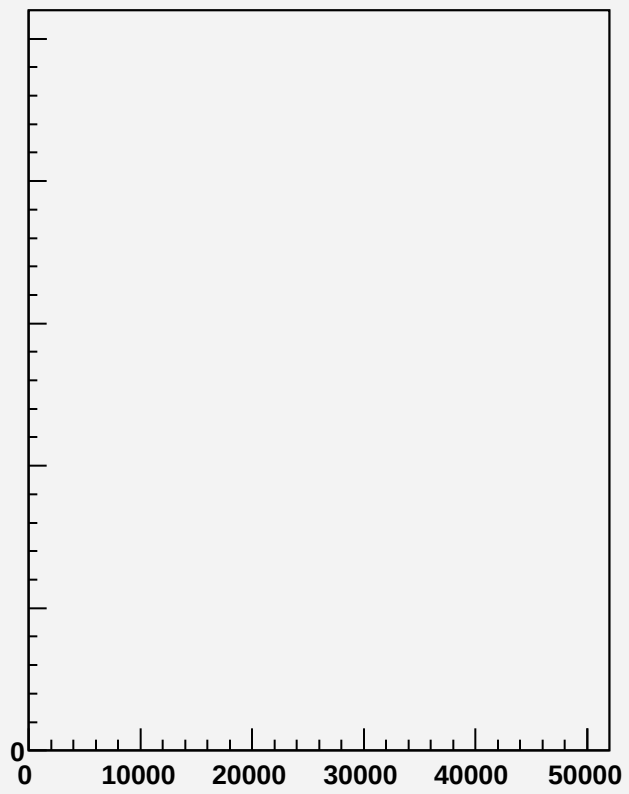

h455_tofr_leading_ToF_vs_chan
0
0 20 40 60 80 100 120 140 160 180


h456_tofr_trailing_ToF_vs_chan
0
0 20 40 60 80 100 120 140 160 180

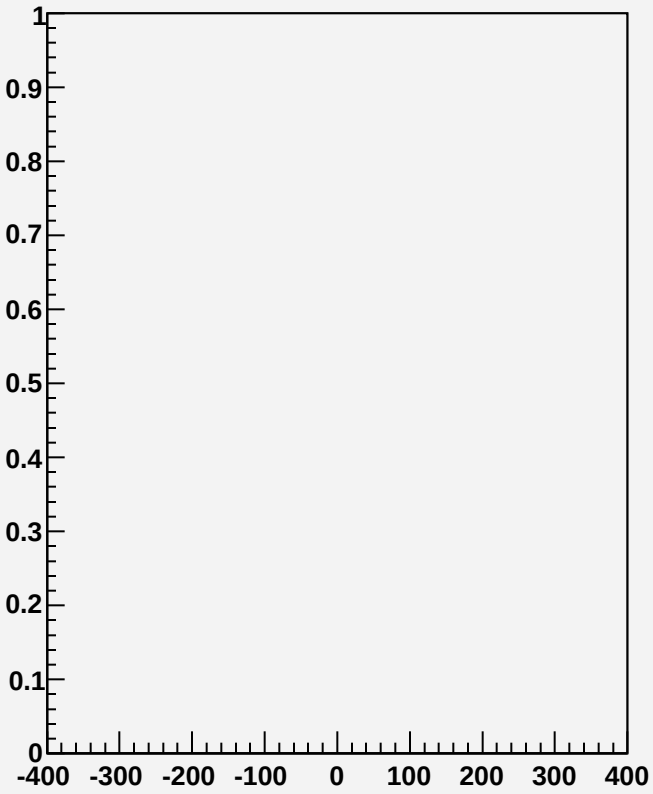
h457_tofr_pvpd_le_t0west_t0east



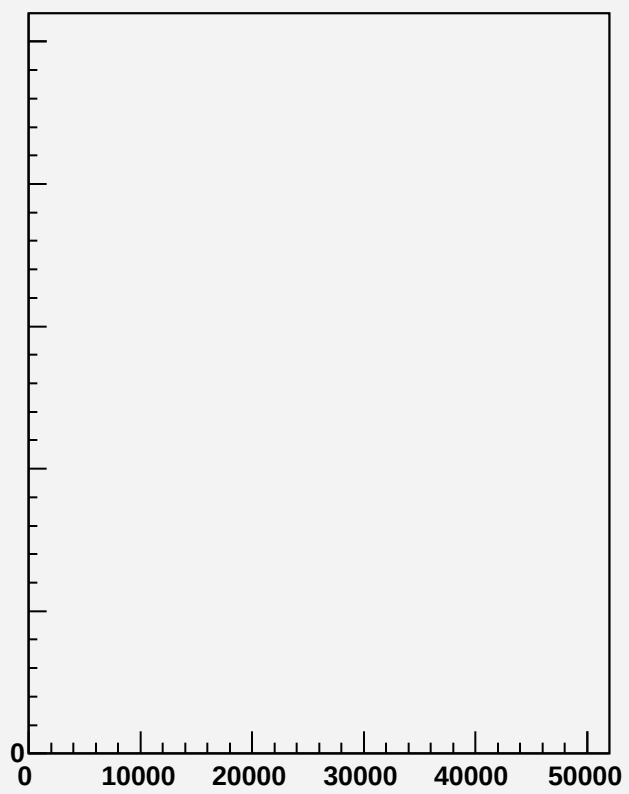
h458_tofr_pvpd_le_t0west_vs_t0east



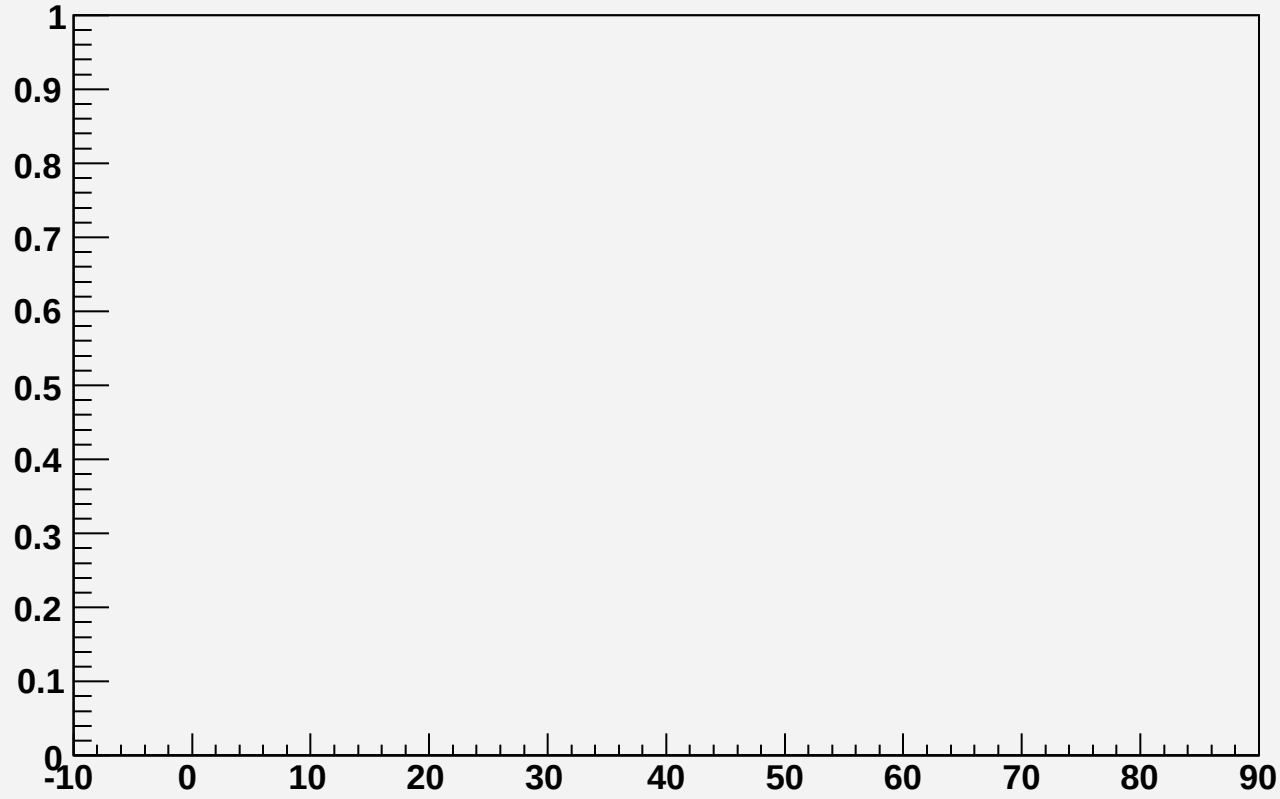
h459_tofr_pvpd_te_t0west_t0east



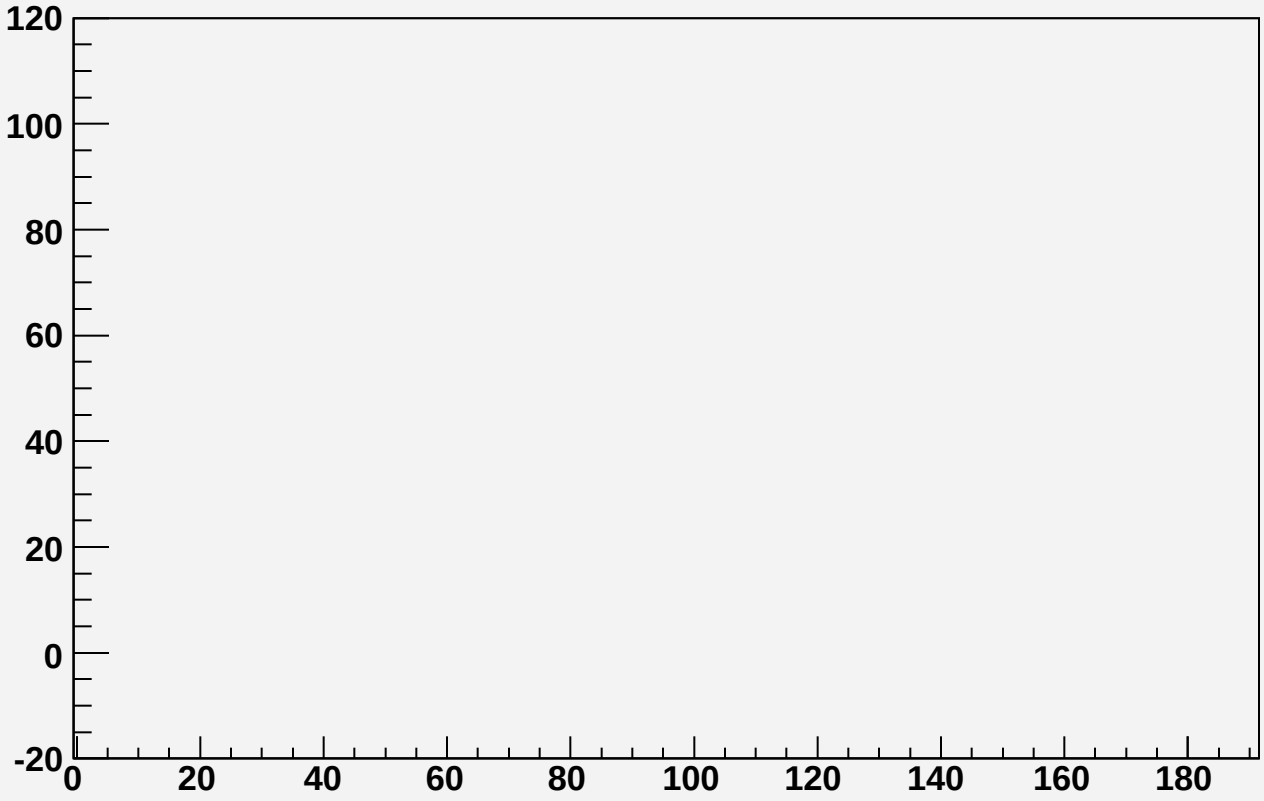
h460_tofr_pvpd_te_t0west_vs_t0east

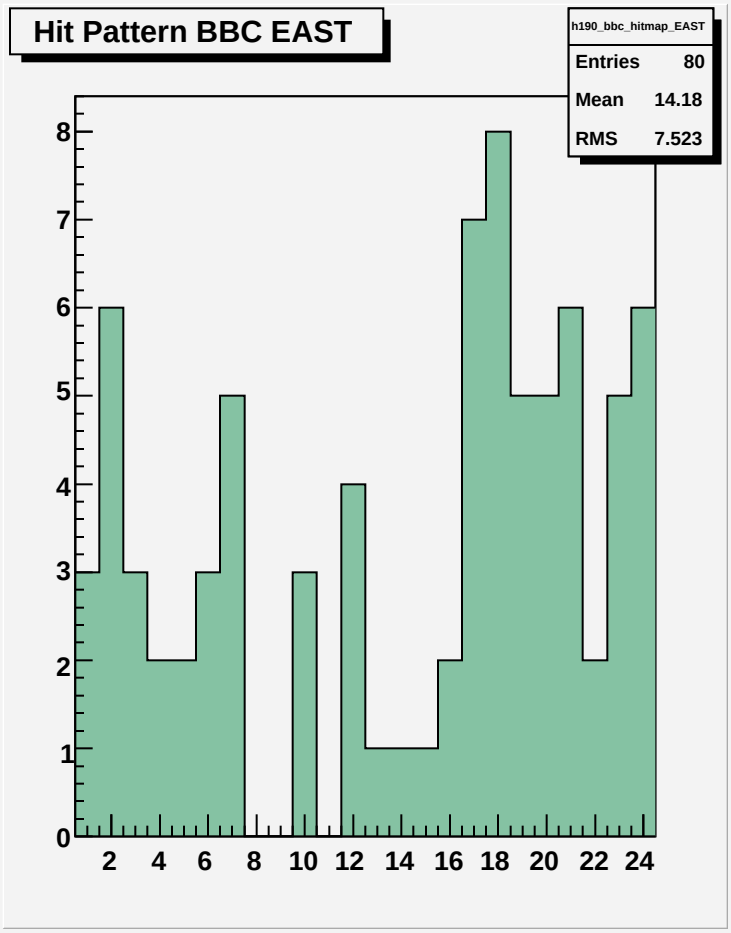


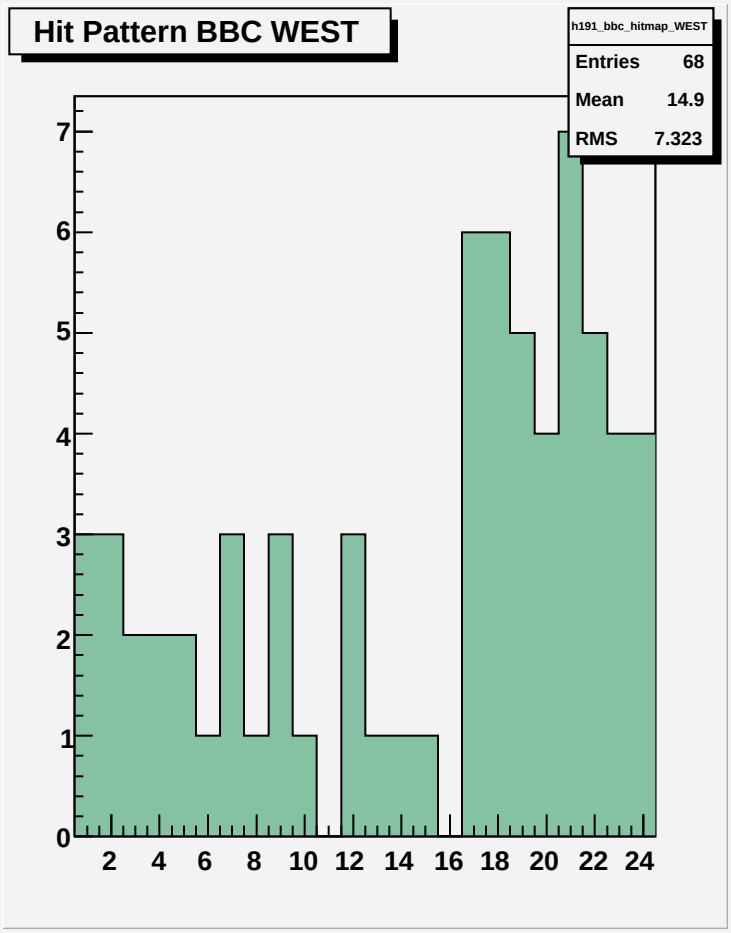
h461_tofr_ToT

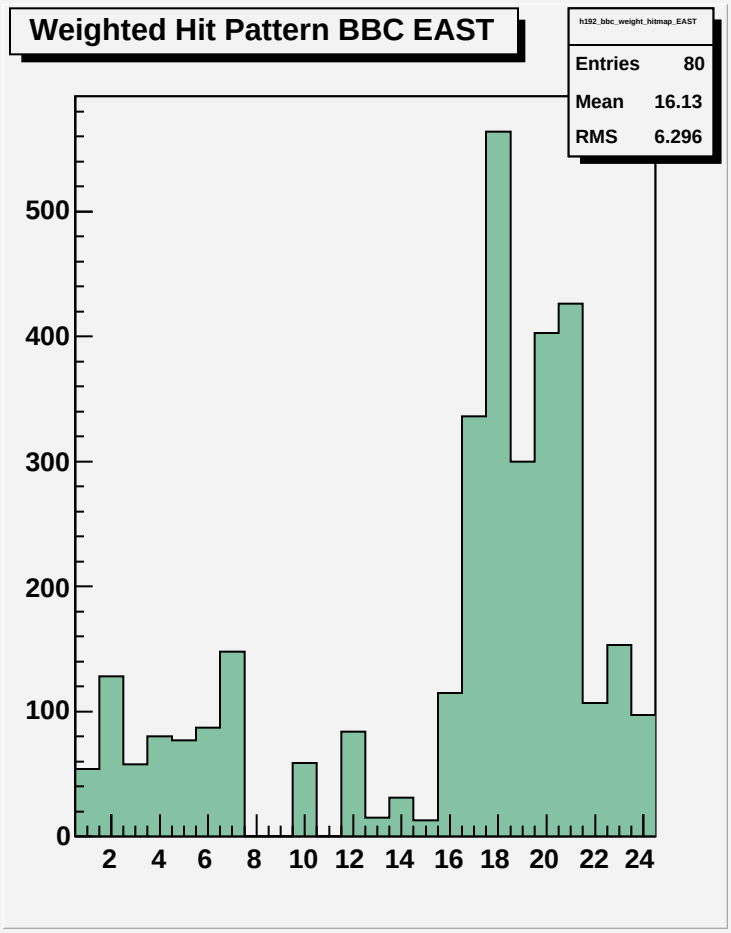


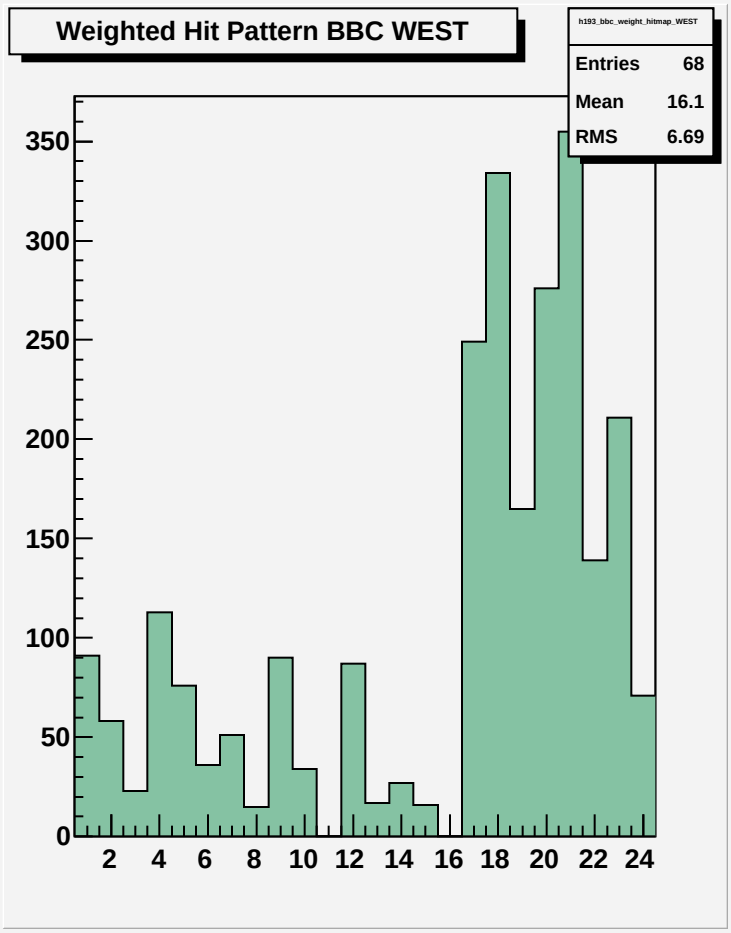
h462_tofr_ToT_vs_chan

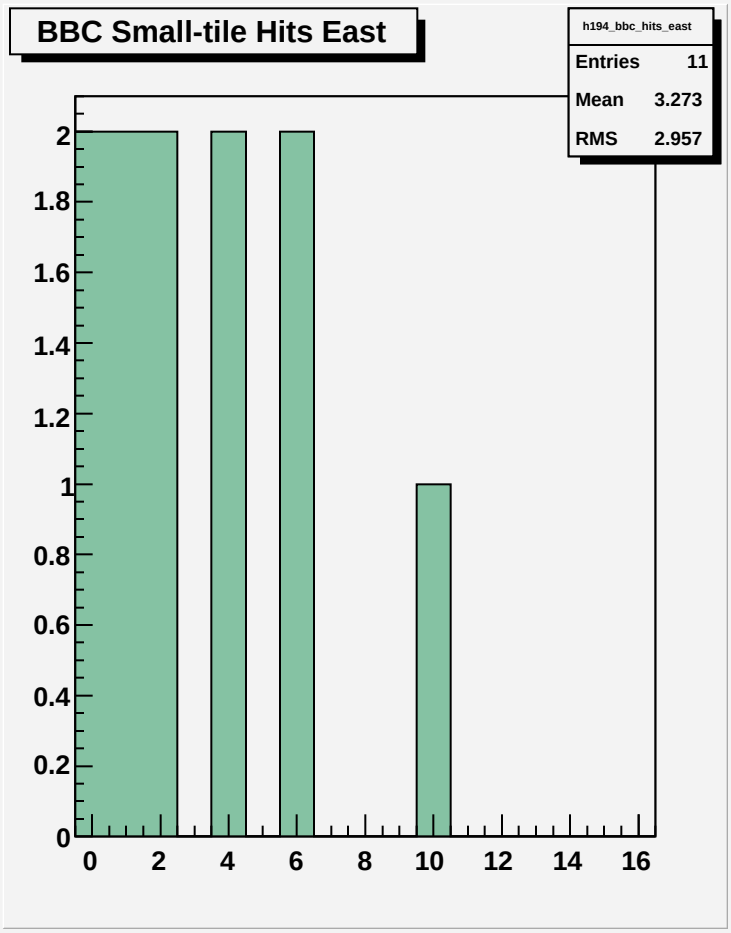


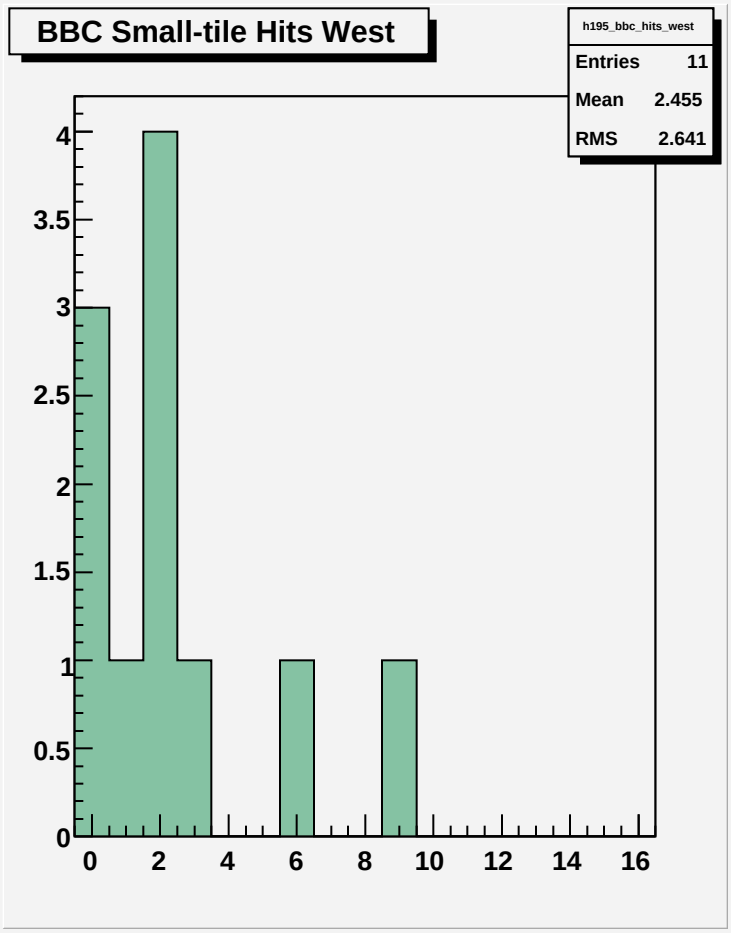

Hit Pattern BBC EAST
h190_bbc_hitmap_EAST
Entries 80
Mean 14.18
RMS 7.523

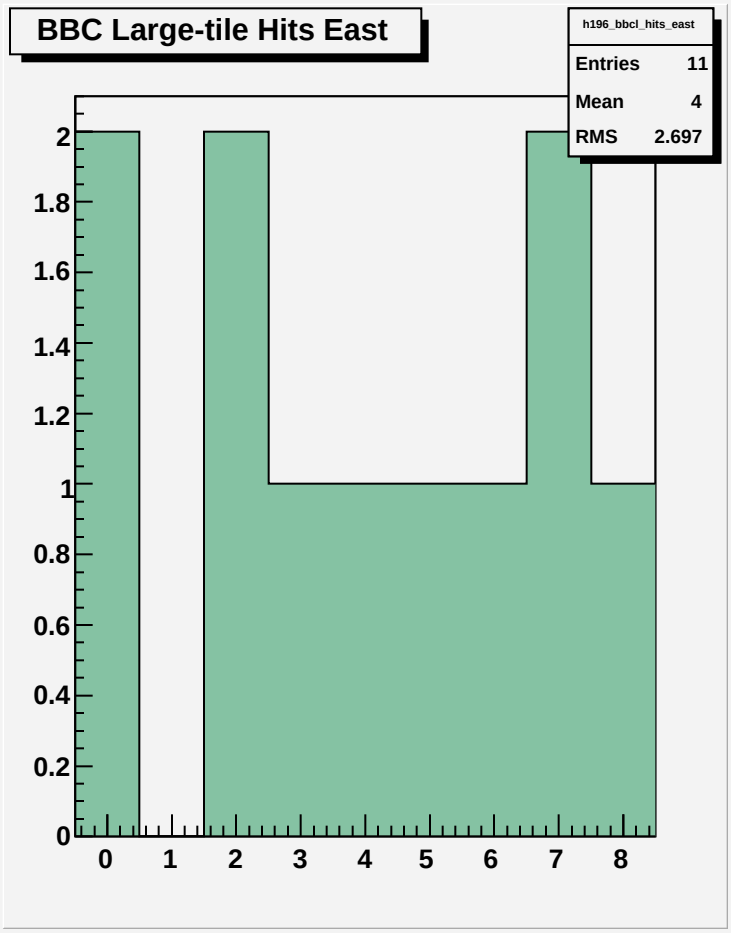

Hit Pattern BBC WEST
h191_bbc_hitmap_WEST
Entries 68
Mean 14.9
RMS 7.323

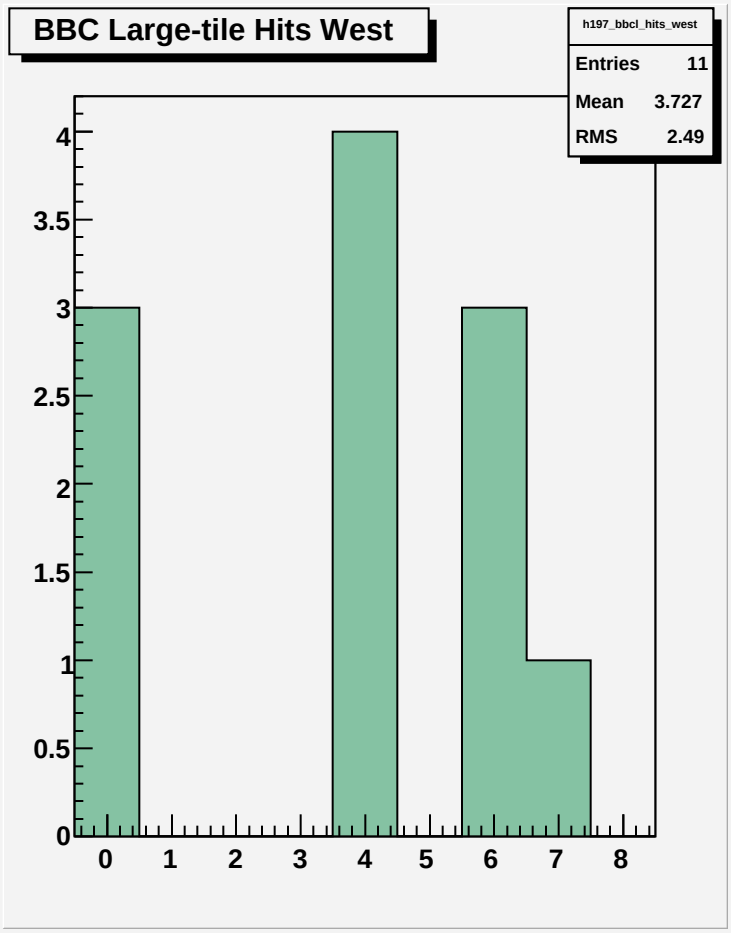

Weighted Hit Pattern BBC EAST
h192_bbc_weight_hitmap_EAST
Entries 80
Mean 16.13
RMS 6.296

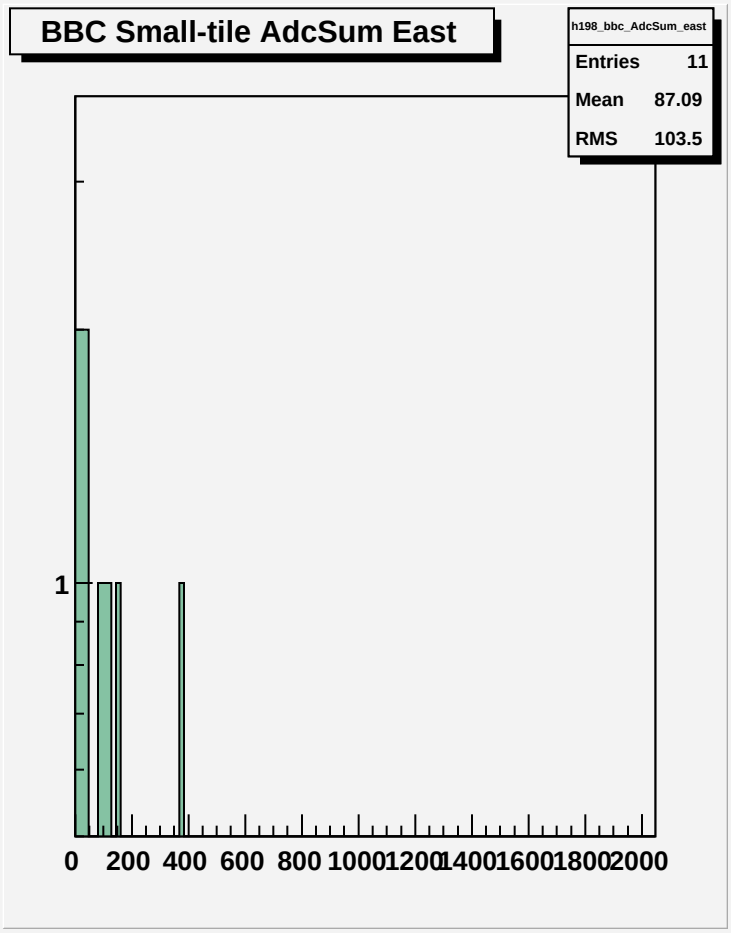

Weighted Hit Pattern BBC WEST
h193_bbc_weight_hitmap_WEST
Entries 68
Mean 16.1
RMS 6.69

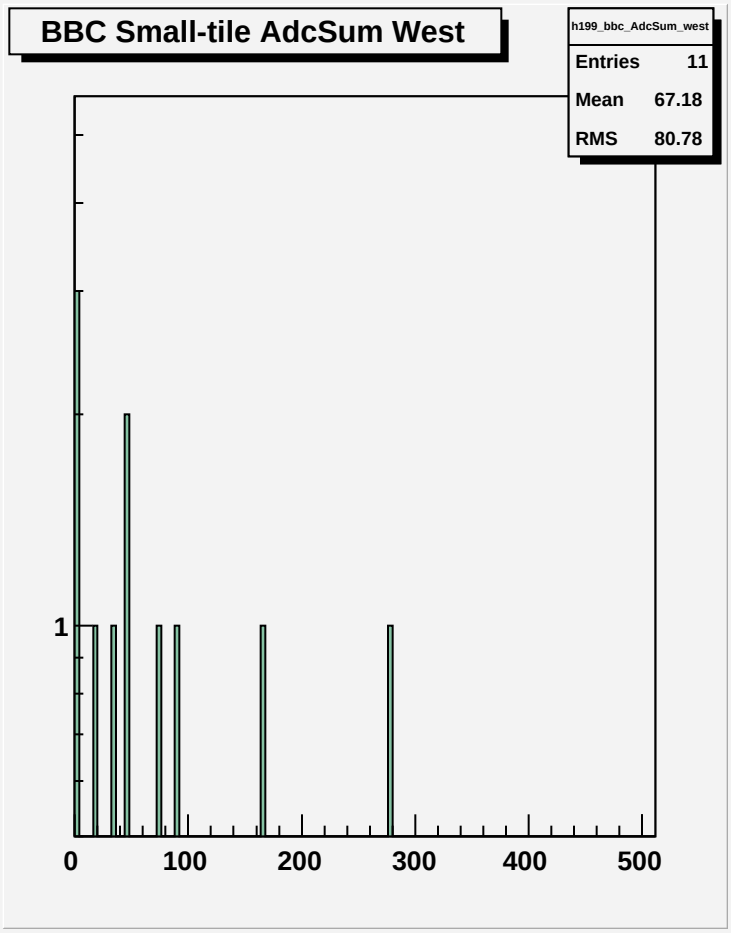

BBC Small-tile Hits East
h194_bbc_hits_east
Entries 11
Mean 3.273
RMS 2.957

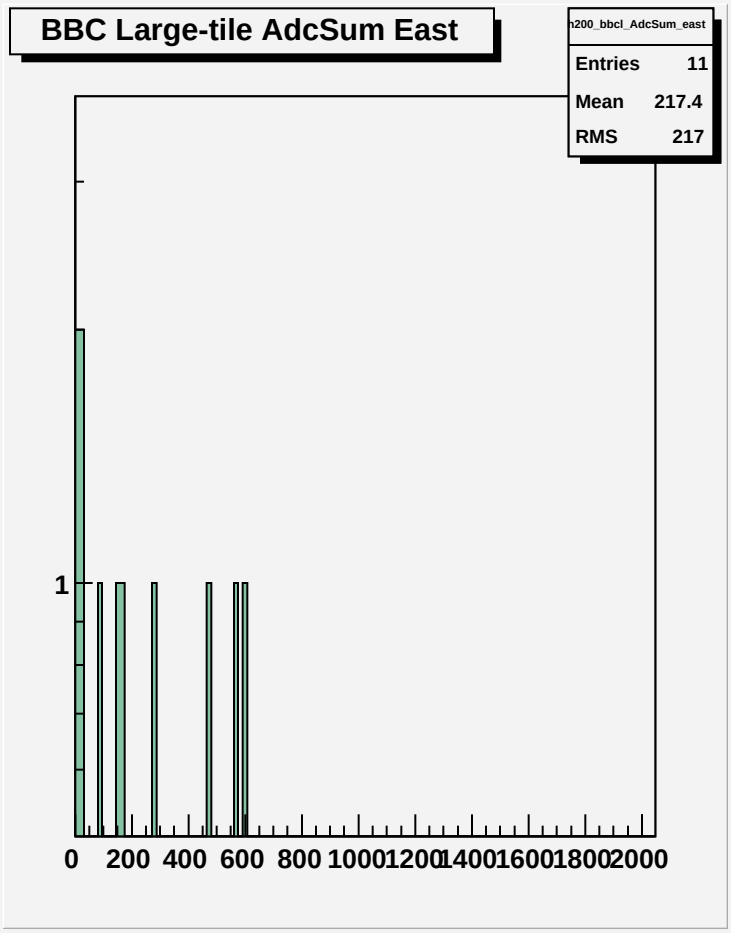

BBC Small-tile Hits West
h195_bbc_hits_west
Entries 11
Mean 2.455
RMS 2.641

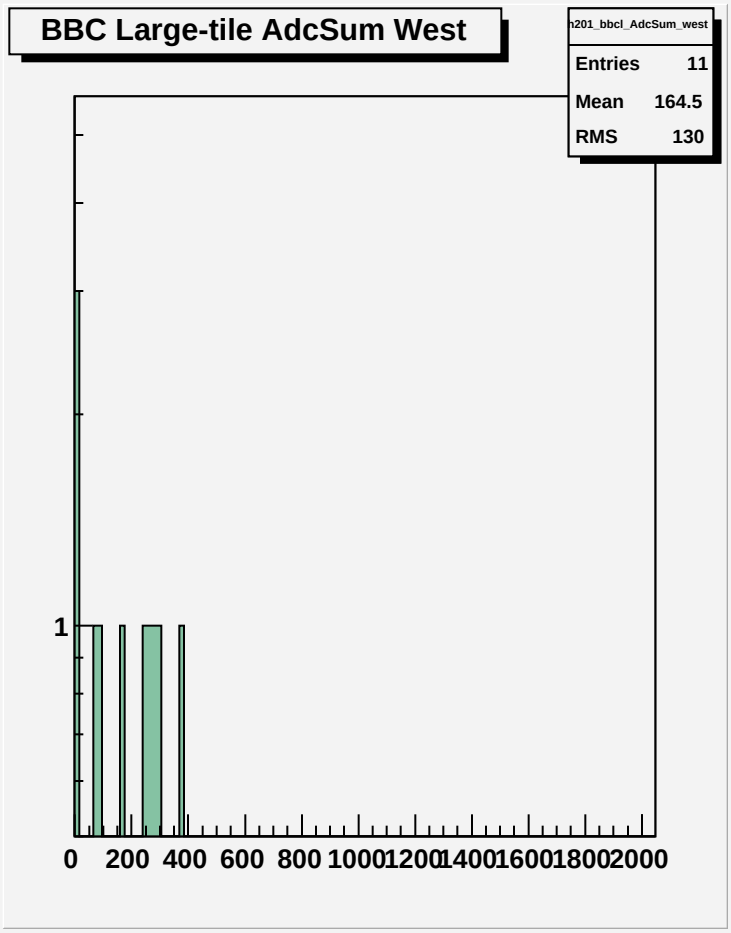

BBC Large-tile Hits East
h196_bbcl_hits_east
Entries 11
Mean 4
RMS 2.697

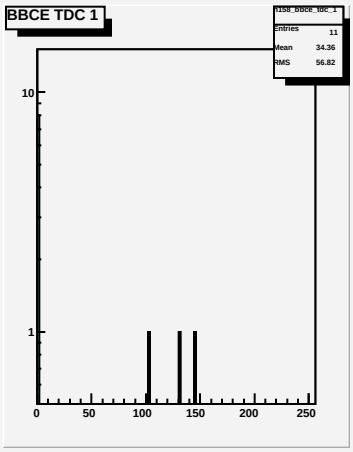

BBC Large-tile Hits West
h197_bbcl_hits_west
Entries 11
Mean 3.727
RMS 2.49

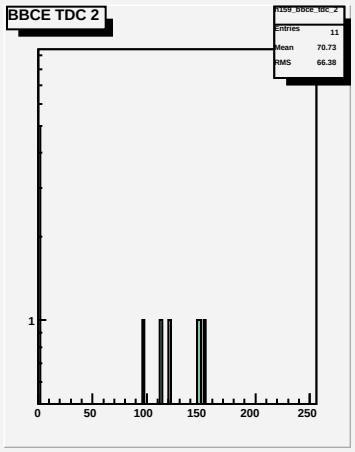

BBC Small-tile AdcSum East
h198_bbc_AdcSum_east
Entries 11
Mean 87.09
RMS 103.5
1
0 200 400 600 800 1000 1200 1400 1600 1800 2000

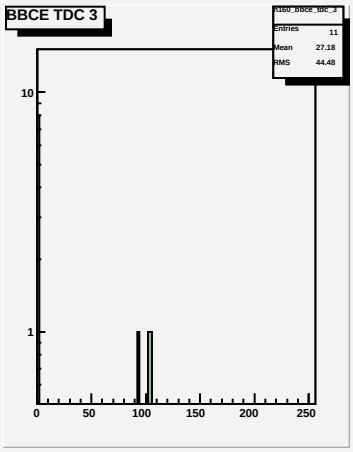

BBC Small-tile AdcSum West
h199_bbc_AdcSum_west
Entries 11
Mean 67.18
RMS 80.78
1
0 100 200 300 400 500

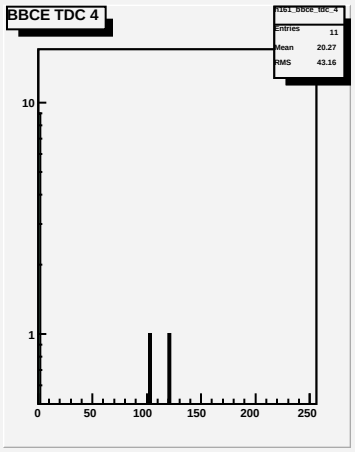

BBC Large-tile AdcSum East
h200_bbcl_AdcSum_east
Entries 11
Mean 217.4
RMS 217
1
0 200 400 600 800 1000 1200 1400 1600 1800 2000

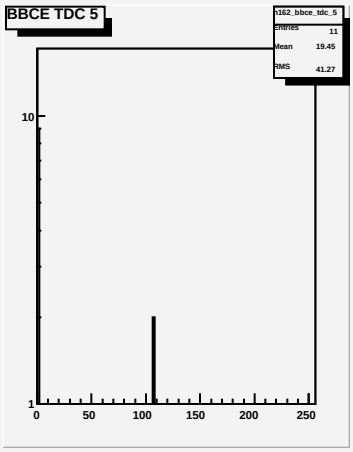

BBC Large-tile AdcSum West
h201_bbcl_AdcSum_west
Entries 11
Mean 164.5
RMS 130
1
0 200 400 600 800 1000 1200 1400 1600 1800 2000

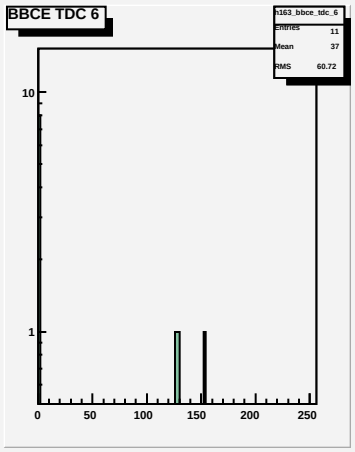

BBCE TDC 1
h158_bbce_tdc_1
Entries 11
Mean 34.36
RMS 56.82

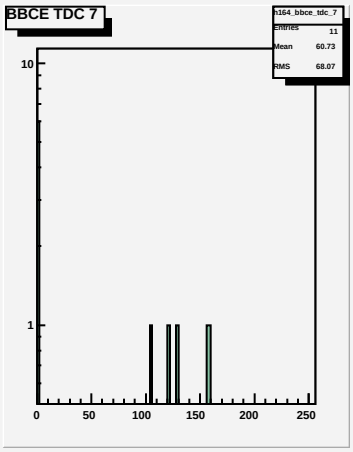

BBCE TDC 2
h159_bbce_tdc_2
Entries 11
Mean 70.73
RMS 66.38

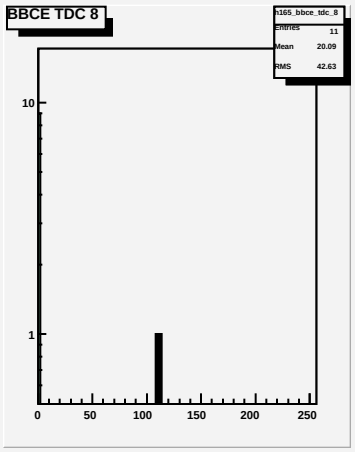

BBCE TDC 3
h160_bbce_tdc_3
Entries 11
Mean 27.18
RMS 44.48

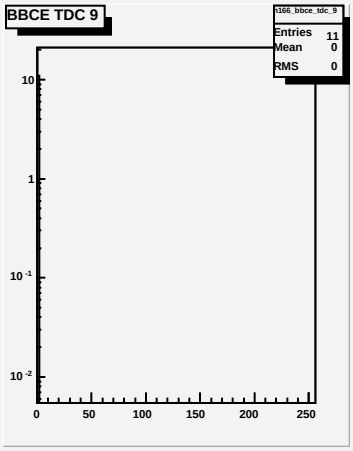

BBCE TDC 4
h161_bbce_tdc_4
Entries 11
Mean 20.27
RMS 43.16

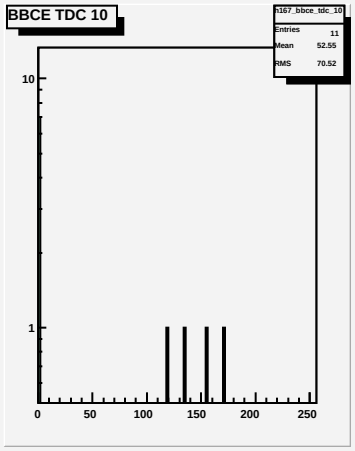

BBCE TDC 5
h162_bbce_tdc_5
Entries 11
Mean 19.45
RMS 41.27

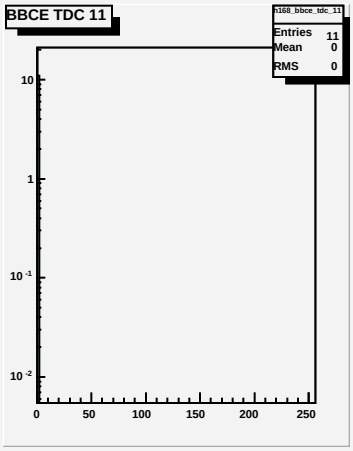

BBCE TDC 6
h163_bbce_tdc_6
Entries 11
Mean 37
RMS 60.72

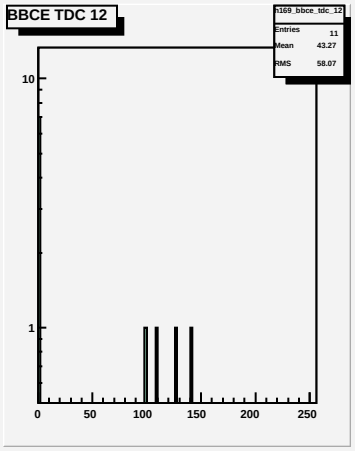

BBCE TDC 7
h164_bbce_tdc_7
Entries 11
Mean 60.73
RMS 68.07

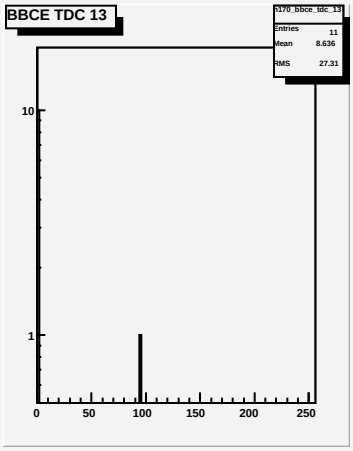

BBCE TDC 8
h165_bbce_tdc_8
Entries 11
Mean 20.09
RMS 42.63

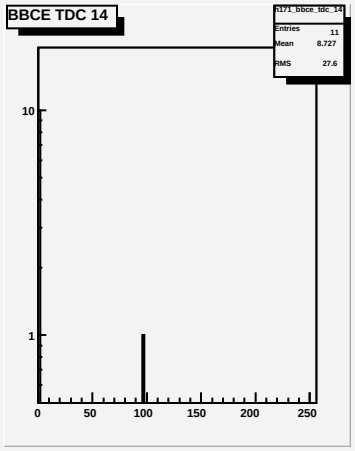

BBCE TDC 9
h166_bbce_tdc_9
Entries 11
Mean 0
RMS 0

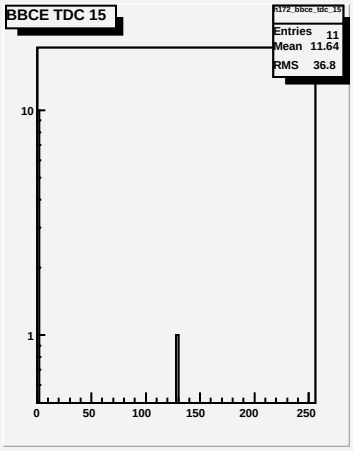

BBCE TDC 10
h167_bbce_tdc_10
Entries 11
Mean 52.55
RMS 70.52

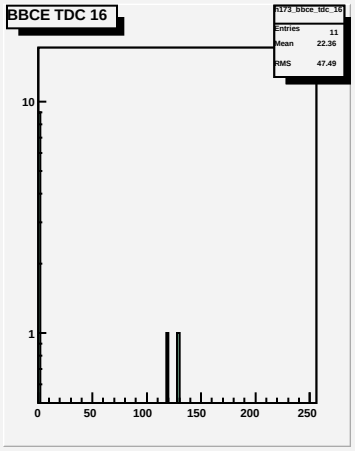

BBCE TDC 11
h168_bbce_tdc_11
Entries 11
Mean 0
RMS 0

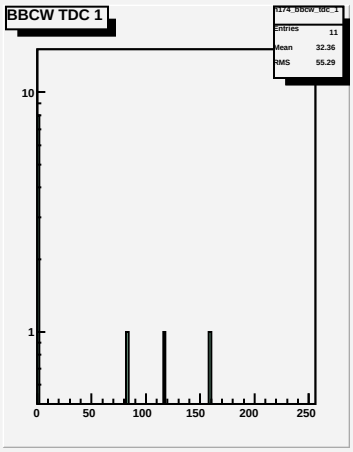

BBCE TDC 12
h169_bbce_tdc_12
Entries 11
Mean 43.27
RMS 58.07

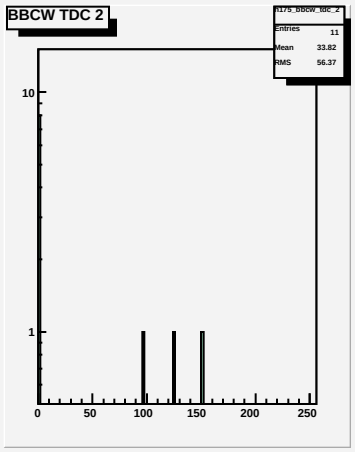

BBCE TDC 13
h170_bbce_tdc_13
Entries 11
Mean 8.636
RMS 27.31

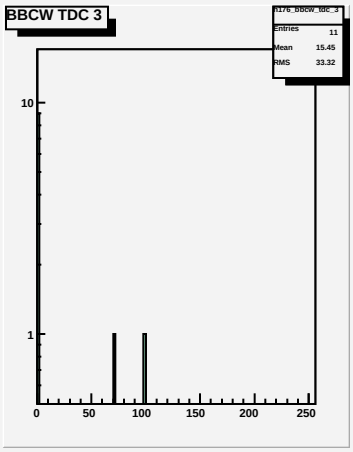

BBCE TDC 14
h171_bbce_tdc_14
Entries 11
Mean 8.727
RMS 27.6

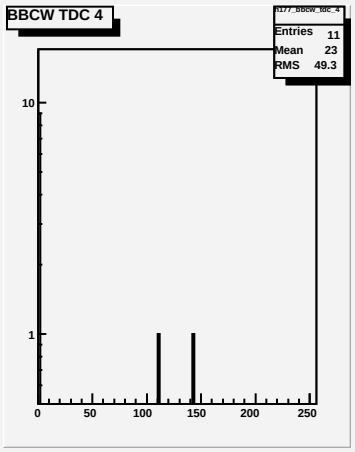

BBCE TDC 15
h172_bbce_tdc_15
Entries 11
Mean 11.64
RMS 36.8

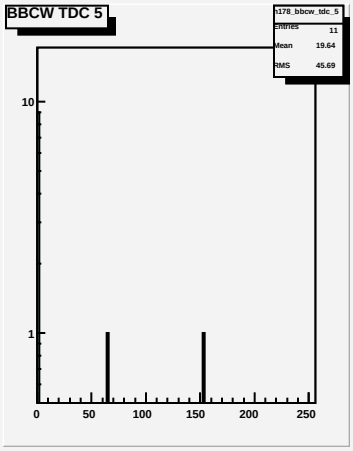

BBCE TDC 16
h173_bbce_tdc_16
Entries 11
Mean 22.36
RMS 47.49

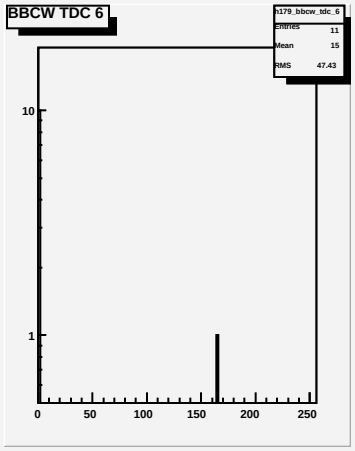

BBCW TDC 1
h174_bbcw_tdc_1
Entries 11
Mean 32.36
RMS 55.29

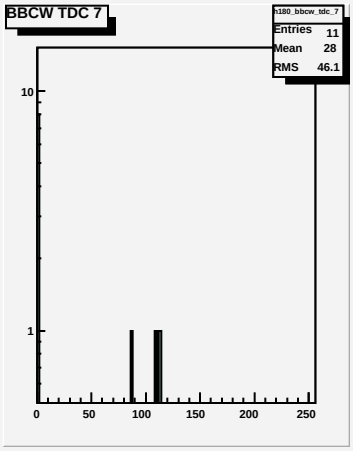

BBCW TDC 2
h175_bbcw_tdc_2
Entries 11
Mean 33.82
RMS 56.37

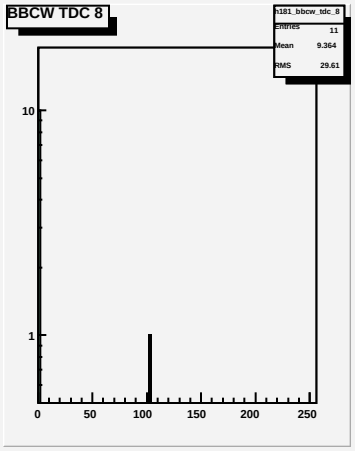

BBCW TDC 3
h176_bbcw_tdc_3
Entries 11
Mean 15.45
RMS 33.32

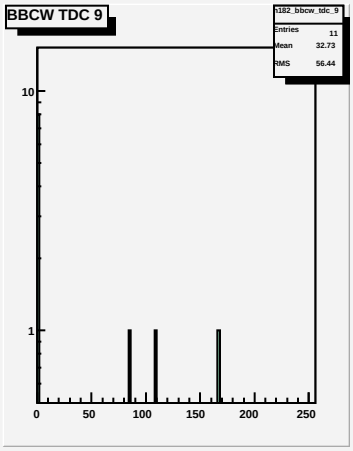

BBCW TDC 4
h177_bbcw_tdc_4
Entries 11
Mean 23
RMS 49.3

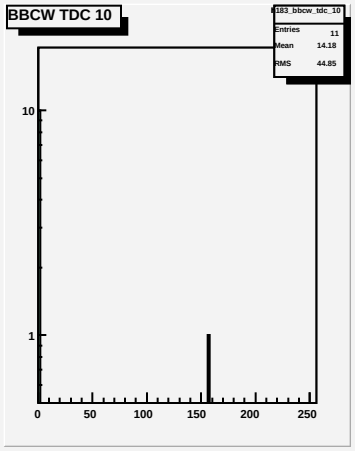

BBCW TDC 5
h178_bbcw_tdc_5
Entries 11
Mean 19.64
RMS 45.69

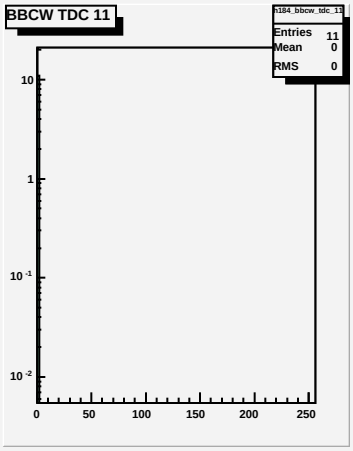

BBCW TDC 6
h179_bbcw_tdc_6
Entries 11
Mean 15
RMS 47.43

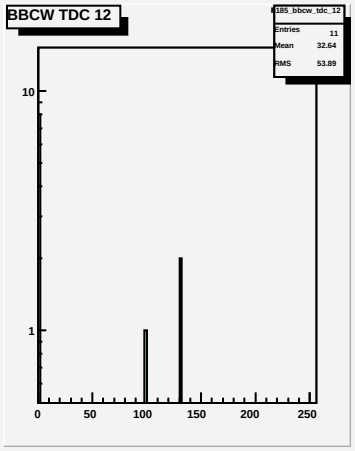

BBCW TDC 7
h180_bbcw_tdc_7
Entries 11
Mean 28
RMS 46.1

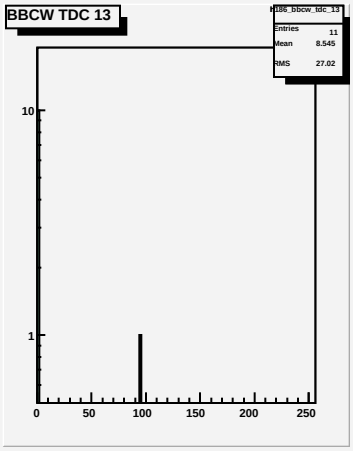

BBCW TDC 8
h181_bbcw_tdc_8
Entries 11
Mean 9.364
RMS 29.61

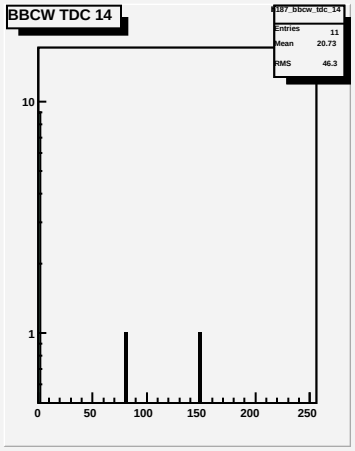

BBCW TDC 9
h182_bbcw_tdc_9
Entries 11
Mean 32.73
RMS 56.44

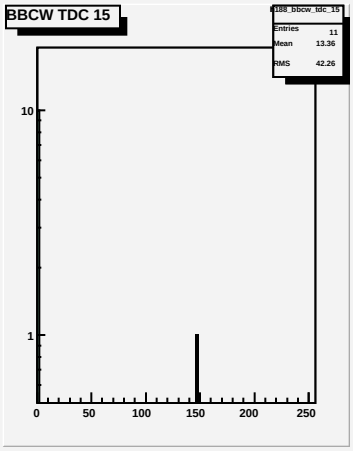

BBCW TDC 10
h183_bbcw_tdc_10
Entries 11
Mean 14.18
RMS 44.85

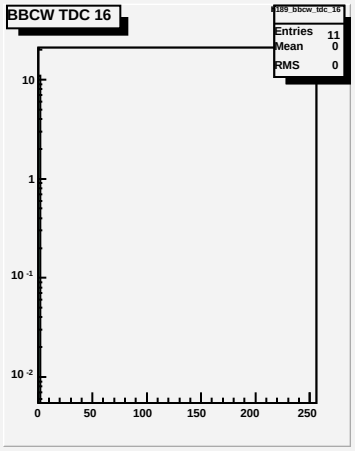

BBCW TDC 11
h184_bbcw_tdc_11
Entries 11
Mean 0
RMS 0

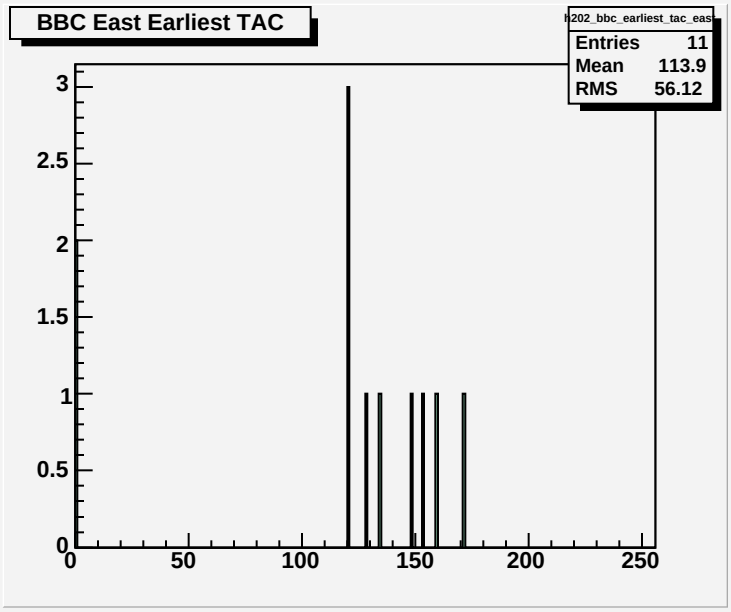

BBCW TDC 12
h185_bbcw_tdc_12
Entries 11
Mean 32.64
RMS 53.89

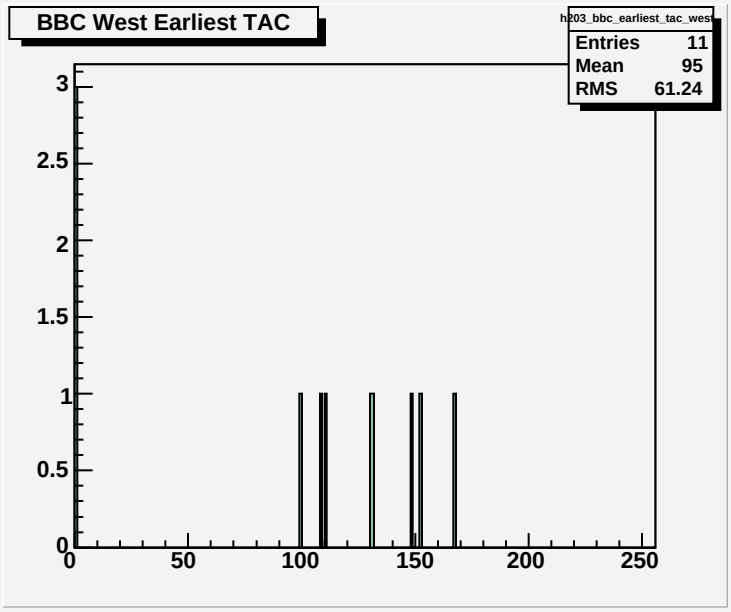

BBCW TDC 13
h186_bbcw_tdc_13
Entries 11
Mean 8.545
RMS 27.02

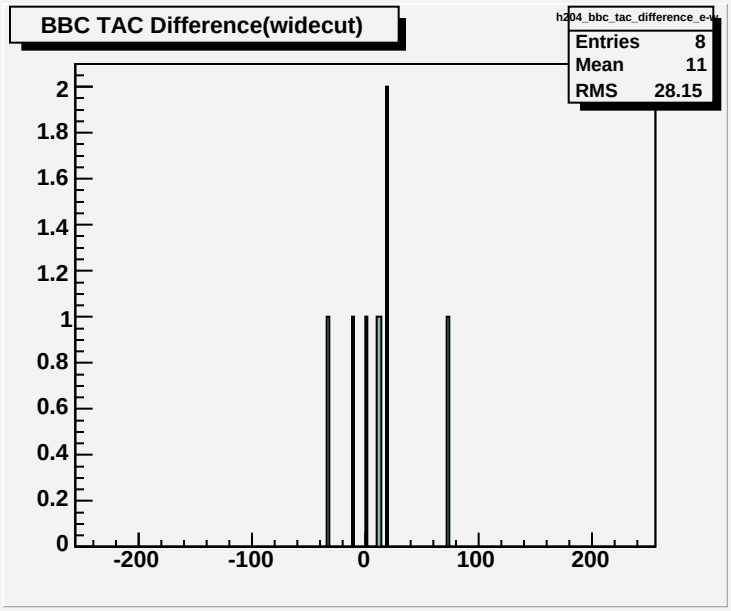

BBCW TDC 14
h187_bbcw_tdc_14
Entries 11
Mean 20.73
RMS 46.3

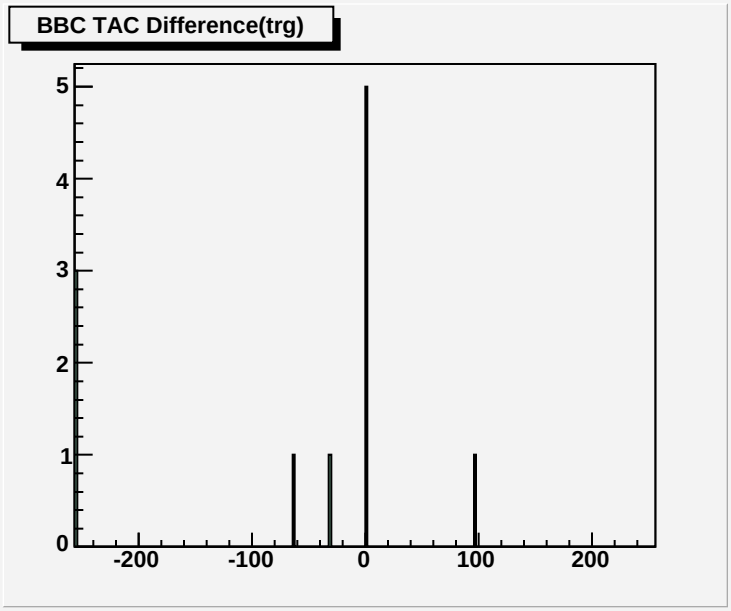

BBCW TDC 15
h188_bbcw_tdc_15
Entries 11
Mean 13.36
RMS 42.26

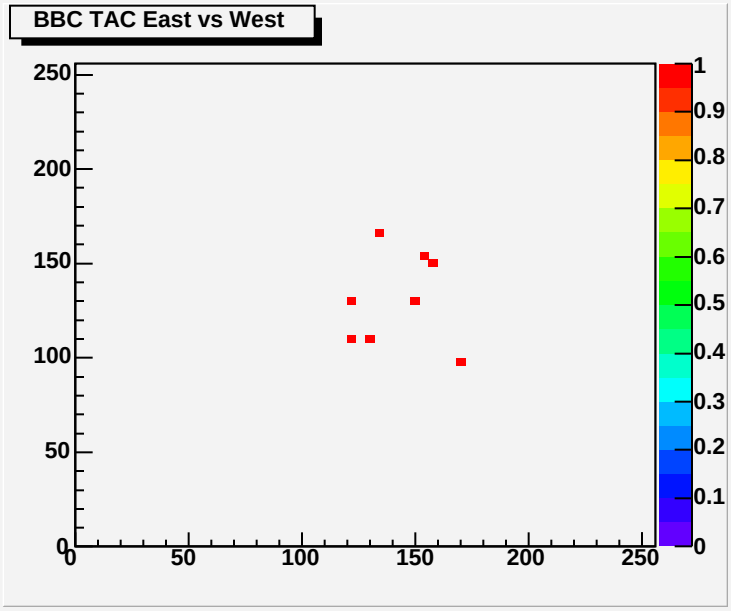

BBCW TDC 16
h189_bbcw_tdc_16
Entries 11
Mean 0
RMS 0

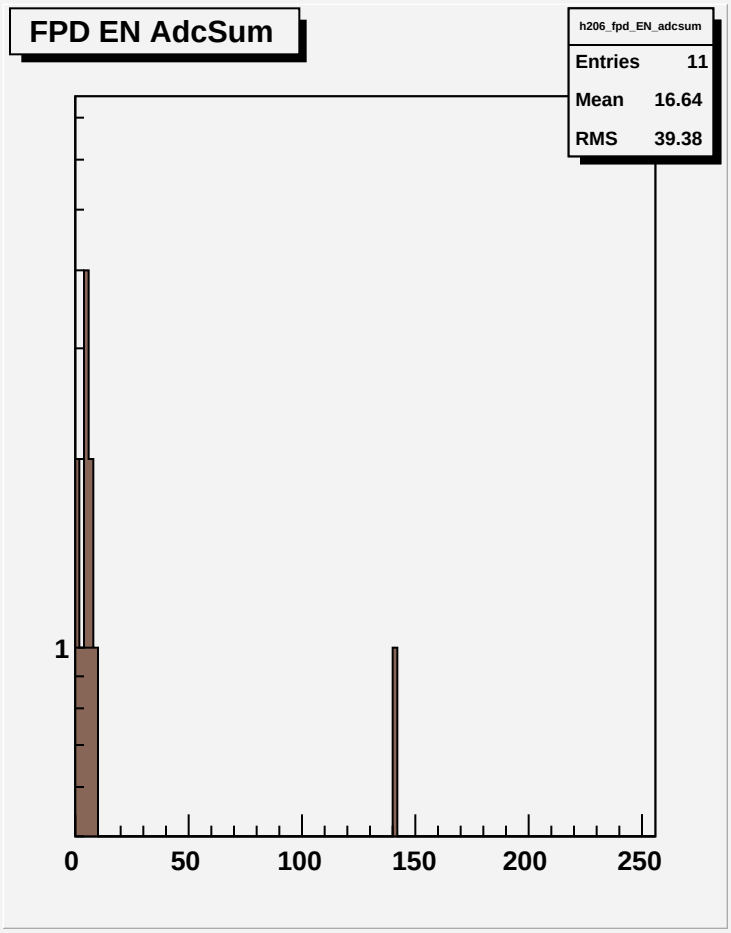

BBC East Earliest TAC
h202_bbc_earliest_tac_east
Entries 11
Mean 113.9
RMS 56.12

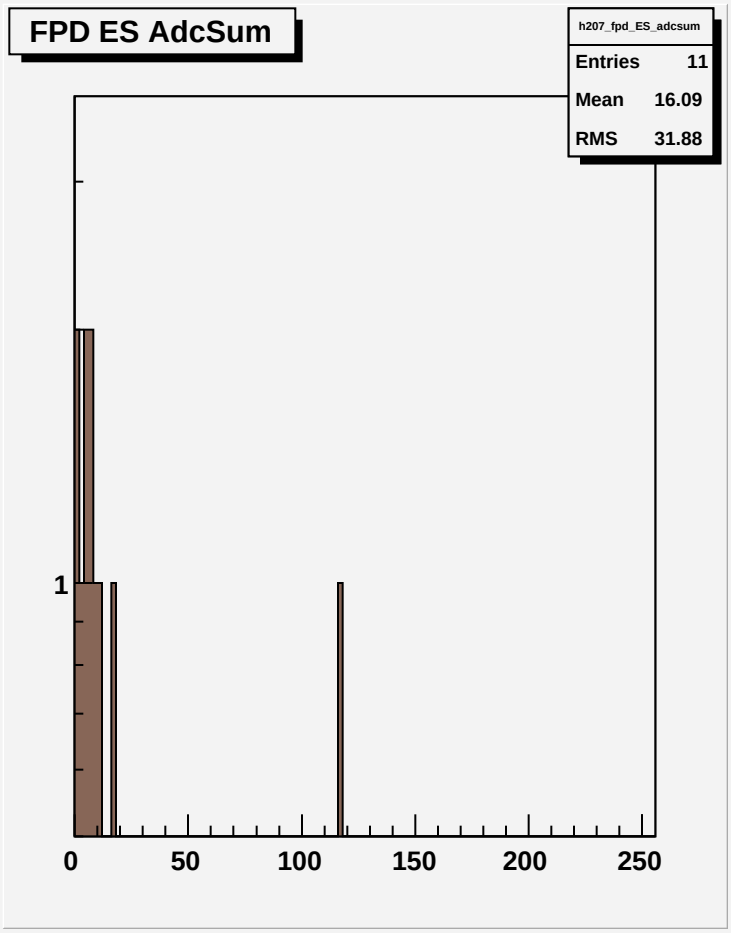

BBC West Earliest TAC
h203_bbc_earliest_tac_west
Entries 11
Mean 95
RMS 61.24

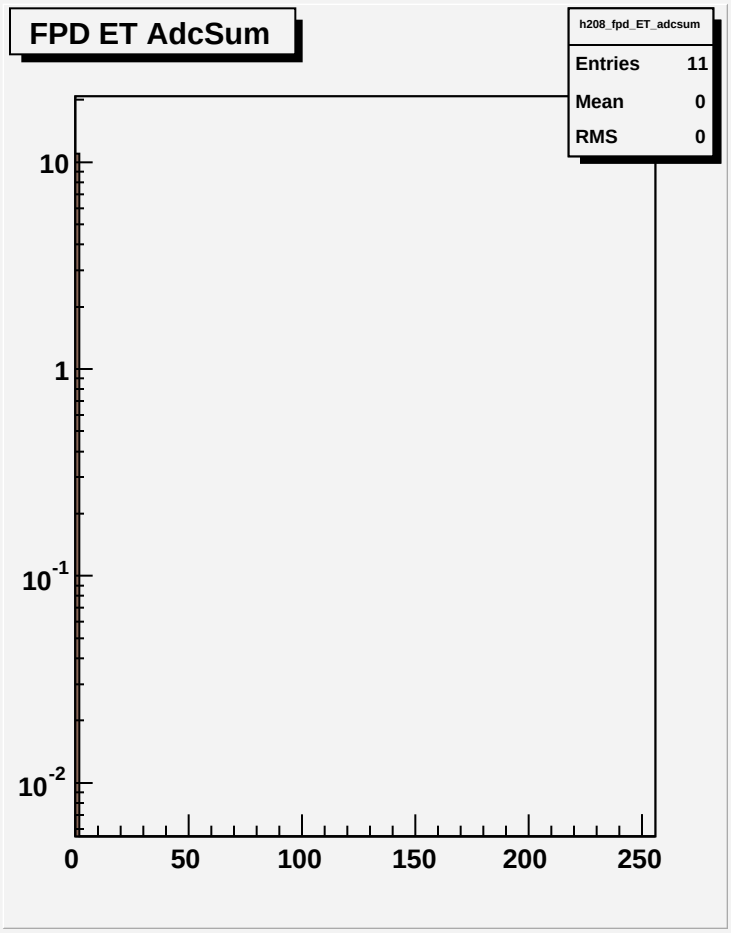

BBC TAC Difference(widecut)
h204_bbc_tac_difference_e-w
Entries 8
Mean 11
RMS 28.15

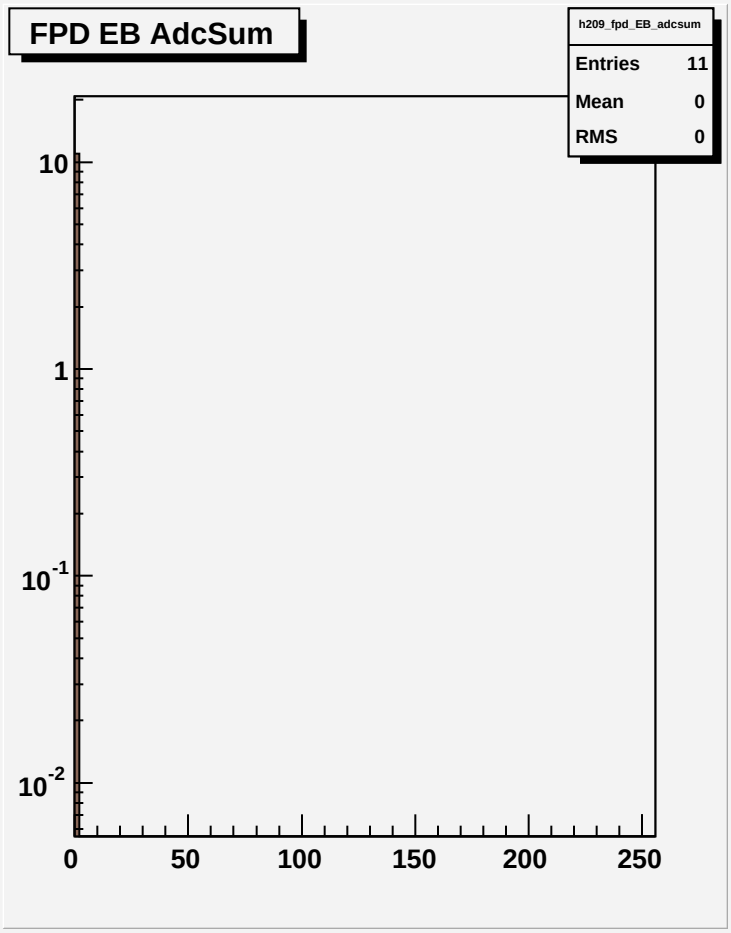

BBC TAC Difference(trg)

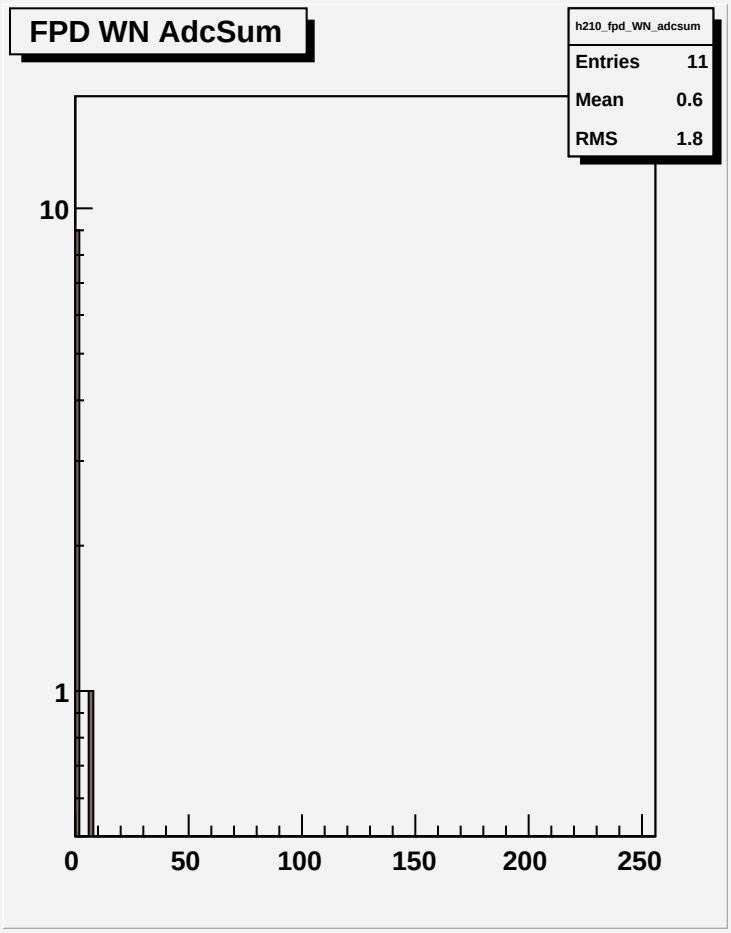

BBC TAC East vs West

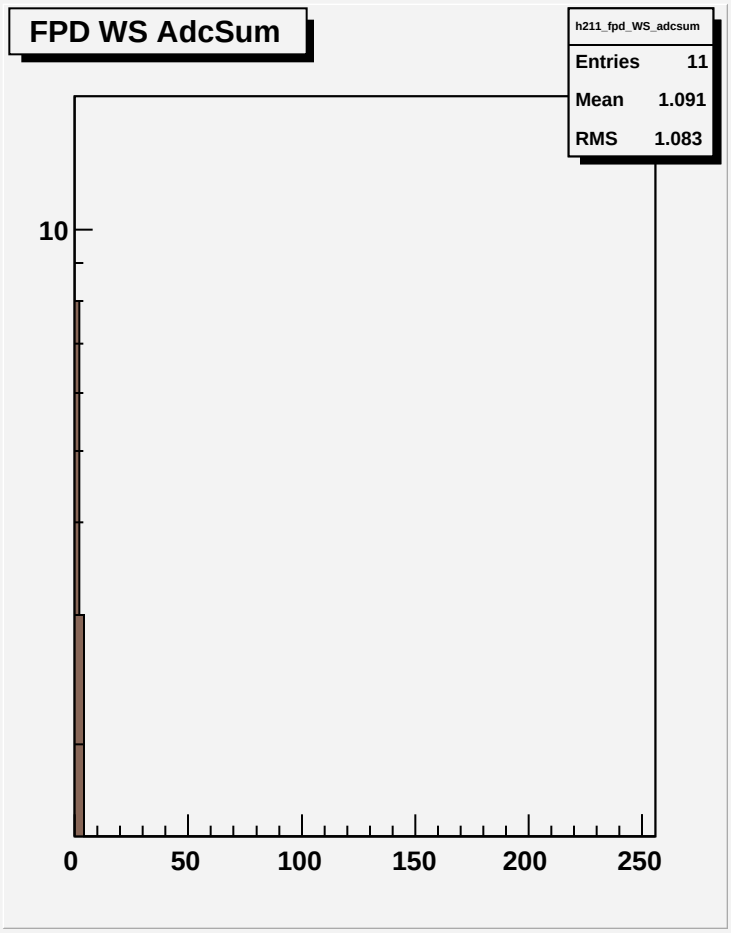

FPD EN AdcSum
h206_fpd_EN_adcsum
Entries 11
Mean 16.64
RMS 39.38
1
0
50
100
150
200
250

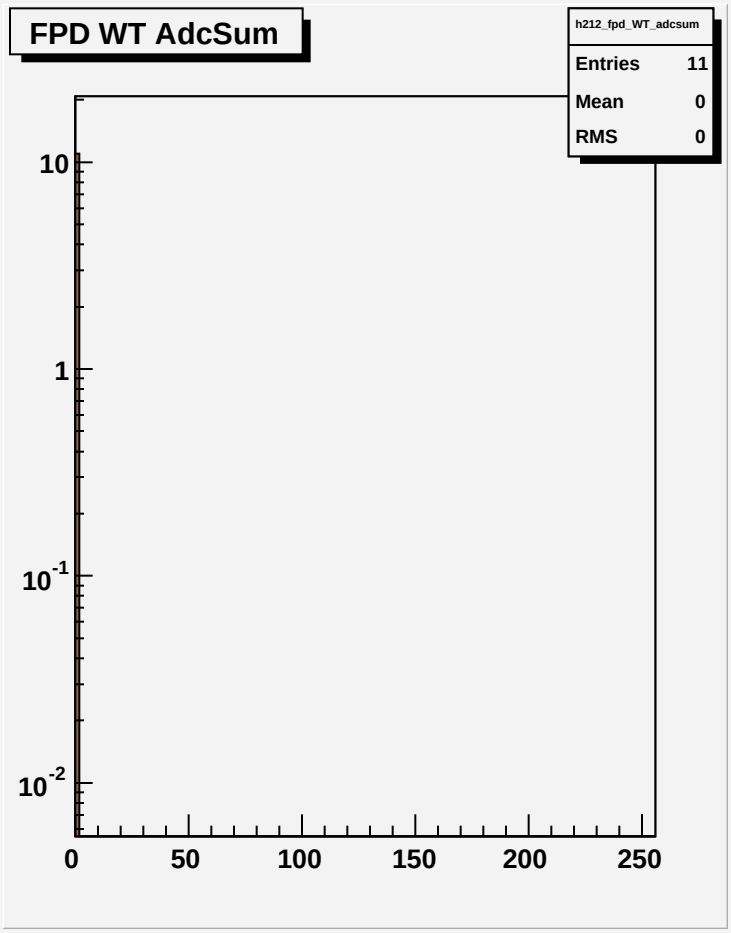

FPD ES AdcSum
h207_fpd_ES_adcsum
Entries 11
Mean 16.09
RMS 31.88
1
0
50
100
150
200
250

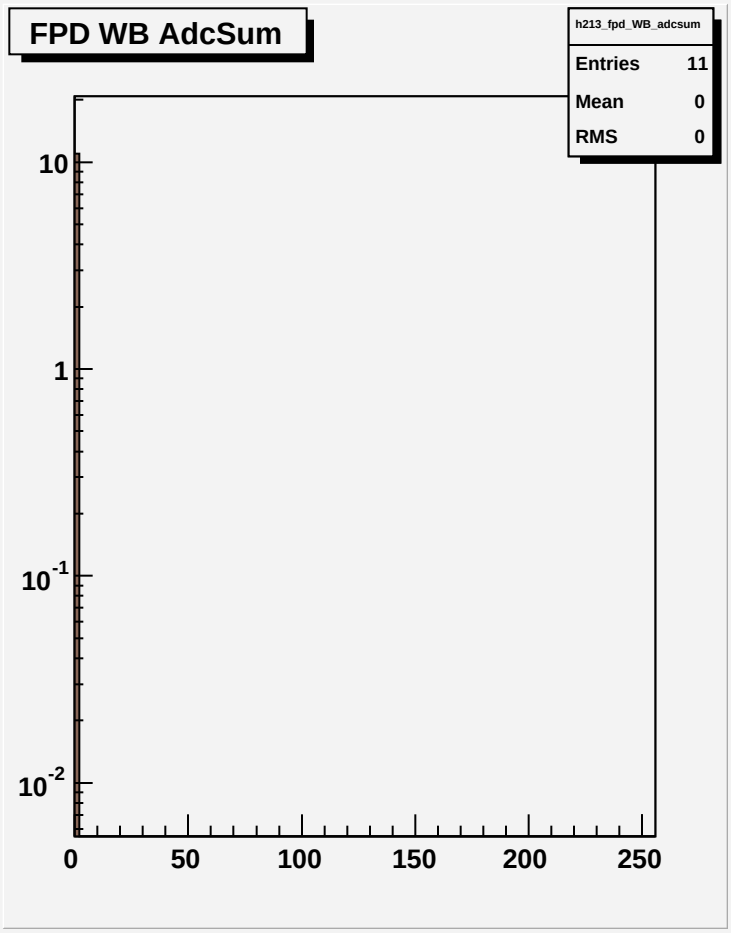

FPD ET AdcSum
h208_fpd_ET_adcsum
Entries 11
Mean 0
RMS 0
10
1
10^-1
10^-2
0
50
100
150
200
250

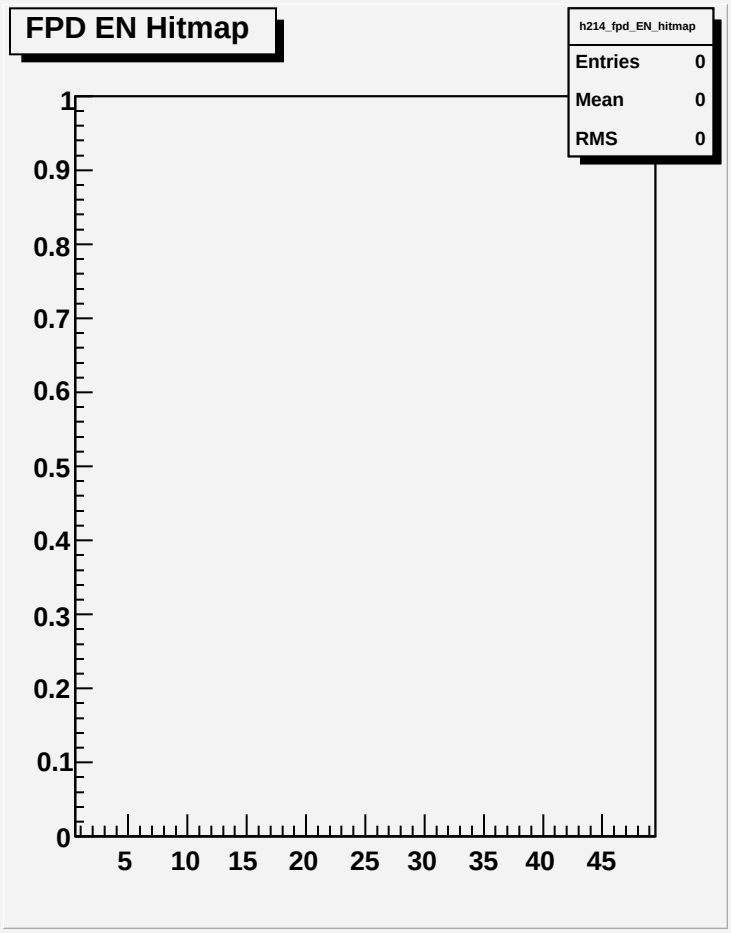

FPD EB AdcSum
h209_fpd_EB_adcsum
Entries 11
Mean 0
RMS 0
10
1
10^-1
10^-2
0
50
100
150
200
250

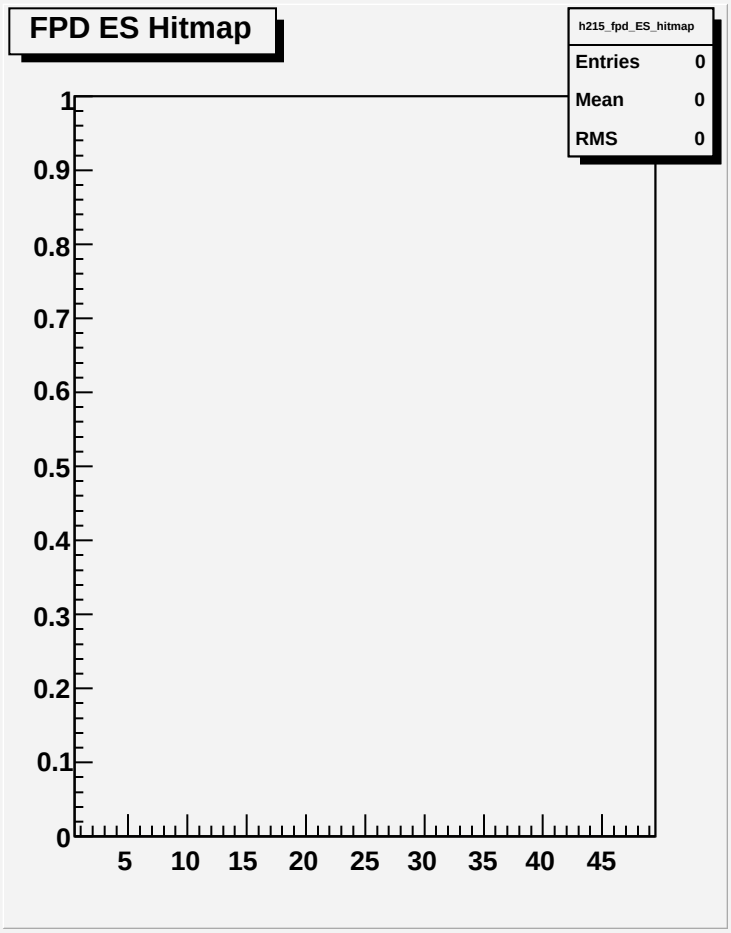

FPD WN AdcSum
h210_fpd_WN_adcsum
Entries 11
Mean 0.6
RMS 1.8
10
1
0
50
100
150
200
250

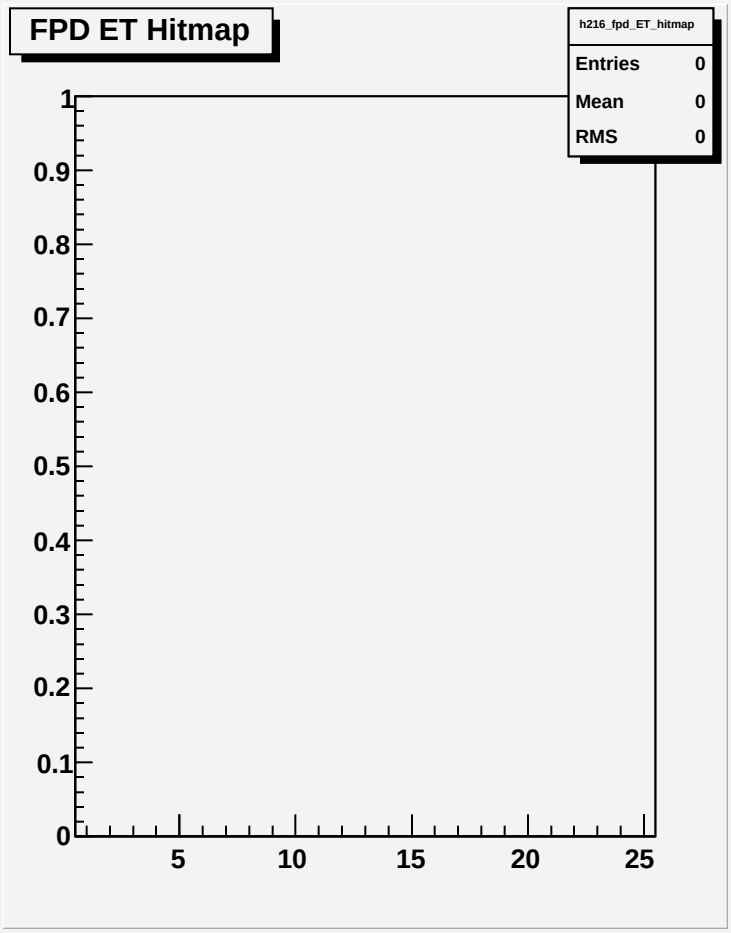

FPD WS AdcSum
h211_fpd_WS_adcsum
Entries 11
Mean 1.091
RMS 1.083
10
0
50
100
150
200
250

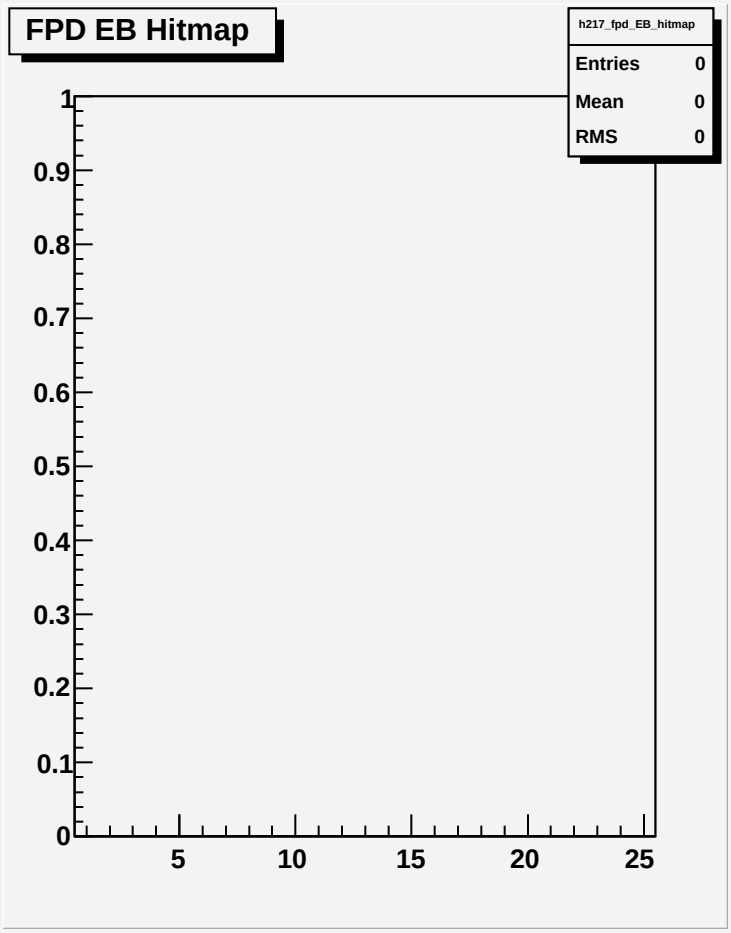

FPD WT AdcSum
h212_fpd_WT_adcsum
Entries 11
Mean 0
RMS 0
10
1
10^-1
10^-2
0
50
100
150
200
250

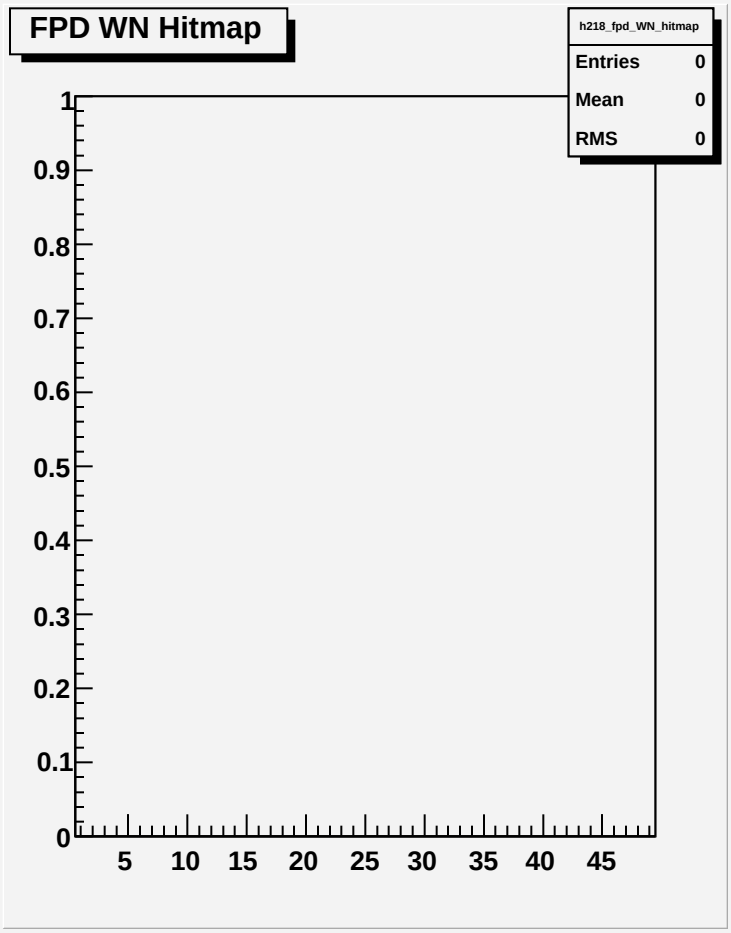

FPD WB AdcSum
h213_fpd_WB_adcsum
Entries 11
Mean 0
RMS 0
10
1
10^-1
10^-2
0
50
100
150
200
250

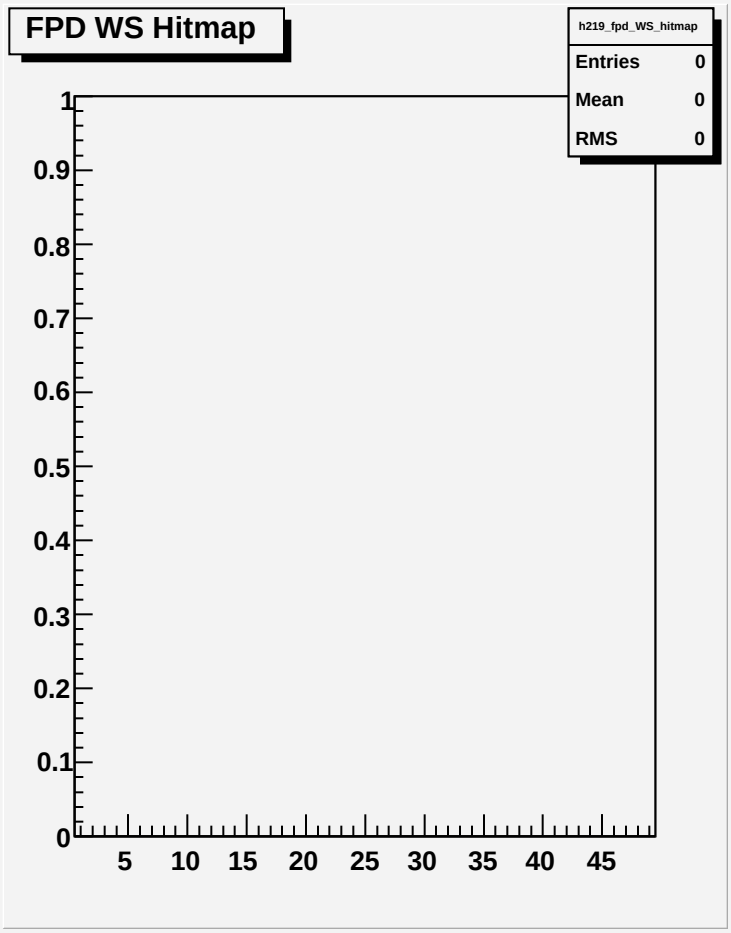

FPD EN Hitmap
h214_fpd_EN_hitmap
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
5 10 15 20 25 30 35 40 45

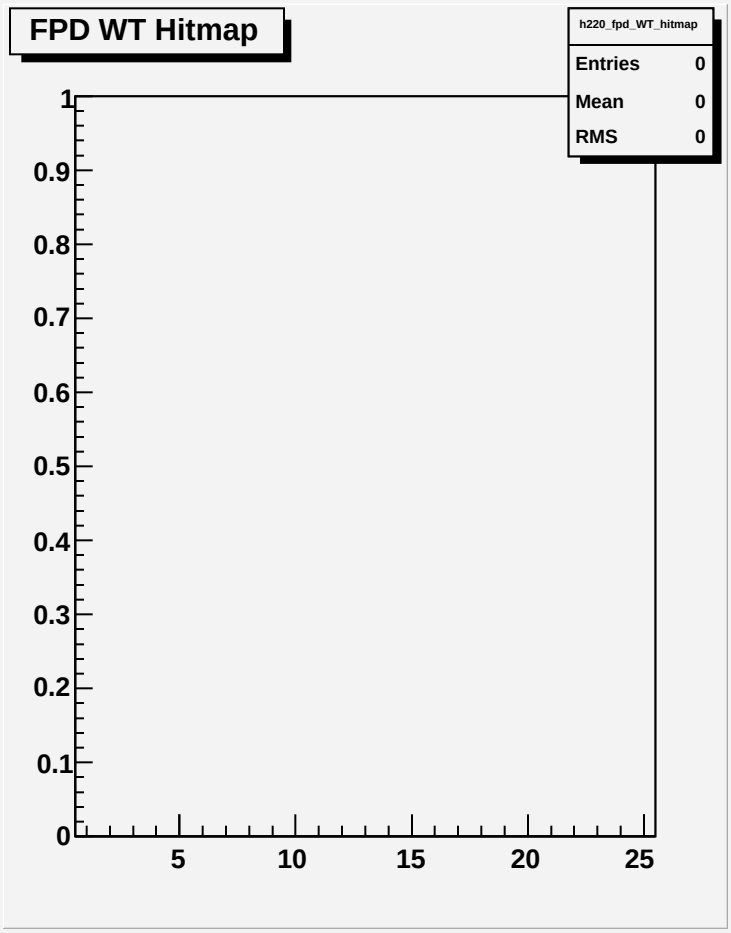

FPD ES Hitmap
h215_fpd_ES_hitmap
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
5 10 15 20 25 30 35 40 45


FPD ET Hitmap
h216_fpd_ET_hitmap
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
5 10 15 20 25


FPD EB Hitmap
h217_fpd_EB_hitmap
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
5 10 15 20 25


FPD WN Hitmap
h218_fpd_WN_hitmap
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
5 10 15 20 25 30 35 40 45


FPD WS Hitmap
h219_fpd_WS_hitmap
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
5 10 15 20 25 30 35 40 45


FPD WT Hitmap
h220_fpd_WT_hitmap
Entries 0
Mean 0
RMS 0
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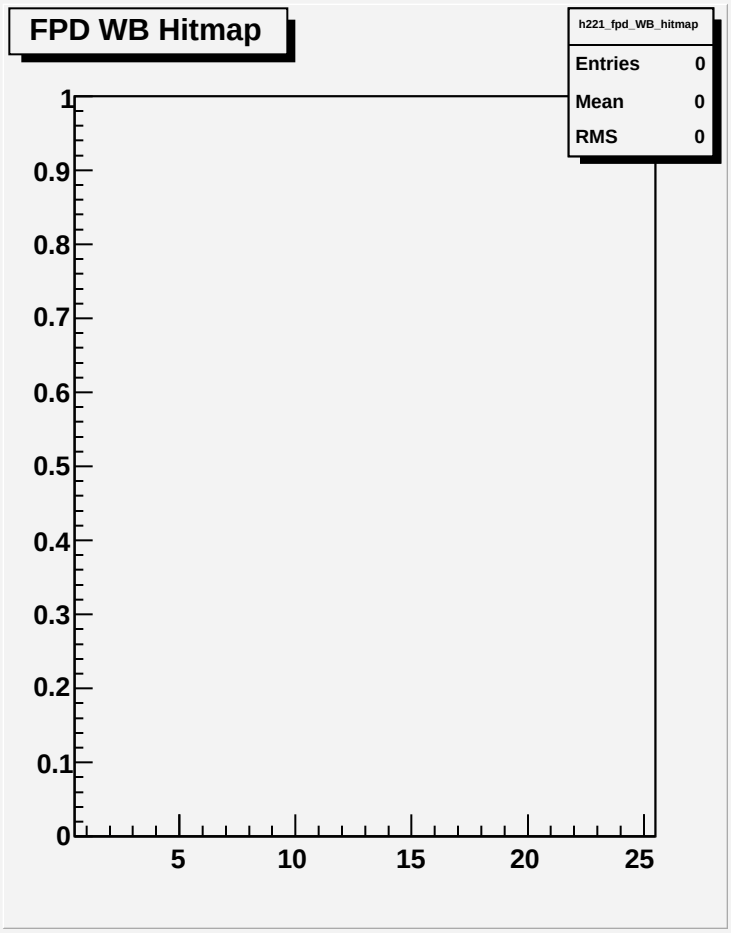
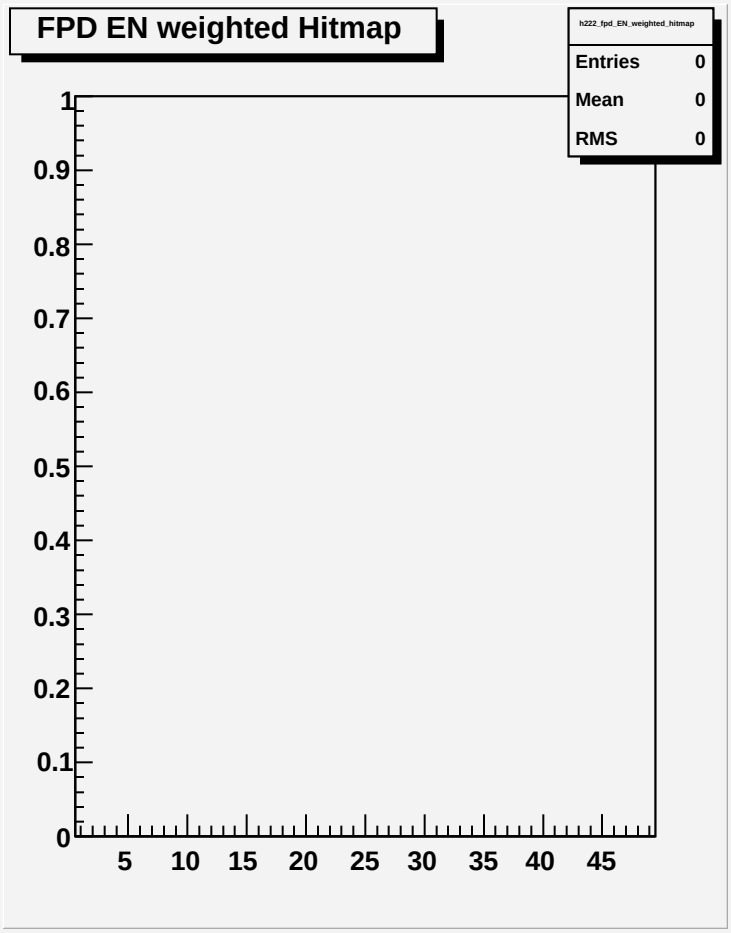
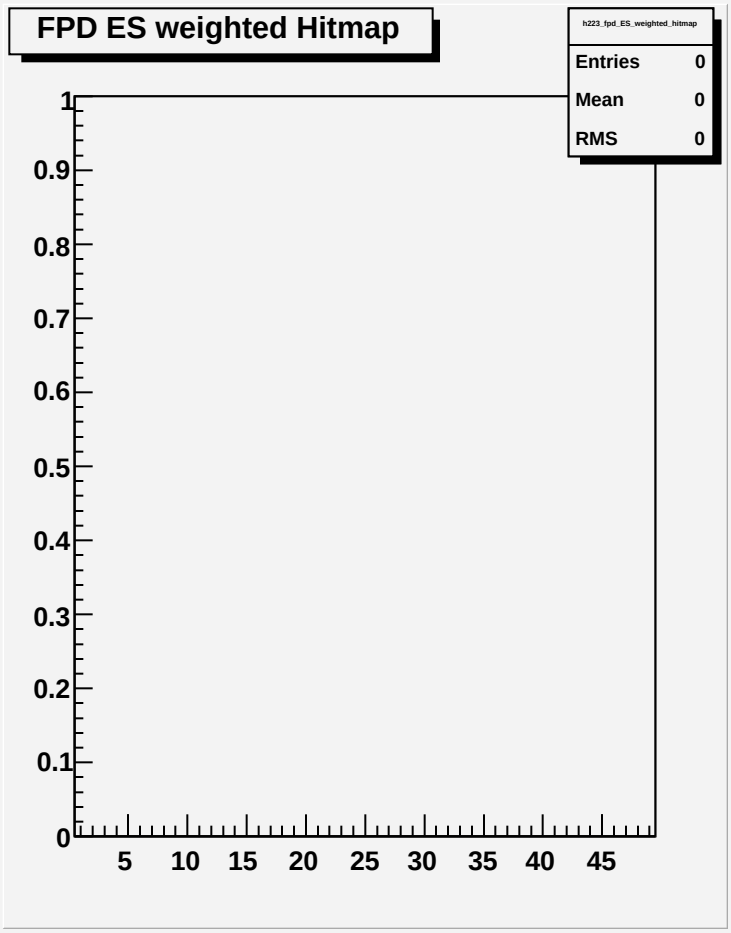
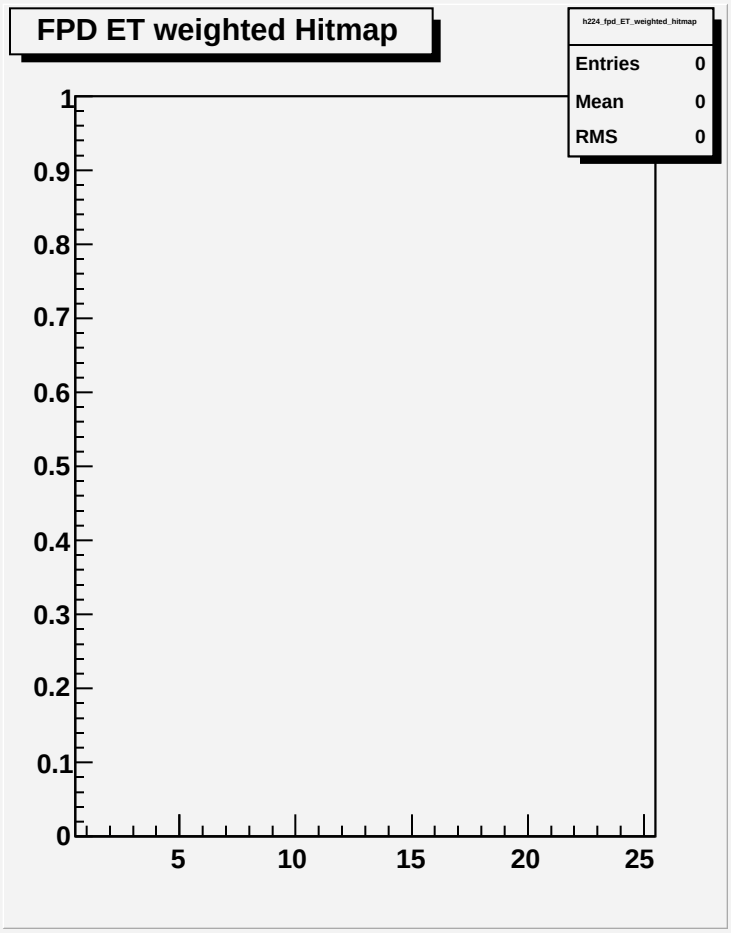
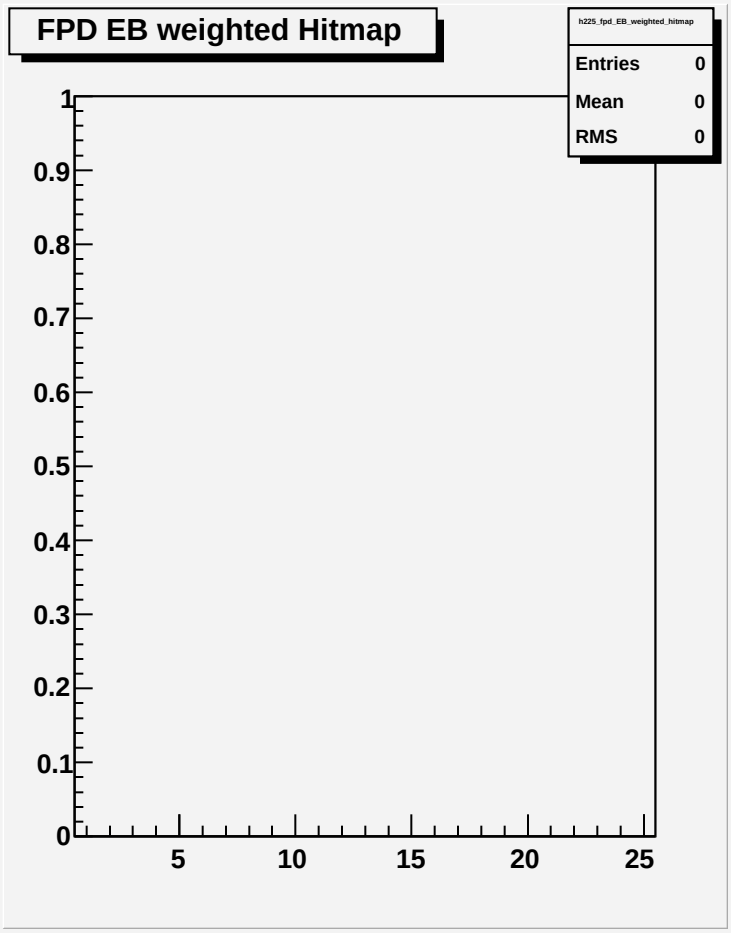
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
5 10 15 20 25

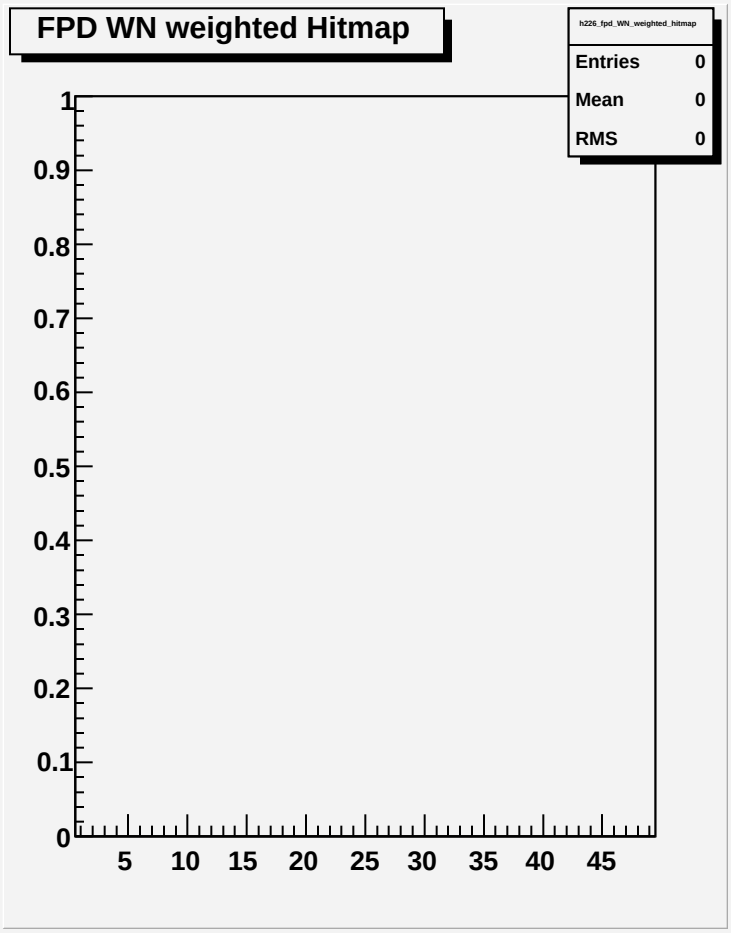

FPD WB Hitmap
h221_fpd_WB_hitmap
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
5 10 15 20 25

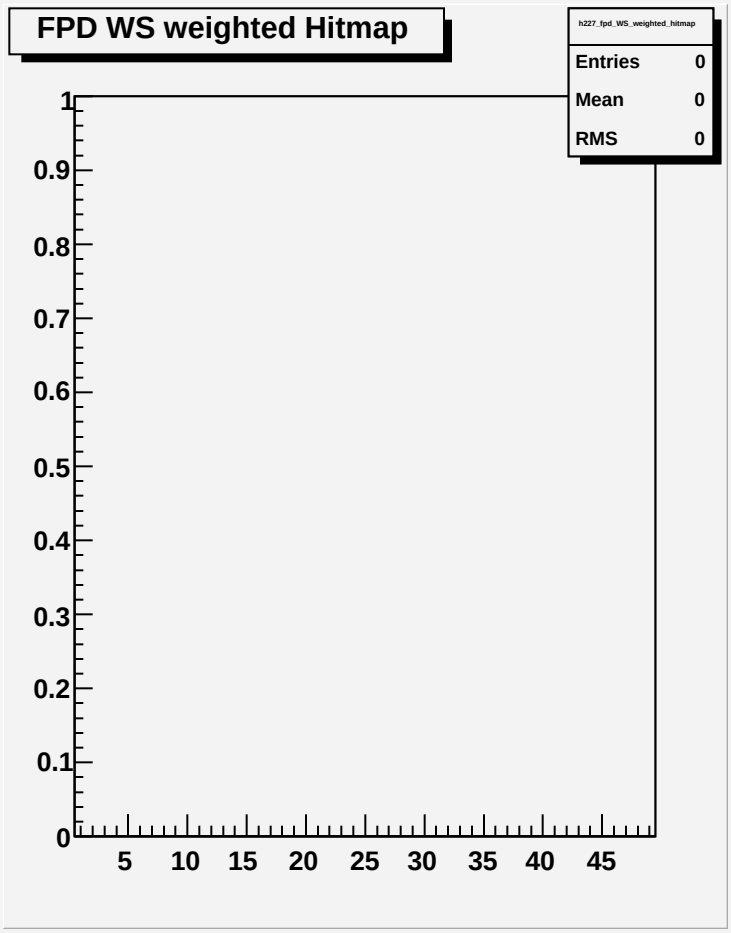

FPD EN weighted Hitmap
h222_fpd_EN_weighted_hitmap
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
5 10 15 20 25 30 35 40 45

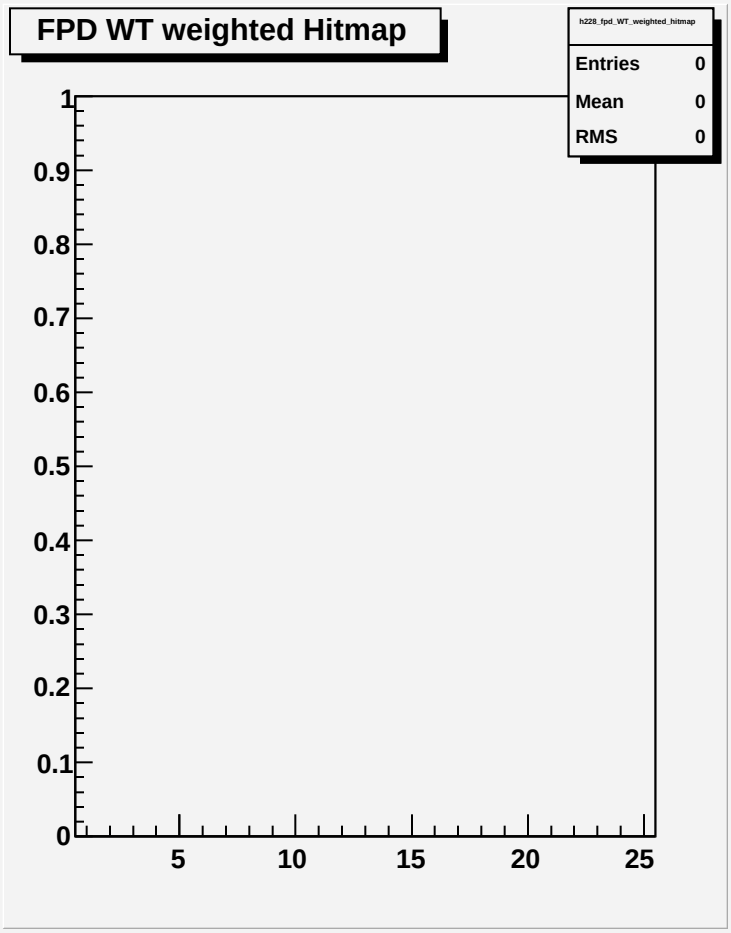

FPD ES weighted Hitmap
h223_fpd_ES_weighted_hitmap
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
5 10 15 20 25 30 35 40 45

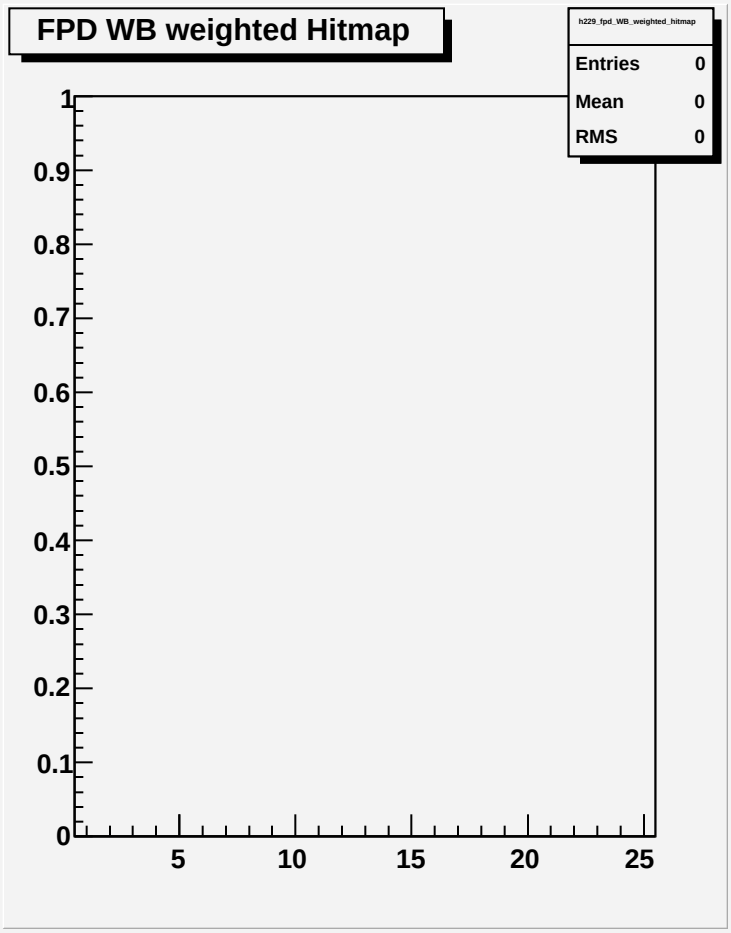

FPD ET weighted Hitmap
h224_fpd_ET_weighted_hitmap
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
5 10 15 20 25

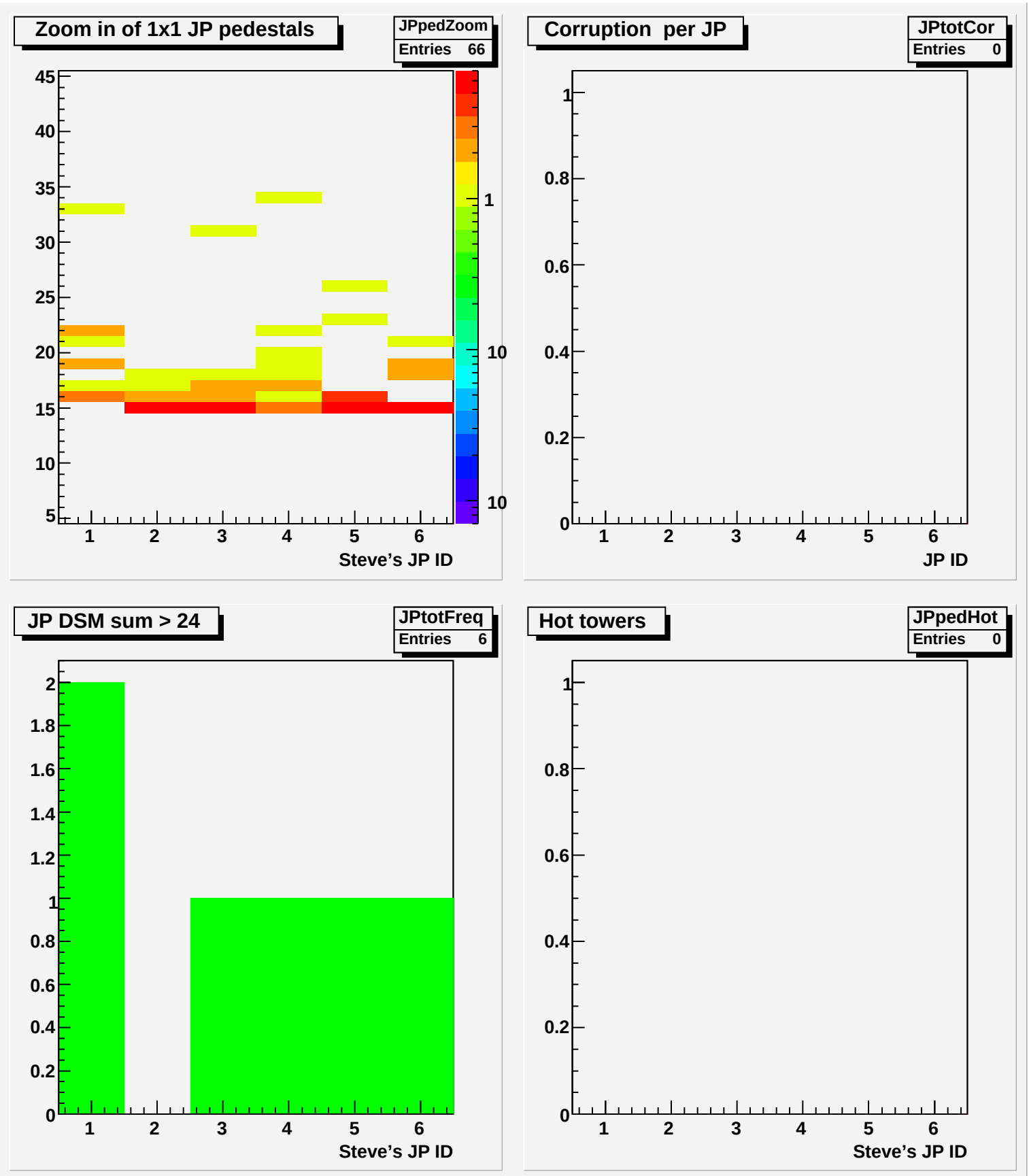

FPD EB weighted Hitmap
h225_fpd_EB_weighted_hitmap
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
5 10 15 20 25


FPD WN weighted Hitmap
h226_fpd_WN_weighted_hitmap
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
5 10 15 20 25 30 35 40 45


FPD WS weighted Hitmap
h227_fpd_WS_weighted_hitmap
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
5 10 15 20 25 30 35 40 45

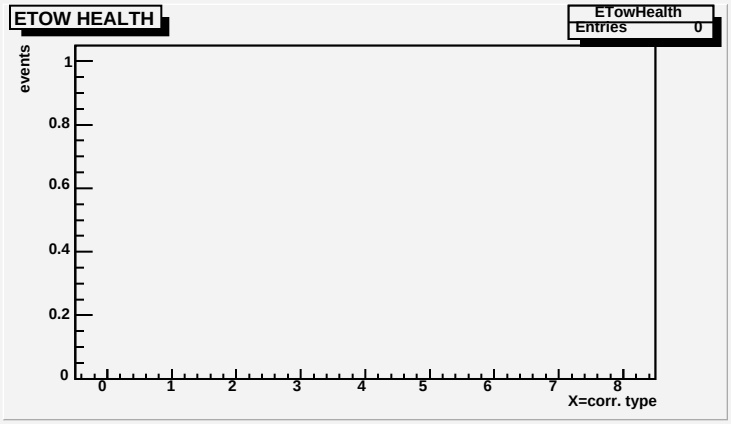

FPD WT weighted Hitmap
h228_fpd_WT_weighted_hitmap
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
5 10 15 20 25

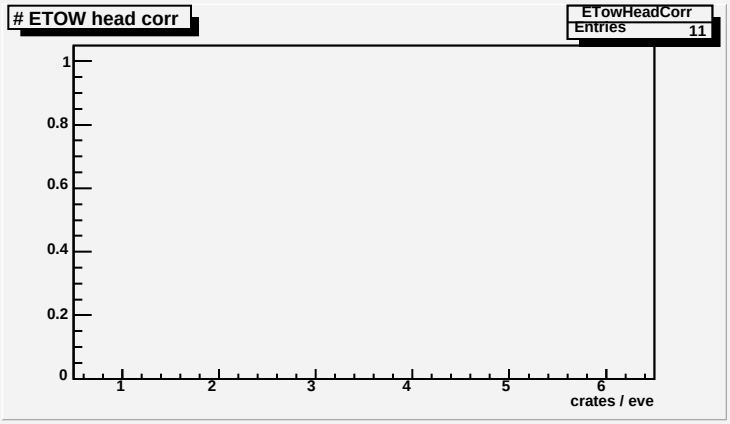

FPD WB weighted Hitmap
h229_fpd_WB_weighted_hitmap
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
5 10 15 20 25

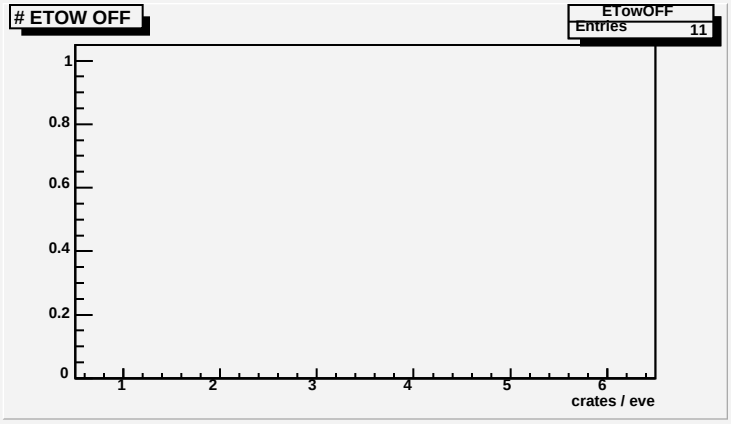


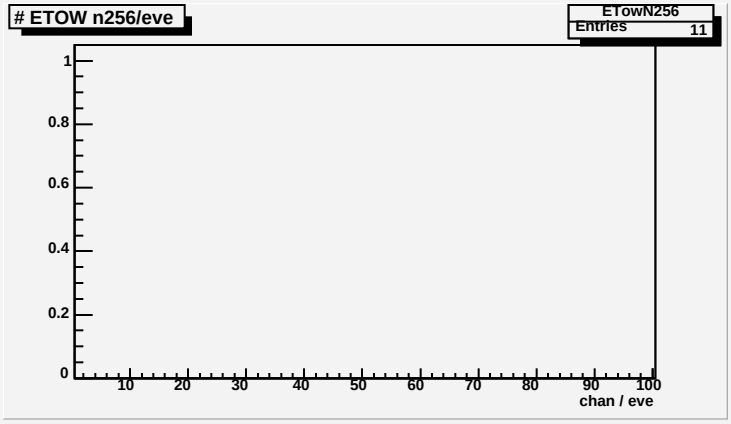
ETOW masked (cr-ch-name): 1-97-12TC05, 4-117-07TC05, 4-18-06TA03, 1-40-12TD01,

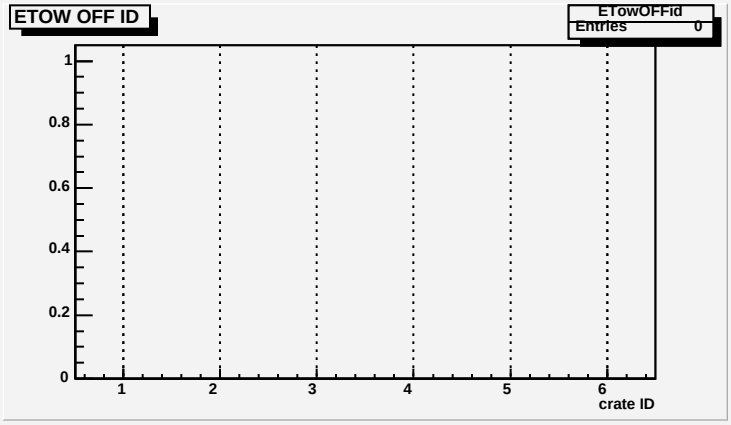
--

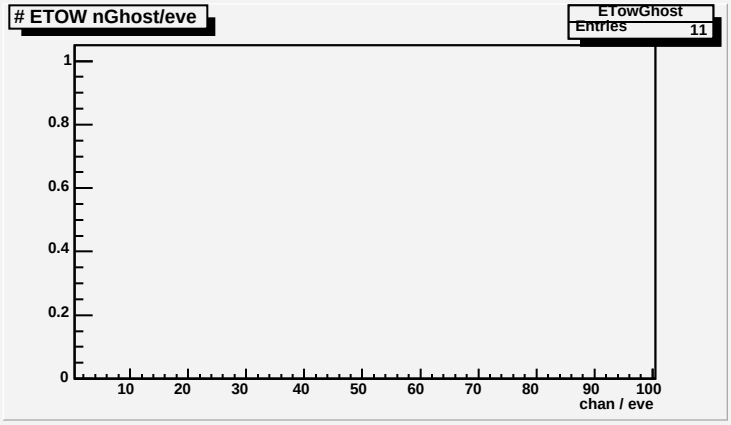

ETOW HEALTH
ETowHealth
Entries 0
events
X=corr. type

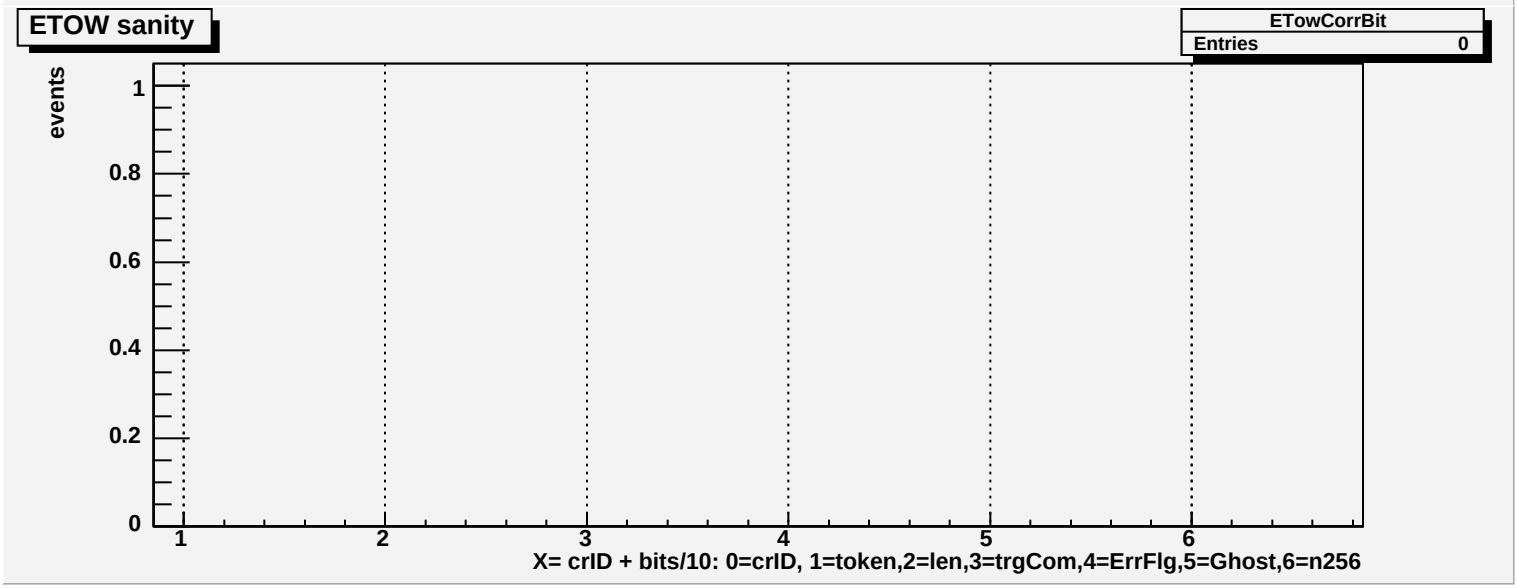

ETOW head corr
ETowHeadCorr
Entries 11
crates / eve

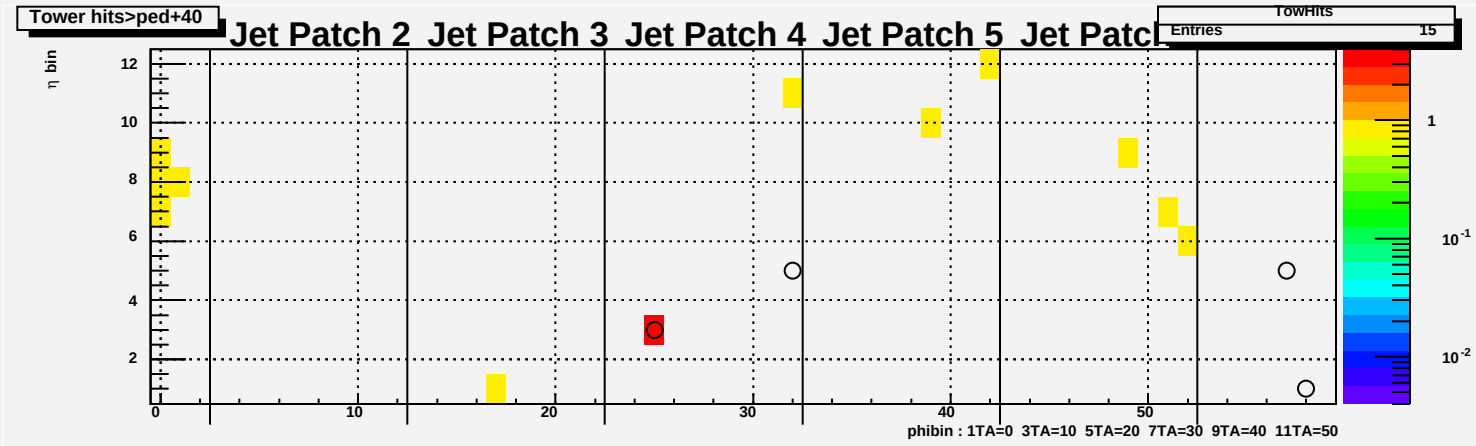

ETOW OFF
ETowOFF
Entries 11
crates / eve

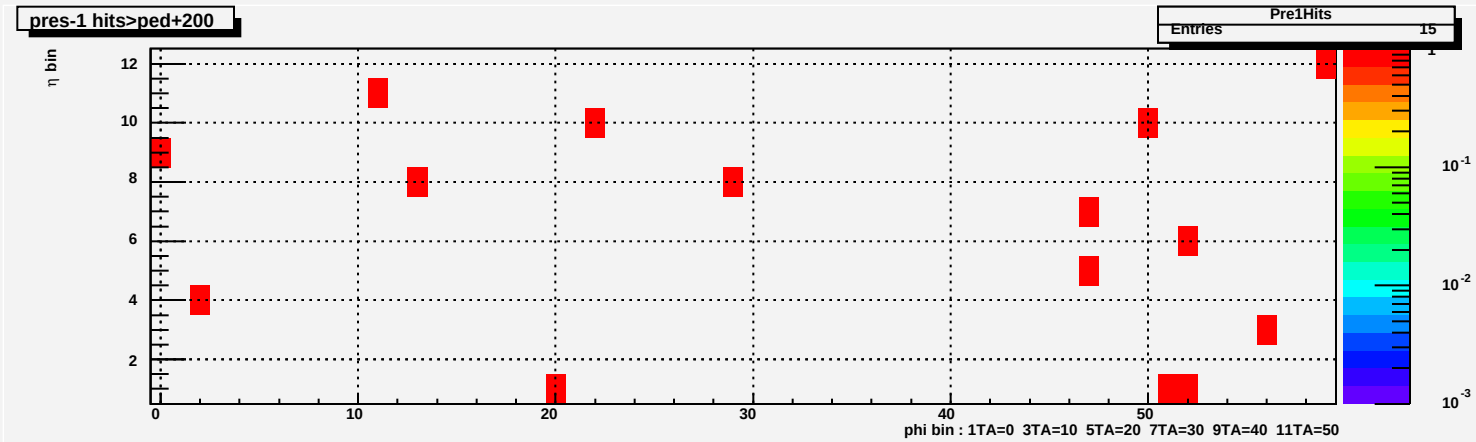

ETOW n256/eve
ETowN256
Entries 11
chan / eve

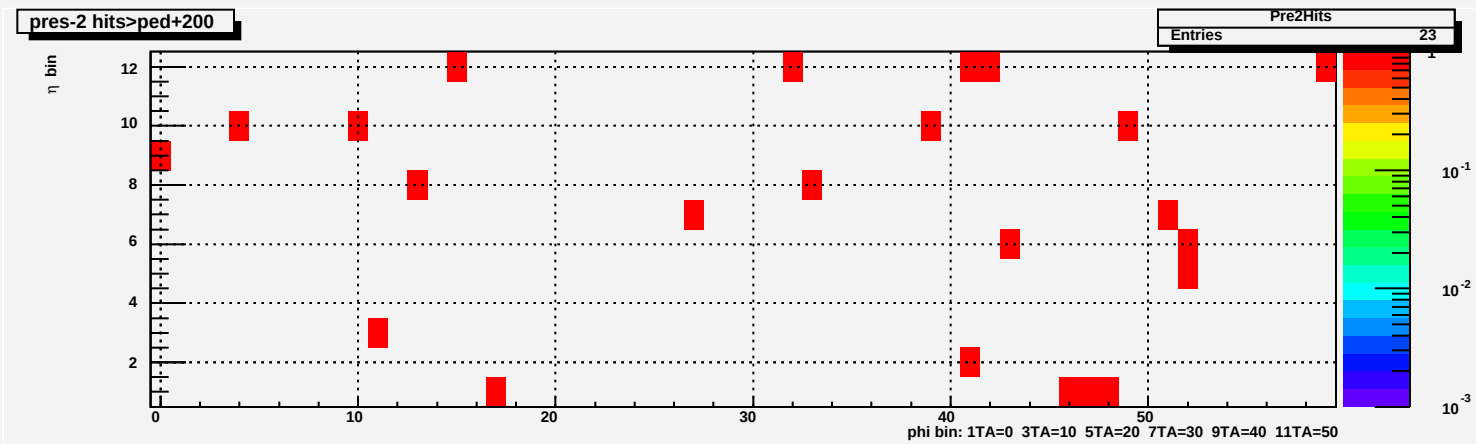

ETOW OFF ID
ETowOFFid
Entries 0
crate ID

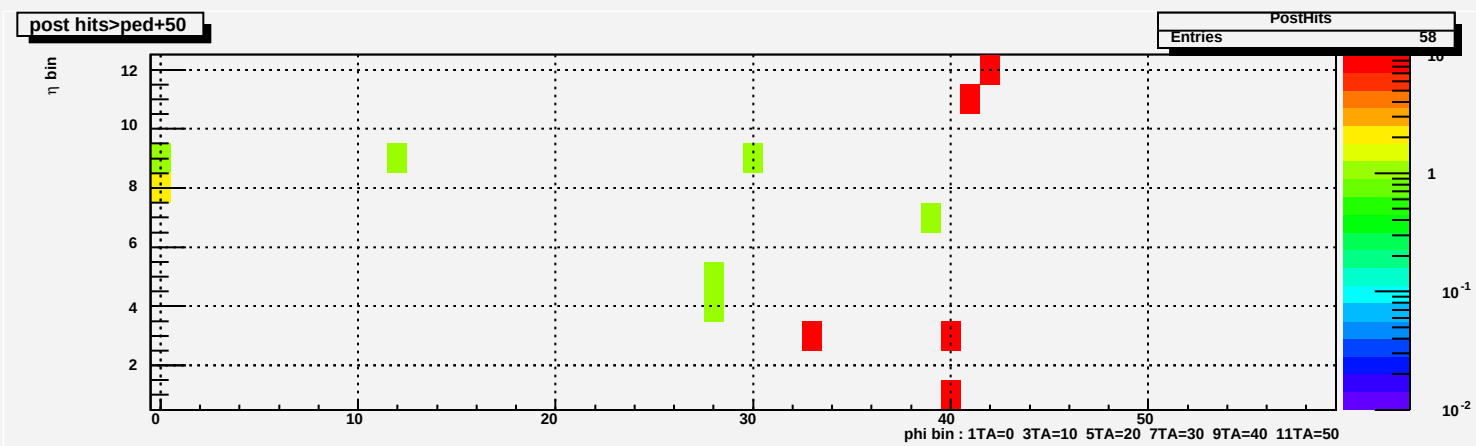

ETOW nGhost/eve
ETowGhost
Entries 11
chan / eve

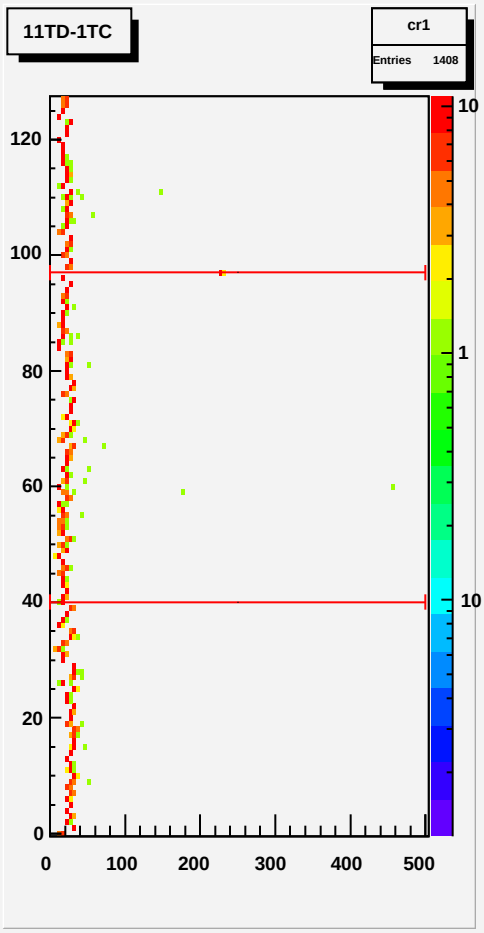

ETOW sanity
ETowCorrBit
Entries 0
events
X= crID + bits/10: 0=crID, 1=token,2=len,3=trgCom,4=ErrFlg,5=Ghost,6=n256

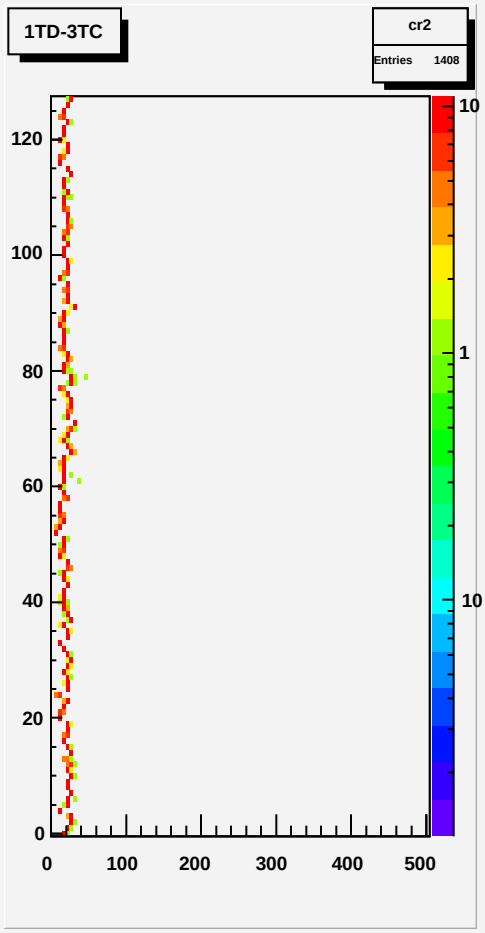

Tower hits>ped+40
Jet Patch 2 Jet Patch 3 Jet Patch 4 Jet Patch 5 Jet Patch
TowHits
Entries 15
η bin
phibin : 1TA=0 3TA=10 5TA=20 7TA=30 9TA=40 11TA=50

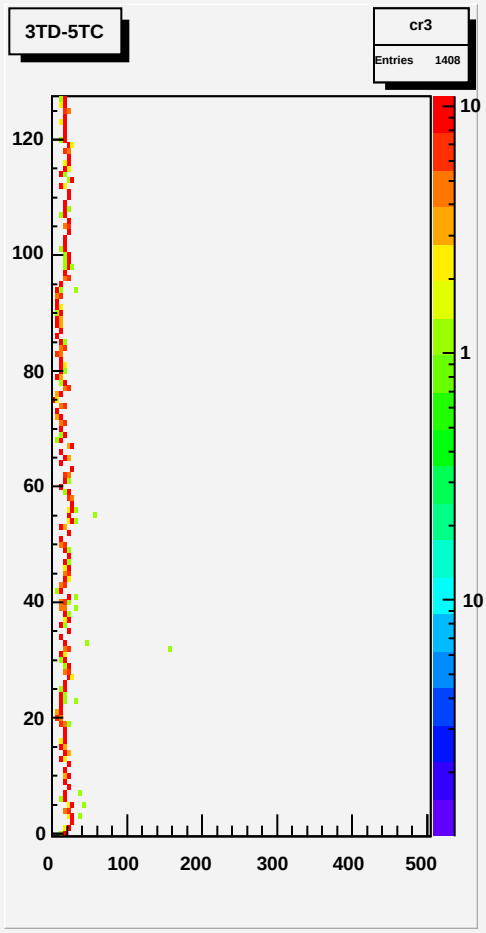

pres-1 hits>ped+200
Pre1Hits
Entries 15
η bin
phi bin : 1TA=0 3TA=10 5TA=20 7TA=30 9TA=40 11TA=50

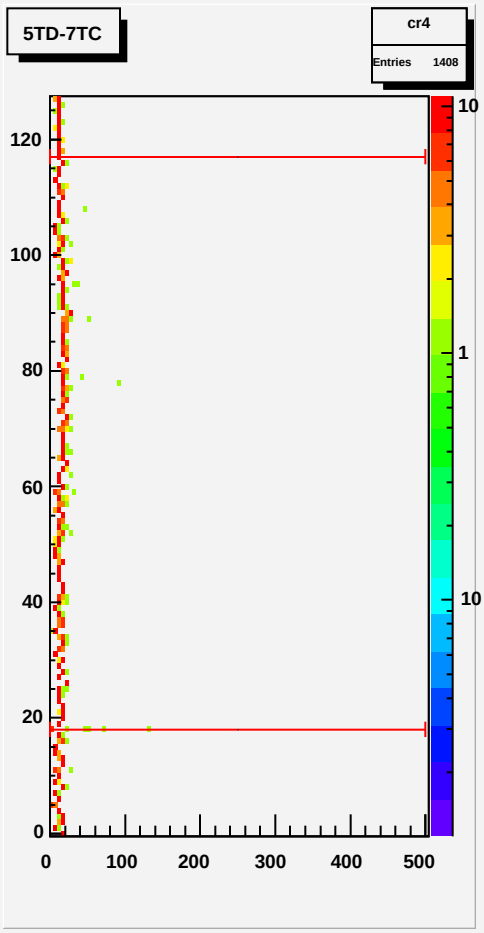

pres-2 hits>ped+200
Pre2Hits
Entries 23
η bin
phi bin: 1TA=0 3TA=10 5TA=20 7TA=30 9TA=40 11TA=50

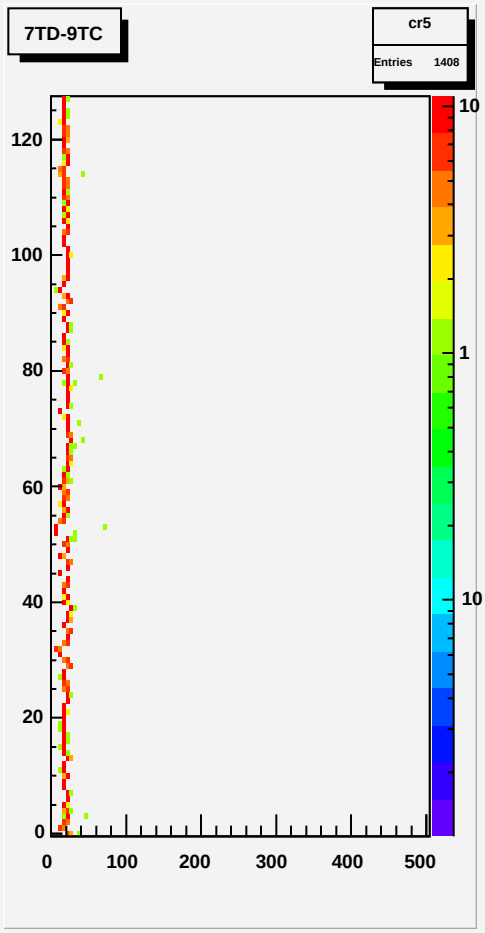

post hits>ped+50
PostHits
Entries 58
η bin
phi bin : 1TA=0 3TA=10 5TA=20 7TA=30 9TA=40 11TA=50

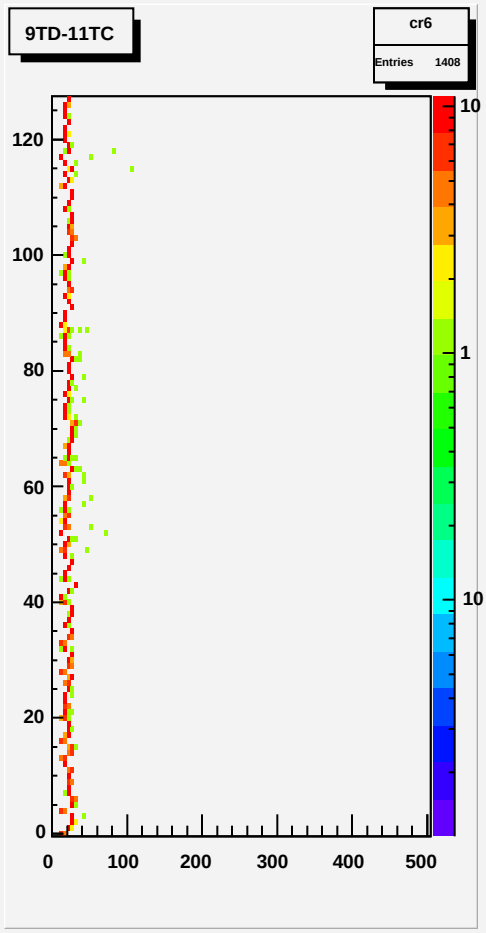

11TD-1TC
cr1
Entries 1408

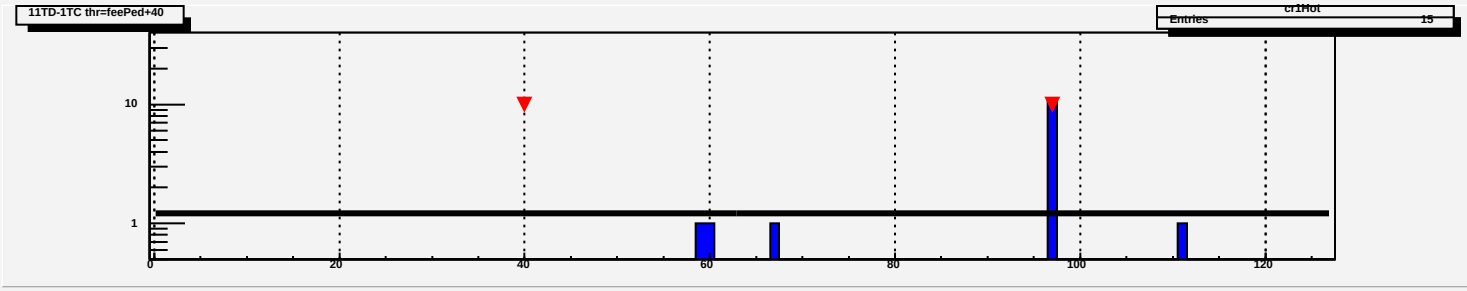

1TD-3TC
cr2
Entries 1408

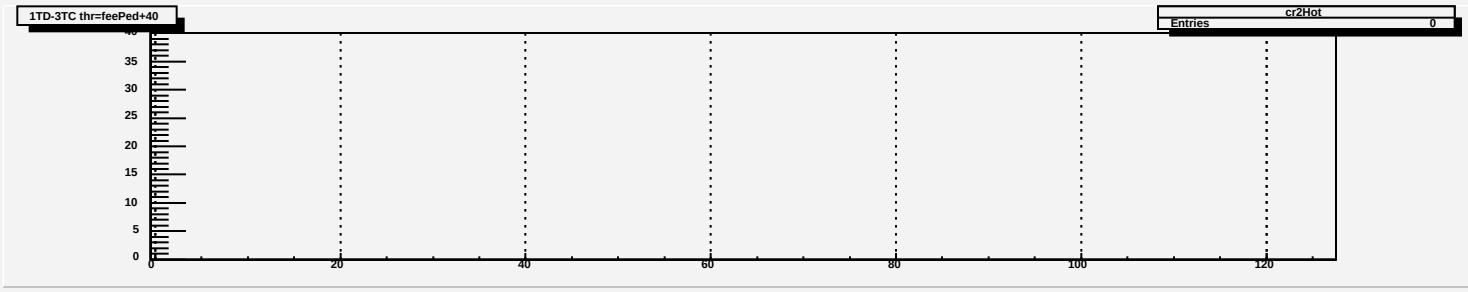

3TD-5TC
cr3
Entries 1408

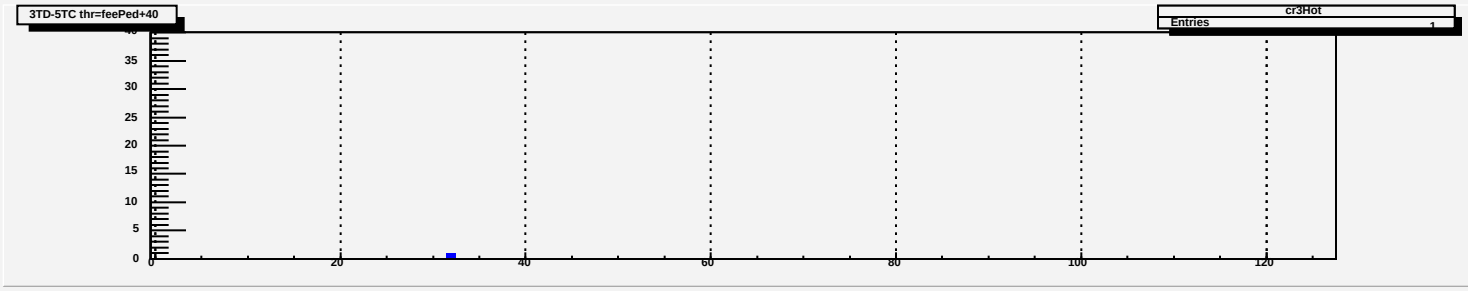

5TD-7TC
cr4
Entries 1408

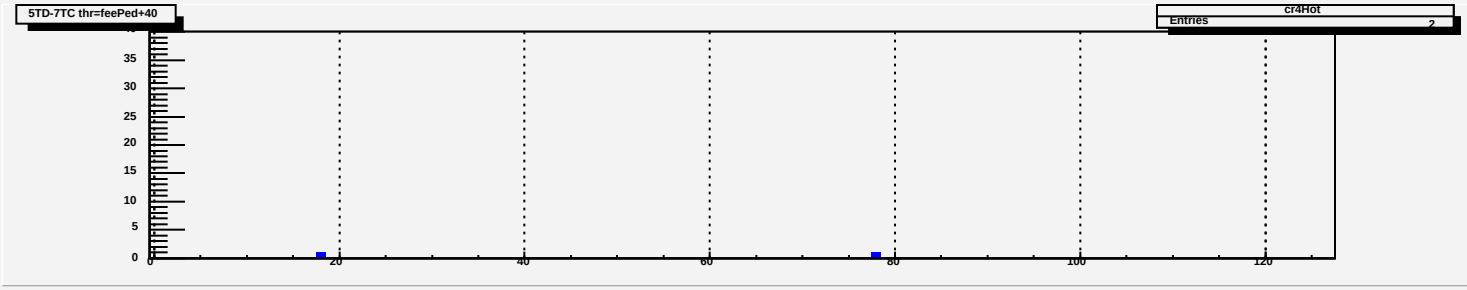

7TD-9TC
cr5
Entries 1408

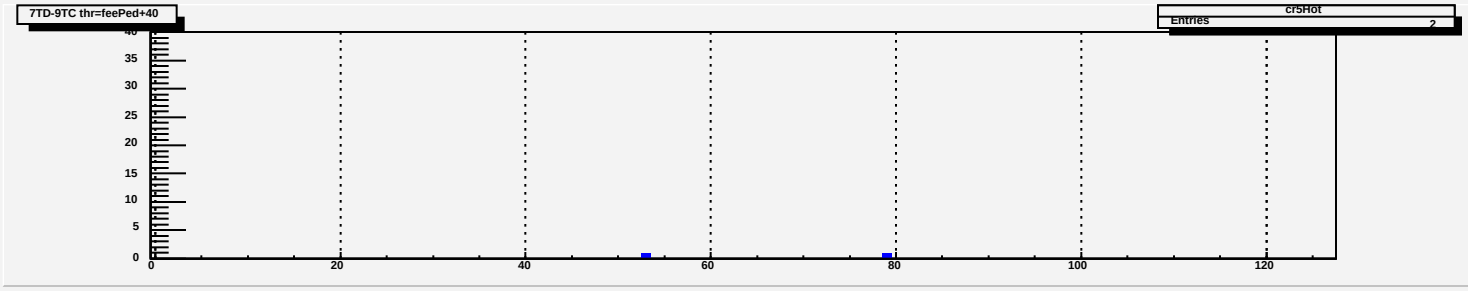

9TD-11TC
cr6
Entries 1408

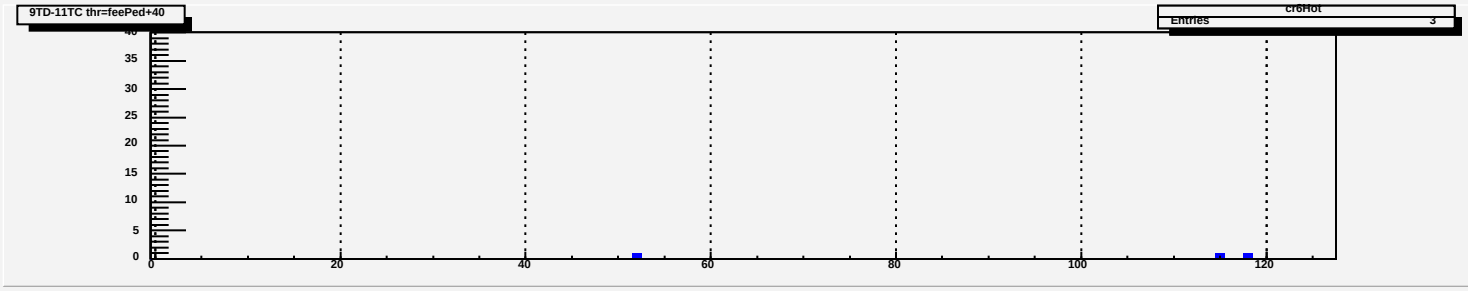

11TD-1TC thr=feePed+40
cr1Hot
Entries 15
10
1
0
20
40
60
80
100
120

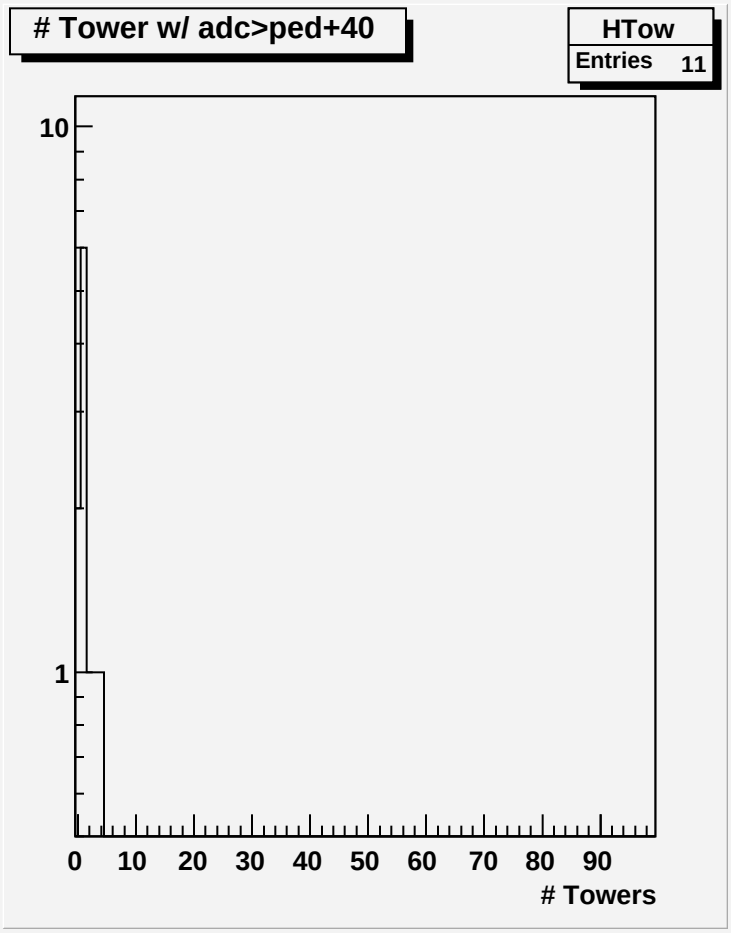

1TD-3TC thr=feePed+40
cr2Hot
Entries 0
35
30
25
20
15
10
5
0
0
20
40
60
80
100
120

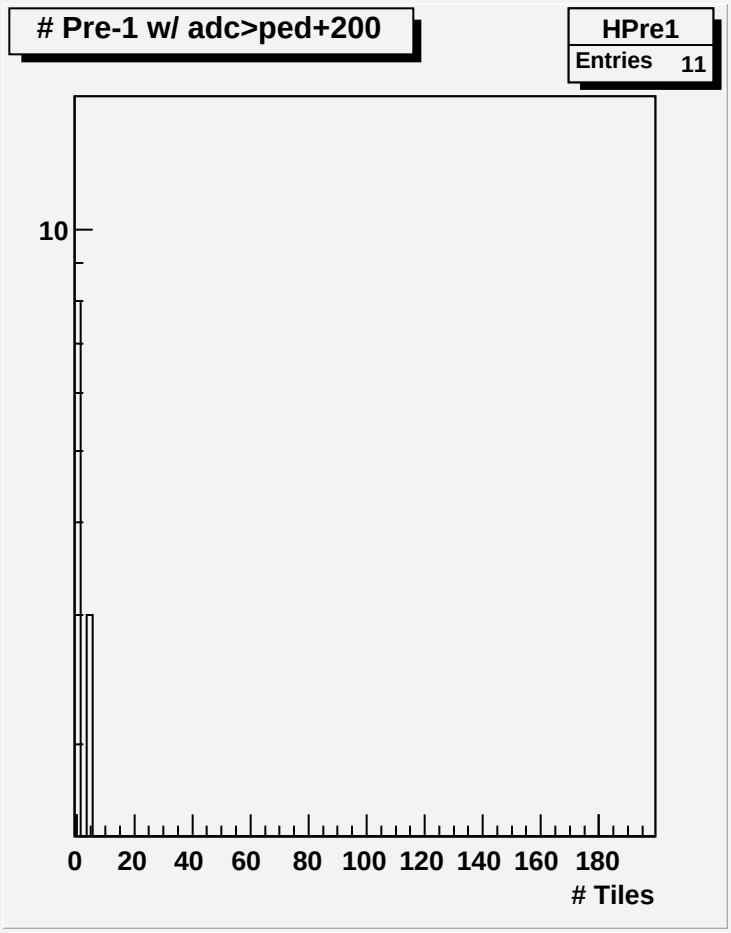

3TD-5TC thr=feePed+40
cr3Hot
Entries 1
35
30
25
20
15
10
5
0
0
20
40
60
80
100
120

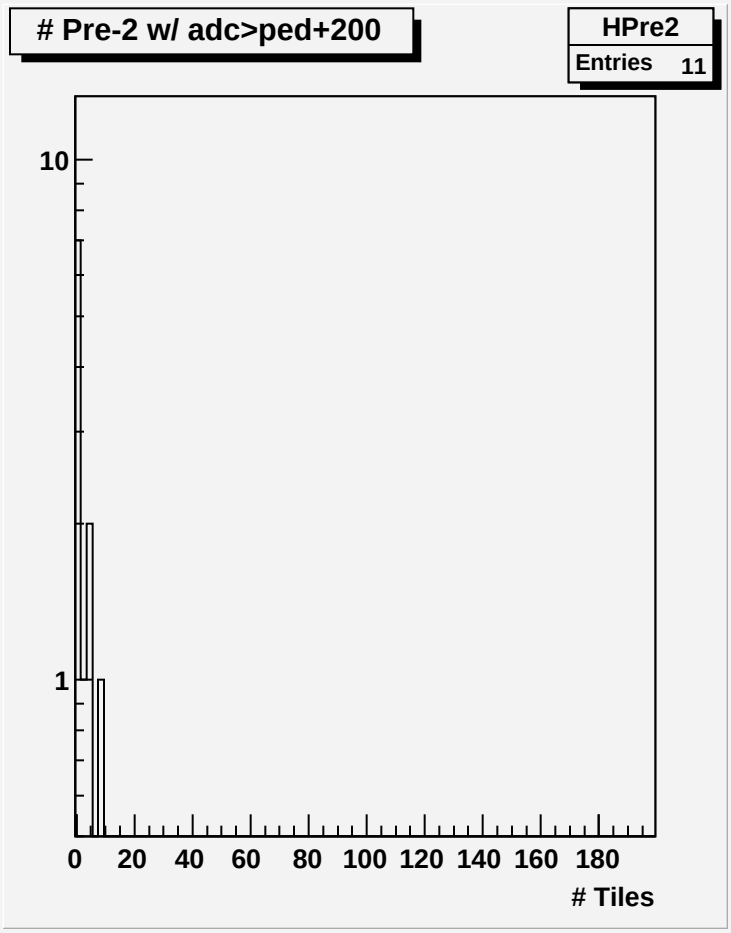

5TD-7TC thr=feePed+40
cr4Hot
Entries 2
35
30
25
20
15
10
5
0
0
20
40
60
80
100
120

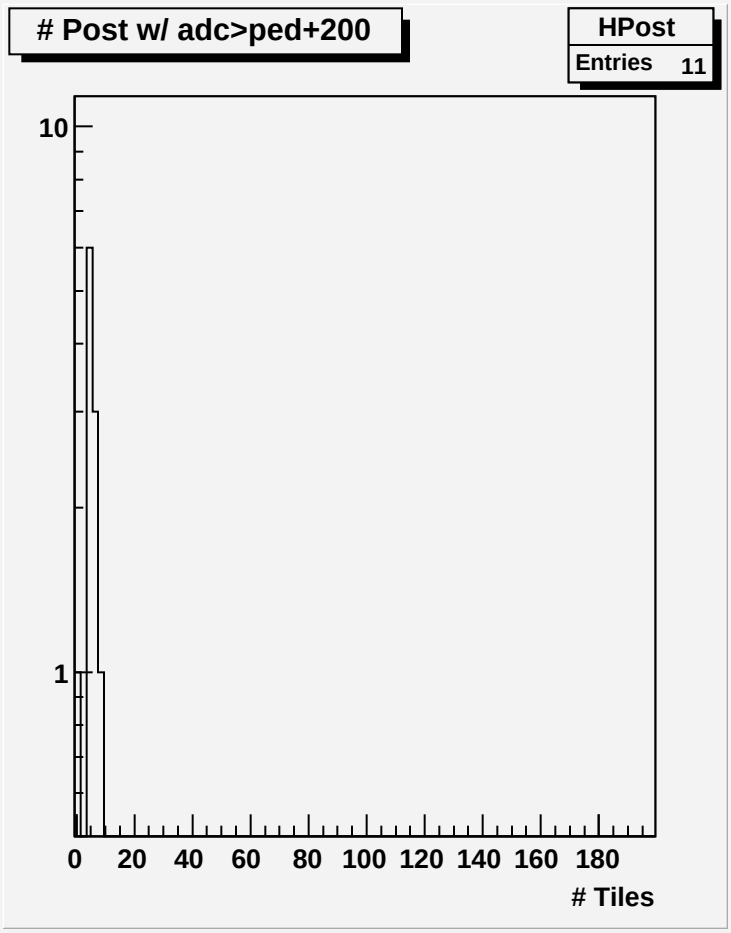

7TD-9TC thr=feePed+40
cr5Hot
Entries 2
35
30
25
20
15
10
5
0
0
20
40
60
80
100
120


9TD-11TC thr=feePed+40
cr6Hot
Entries 3
35
30
25
20
15
10
5
0
0
20
40
60
80
100
120

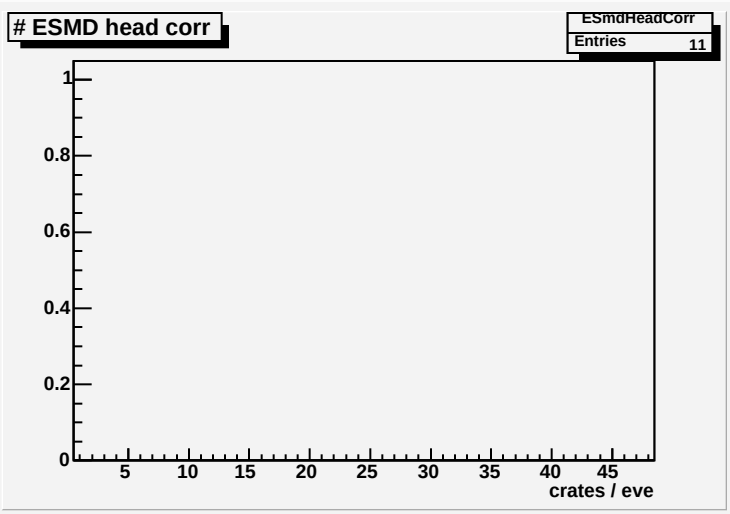

Tower w/ adc>ped+40
HTow
Entries 11
10
1
0 10 20 30 40 50 60 70 80 90
Towers

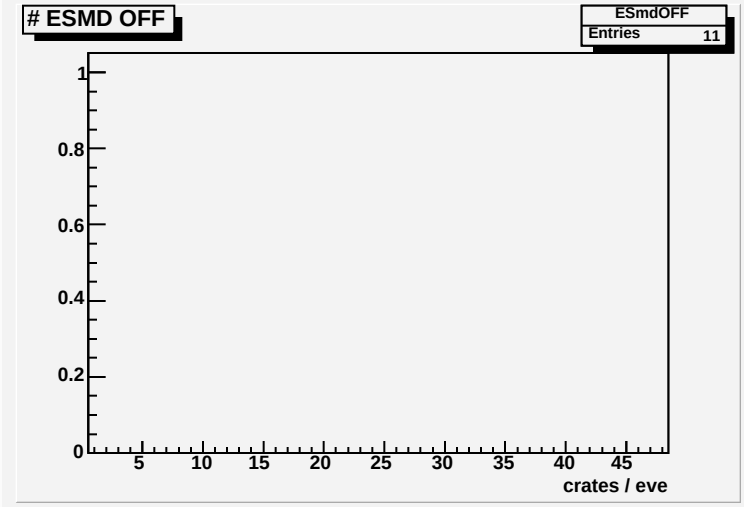

Pre-1 w/ adc>ped+200
HPre1
Entries 11
10
0 20 40 60 80 100 120 140 160 180
Tiles

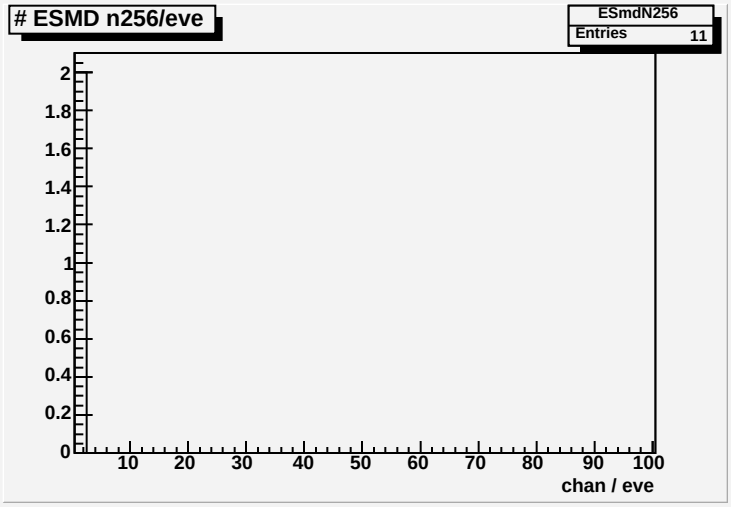

Pre-2 w/ adc>ped+200
HPre2
Entries 11
10
1
0 20 40 60 80 100 120 140 160 180
Tiles

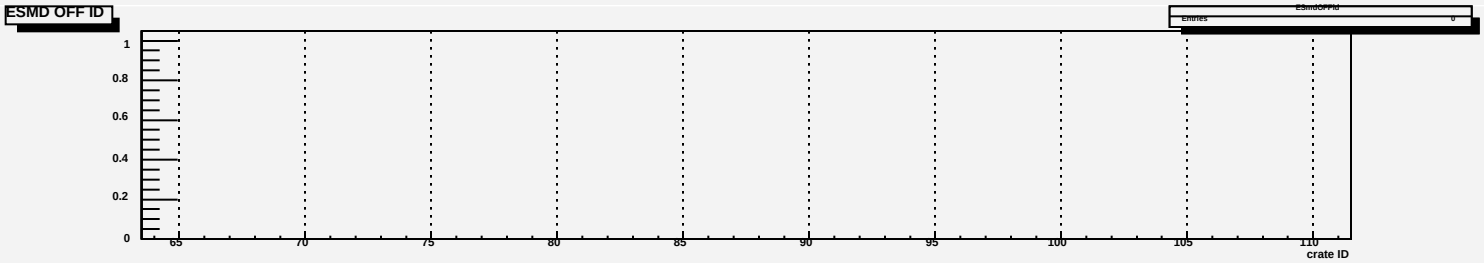

Post w/ adc>ped+200
HPost
Entries 11
10
1
0 20 40 60 80 100 120 140 160 180
Tiles

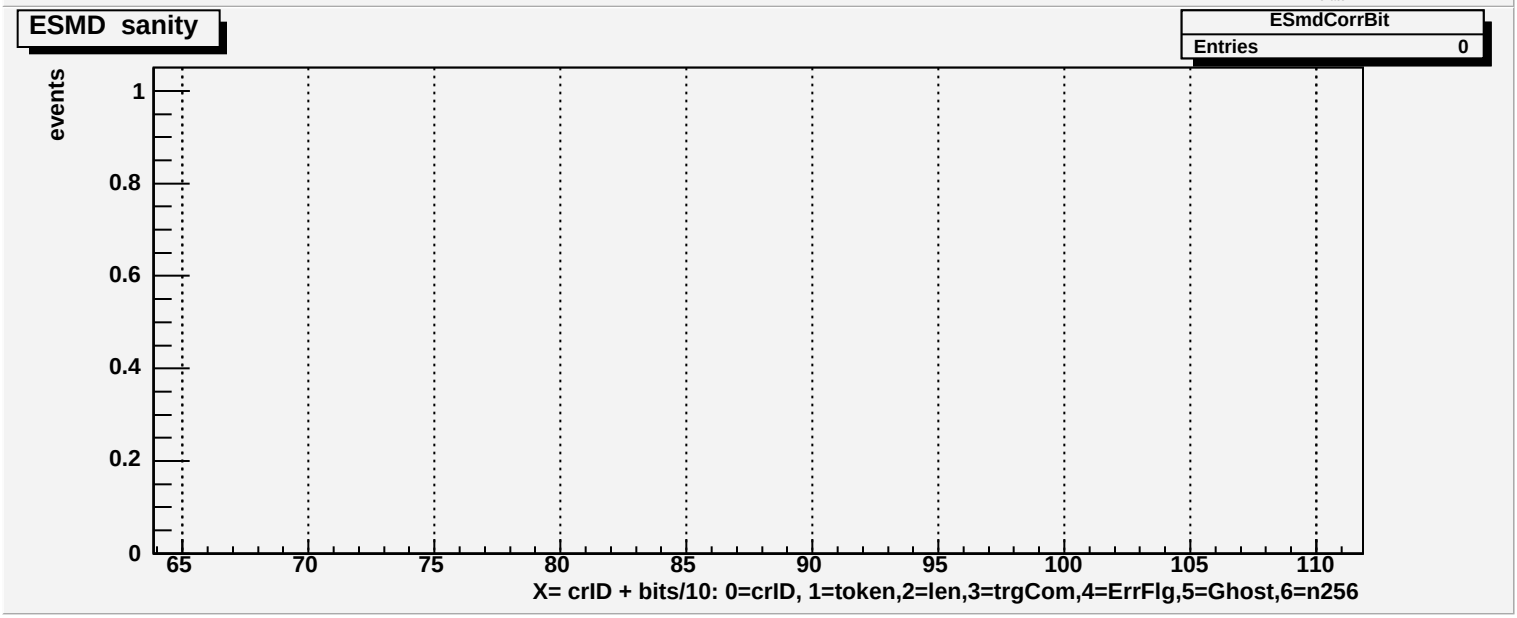
ESMD HEALTH
ESmdHealth
Entries 0
events
X=corr. type

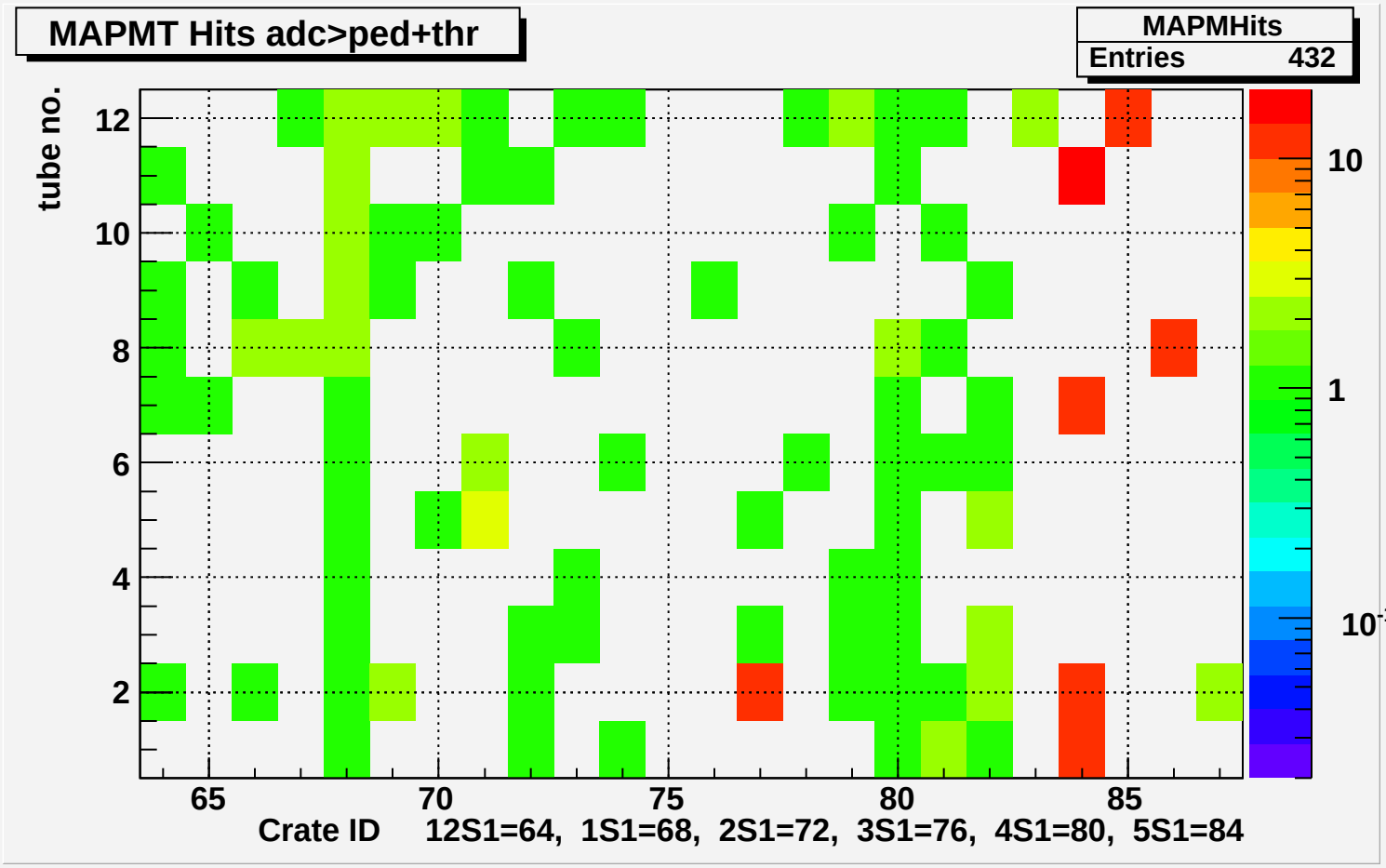

ESMD head corr
ESmdHeadCorr
Entries 11
crates / eve

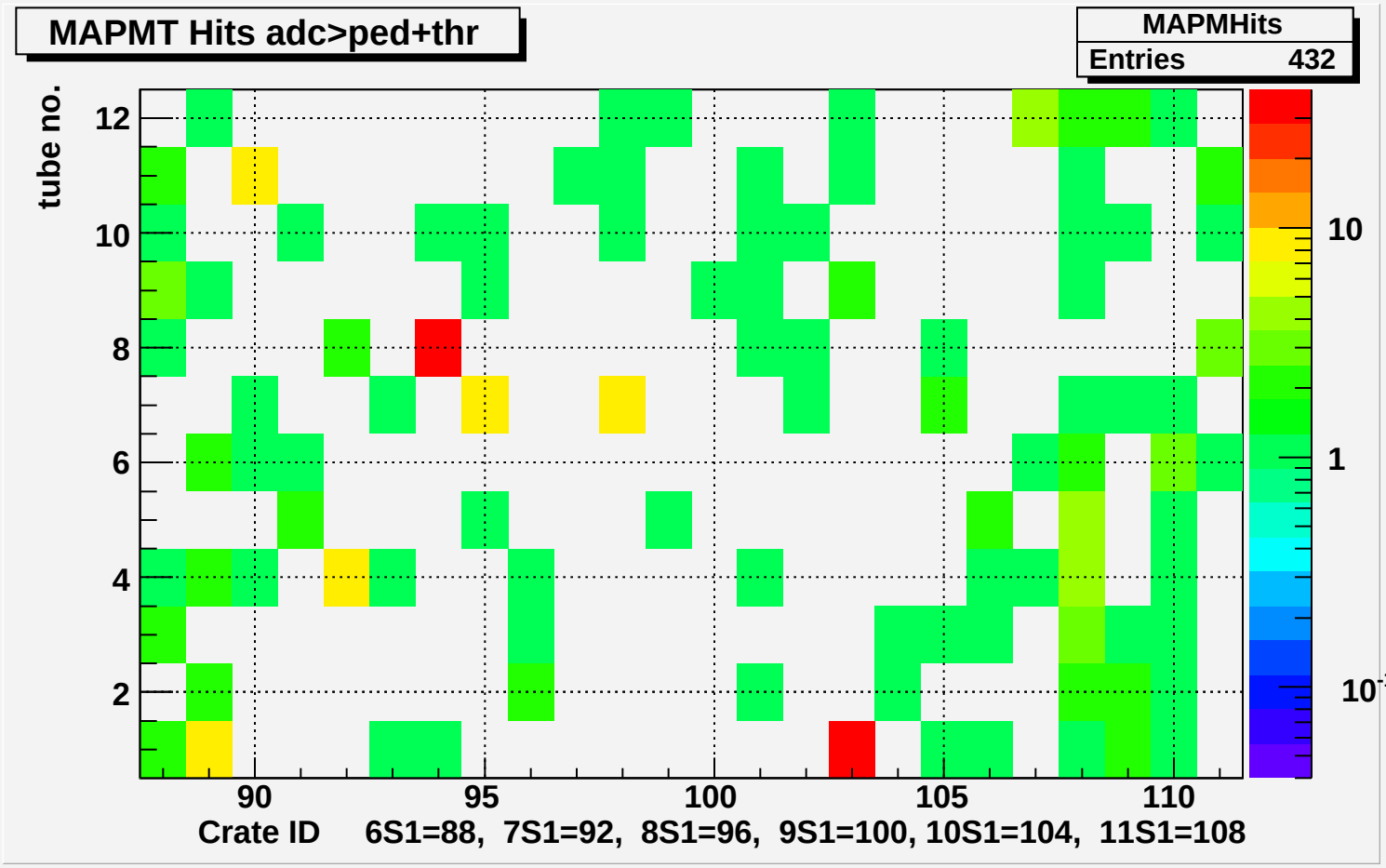

ESMD OFF
ESmdOFF
Entries 11
crates / eve

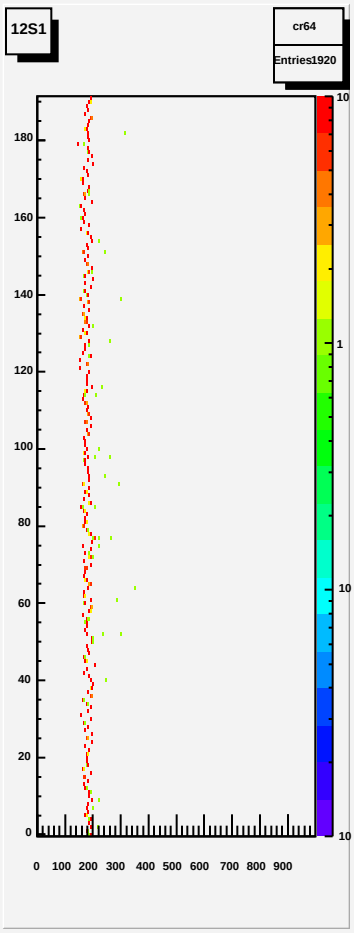

ESMD n256/eve
ESmdN256
Entries 11
chan / eve

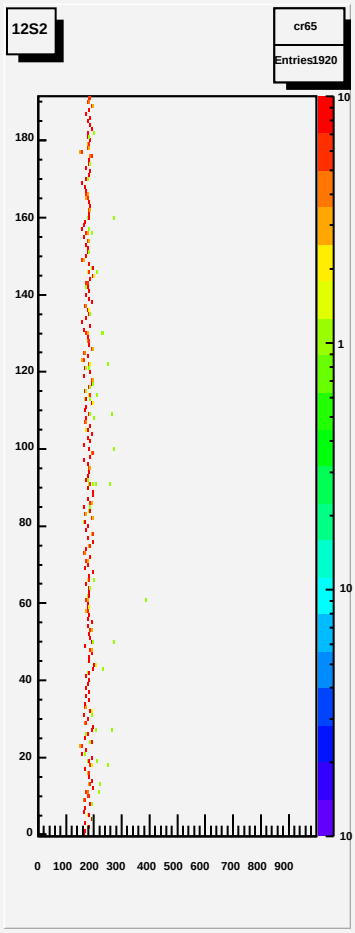

ESMD OFF ID
crate ID

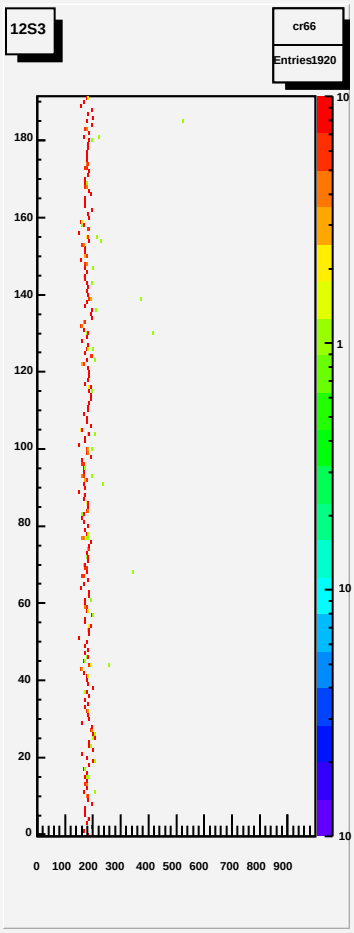

ESMD sanity
ESmdCorrBit
Entries 0
events
X= crID + bits/10: 0=crID, 1=token,2=len,3=trgCom,4=ErrFlg,5=Ghost,6=n256

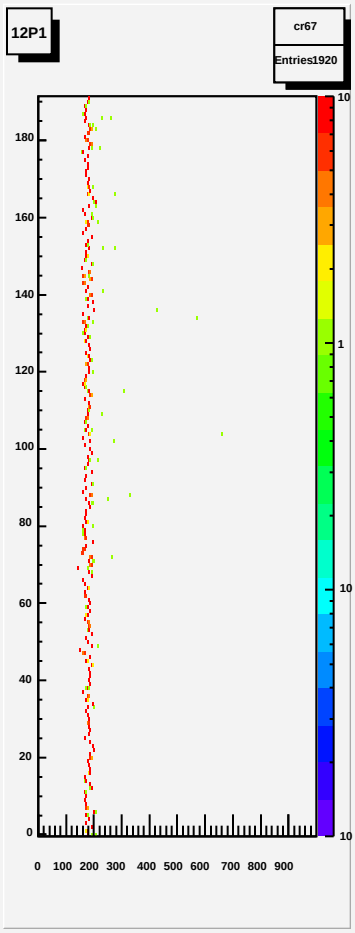

MAPMT Hits adc>ped+thr
MAPMHits
Entries 432
tube no.
12
10
8
6
4
2
65
70
75
80
85
Crate ID 12S1=64, 1S1=68, 2S1=72, 3S1=76, 4S1=80, 5S1=84
10
1
10-1

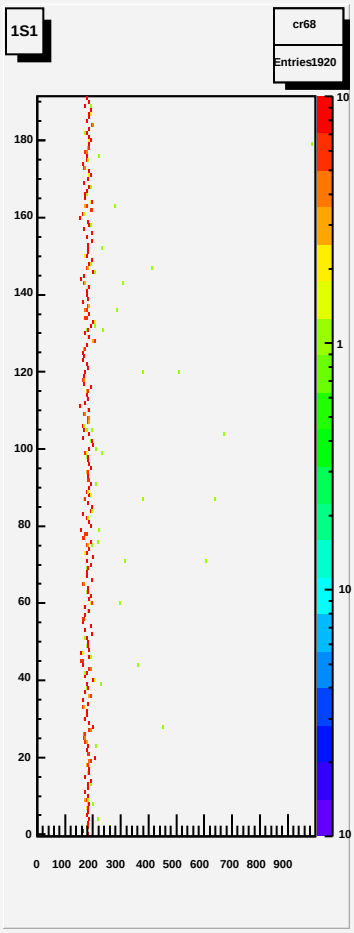

MAPMT Hits adc>ped+thr
MAPMHits
Entries 432
tube no.
12
10
8
6
4
2
90
95
100
105
110
Crate ID 6S1=88, 7S1=92, 8S1=96, 9S1=100, 10S1=104, 11S1=108
10
1
10-1

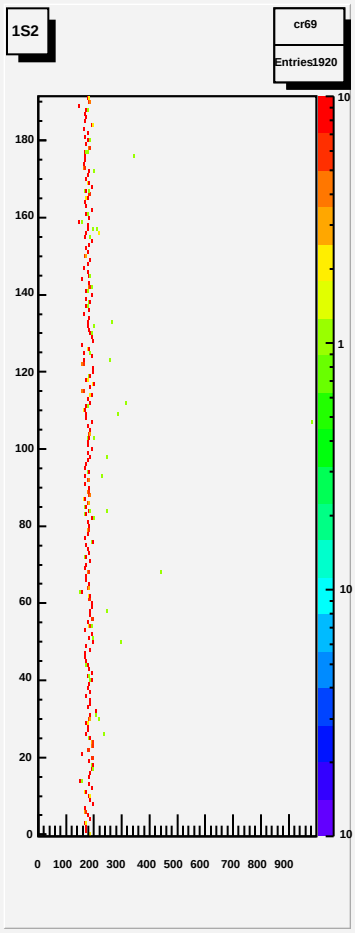

12S1
cr64
Entries1920

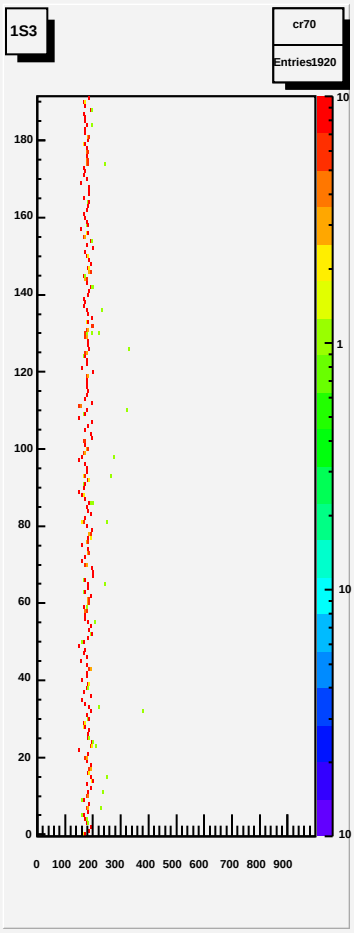

12S2
cr65
Entries1920

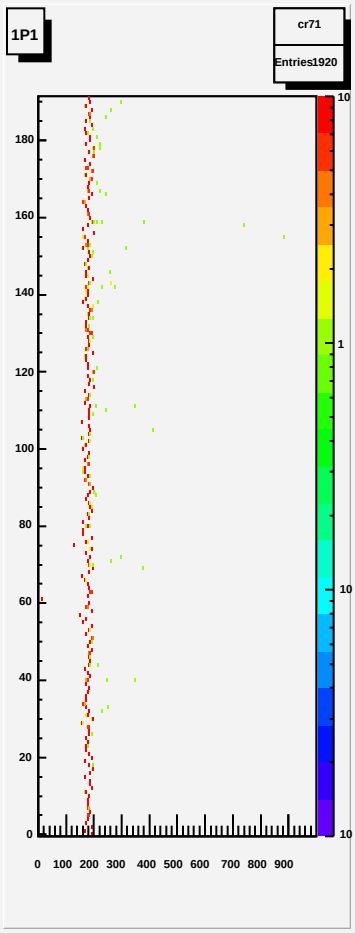

12S3
cr66
Entries1920

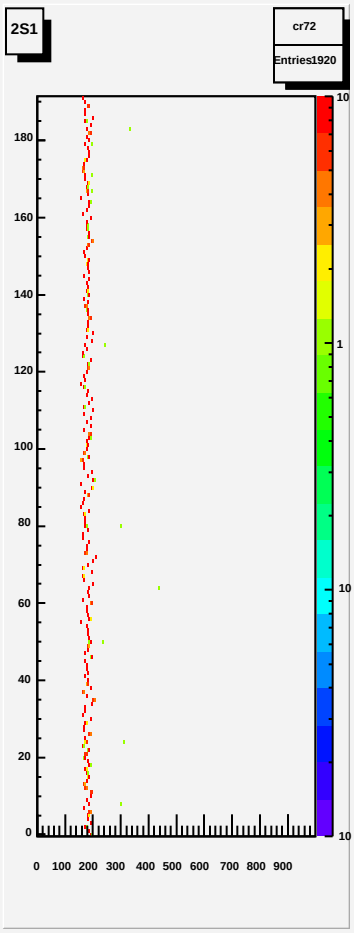

12P1
cr67
Entries1920

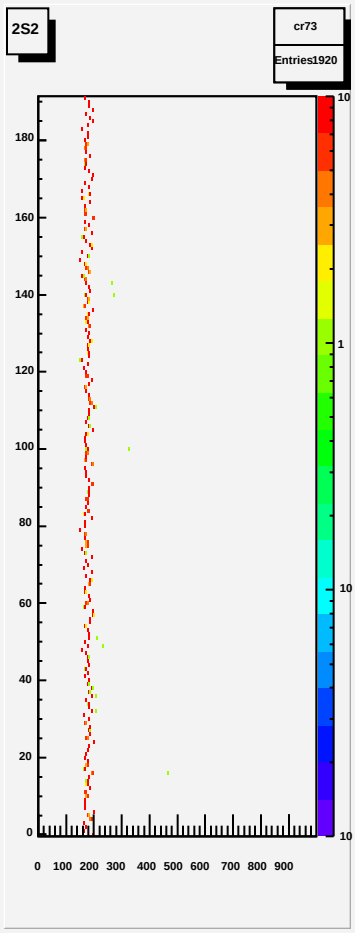

1S1
cr68
Entries1920

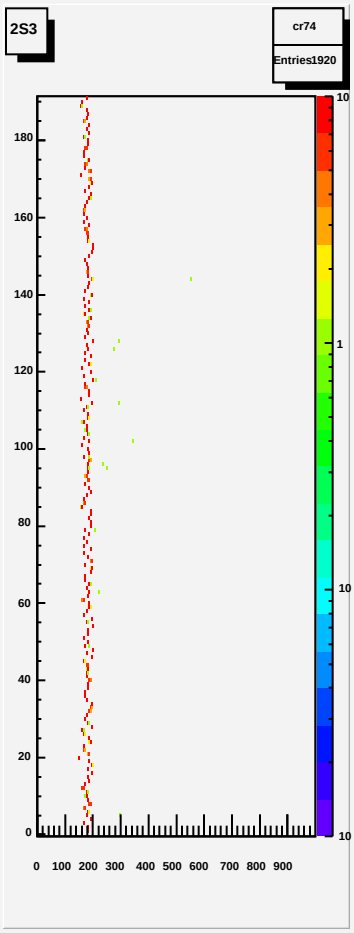

1S2
cr69
Entries1920

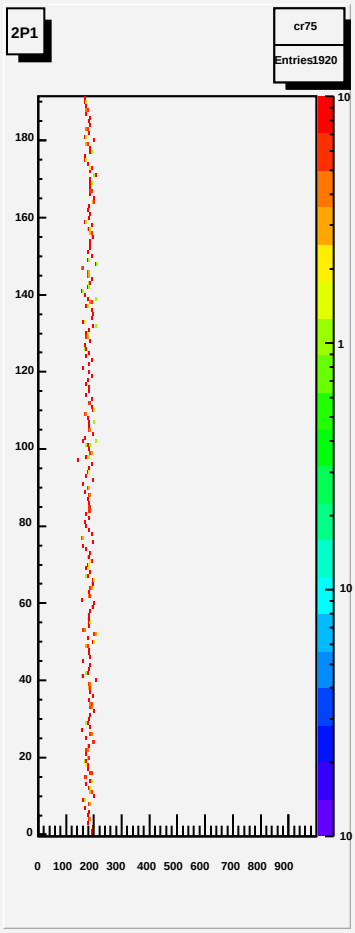

1S3
cr70
Entries1920

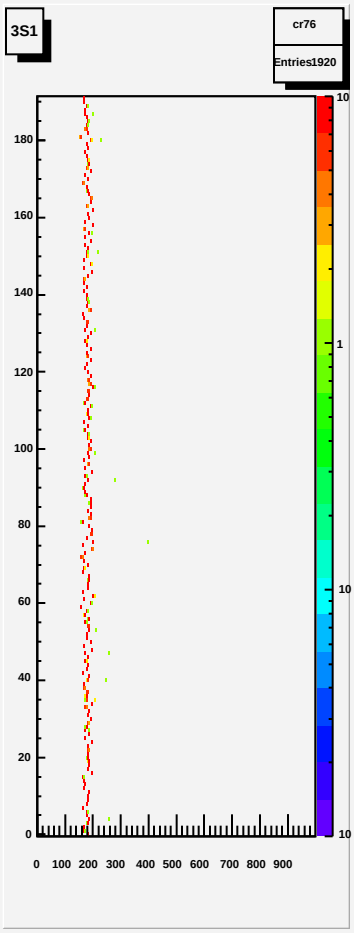

1P1
cr71
Entries1920

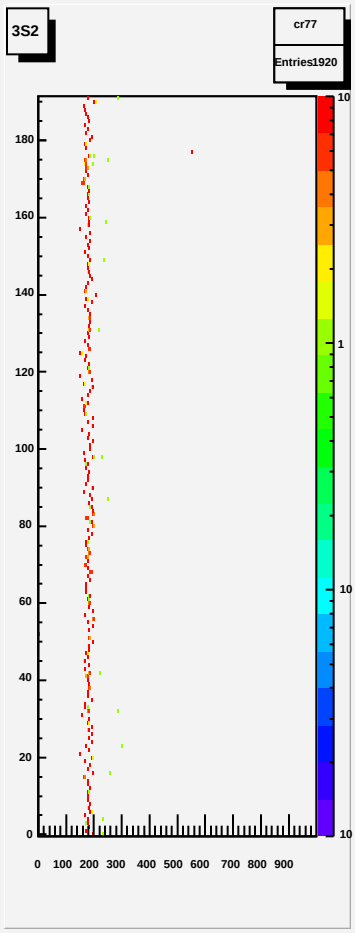

2S1
cr72
Entries1920

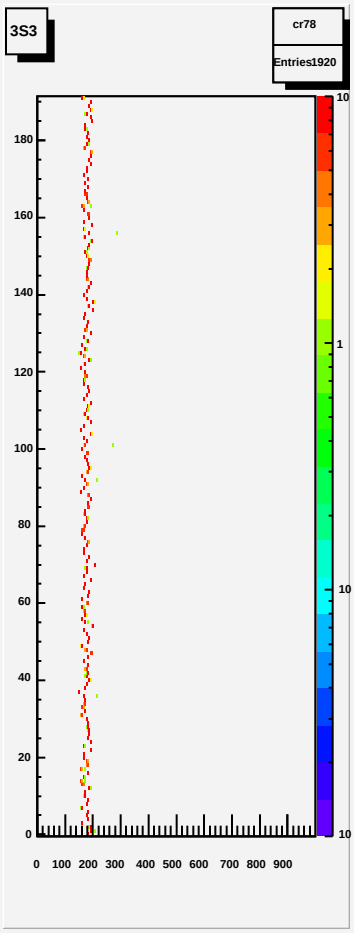

2S2
cr73
Entries1920

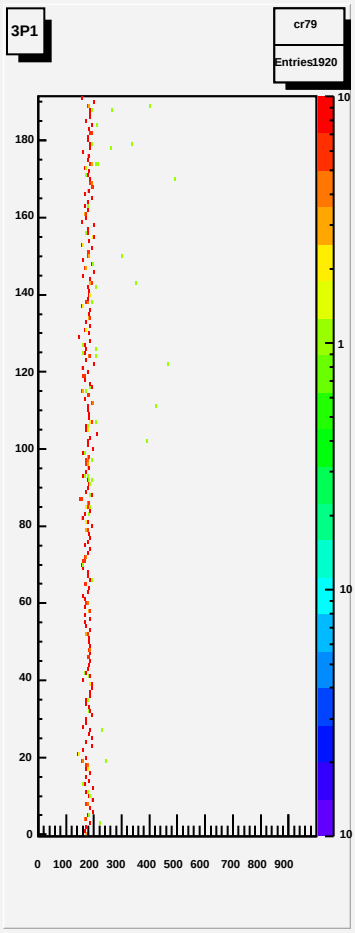

2S3
cr74
Entries1920

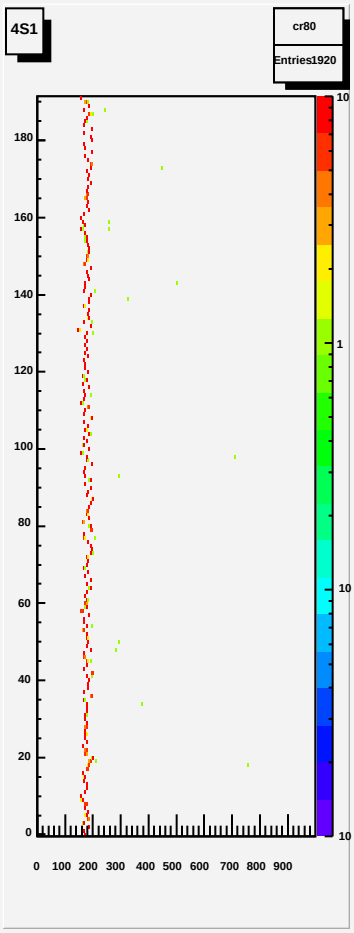

2P1
cr75
Entries1920

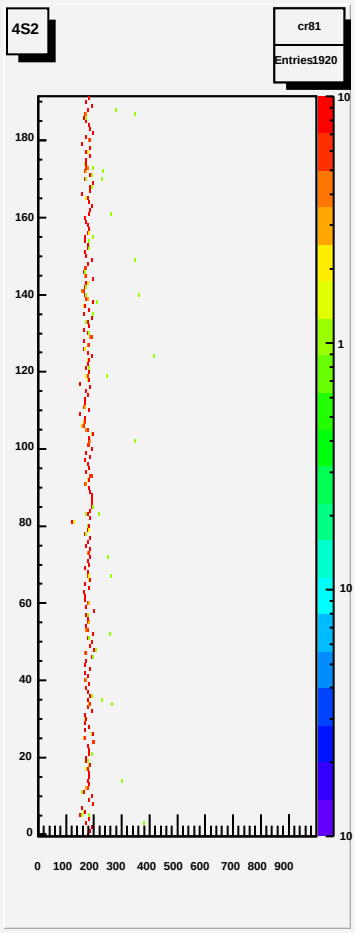

3S1
cr76
Entries1920

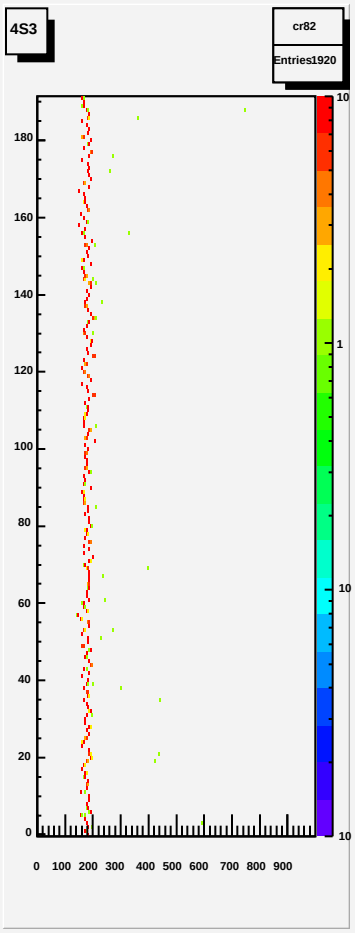

3S2
cr77
Entries1920

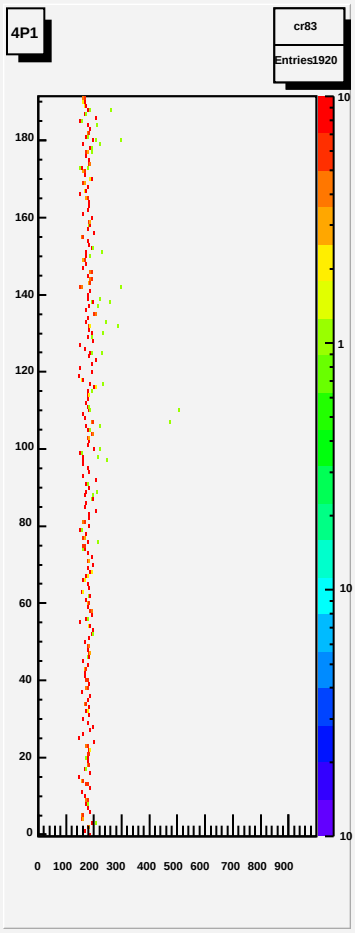

3S3
cr78
Entries1920

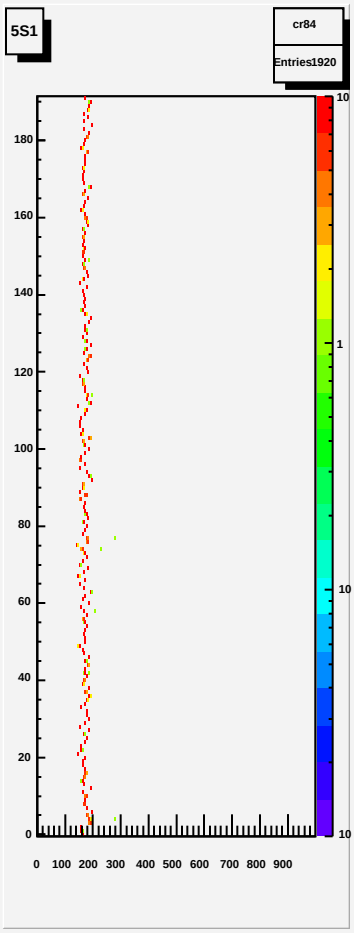

3P1
cr79
Entries1920

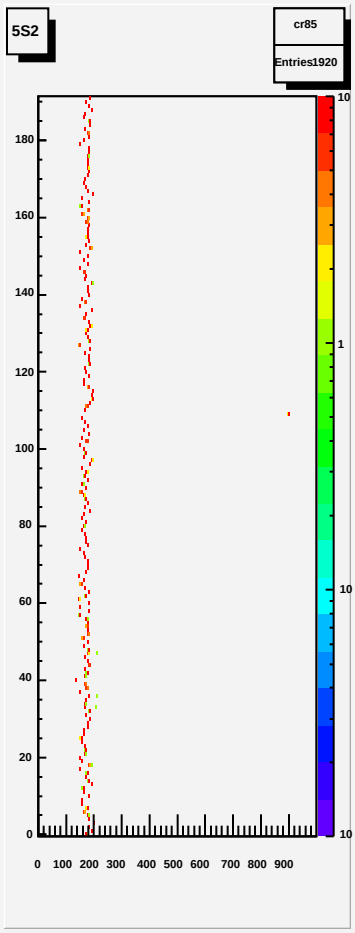

4S1
cr80
Entries1920

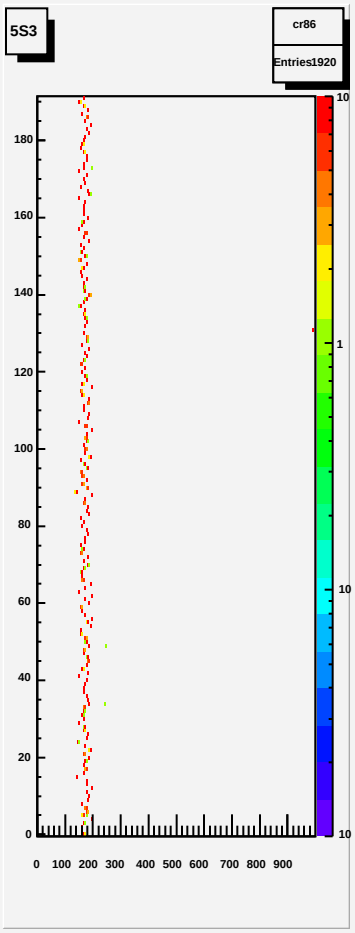

4S2
cr81
Entries1920

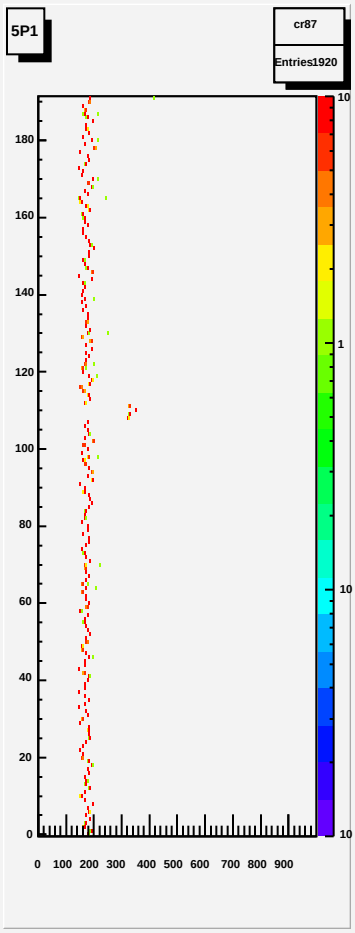

4S3
cr82
Entries1920

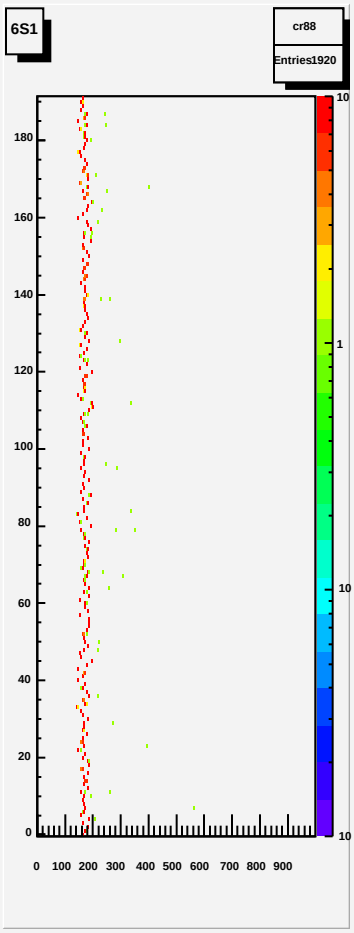

4P1
cr83
Entries1920

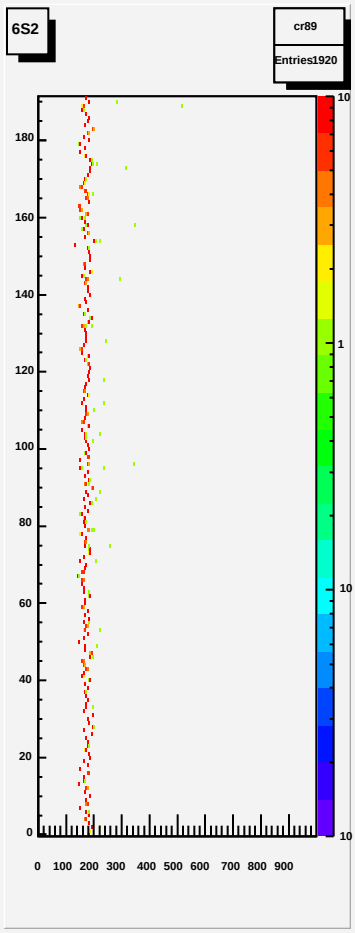

5S1
cr84
Entries1920

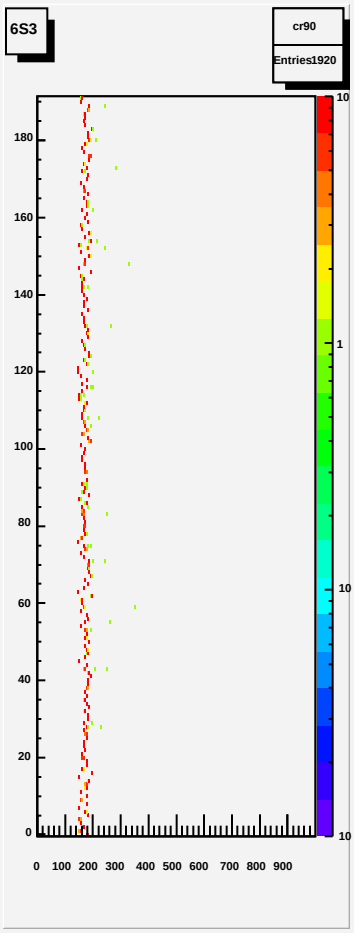

5S2
cr85
Entries1920

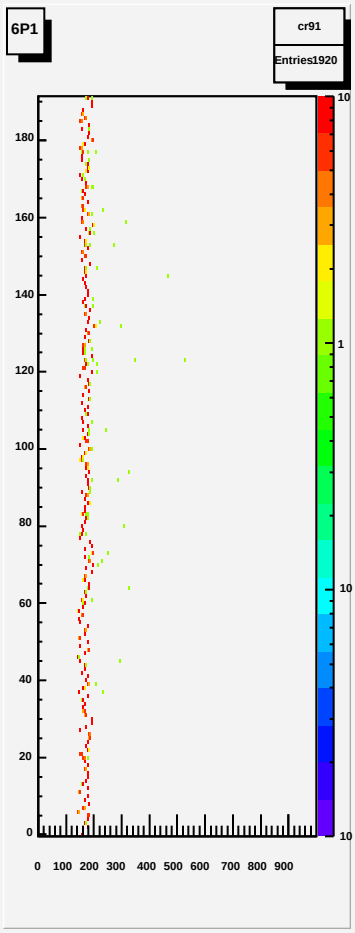

5S3
cr86
Entries1920

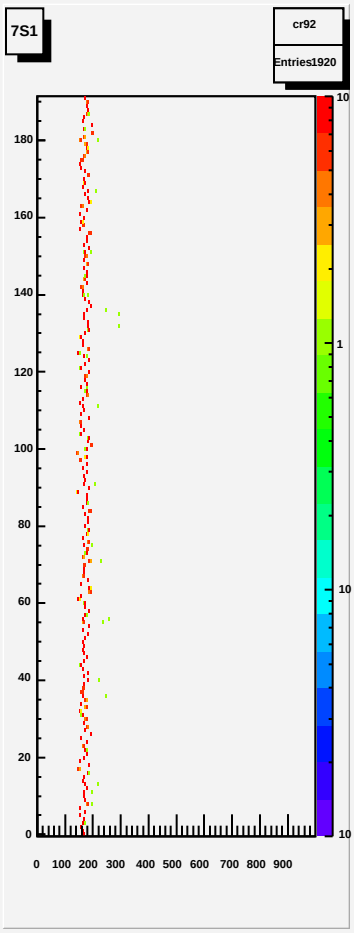

5P1
cr87
Entries1920

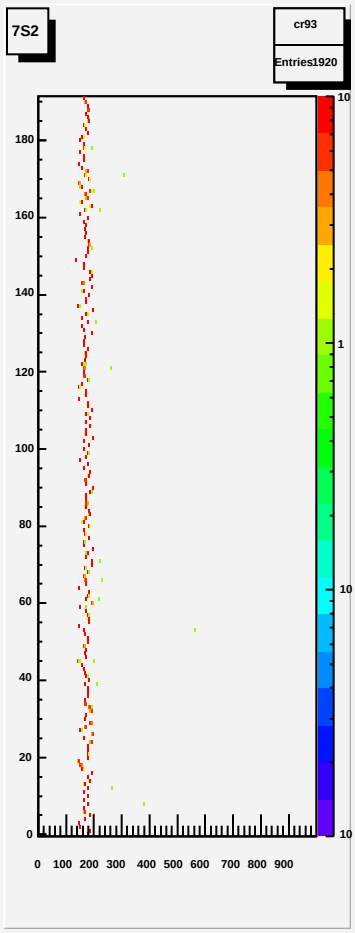

6S1
cr88
Entries1920

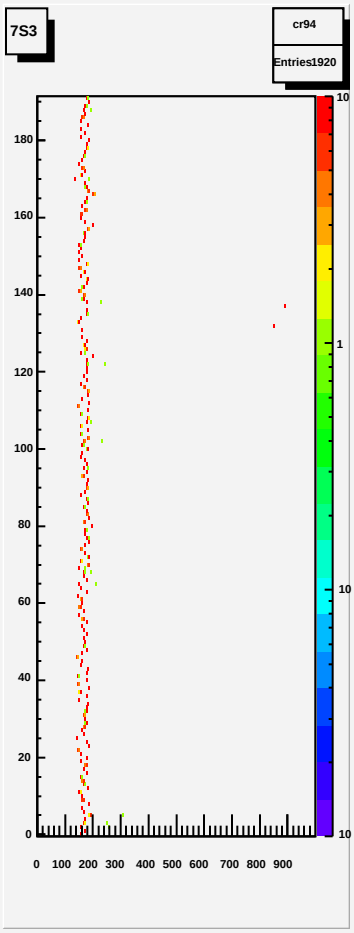

6S2
cr89
Entries1920

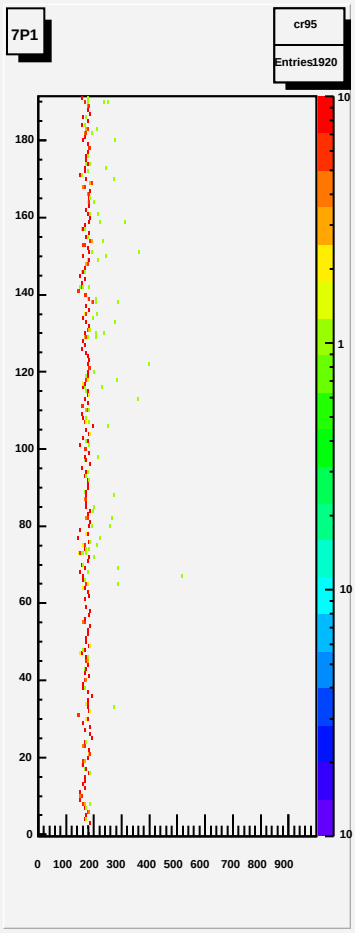

6S3
cr90
Entries1920

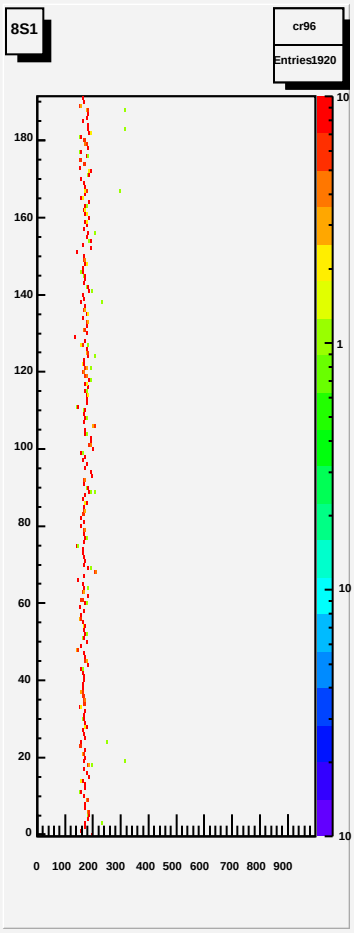

6P1
cr91
Entries1920

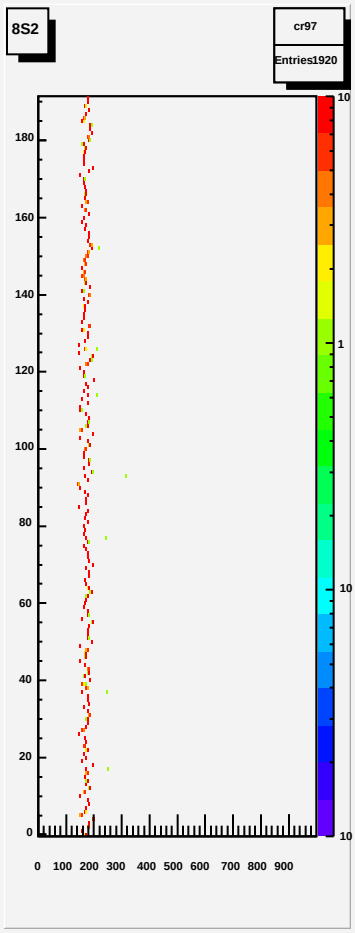

7S1
cr92
Entries1920

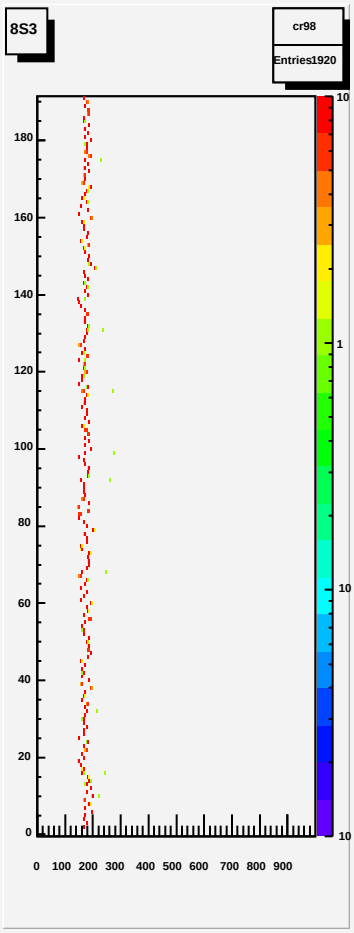

7S2
cr93
Entries1920

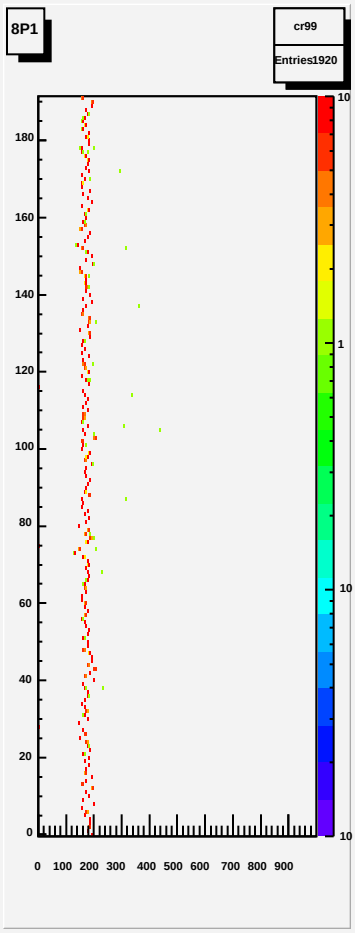

7S3
cr94
Entries1920

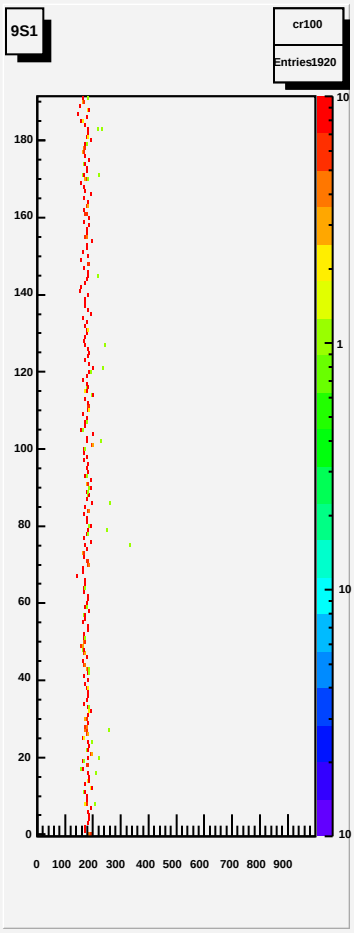

7P1
cr95
Entries1920

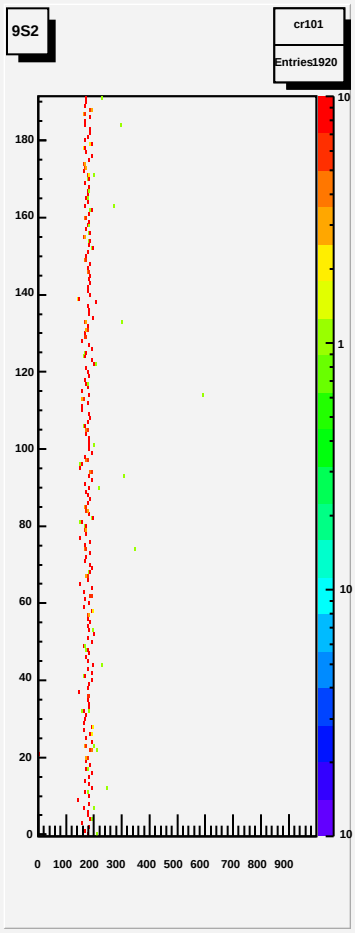

8S1
cr96
Entries1920

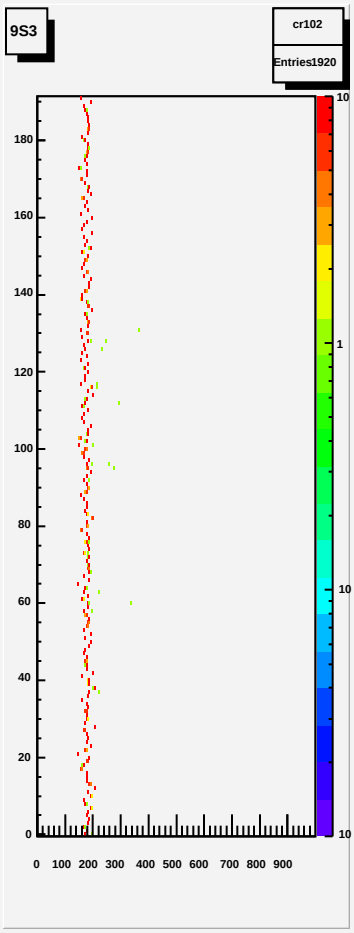

8S2
cr97
Entries1920

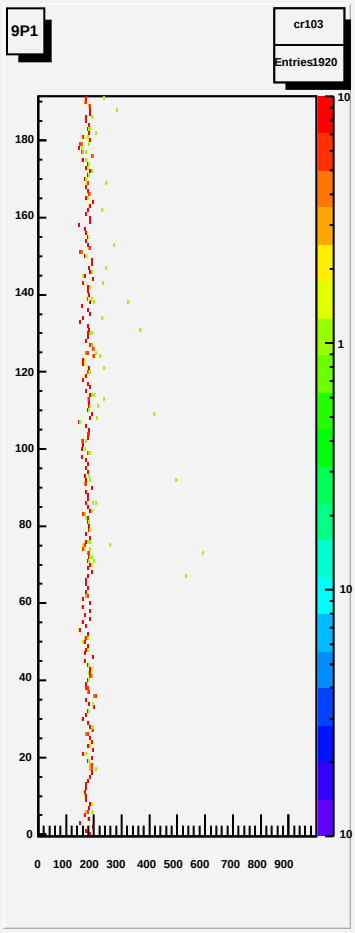

8S3
cr98
Entries1920

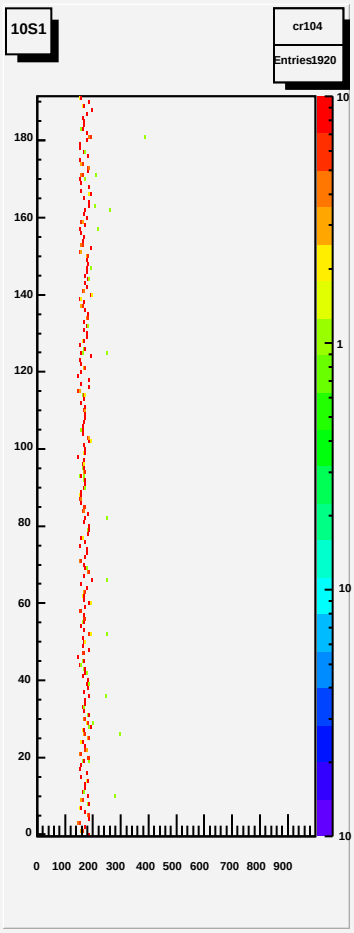

8P1
cr99
Entries1920


9S1
cr100
Entries1920


9S2
cr101
Entries1920


9S3
cr102
Entries1920


9P1
cr103
Entries1920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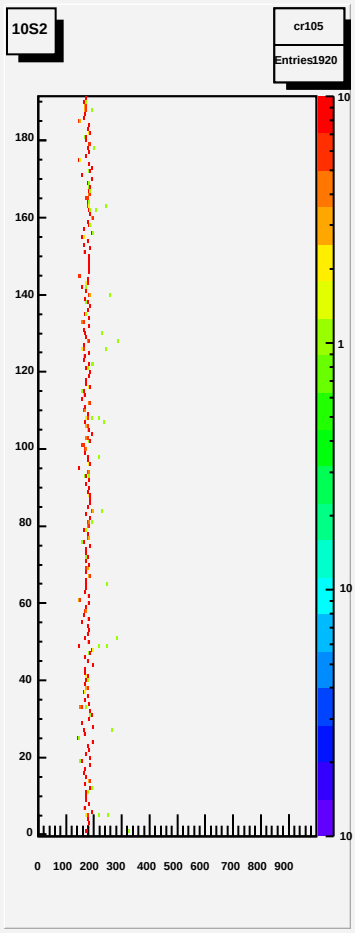
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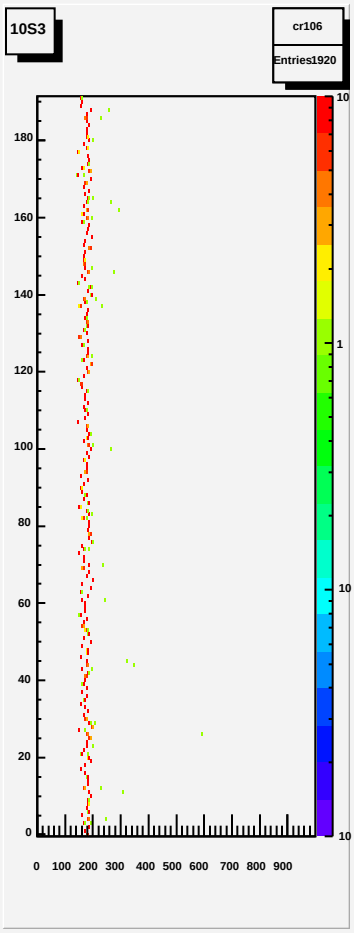
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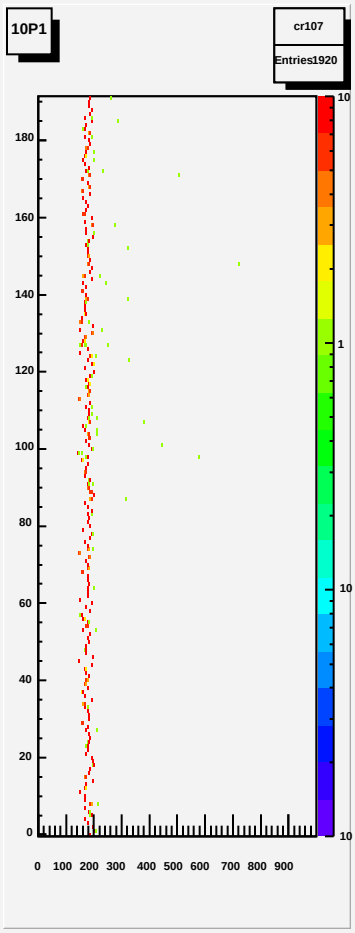
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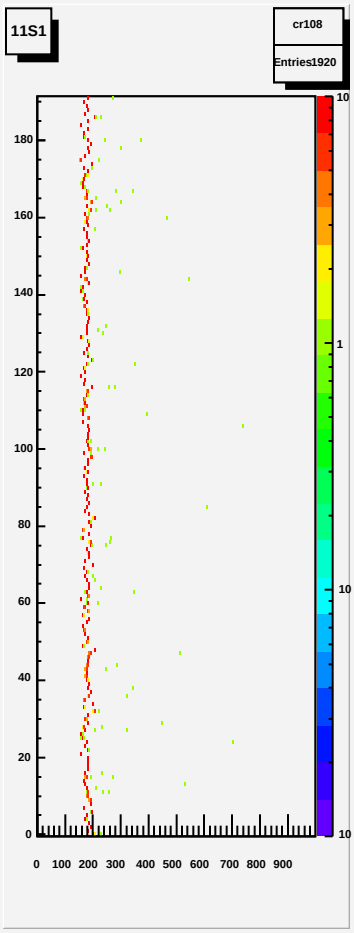
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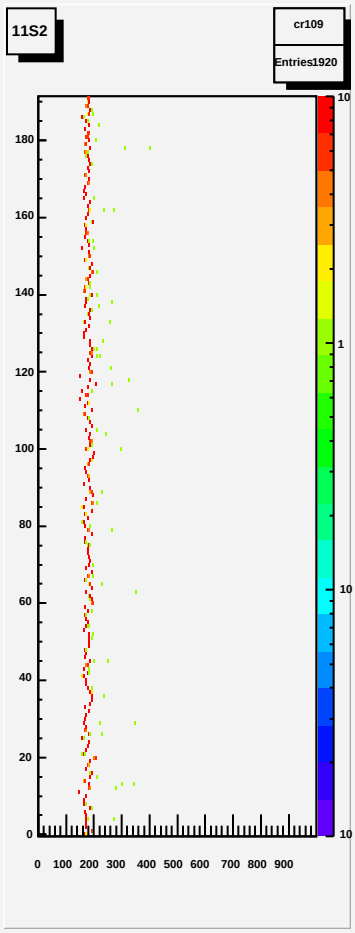
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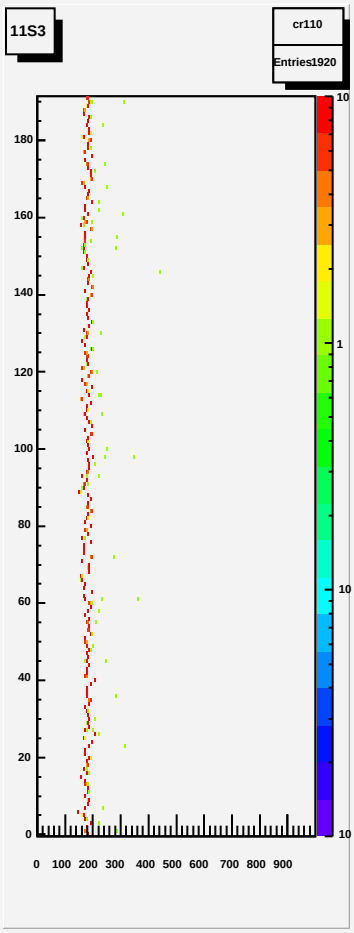
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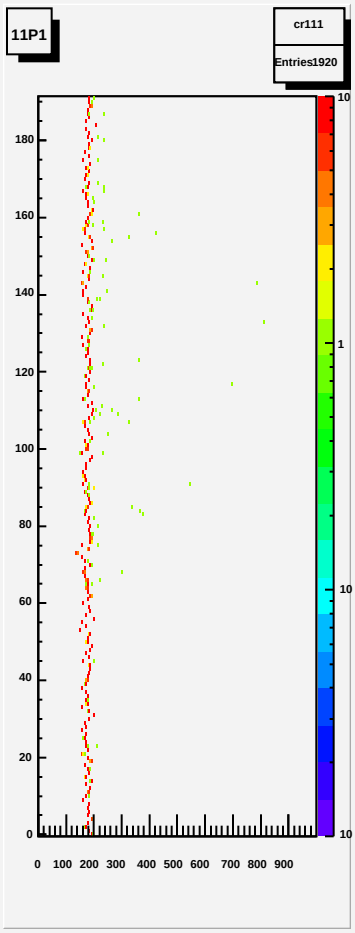
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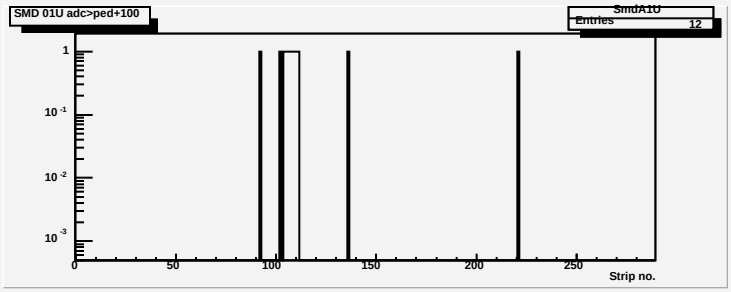
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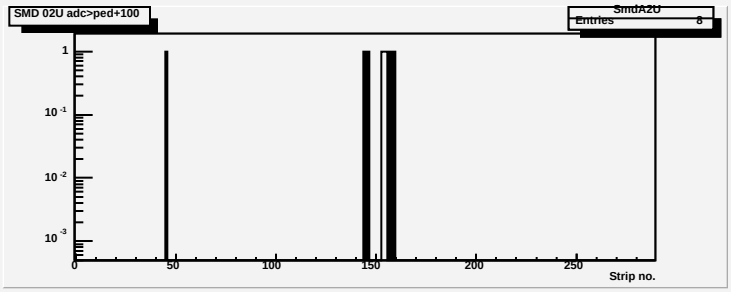
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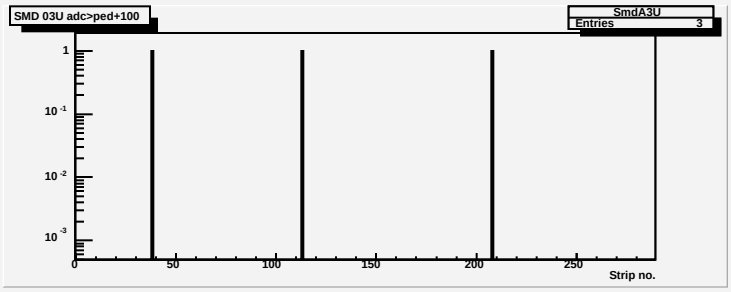
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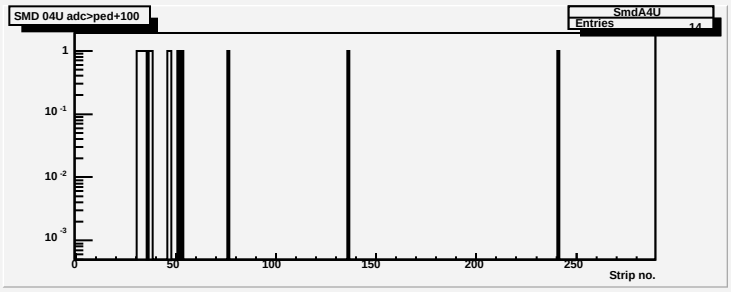
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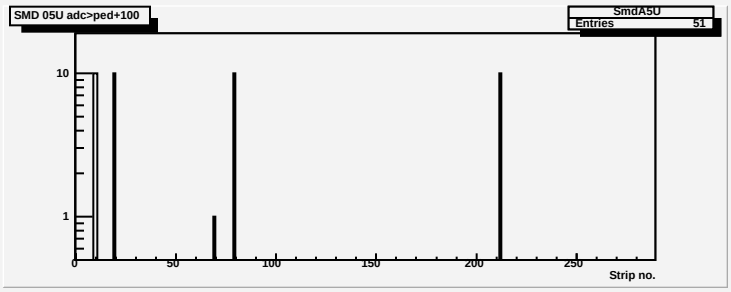
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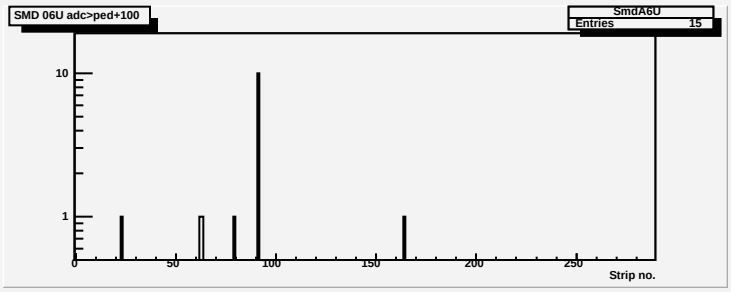
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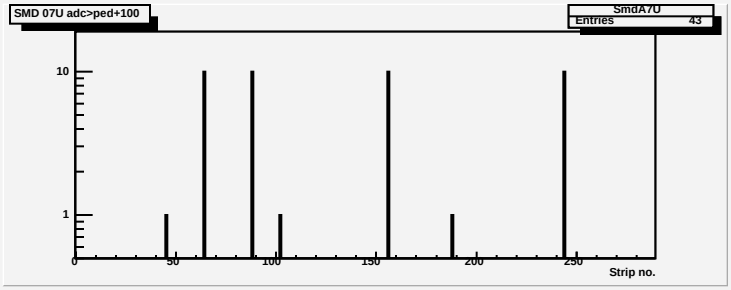
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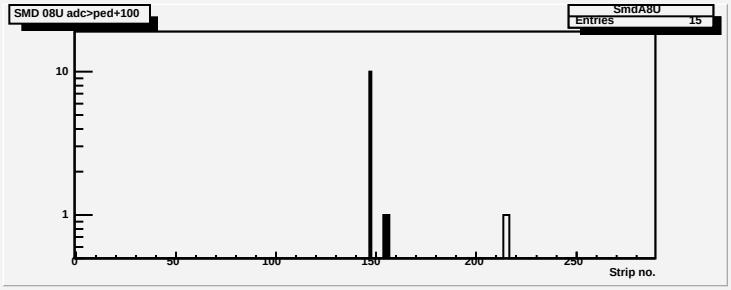
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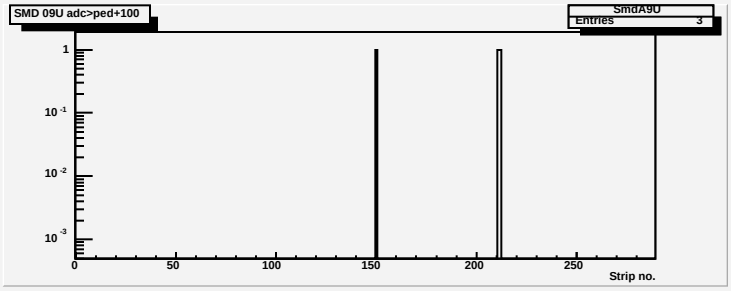
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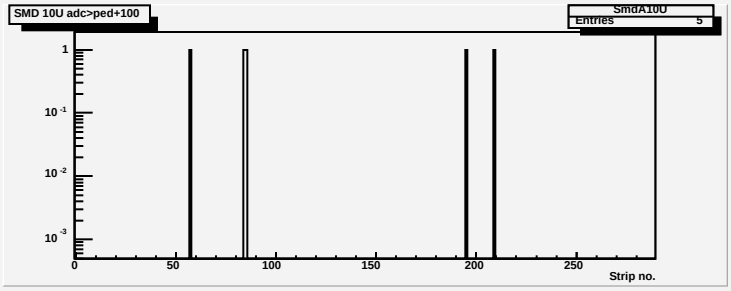
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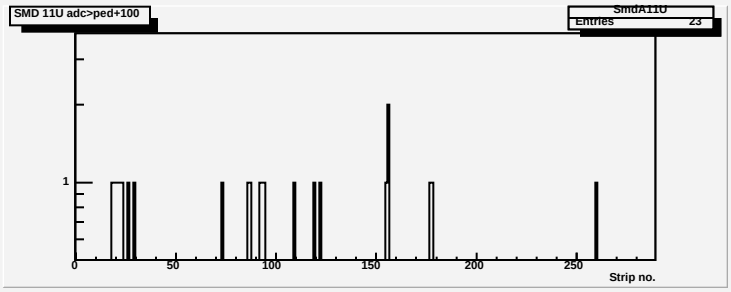
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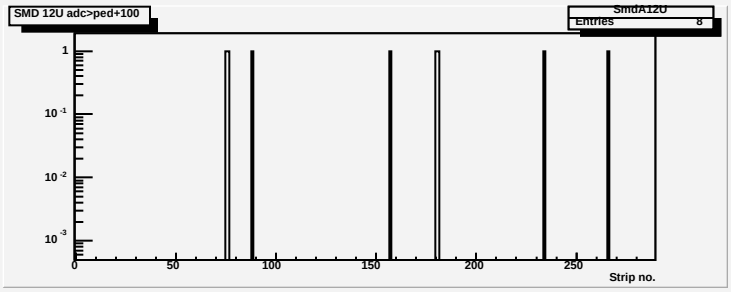
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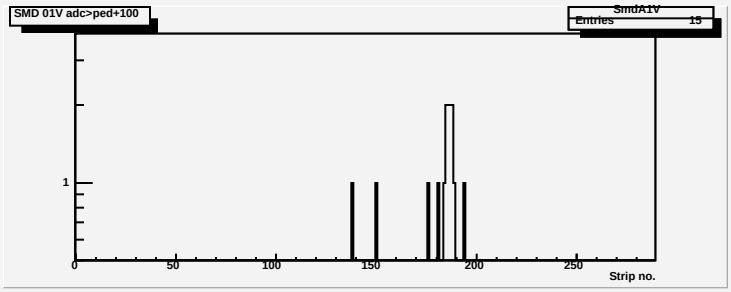
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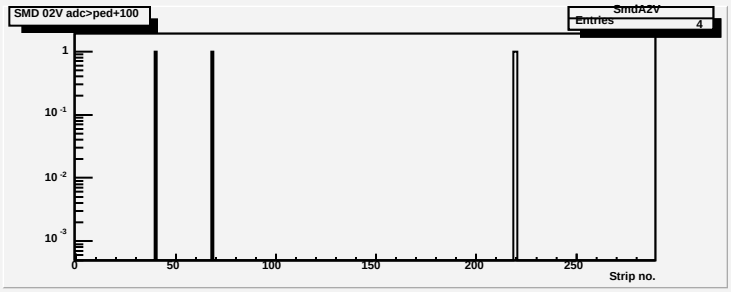
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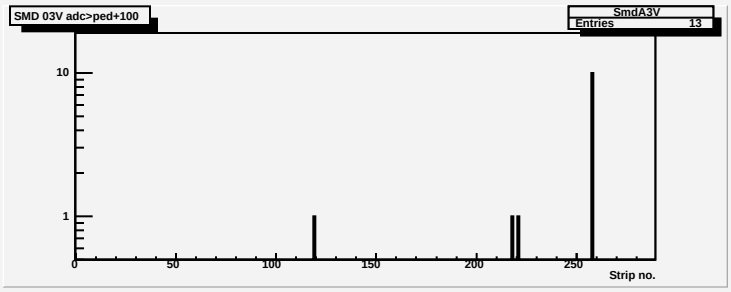
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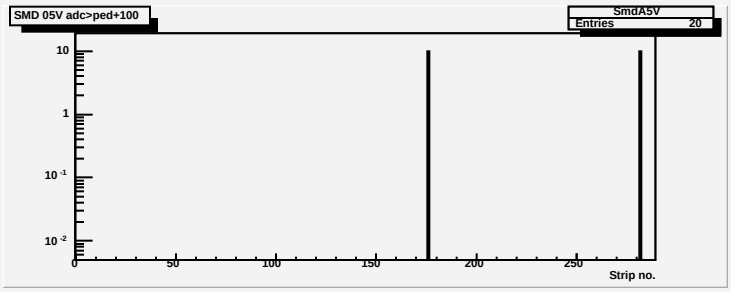
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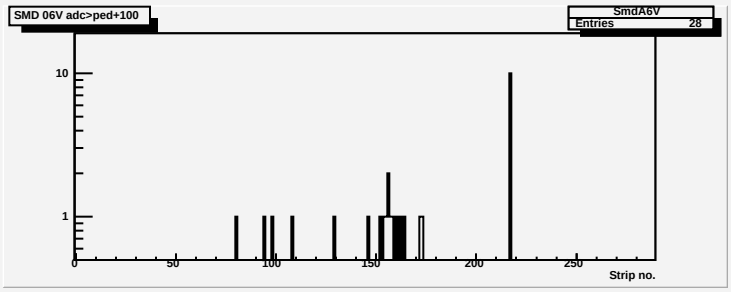
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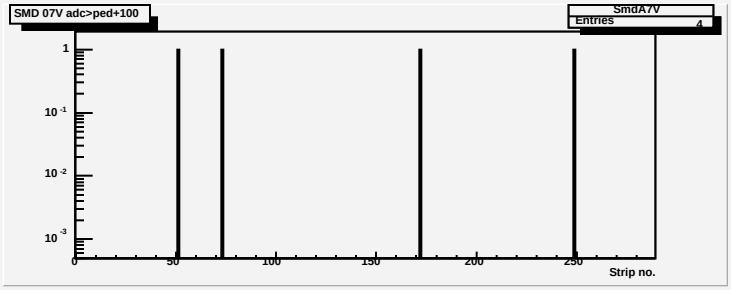
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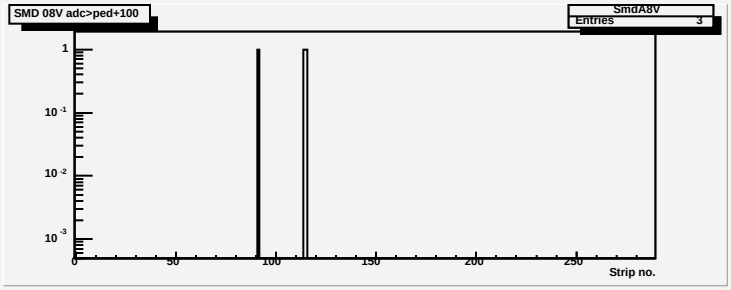
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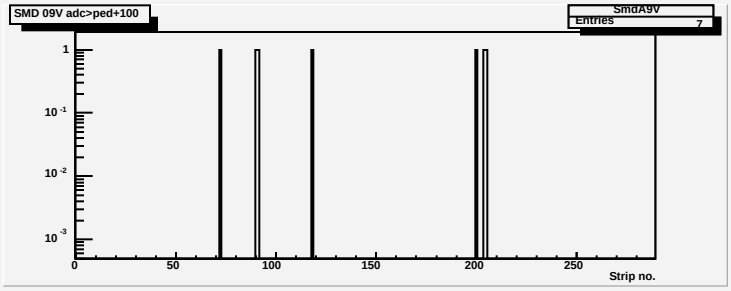
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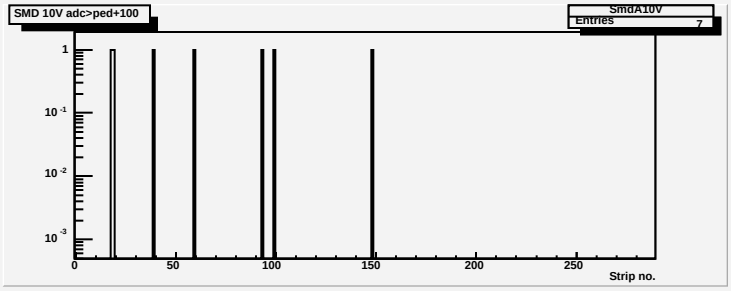
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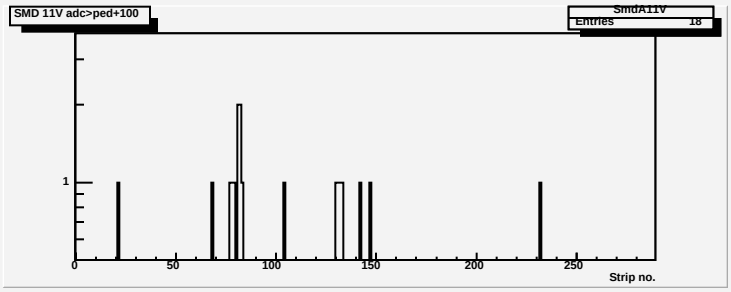
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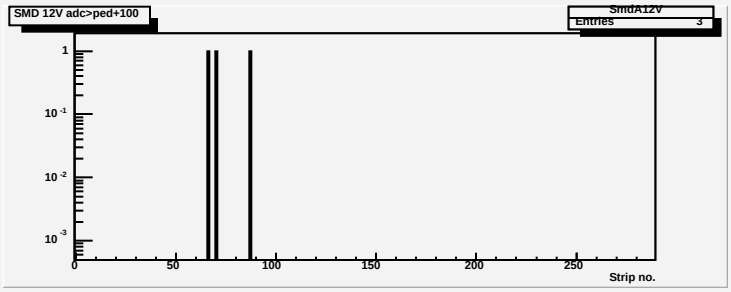
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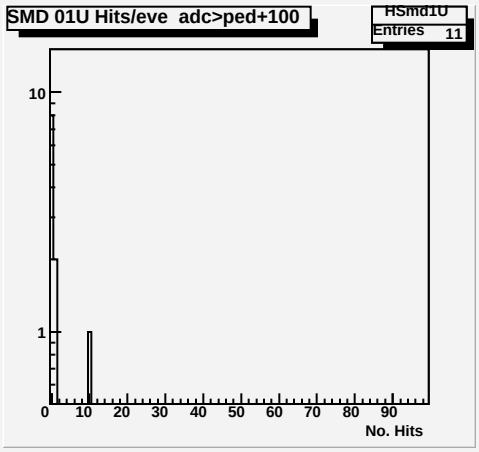
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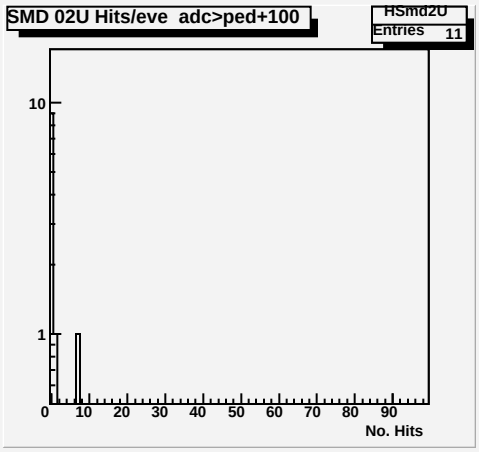
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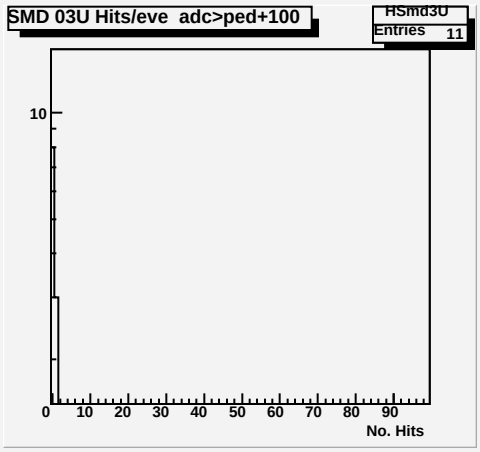
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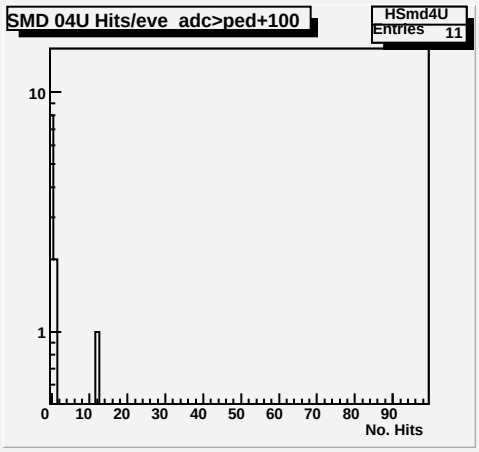
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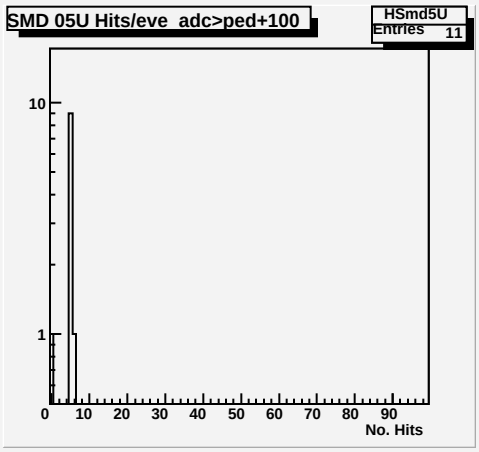
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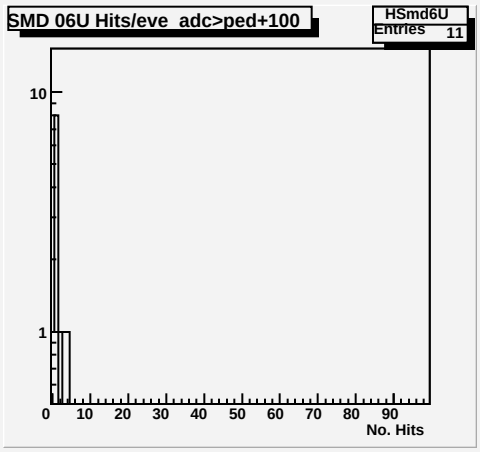
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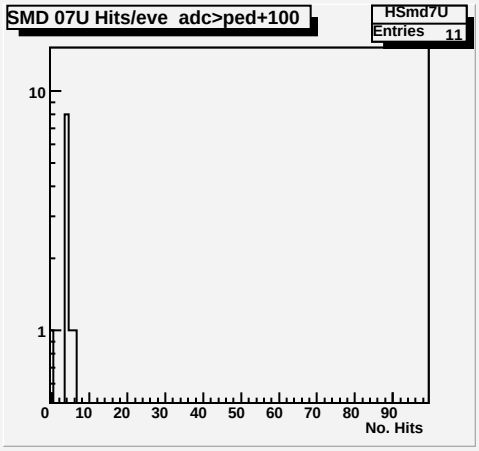
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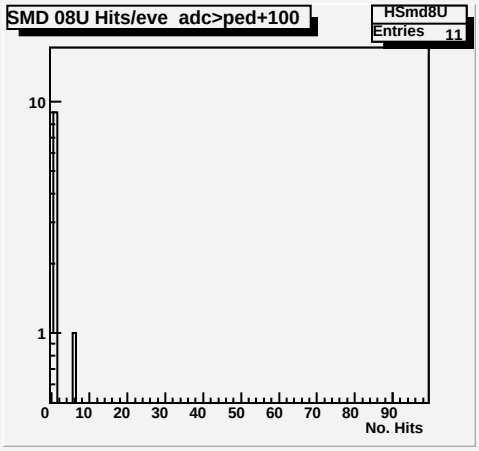
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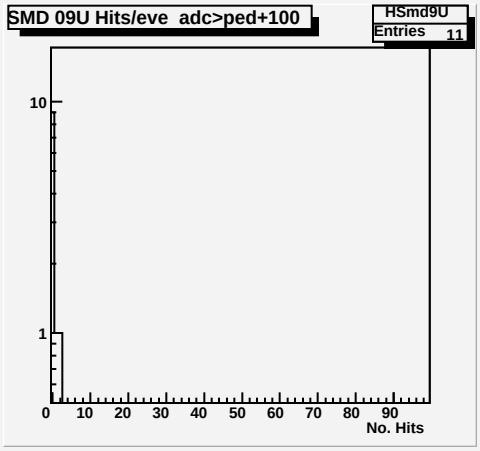
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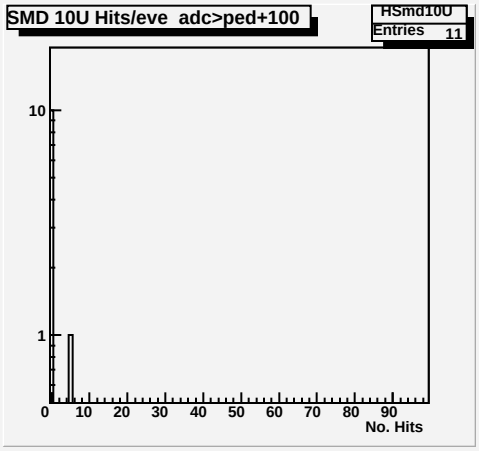
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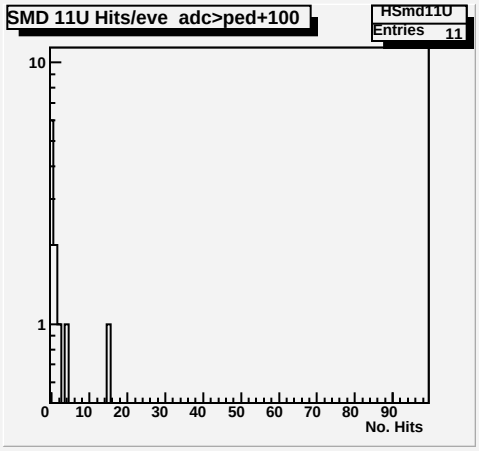
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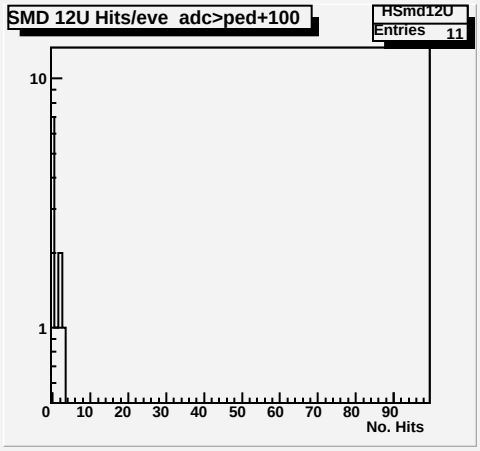
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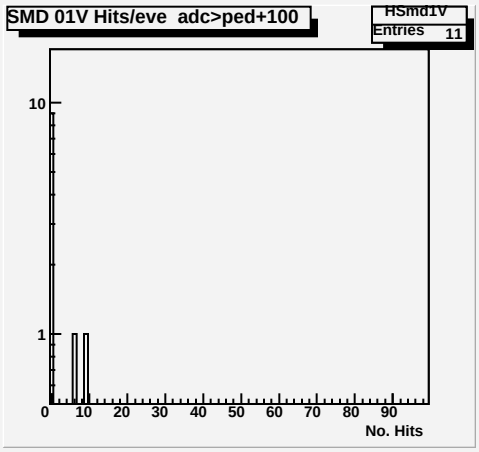
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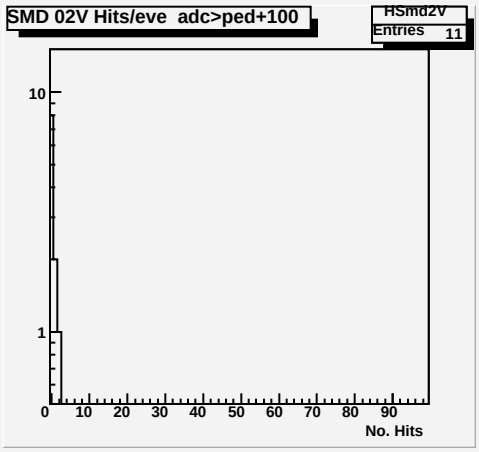
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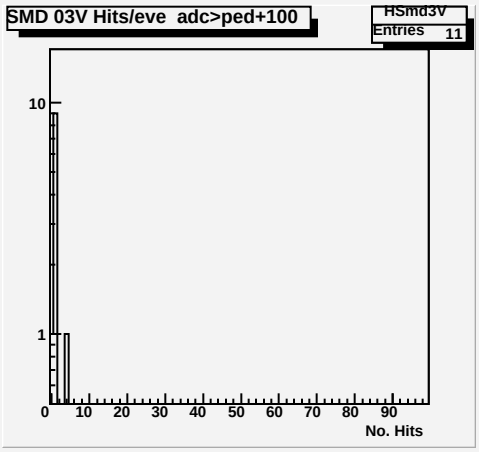
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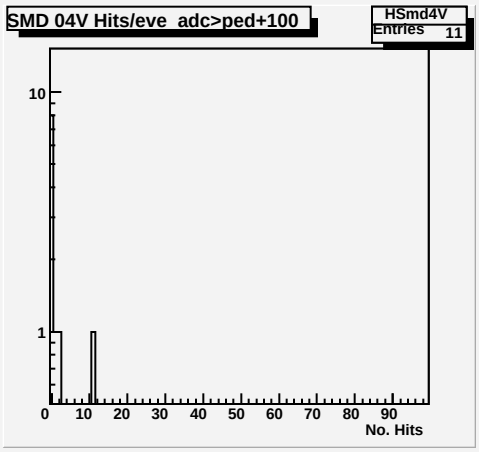
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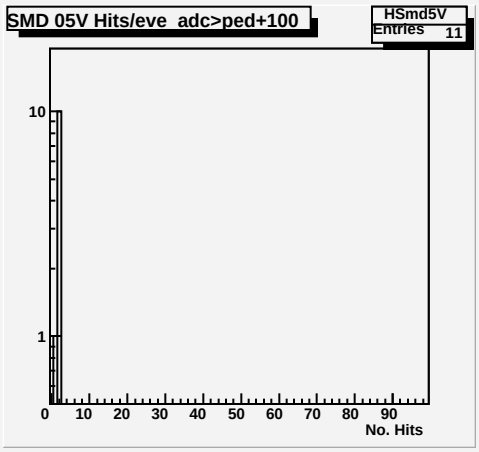
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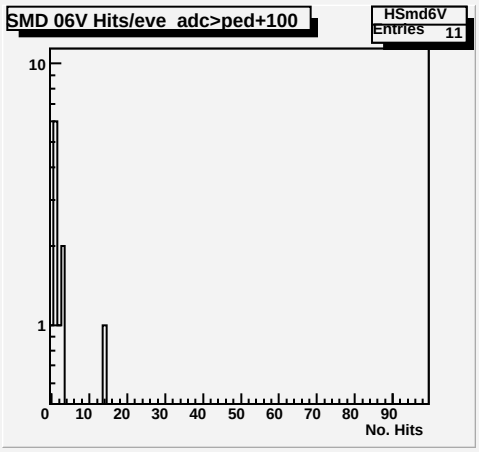
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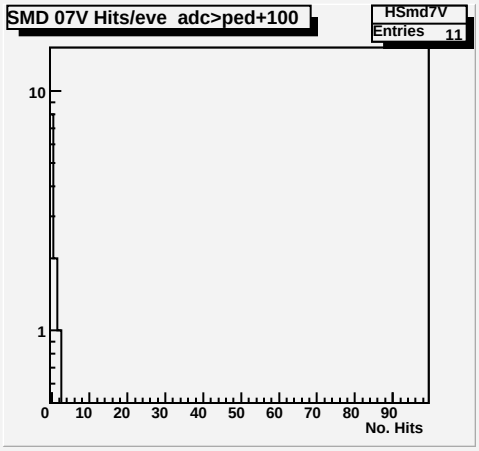
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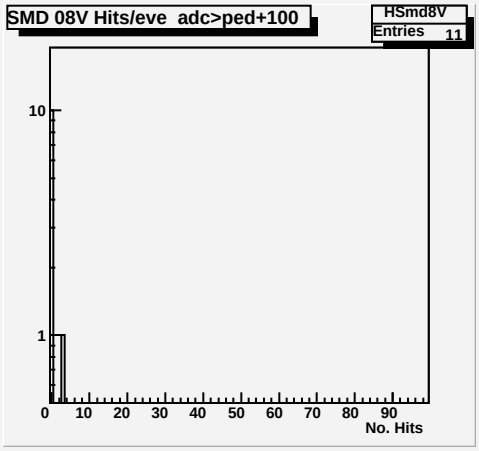
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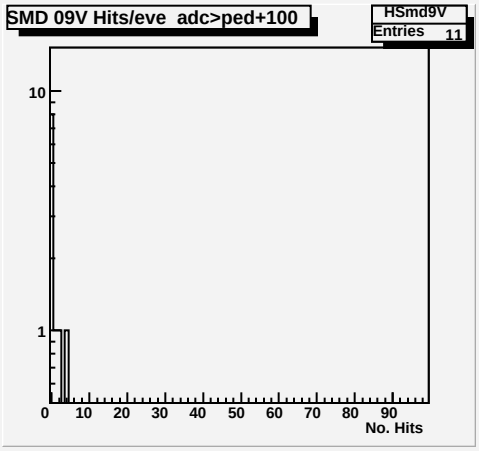
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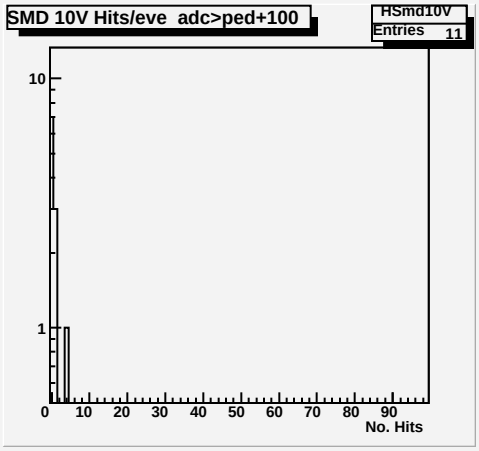
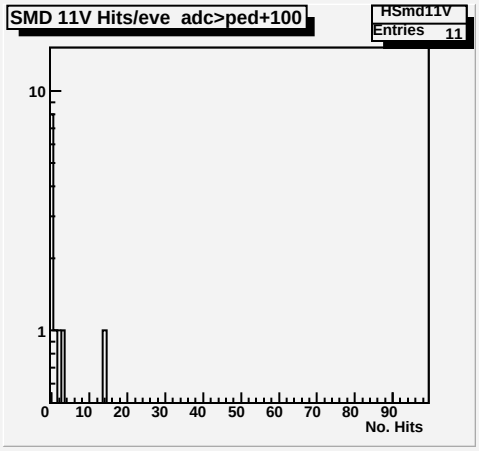
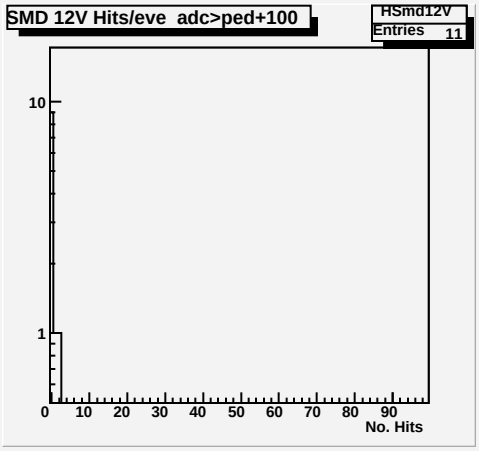
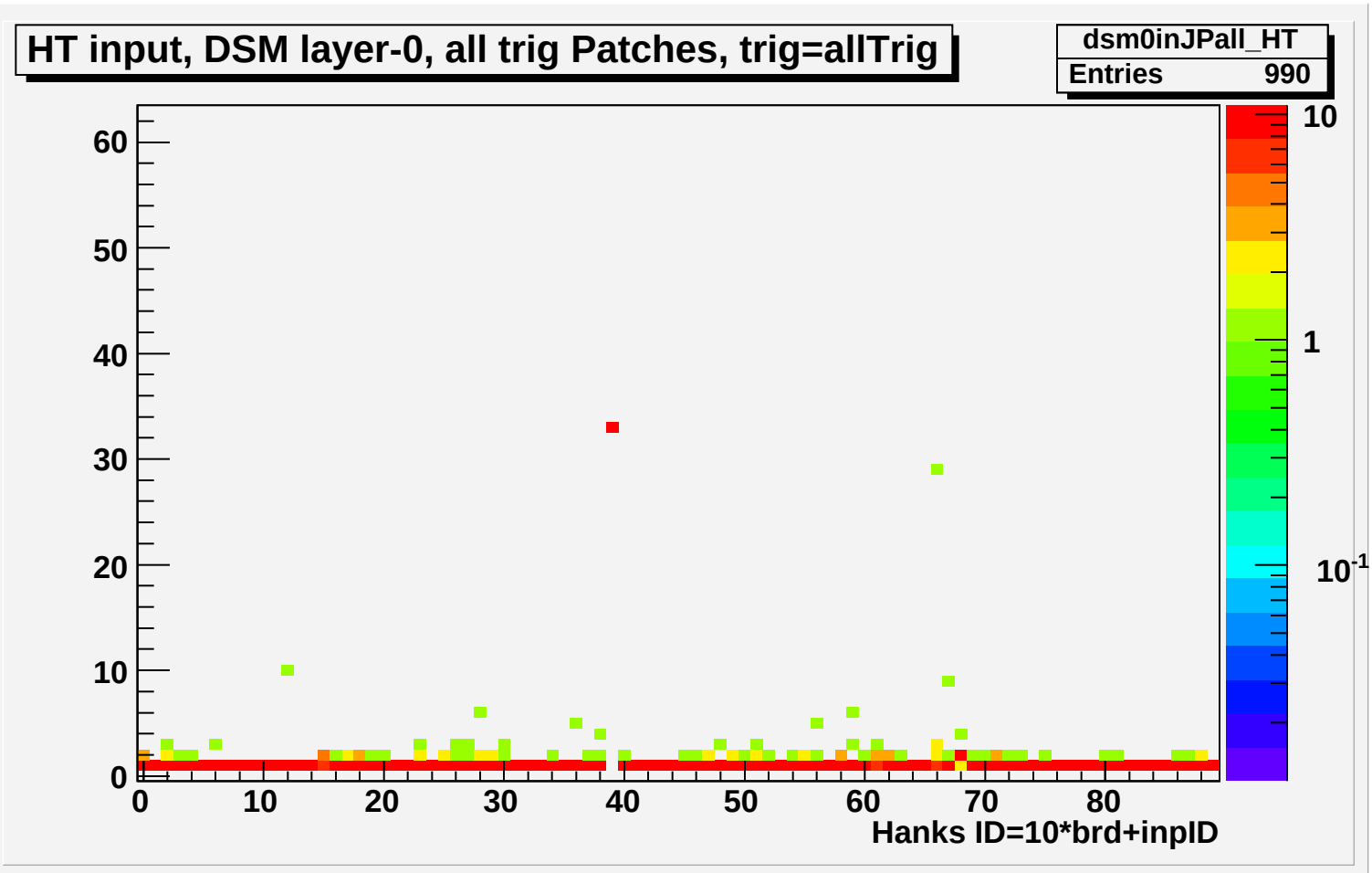
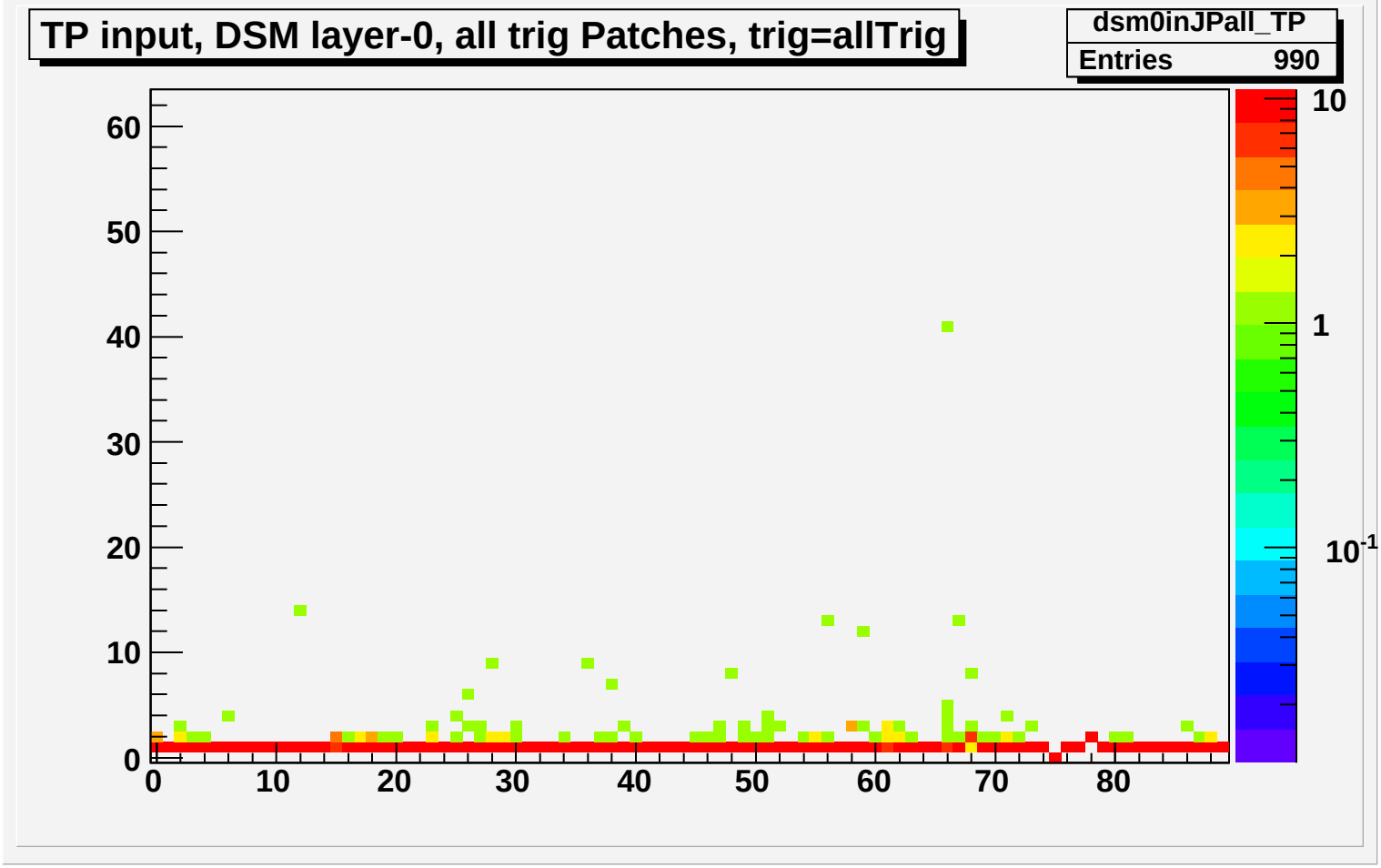
HSmd9V
Entries 11
No. Hits

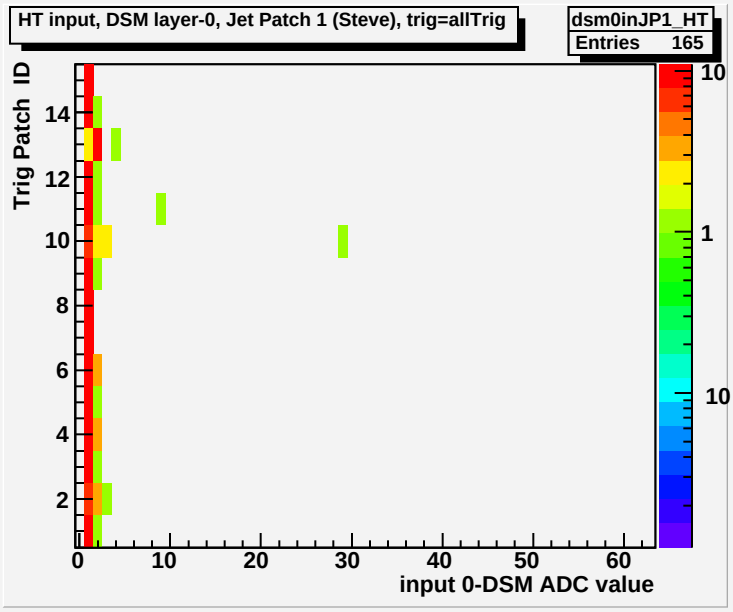

SMD 10V Hits/eve adc>ped+100
HSmd10V
Entries 11
No. Hits

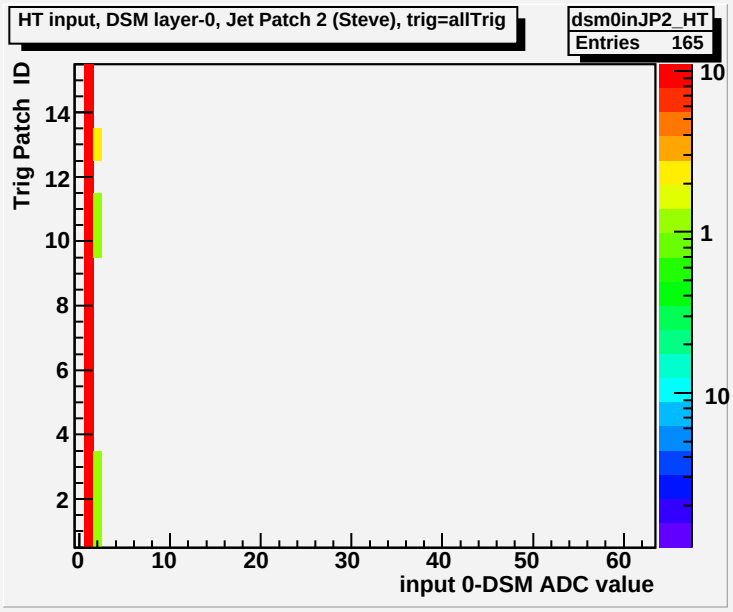

SMD 11V Hits/eve adc>ped+100
HSmd11V
Entries 11
No. Hits

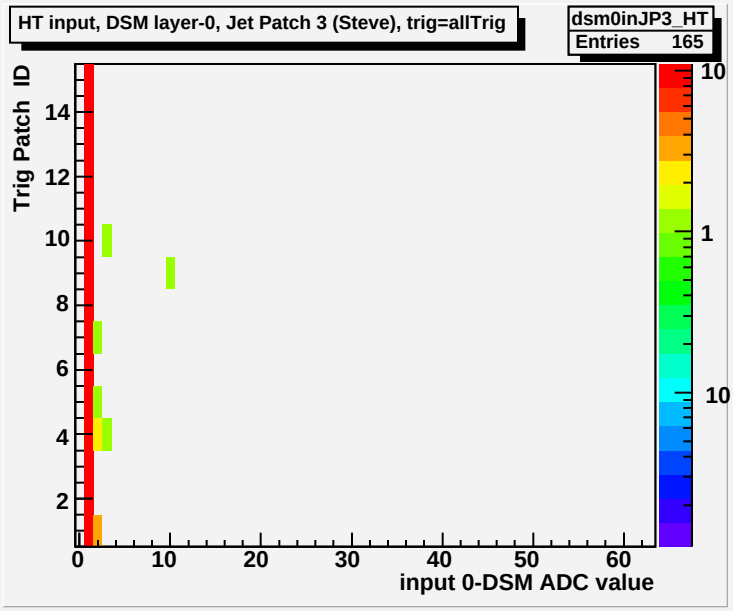

SMD 12V Hits/eve adc>ped+100
HSmd12V
Entries 11
No. Hits

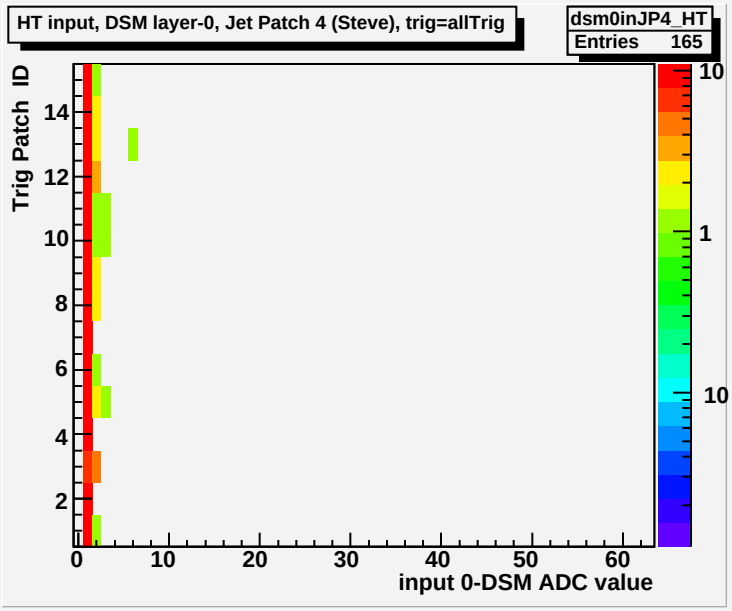

HT input, DSM layer-0, all trig Patches, trig=allTrig
dsm0inJPall_HT
Entries 990
Hanks ID=10*brd+inpID

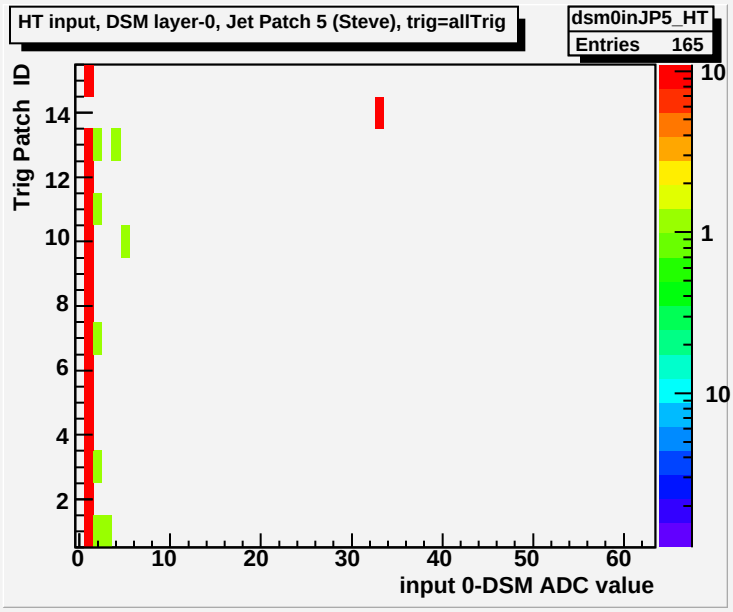

TP input, DSM layer-0, all trig Patches, trig=allTrig
dsm0inJPall_TP
Entries 990

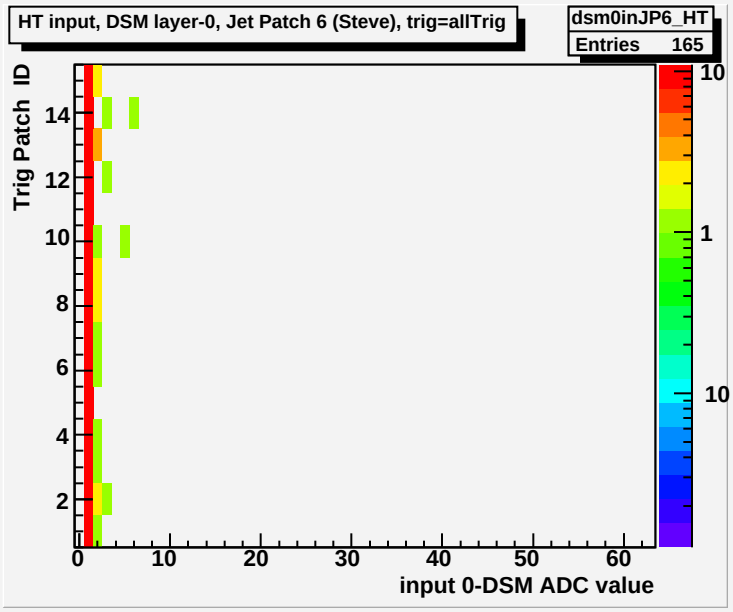

HT input, DSM layer-0, Jet Patch 1 (Steve), trig=allTrig
dsm0inJP1_HT
Entries 165
Trig Patch ID
input 0-DSM ADC value

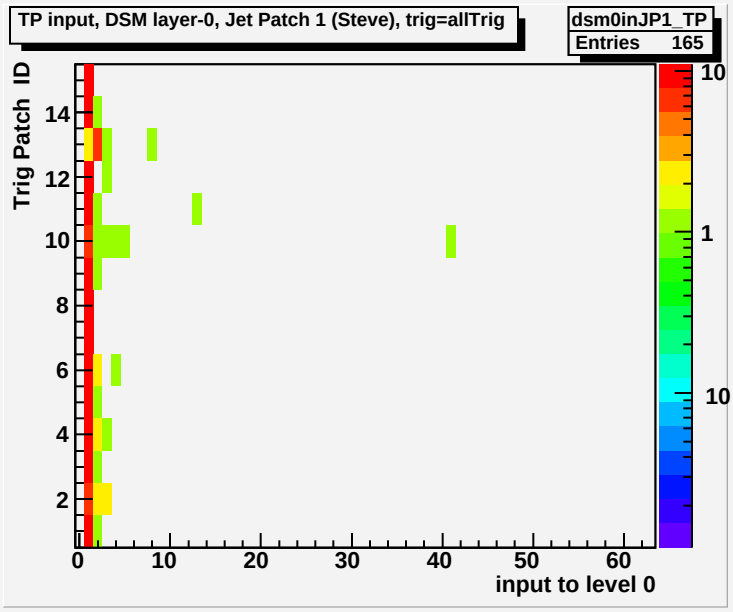

HT input, DSM layer-0, Jet Patch 2 (Steve), trig=allTrig
dsm0inJP2_HT
Entries 165
Trig Patch ID
input 0-DSM ADC value

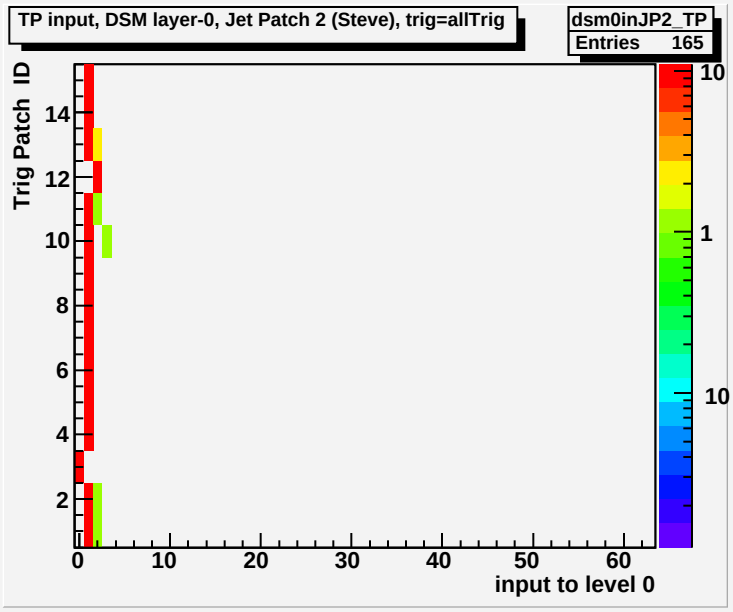

HT input, DSM layer-0, Jet Patch 3 (Steve), trig=allTrig
dsm0inJP3_HT
Entries 165
Trig Patch ID
input 0-DSM ADC value

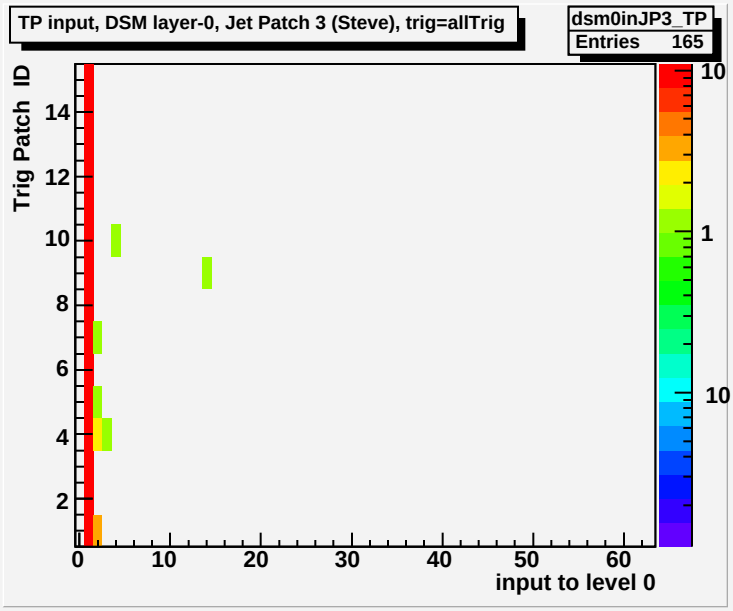

HT input, DSM layer-0, Jet Patch 4 (Steve), trig=allTrig
dsm0inJP4_HT
Entries 165
Trig Patch ID
input 0-DSM ADC value

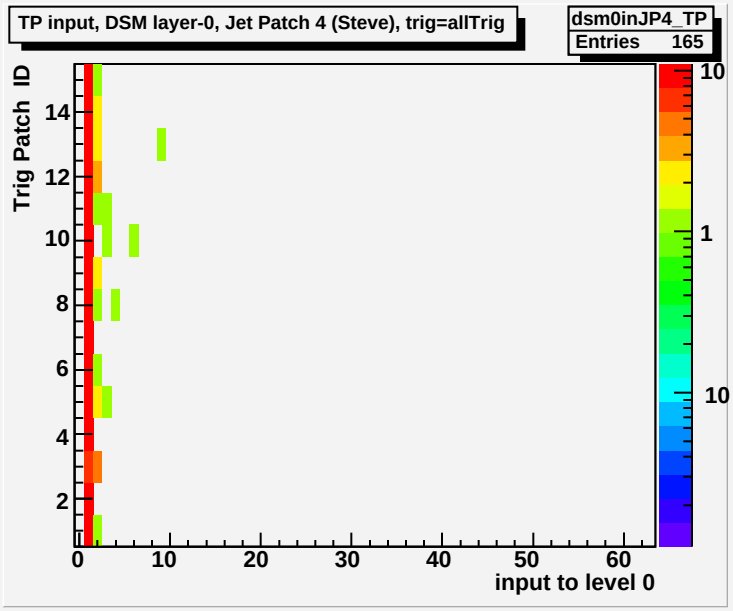

HT input, DSM layer-0, Jet Patch 5 (Steve), trig=allTrig
dsm0inJP5_HT
Entries 165
Trig Patch ID
input 0-DSM ADC value

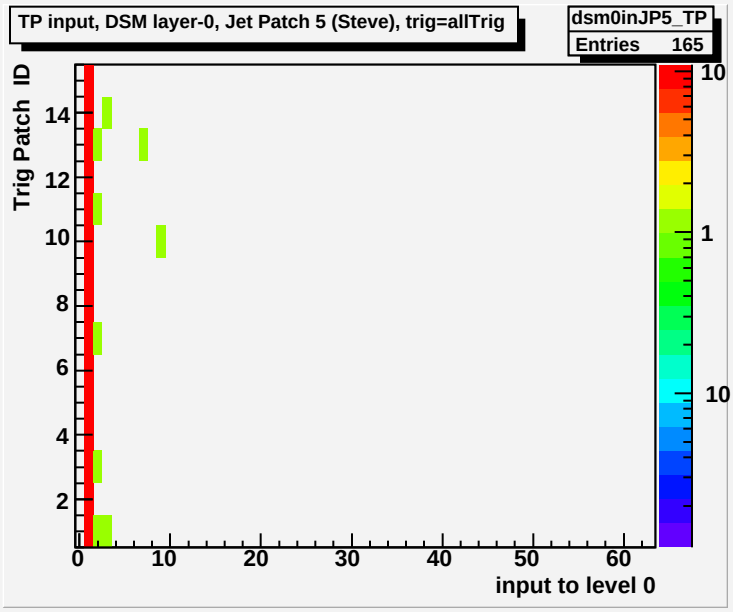

HT input, DSM layer-0, Jet Patch 6 (Steve), trig=allTrig
dsm0inJP6_HT
Entries 165
Trig Patch ID
input 0-DSM ADC value

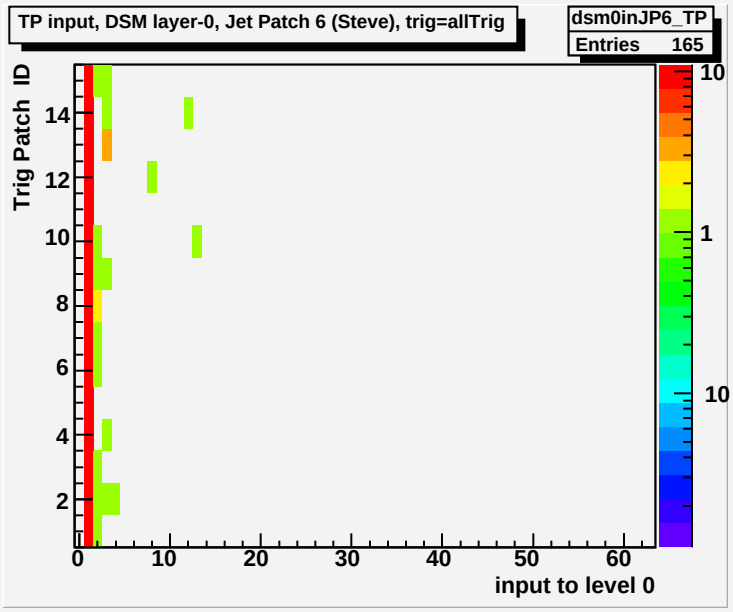

TP input, DSM layer-0, Jet Patch 1 (Steve), trig=allTrig
dsm0inJP1_TP
Entries 165
Trig Patch ID
input to level 0

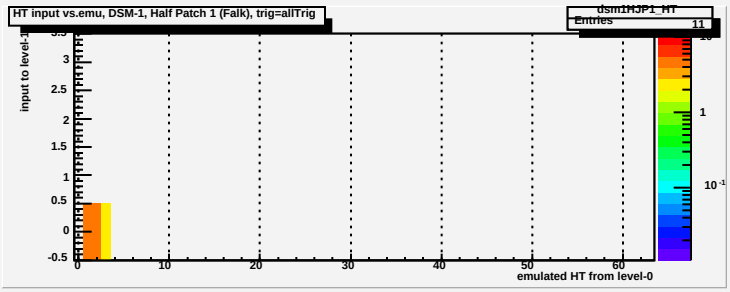

TP input, DSM layer-0, Jet Patch 2 (Steve), trig=allTrig
dsm0inJP2_TP
Entries 165
Trig Patch ID
input to level 0

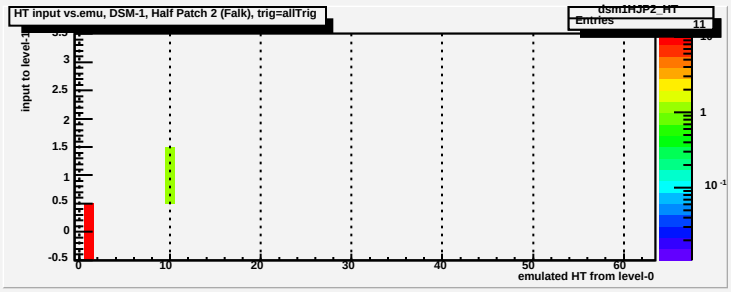

TP input, DSM layer-0, Jet Patch 3 (Steve), trig=allTrig
dsm0inJP3_TP
Entries 165
Trig Patch ID
input to level 0

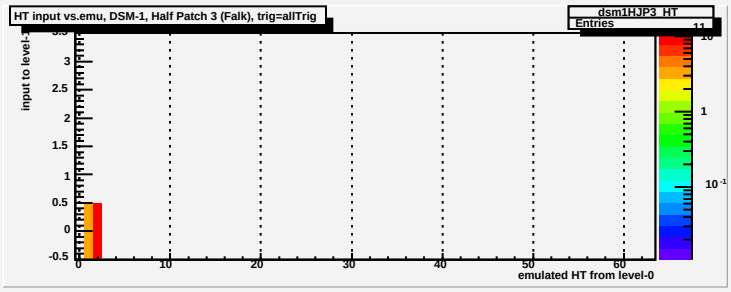

TP input, DSM layer-0, Jet Patch 4 (Steve), trig=allTrig
dsm0inJP4_TP
Entries 165
Trig Patch ID
input to level 0

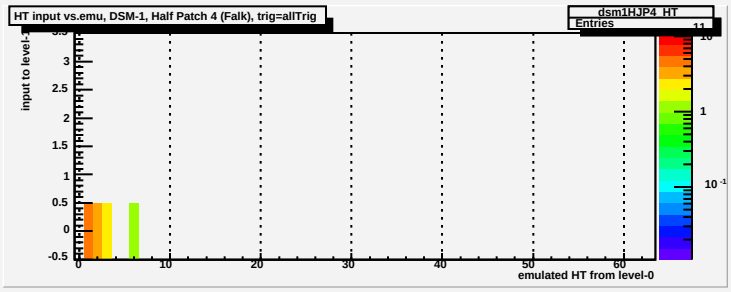

TP input, DSM layer-0, Jet Patch 5 (Steve), trig=allTrig
dsm0inJP5_TP
Entries 165
Trig Patch ID
input to level 0

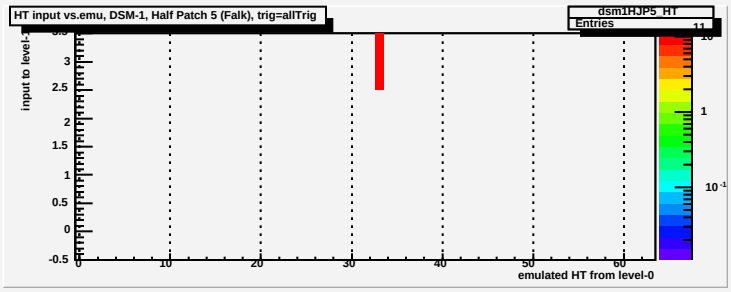

TP input, DSM layer-0, Jet Patch 6 (Steve), trig=allTrig
dsm0inJP6_TP
Entries 165
Trig Patch ID
input to level 0

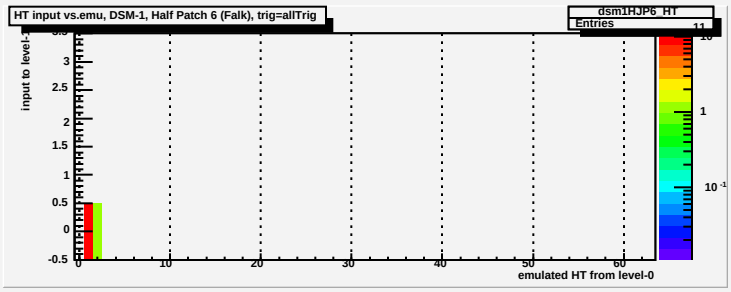

HT input vs.emu, DSM-1, Half Patch 1 (Falk), trig=allTrig
dsm1HJP1_HT
Entries 11
input to level-1
emulated HT from level-0

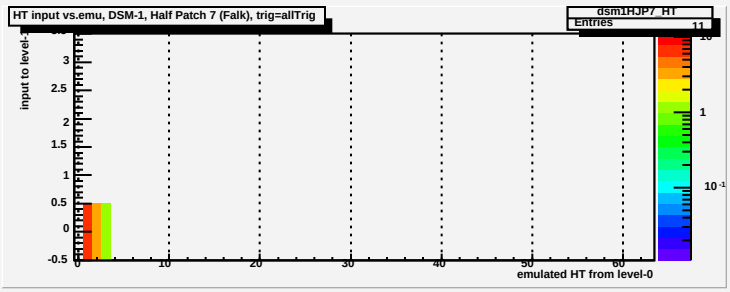

HT input vs.emu, DSM-1, Half Patch 2 (Falk), trig=allTrig
dsm1HJP2_HT
Entries 11
input to level-1
emulated HT from level-0

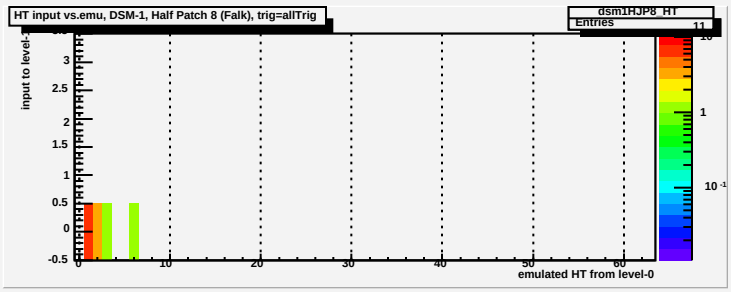

HT input vs.emu, DSM-1, Half Patch 3 (Falk), trig=allTrig
dsm1HJP3_HT
Entries 11
input to level-1
emulated HT from level-0

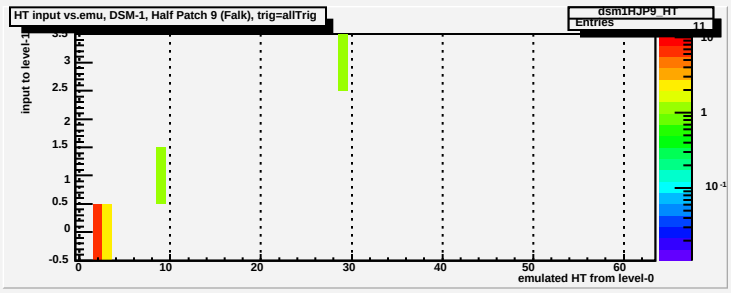

HT input vs.emu, DSM-1, Half Patch 4 (Falk), trig=allTrig
dsm1HJP4_HT
Entries 11
input to level-1
emulated HT from level-0

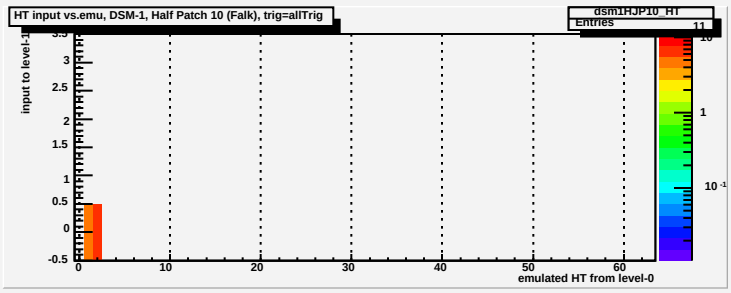

HT input vs.emu, DSM-1, Half Patch 5 (Falk), trig=allTrig
dsm1HJP5_HT
Entries 11
input to level-1
emulated HT from level-0

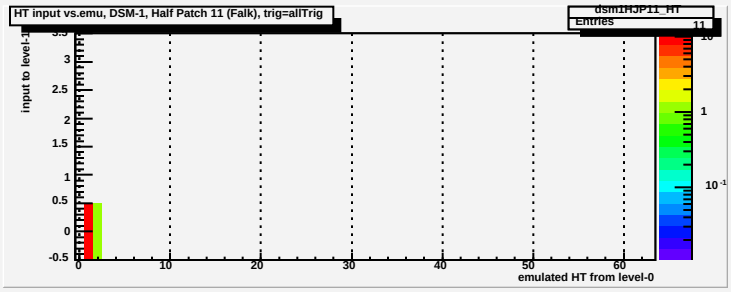

HT input vs.emu, DSM-1, Half Patch 6 (Falk), trig=allTrig
dsm1HJP6_HT
Entries 11
input to level-1
emulated HT from level-0

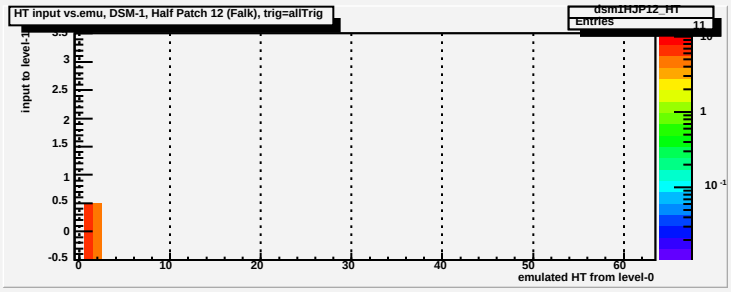

HT input vs.emu, DSM-1, Half Patch 7 (Falk), trig=allTrig
dsm1HJP7_HT
Entries 11
input to level-1
emulated HT from level-0

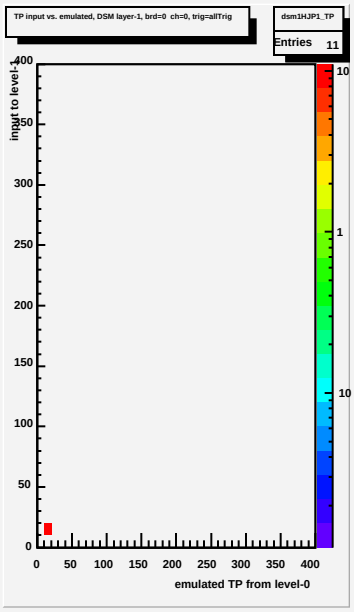

HT input vs.emu, DSM-1, Half Patch 8 (Falk), trig=allTrig
dsm1HJP8_HT
Entries 11
input to level-1
emulated HT from level-0

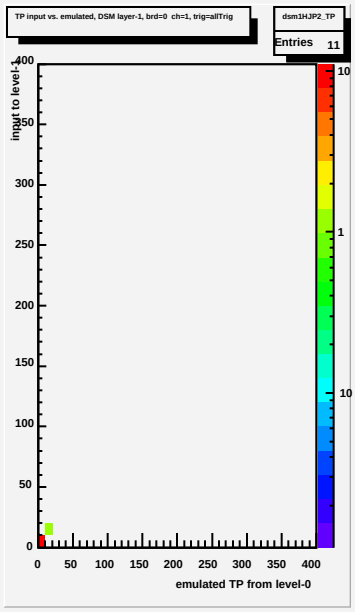

HT input vs.emu, DSM-1, Half Patch 9 (Falk), trig=allTrig
dsm1HJP9_HT
Entries 11
input to level-1
emulated HT from level-0

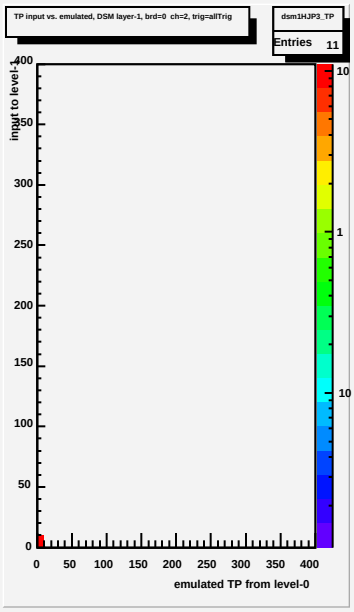

HT input vs.emu, DSM-1, Half Patch 10 (Falk), trig=allTrig
dsm1HJP10_HT
Entries 11
input to level-1
emulated HT from level-0

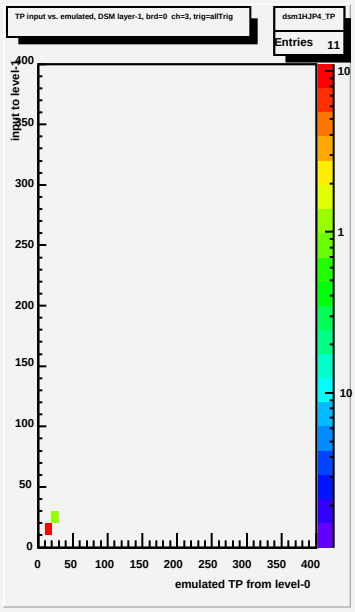

HT input vs.emu, DSM-1, Half Patch 11 (Falk), trig=allTrig
dsm1HJP11_HT
Entries 11
input to level-1
emulated HT from level-0

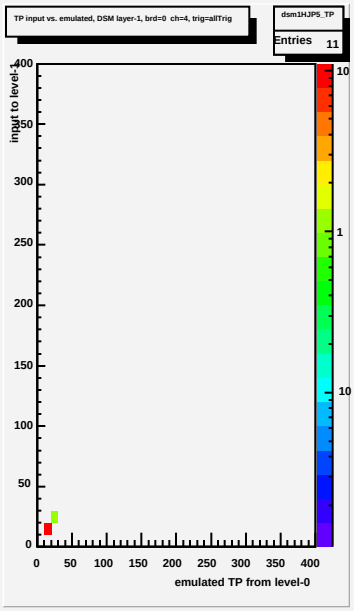

HT input vs.emu, DSM-1, Half Patch 12 (Falk), trig=allTrig
dsm1HJP12_HT
Entries 11
input to level-1
emulated HT from level-0

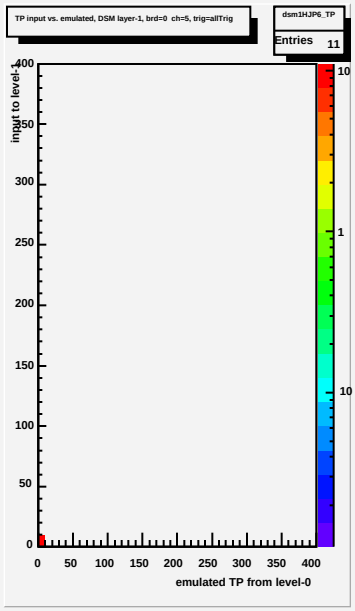

TP input vs. emulated, DSM layer-1, brd=0 ch=0, trig=allTrig
dsm1HJP1_TP
Entries 11
input to level-1
emulated TP from level-0

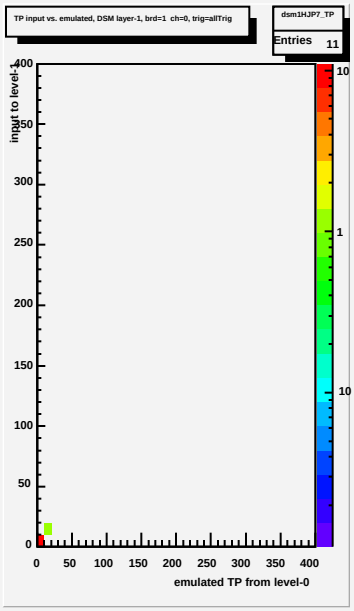

TP input vs. emulated, DSM layer-1, brd=0 ch=1, trig=allTrig
dsm1HJP2_TP
Entries 11
input to level-1
emulated TP from level-0

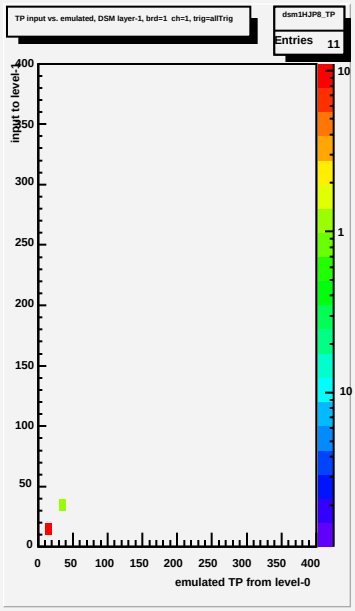

TP input vs. emulated, DSM layer-1, brd=0 ch=2, trig=allTrig
dsm1HJP3_TP
Entries 11
input to level-1
emulated TP from level-0

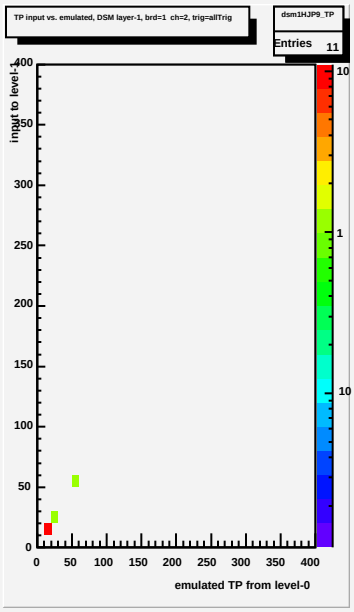

TP input vs. emulated, DSM layer-1, brd=0 ch=3, trig=allTrig
dsm1HJP4_TP
Entries 11
input to level-1
emulated TP from level-0

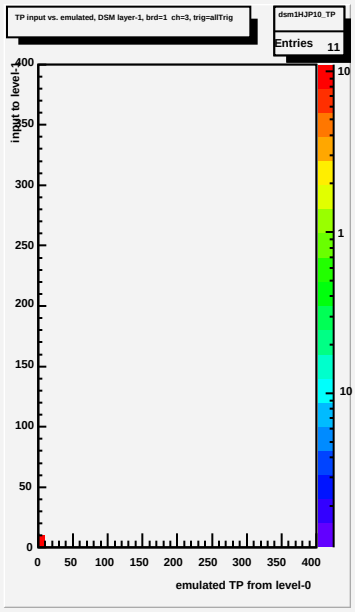

TP input vs. emulated, DSM layer-1, brd=0 ch=4, trig=allTrig
dsm1HJP5_TP
Entries 11
input to level-1
emulated TP from level-0

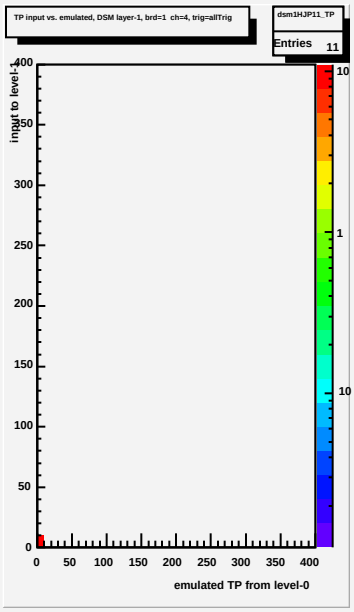

TP input vs. emulated, DSM layer-1, brd=0 ch=5, trig=allTrig
dsm1HJP6_TP
Entries 11
input to level-1
emulated TP from level-0

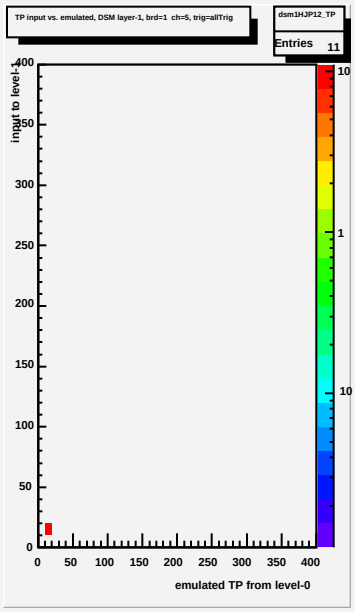

TP input vs. emulated, DSM layer-1, brd=1 ch=0, trig=allTrig
dsm1HJP7_TP
Entries 11
input to level-1
emulated TP from level-0

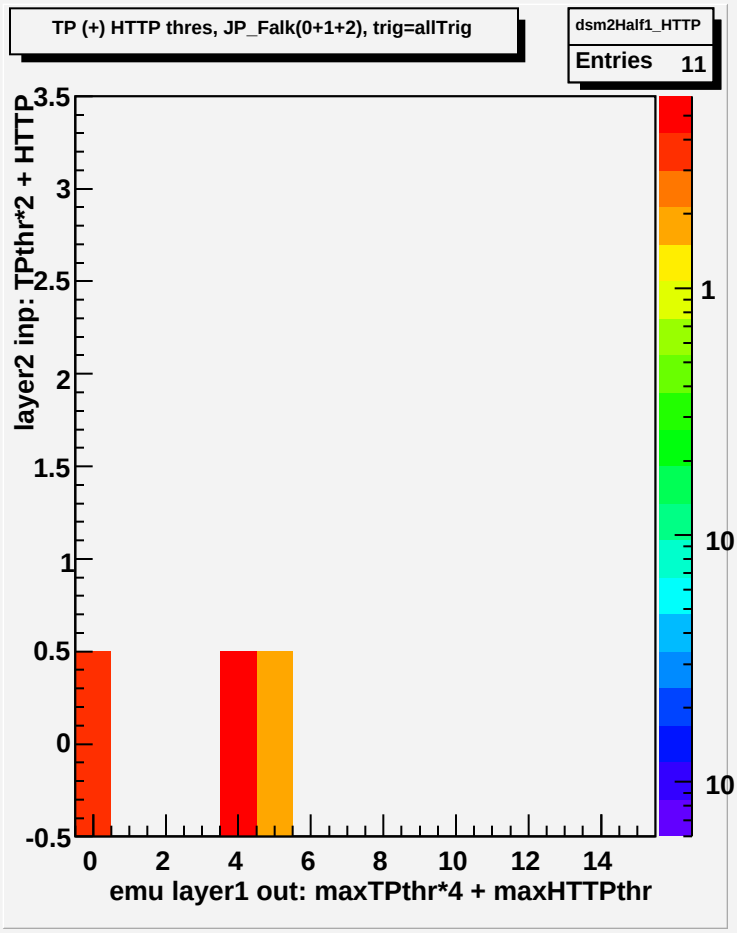

TP input vs. emulated, DSM layer-1, brd=1 ch=1, trig=allTrig
dsm1HJP8_TP
Entries 11
input to level-1
emulated TP from level-0

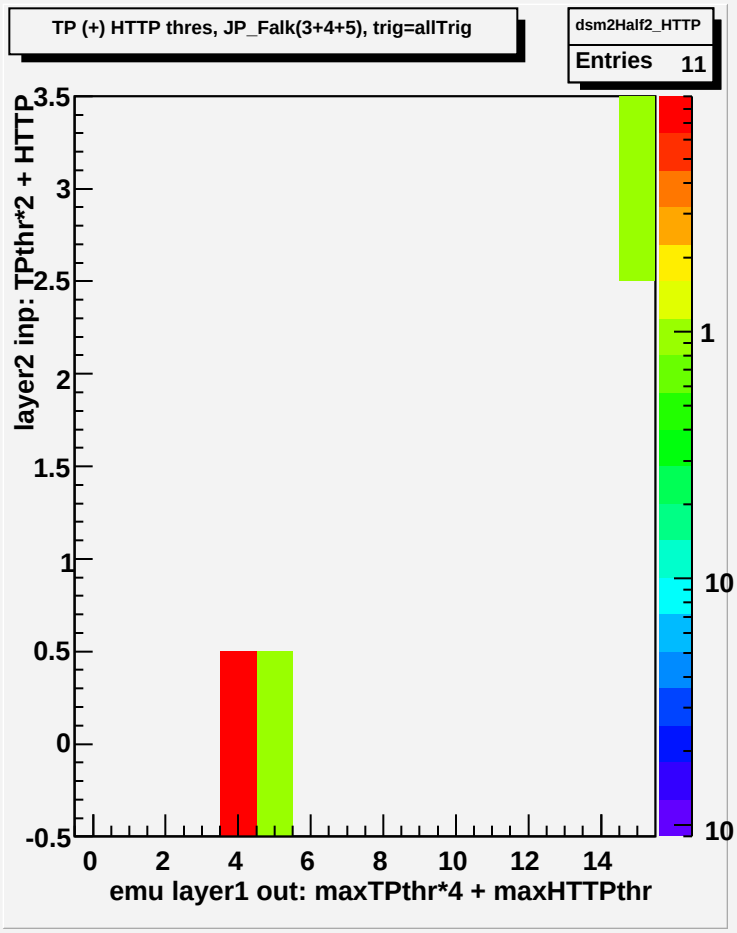

TP input vs. emulated, DSM layer-1, brd=1 ch=2, trig=allTrig
dsm1HJP9_TP
Entries 11
input to level-1
emulated TP from level-0

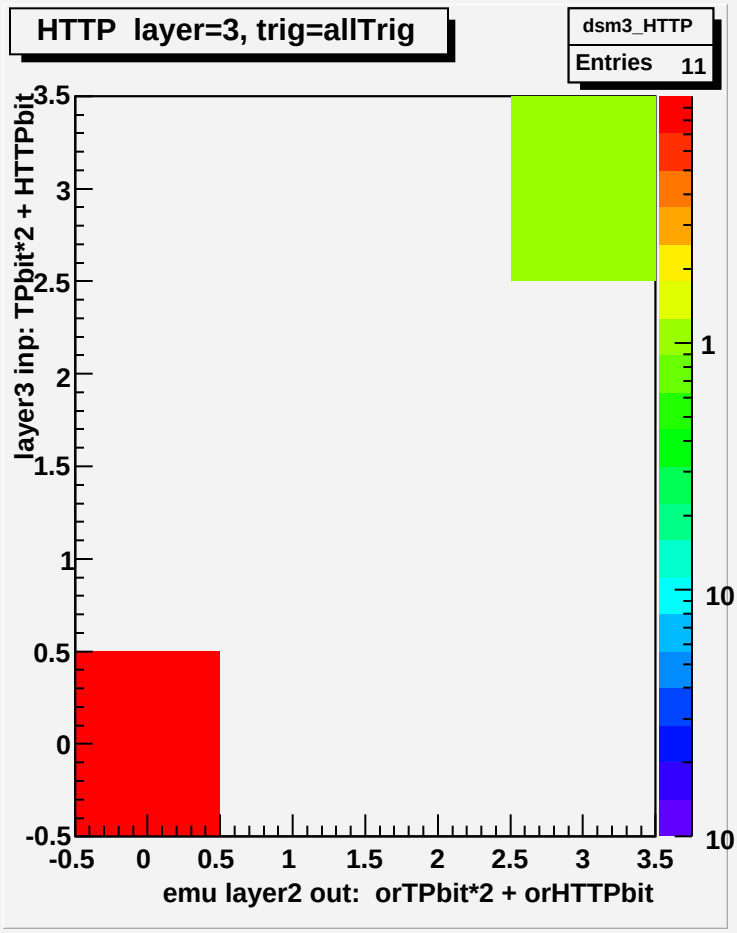

TP input vs. emulated, DSM layer-1, brd=1 ch=3, trig=allTrig
dsm1HJP10_TP
Entries 11
input to level-1
emulated TP from level-0

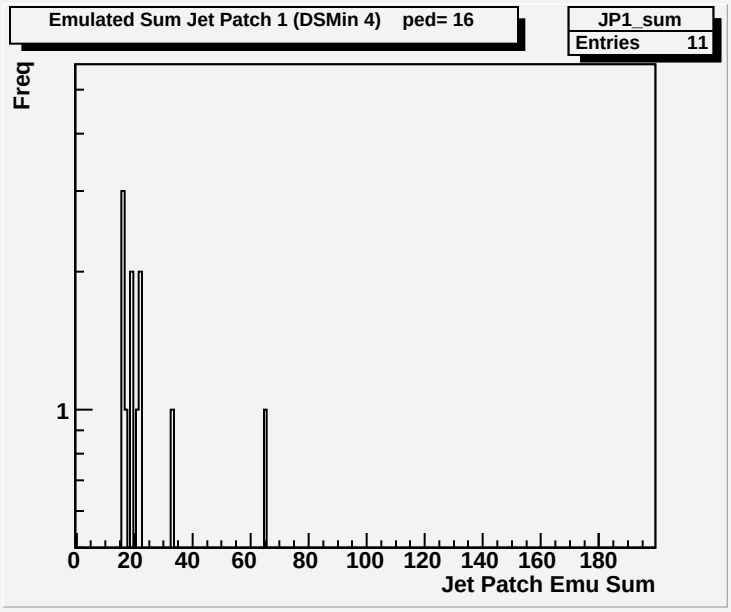

TP input vs. emulated, DSM layer-1, brd=1 ch=4, trig=allTrig
dsm1HJP11_TP
Entries 11
input to level-1
emulated TP from level-0

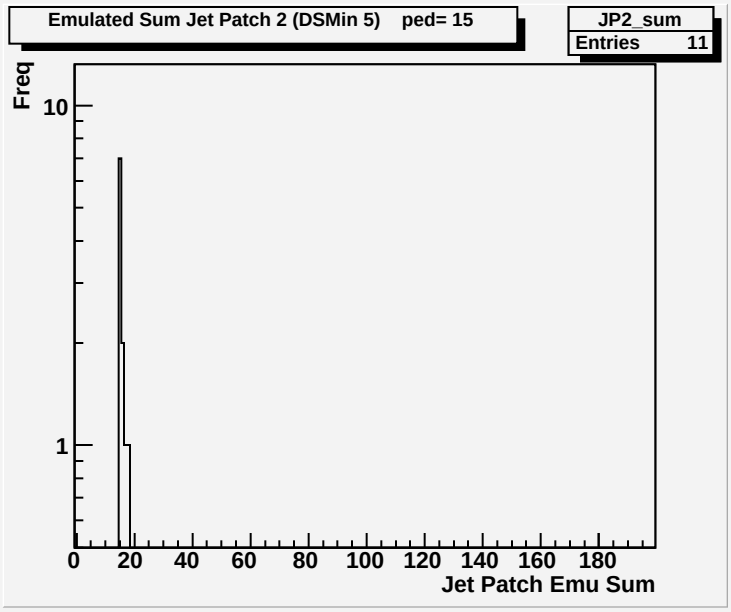

TP input vs. emulated, DSM layer-1, brd=1 ch=5, trig=allTrig
dsm1HJP12_TP
Entries 11
input to level-1
emulated TP from level-0

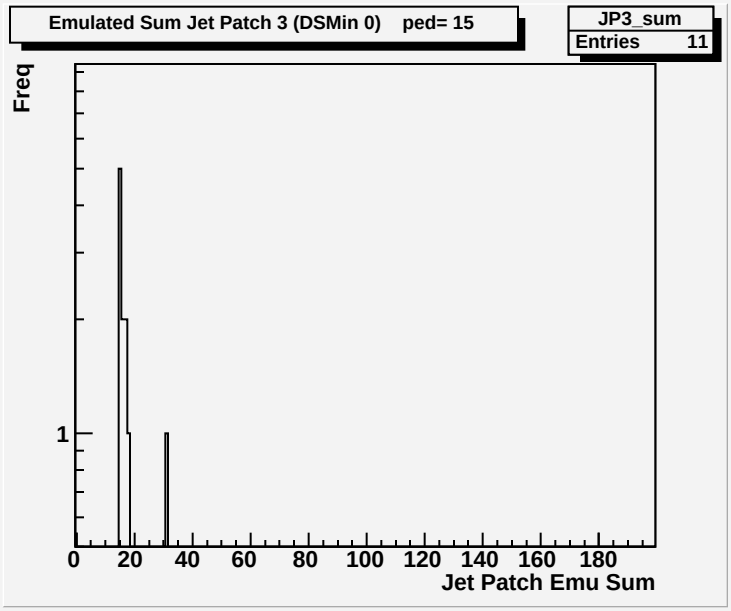

TP (+) HTTP thres, JP_Falk(0+1+2), trig=allTrig
dsm2Half1_HTTP
Entries 11
layer2 inp: TPthr*2 + HTTP
emu layer1 out: maxTPthr*4 + maxHTTPthr

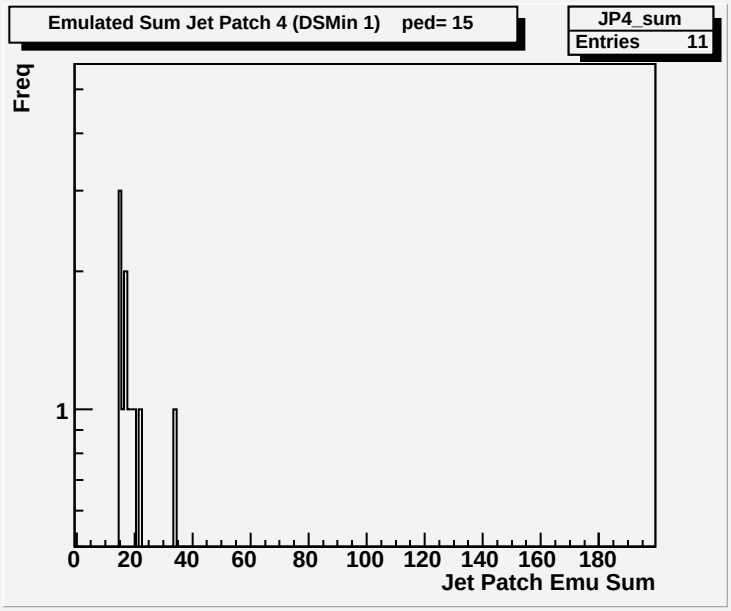

TP (+) HTTP thres, JP_Falk(3+4+5), trig=allTrig
dsm2Half2_HTTP
Entries 11
layer2 inp: TPthr*2 + HTTP
emu layer1 out: maxTPthr*4 + maxHTTPthr

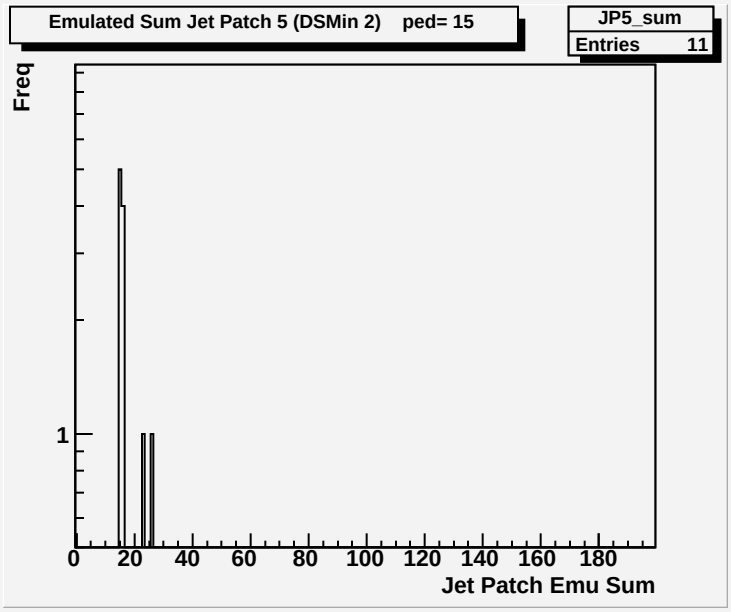

HTTP layer=3, trig=allTrig
dsm3_HTTP
Entries 11
layer3 inp: TPbit*2 + HTTPbit
emu layer2 out: orTPbit*2 + orHTTPbit

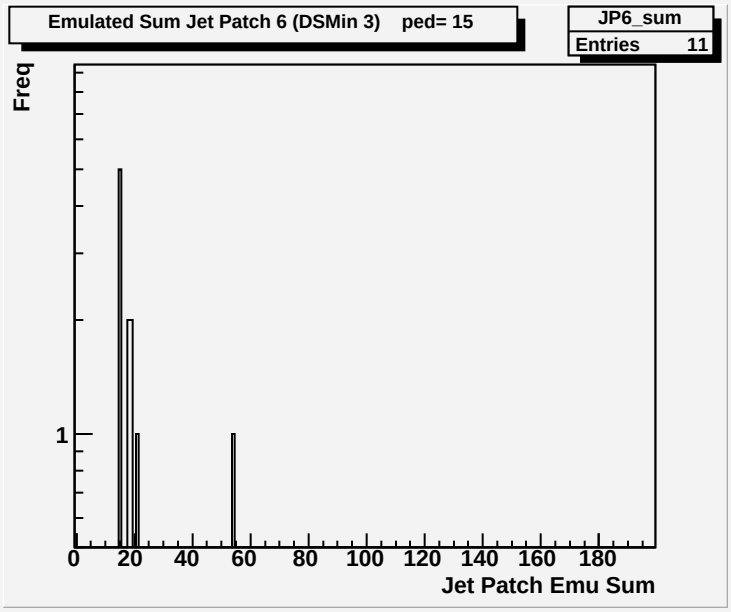

Emulated Sum Jet Patch 1 (DSMin 4) ped= 16
JP1_sum
Entries 11
Freq
1
0 20 40 60 80 100 120 140 160 180
Jet Patch Emu Sum

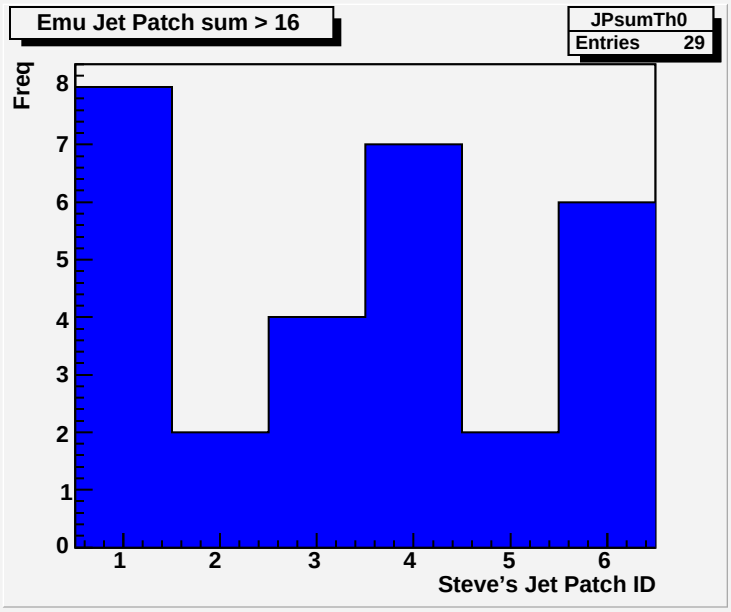

Emulated Sum Jet Patch 2 (DSMin 5) ped= 15
JP2_sum
Entries 11
Freq
10
1
0 20 40 60 80 100 120 140 160 180
Jet Patch Emu Sum

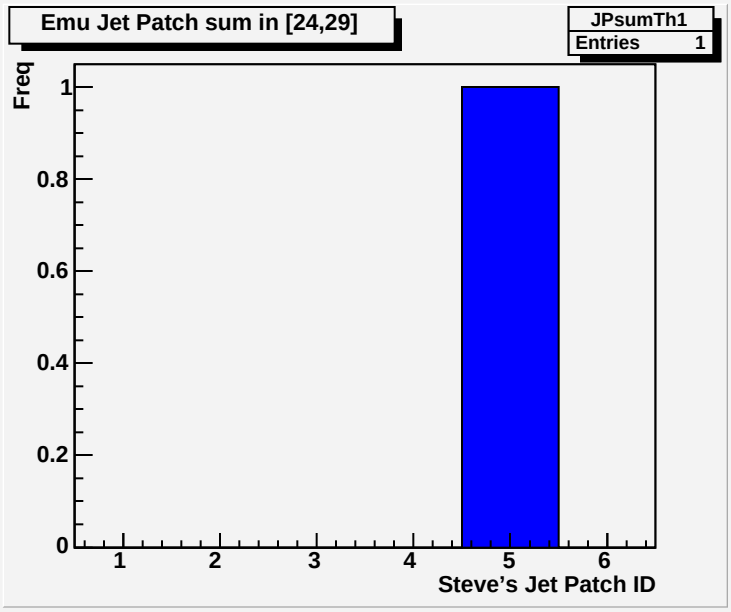

Emulated Sum Jet Patch 3 (DSMin 0) ped= 15
JP3_sum
Entries 11
Freq
1
0 20 40 60 80 100 120 140 160 180
Jet Patch Emu Sum

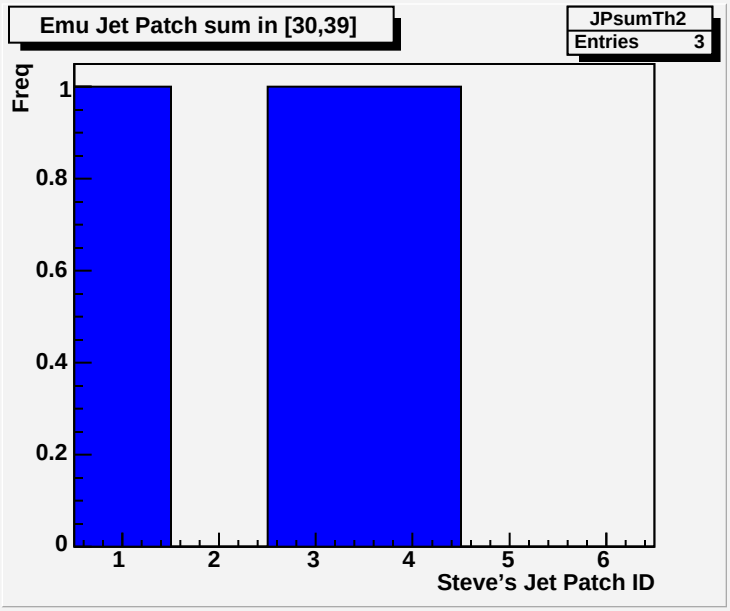

Emulated Sum Jet Patch 4 (DSMin 1) ped= 15
JP4_sum
Entries 11
Freq
1
0 20 40 60 80 100 120 140 160 180
Jet Patch Emu Sum

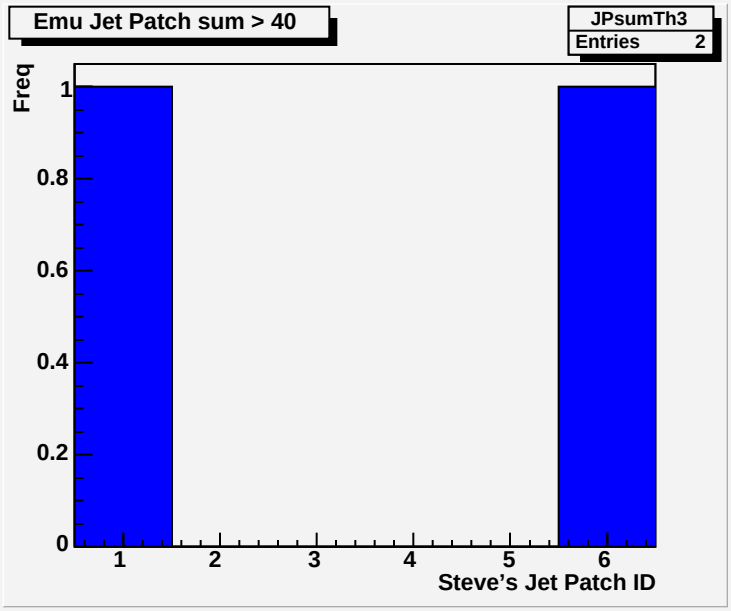

Emulated Sum Jet Patch 5 (DSMin 2) ped= 15
JP5_sum
Entries 11
Freq
1
0 20 40 60 80 100 120 140 160 180
Jet Patch Emu Sum

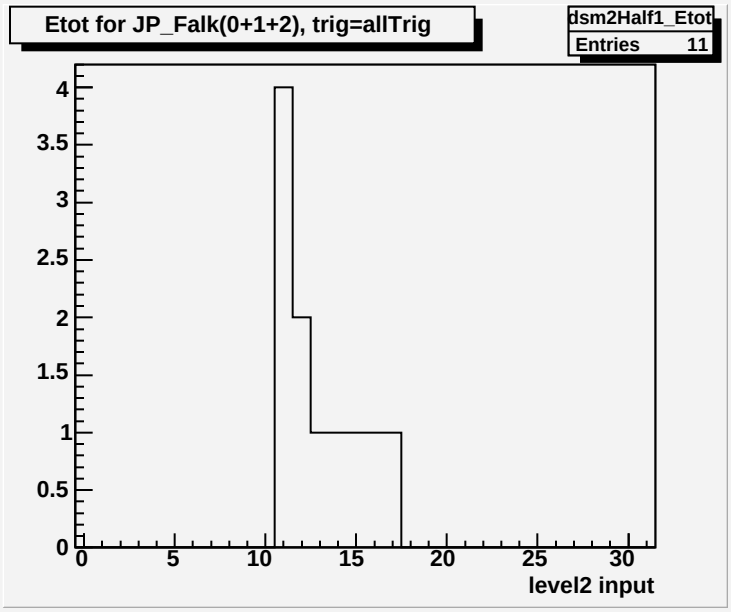

Emulated Sum Jet Patch 6 (DSMin 3) ped= 15
JP6_sum
Entries 11
Freq
1
0 20 40 60 80 100 120 140 160 180
Jet Patch Emu Sum

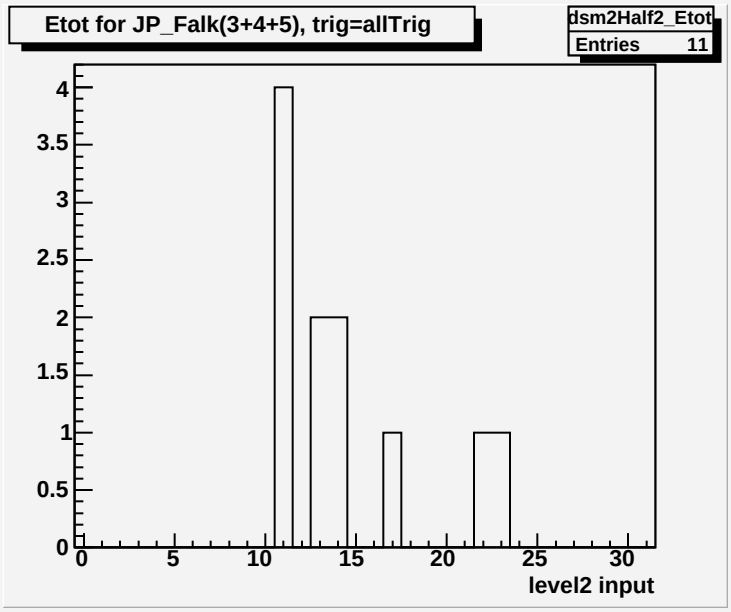

Emu Jet Patch sum > 16
JPsumTh0
Entries 29
Freq
Steve's Jet Patch ID

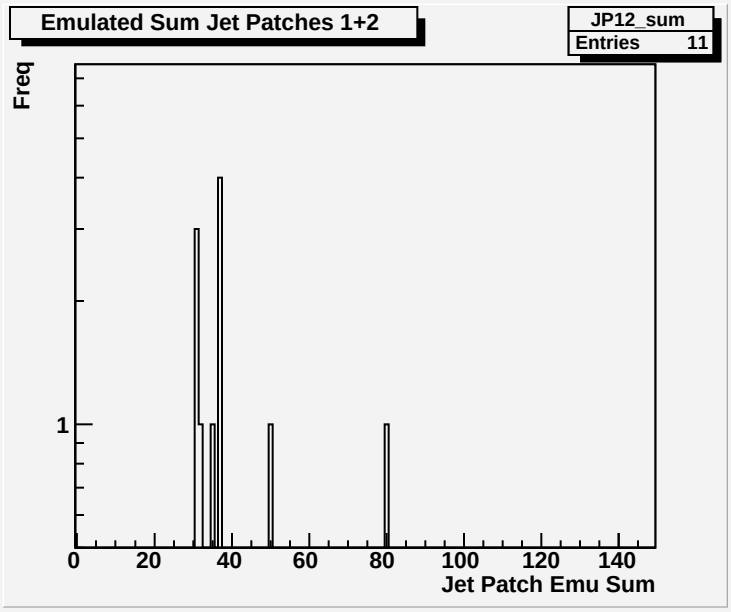

Emu Jet Patch sum in [24,29]
JPsumTh1
Entries 1
Freq
Steve's Jet Patch ID

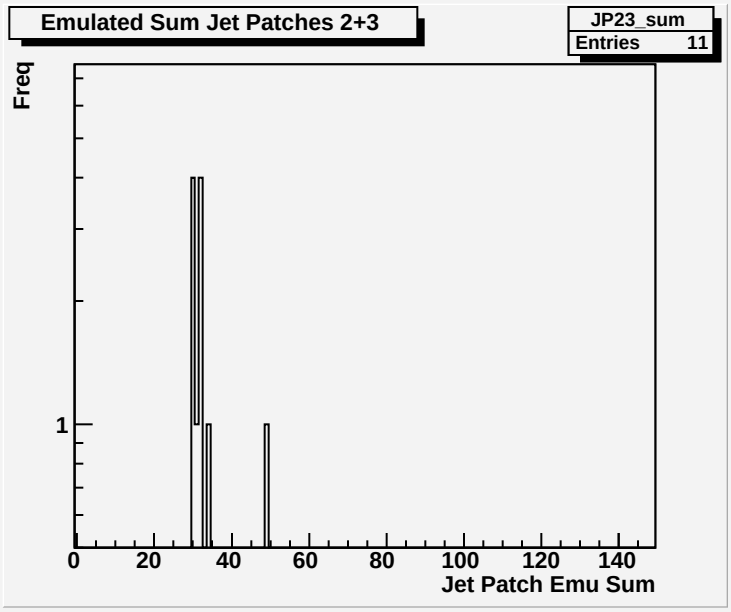

Emu Jet Patch sum in [30,39]
JPsumTh2
Entries 3
Freq
Steve's Jet Patch ID

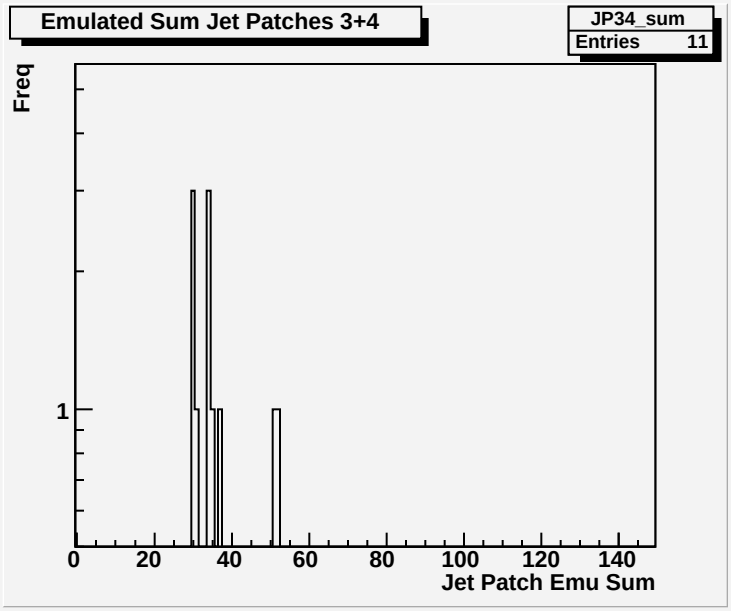

Emu Jet Patch sum > 40
JPsumTh3
Entries 2
Freq
Steve's Jet Patch ID

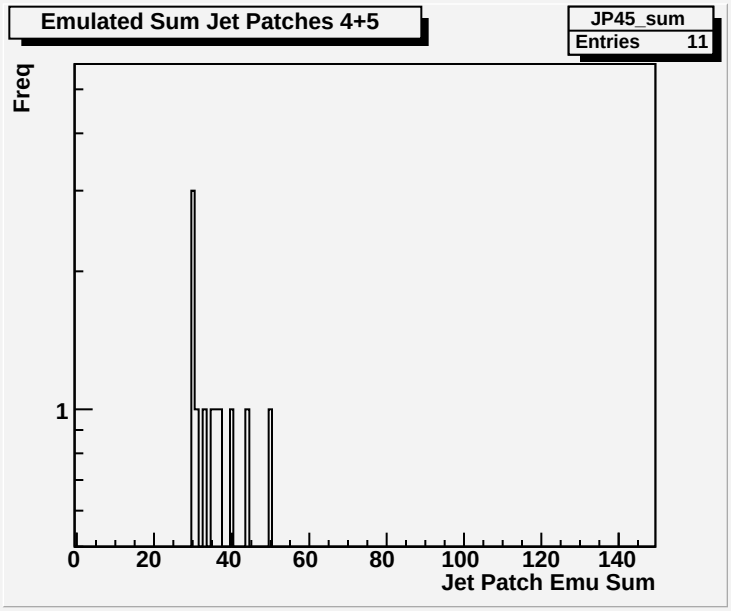

Etot for JP_Falk(0+1+2), trig=allTrig
dsm2Half1_Etot
Entries 11
level2 input

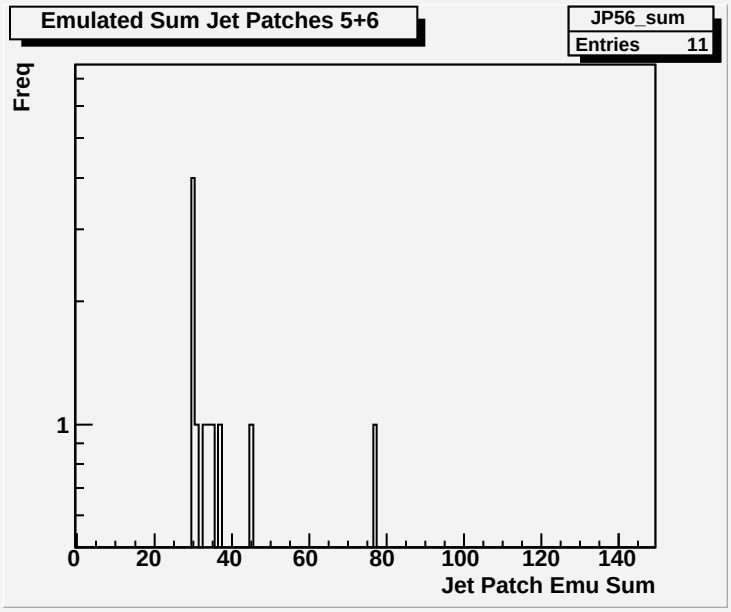

Etot for JP_Falk(3+4+5), trig=allTrig
dsm2Half2_Etot
Entries 11
level2 input

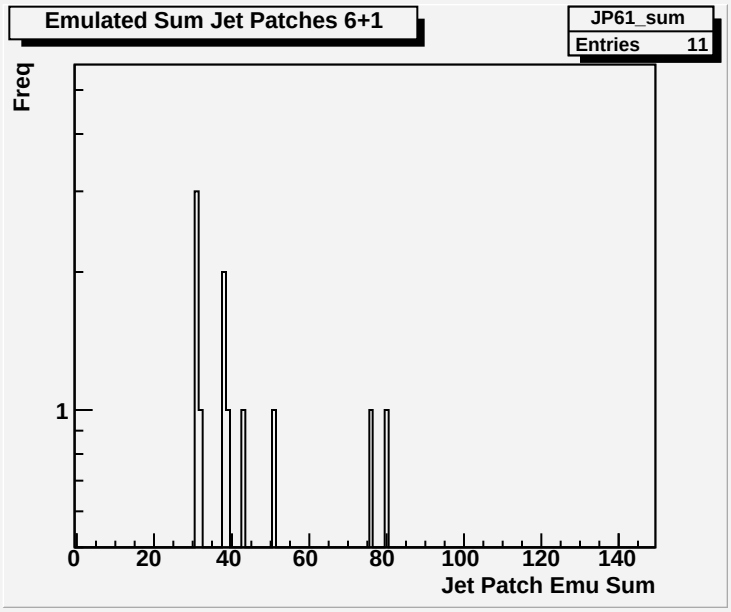

Emulated Sum Jet Patches 1+2
JP12_sum
Entries 11
Freq
1
0 20 40 60 80 100 120 140
Jet Patch Emu Sum

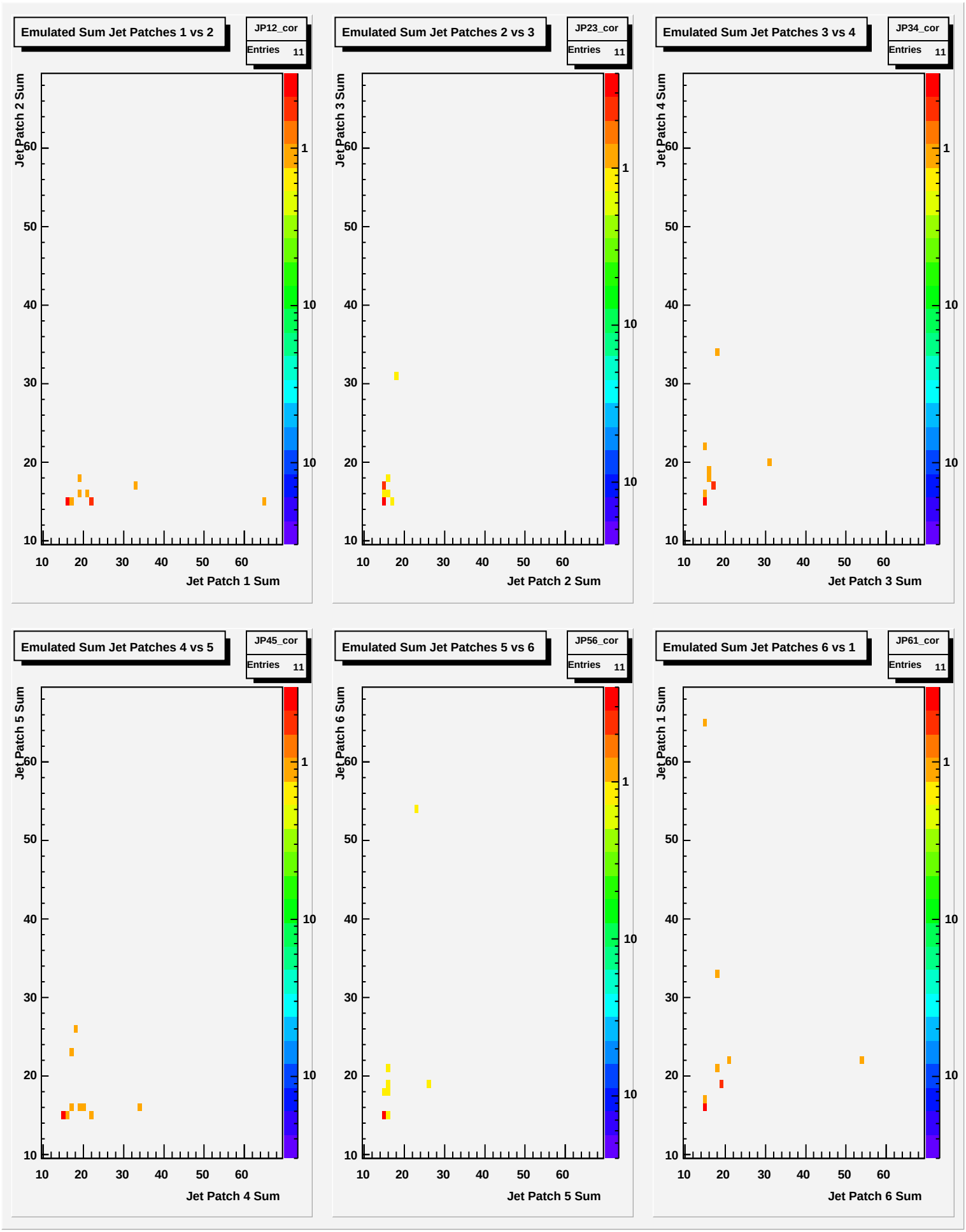

Emulated Sum Jet Patches 2+3
JP23_sum
Entries 11
Freq
1
0 20 40 60 80 100 120 140
Jet Patch Emu Sum

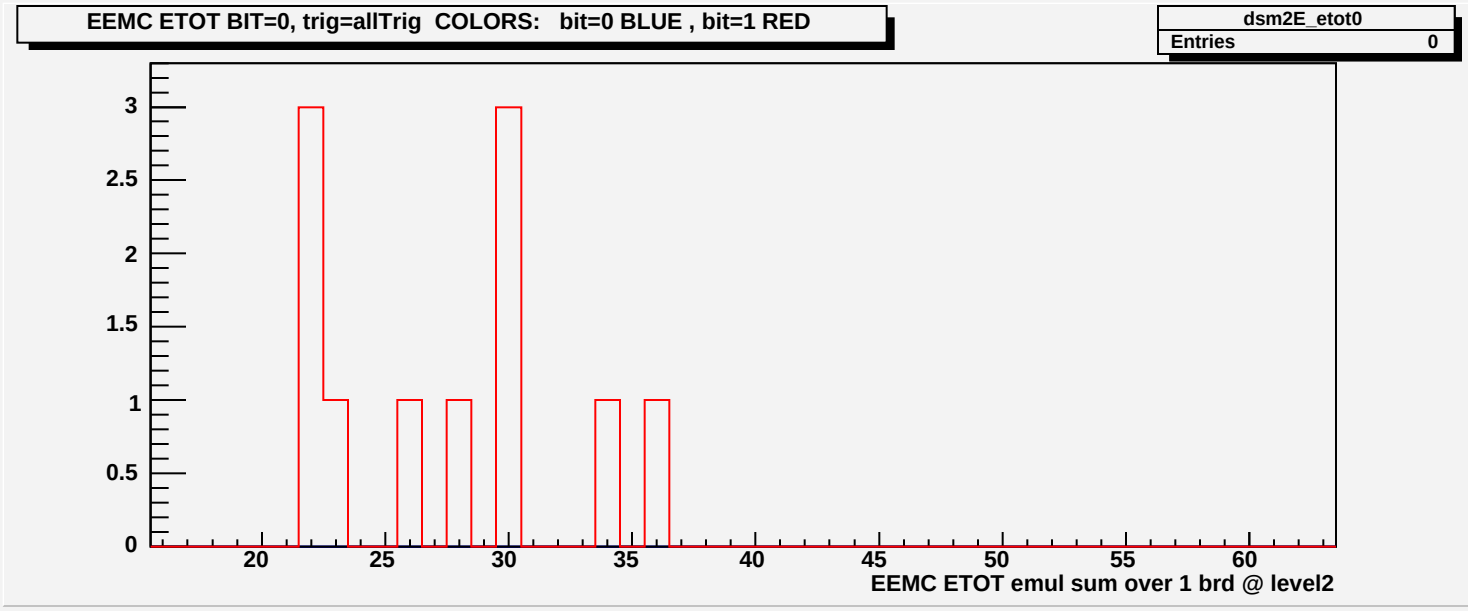

Emulated Sum Jet Patches 3+4
JP34_sum
Entries 11
Freq
1
0 20 40 60 80 100 120 140
Jet Patch Emu Sum

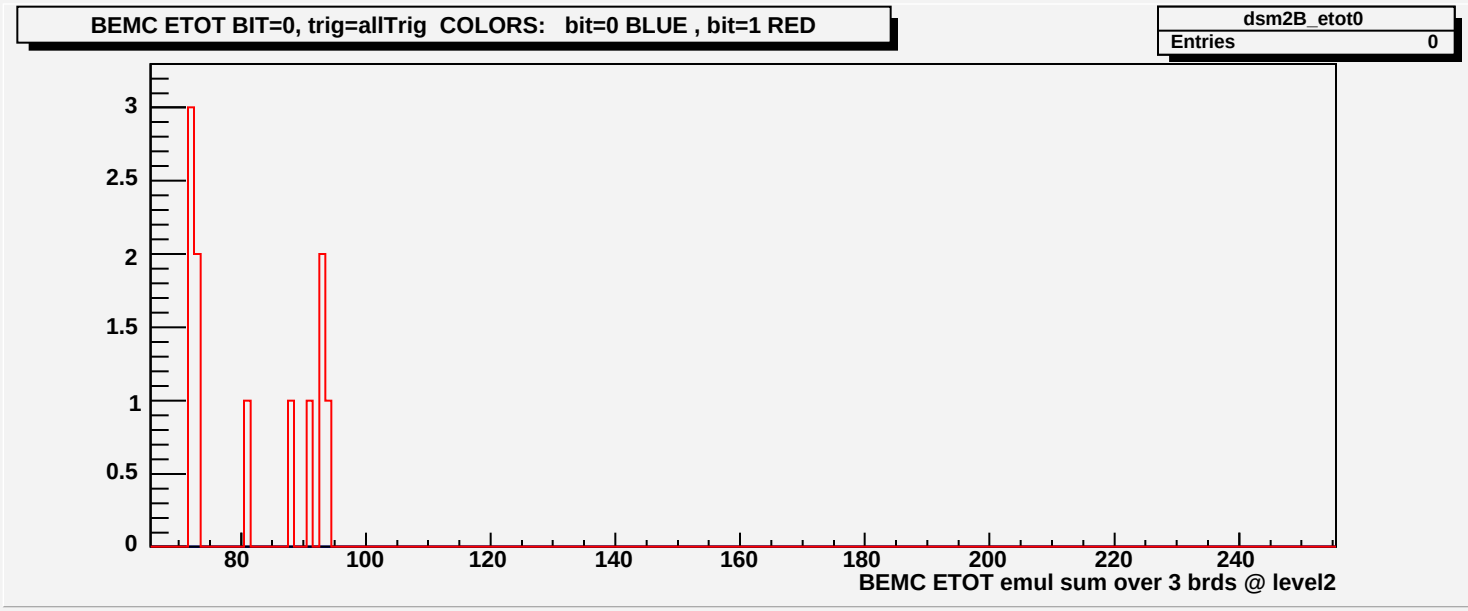

Emulated Sum Jet Patches 4+5
JP45_sum
Entries 11
Freq
1
0 20 40 60 80 100 120 140
Jet Patch Emu Sum

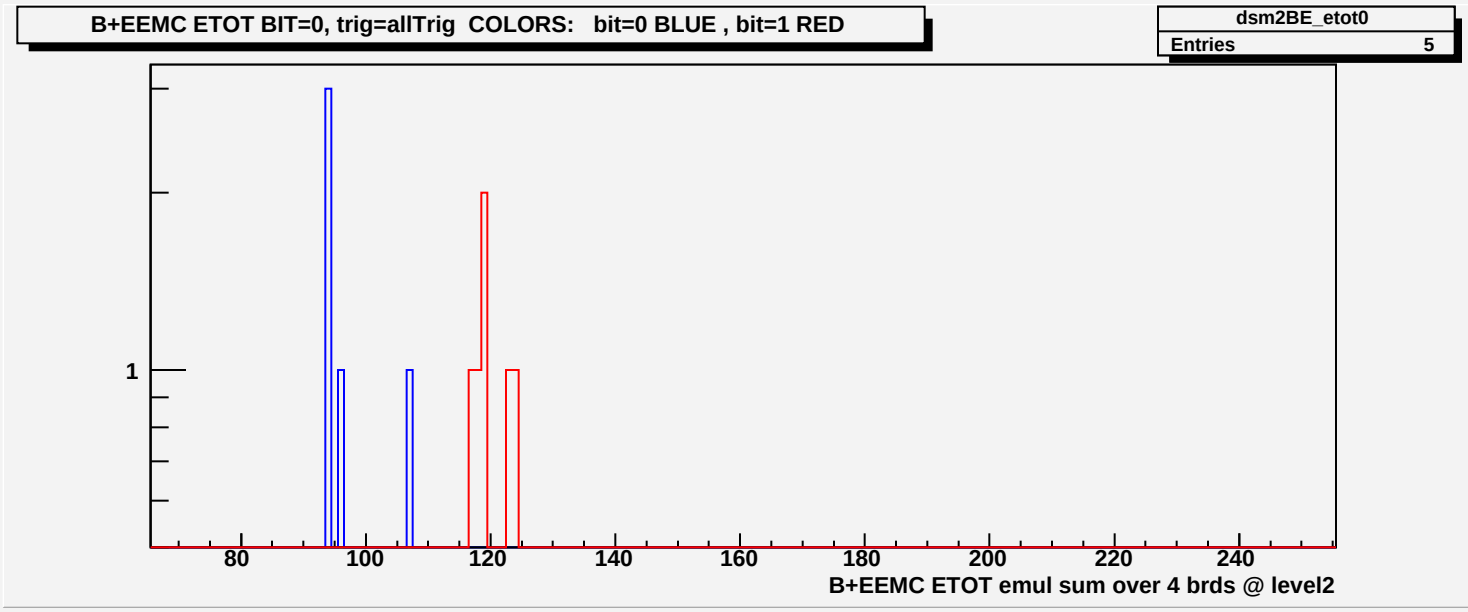

Emulated Sum Jet Patches 5+6
JP56_sum
Entries 11
Freq
1
0 20 40 60 80 100 120 140
Jet Patch Emu Sum

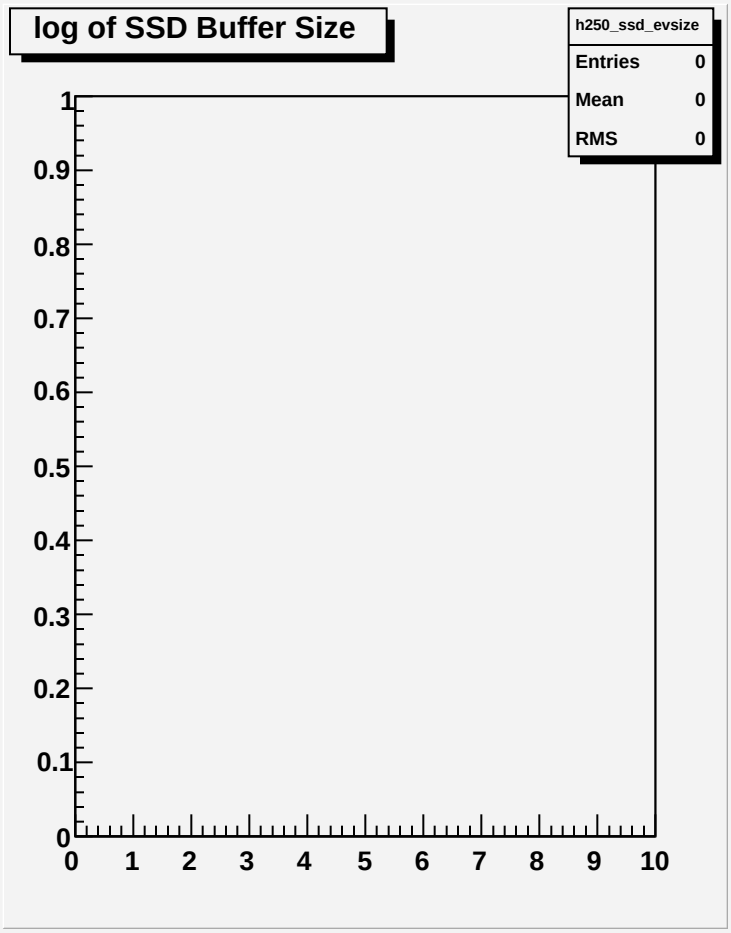

Emulated Sum Jet Patches 6+1
JP61_sum
Entries 11
Freq
1
0 20 40 60 80 100 120 140
Jet Patch Emu Sum

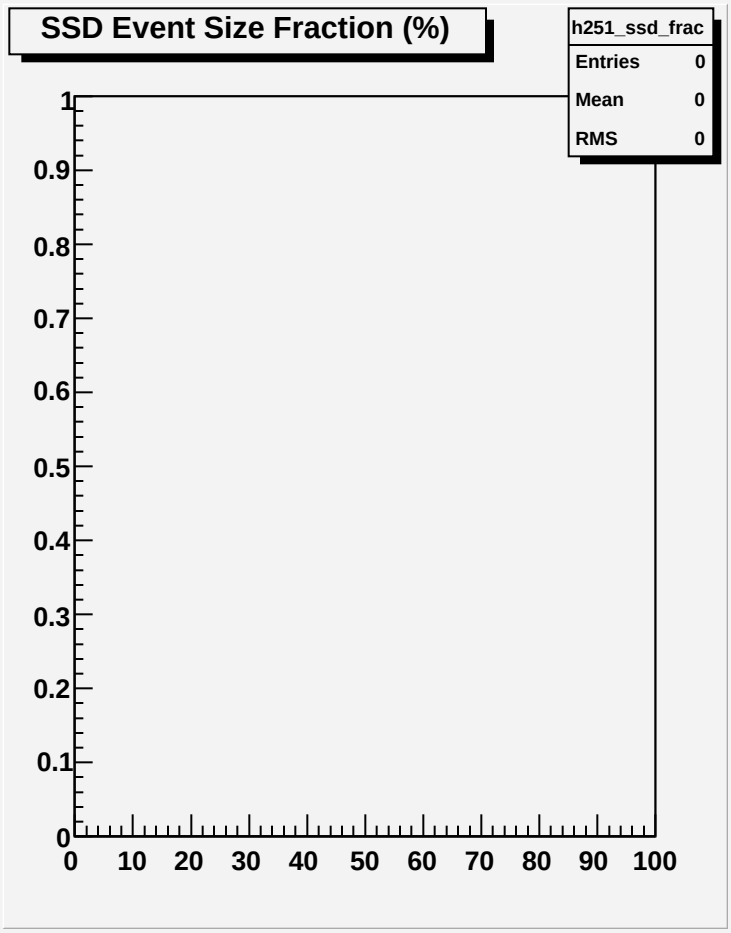

Emulated Sum Jet Patches 1 vs 2
JP12_cor
Entries 11
Jet Patch 2 Sum
Jet Patch 1 Sum
Emulated Sum Jet Patches 2 vs 3
JP23_cor
Entries 11
Jet Patch 3 Sum
Jet Patch 2 Sum
Emulated Sum Jet Patches 3 vs 4
JP34_cor
Entries 11
Jet Patch 4 Sum
Jet Patch 3 Sum
Emulated Sum Jet Patches 4 vs 5
JP45_cor
Entries 11
Jet Patch 5 Sum
Jet Patch 4 Sum
Emulated Sum Jet Patches 5 vs 6
JP56_cor
Entries 11
Jet Patch 6 Sum
Jet Patch 5 Sum
Emulated Sum Jet Patches 6 vs 1
JP61_cor
Entries 11
Jet Patch 1 Sum
Jet Patch 6 Sum

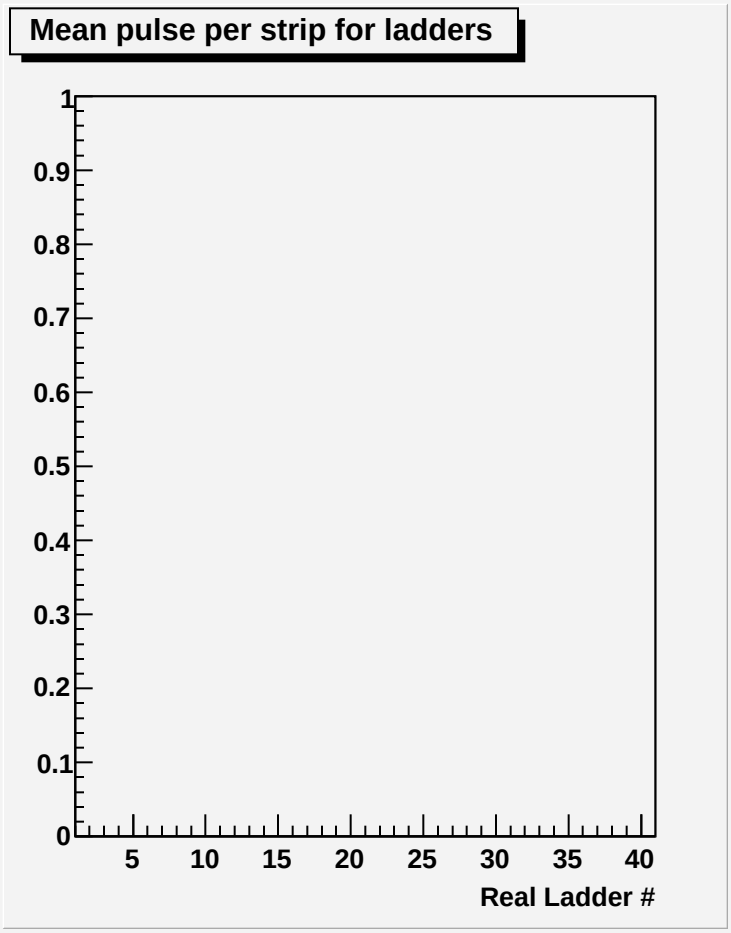

EEMC ETOT BIT=0, trig=allTrig COLORS: bit=0 BLUE , bit=1 RED
dsm2E_etot0
Entries 0
3
2.5
2
1.5
1
0.5
0
20
25
30
35
40
45
50
55
60
EEMC ETOT emul sum over 1 brd @ level2

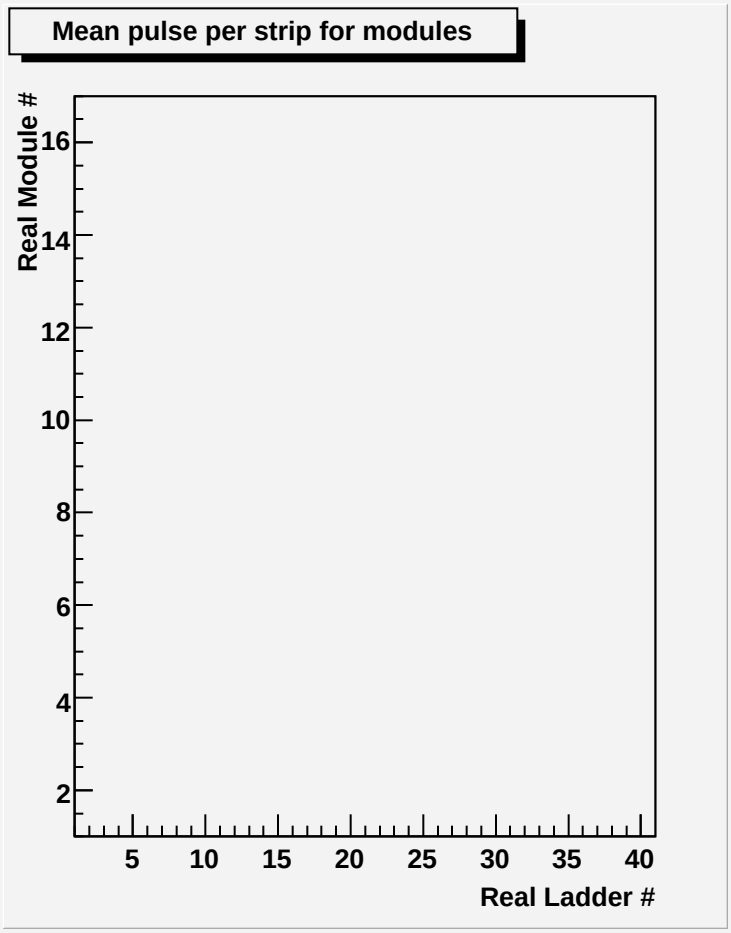

BEMC ETOT BIT=0, trig=allTrig COLORS: bit=0 BLUE , bit=1 RED
dsm2B_etot0
Entries 0
3
2.5
2
1.5
1
0.5
0
80
100
120
140
160
180
200
220
240
BEMC ETOT emul sum over 3 brds @ level2

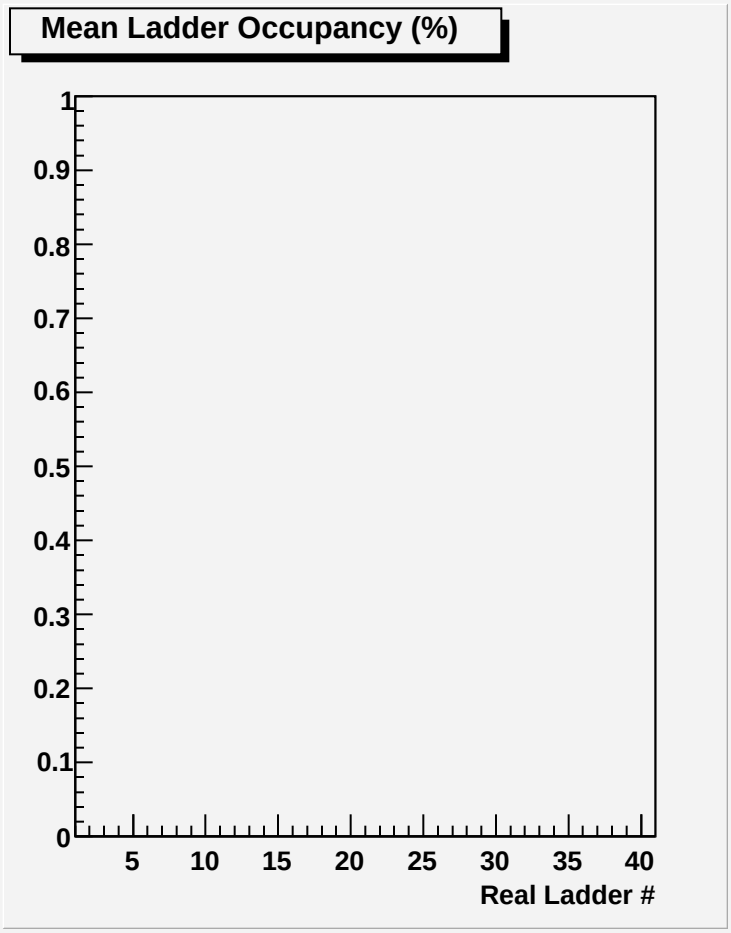

B+EEMC ETOT BIT=0, trig=allTrig COLORS: bit=0 BLUE , bit=1 RED
dsm2BE_etot0
Entries 5
1
80
100
120
140
160
180
200
220
240
B+EEMC ETOT emul sum over 4 brds @ level2

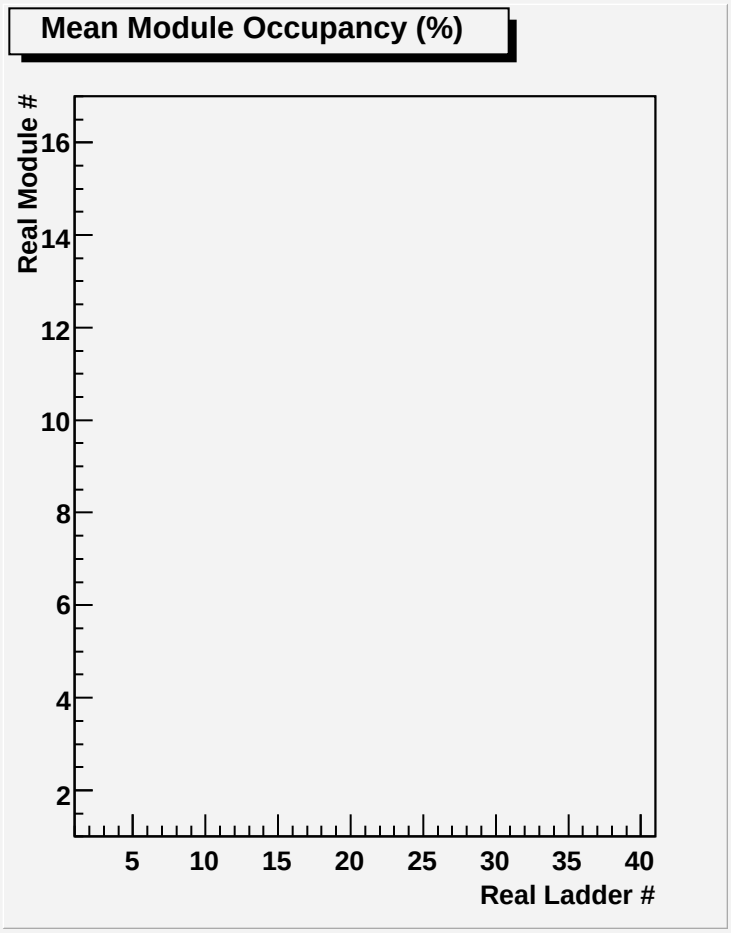

log of SSD Buffer Size
h250_ssd_evsize
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
0 1 2 3 4 5 6 7 8 9 10

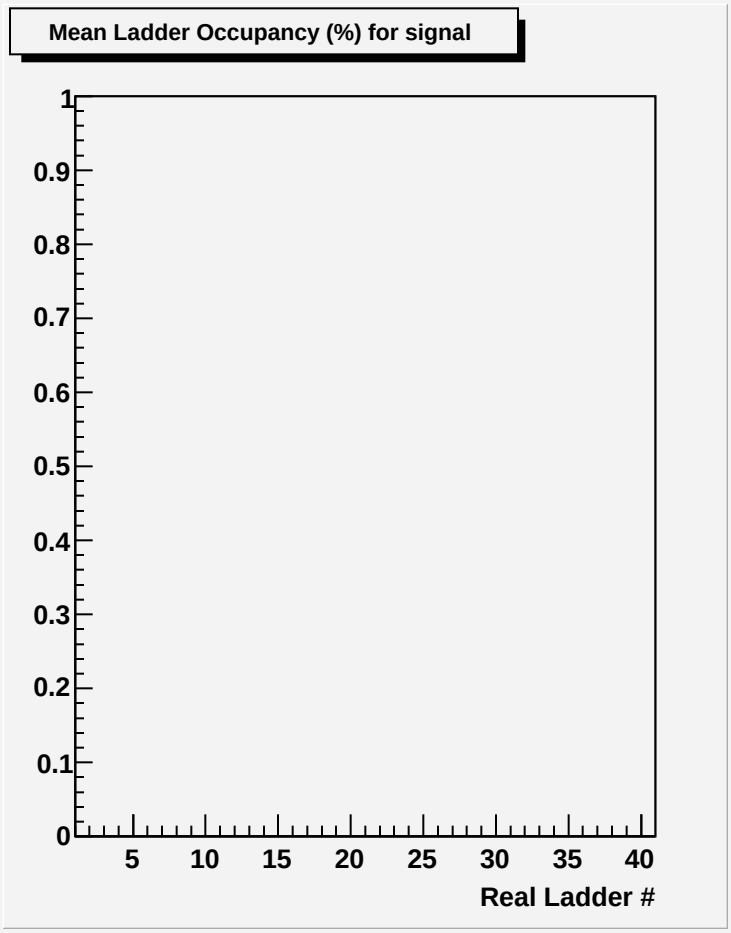

SSD Event Size Fraction (%)
h251_ssd_frac
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
0 10 20 30 40 50 60 70 80 90 100

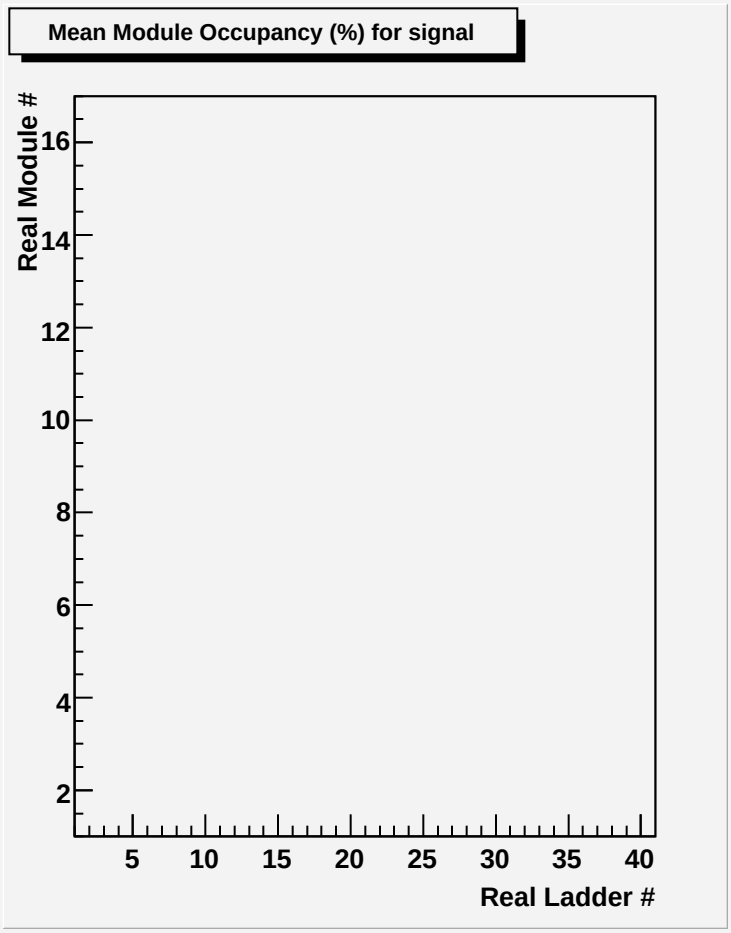

Mean pulse per strip for ladders
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
5 10 15 20 25 30 35 40
Real Ladder #

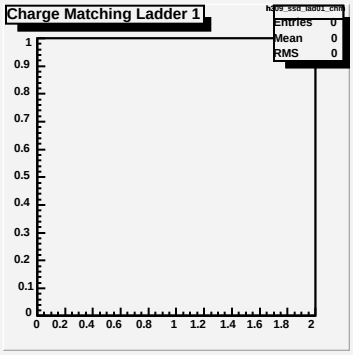

Mean pulse per strip for modules
Real Module #
16
14
12
10
8
6
4
2
5 10 15 20 25 30 35 40
Real Ladder #

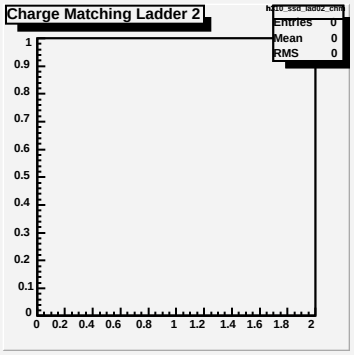

Mean Ladder Occupancy (%)
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
5
10
15
20
25
30
35
40
Real Ladder #

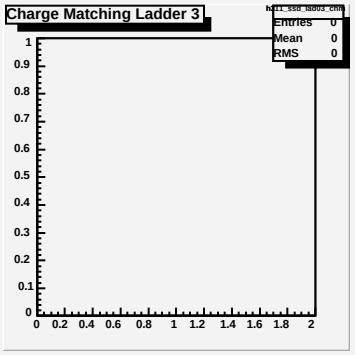

Mean Module Occupancy (%)
Real Module #
16
14
12
10
8
6
4
2
5
10
15
20
25
30
35
40
Real Ladder #

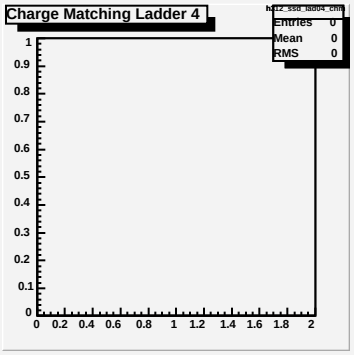

Mean Ladder Occupancy (%) for signal
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
5
10
15
20
25
30
35
40
Real Ladder #

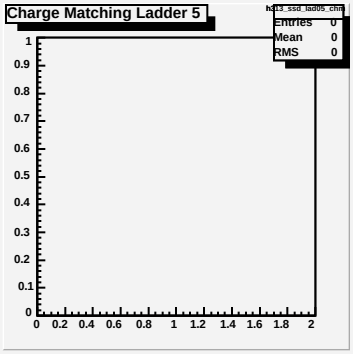

Mean Module Occupancy (%) for signal
Real Module #
16
14
12
10
8
6
4
2
5
10
15
20
25
30
35
40
Real Ladder #

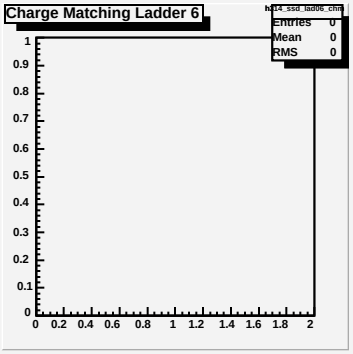

Charge Matching Ladder 1
h209_ssd_lad01_chn
Entries 0
Mean 0
RMS 0

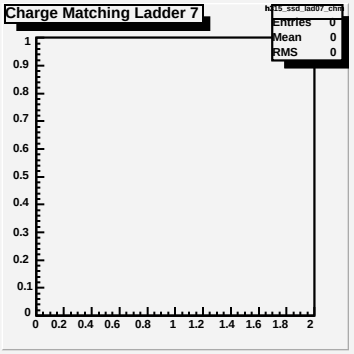

Charge Matching Ladder 2
h210_ssd_lad02_chn
Entries 0
Mean 0
RMS 0

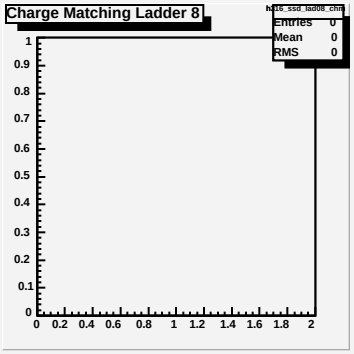

Charge Matching Ladder 3
h211_ssd_lad03_chn
Entries 0
Mean 0
RMS 0

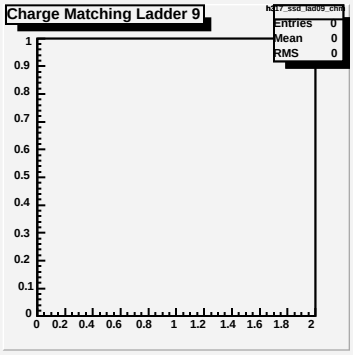

Charge Matching Ladder 4
h212_ssd_lad04_chn
Entries 0
Mean 0
RMS 0

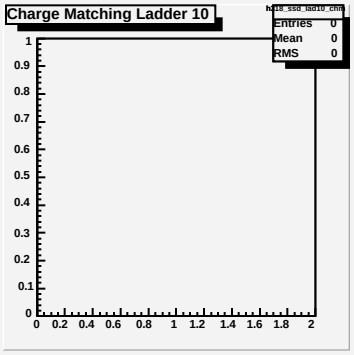

Charge Matching Ladder 5
h213_ssd_lad05_chn
Entries 0
Mean 0
RMS 0

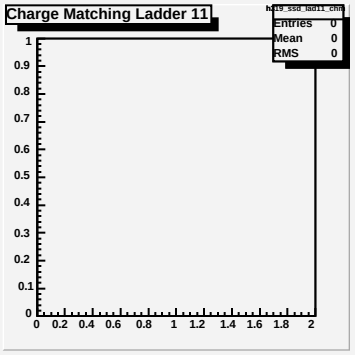

Charge Matching Ladder 6
h214_ssd_lad06_chn
Entries 0
Mean 0
RMS 0

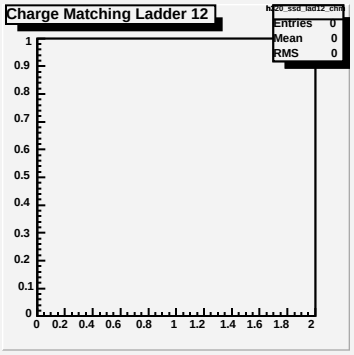

Charge Matching Ladder 7
h215_ssd_lad07_chn
Entries 0
Mean 0
RMS 0

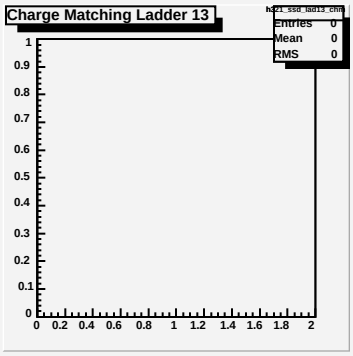

Charge Matching Ladder 8
h216_ssd_lad08_chn
Entries 0
Mean 0
RMS 0

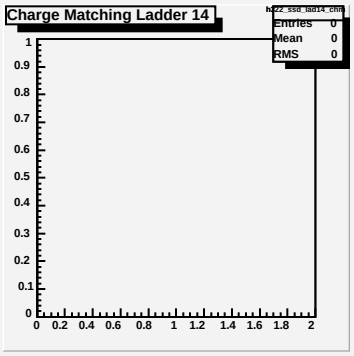

Charge Matching Ladder 9
h217_ssd_lad09_chn
Entries 0
Mean 0
RMS 0

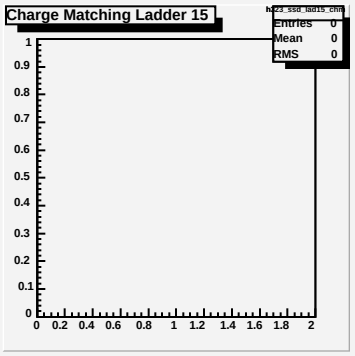

Charge Matching Ladder 10
h218_ssd_lad10_chn
Entries 0
Mean 0
RMS 0

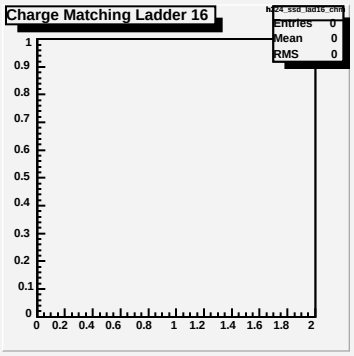

Charge Matching Ladder 11
h219_ssd_lad11_chn
Entries 0
Mean 0
RMS 0

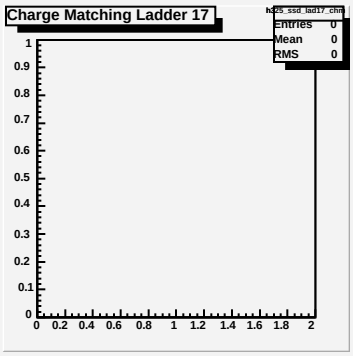

Charge Matching Ladder 12
h220_ssd_lad12_chn
Entries 0
Mean 0
RMS 0

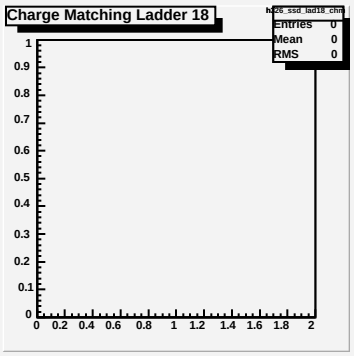

Charge Matching Ladder 13
h221_ssd_lad13_chn
Entries 0
Mean 0
RMS 0

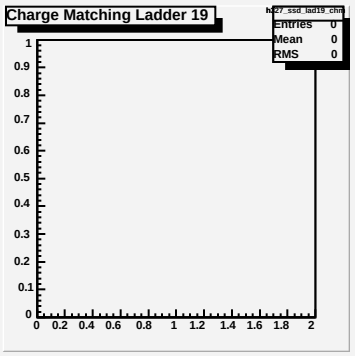

Charge Matching Ladder 14
h222_ssd_lad14_chn
Entries 0
Mean 0
RMS 0

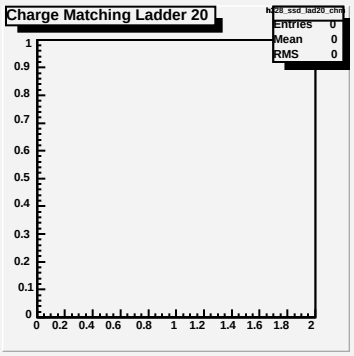

Charge Matching Ladder 15
h223_ssd_lad15_chn
Entries 0
Mean 0
RMS 0

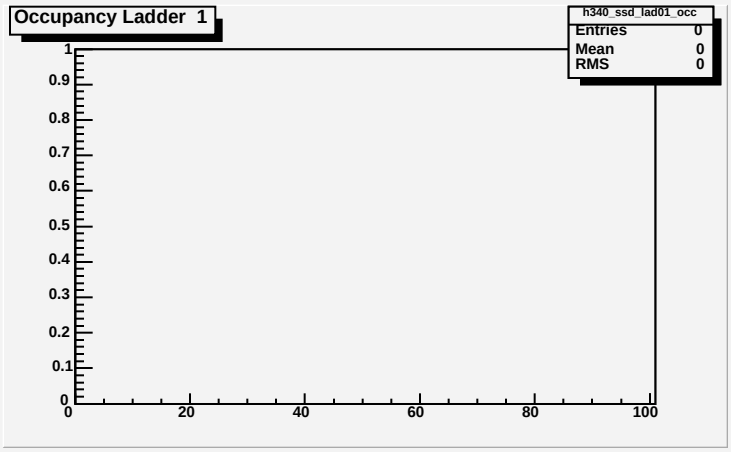

Charge Matching Ladder 16
h224_ssd_lad16_chn
Entries 0
Mean 0
RMS 0

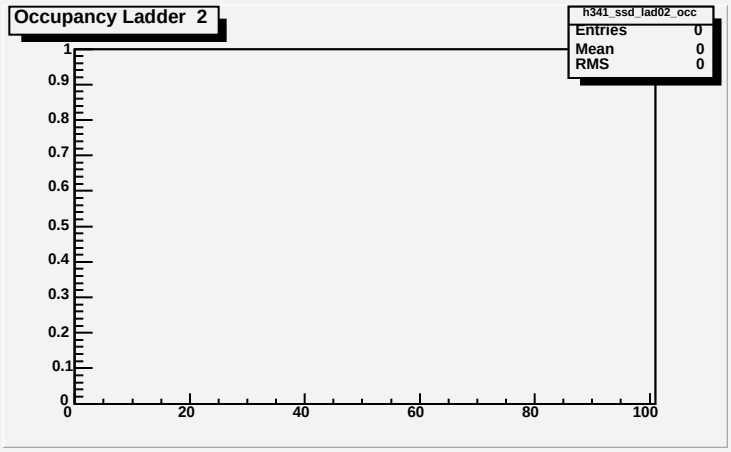

Charge Matching Ladder 17
h225_ssd_lad17_chn
Entries 0
Mean 0
RMS 0


Charge Matching Ladder 18
h226_ssd_lad18_chn
Entries 0
Mean 0
RMS 0


Charge Matching Ladder 19
h227_ssd_lad19_chn
Entries 0
Mean 0
RMS 0


Charge Matching Ladder 20
h228_ssd_lad20_chn
Entries 0
Mean 0
RMS 0


Occupancy Ladder 1
h340_ssd_lad01_occ
Entries 0
Mean 0
RMS 0

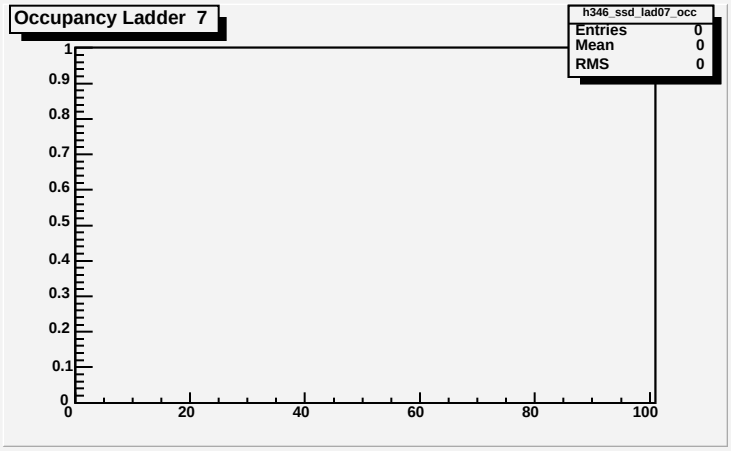

Occupancy Ladder 2
h341_ssd_lad02_occ
Entries 0
Mean 0
RMS 0

Occupancy Ladder 3
h342_ssd_lad03_occ
Entries 0
Mean 0
RMS 0

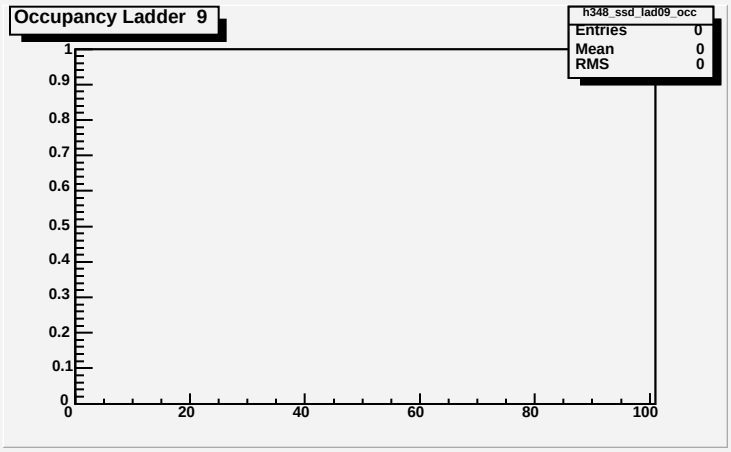
Occupancy Ladder 4
h343_ssd_lad04_occ
Entries 0
Mean 0
RMS 0

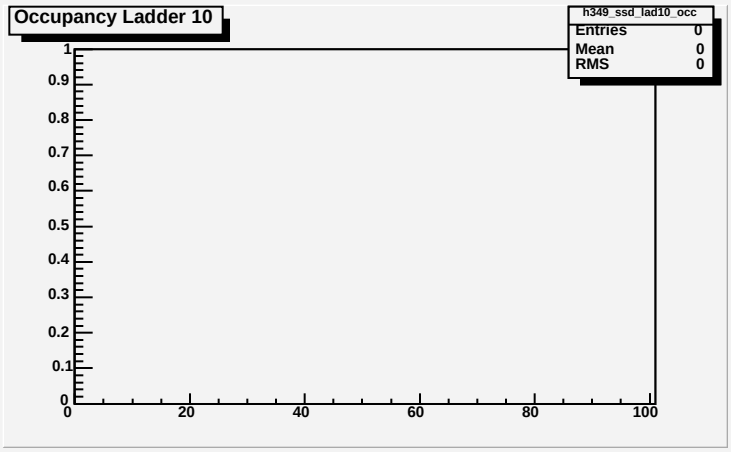
Occupancy Ladder 5
h344_ssd_lad05_occ
Entries 0
Mean 0
RMS 0

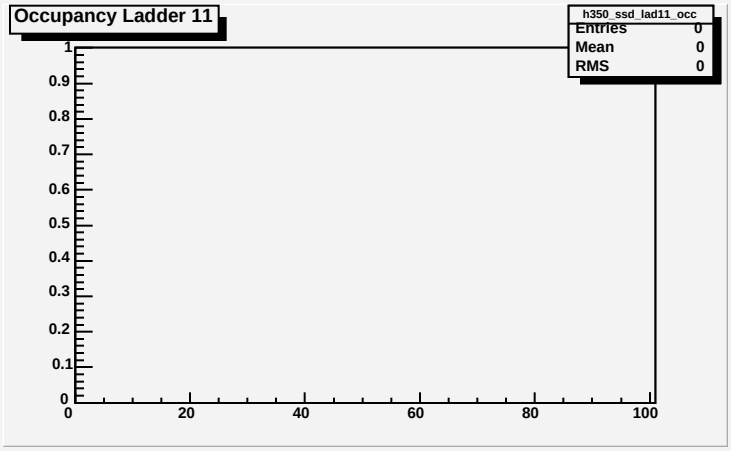
Occupancy Ladder 6
h345_ssd_lad06_occ
Entries 0
Mean 0
RMS 0


Occupancy Ladder 7
h346_ssd_lad07_occ
Entries 0
Mean 0
RMS 0

Occupancy Ladder 8
h347_ssd_lad08_occ
Entries 0
Mean 0
RMS 0


Occupancy Ladder 9
h348_ssd_lad09_occ
Entries 0
Mean 0
RMS 0

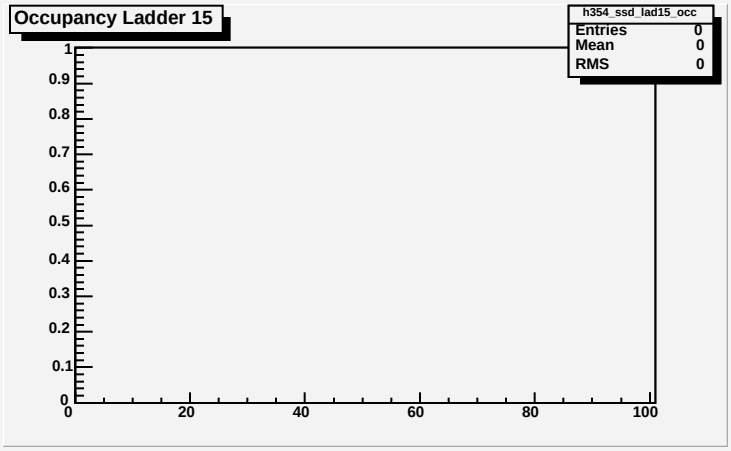

Occupancy Ladder 10
h349_ssd_lad10_occ
Entries 0
Mean 0
RMS 0

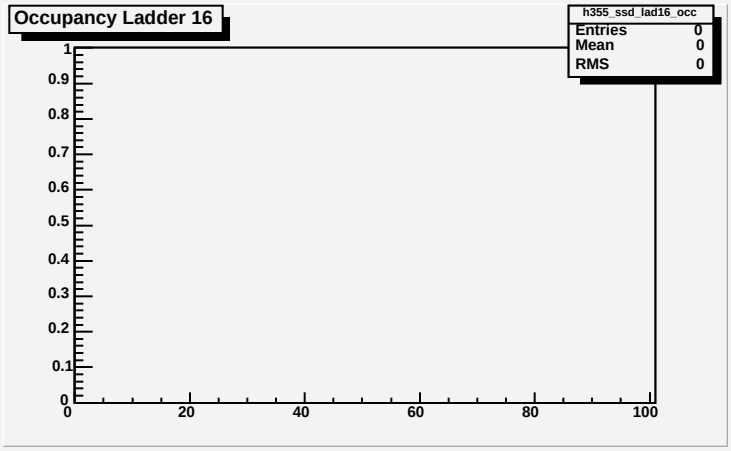

Occupancy Ladder 11
h350_ssd_lad11_occ
Entries 0
Mean 0
RMS 0

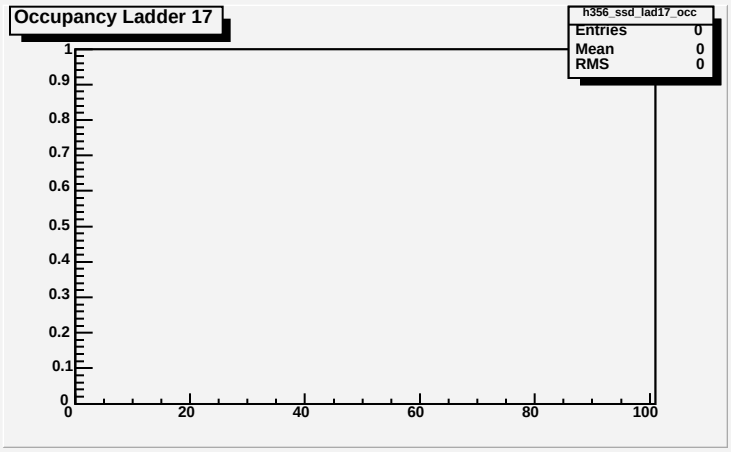
Occupancy Ladder 12
h351_ssd_lad12_occ
Entries 0
Mean 0
RMS 0

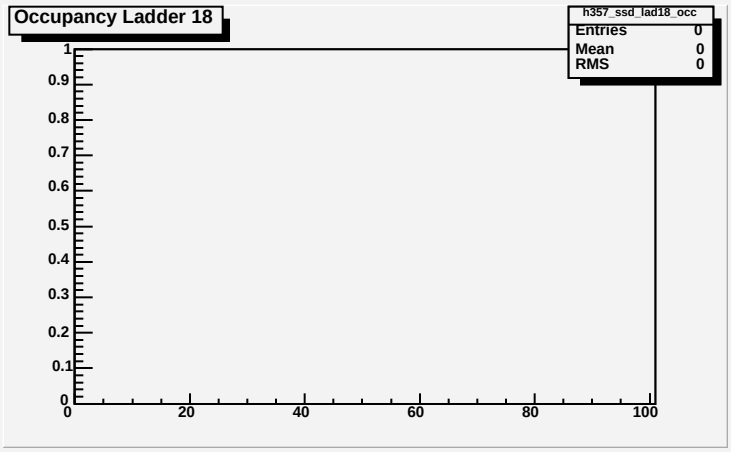
Occupancy Ladder 13
h352_ssd_lad13_occ
Entries 0
Mean 0
RMS 0

Occupancy Ladder 14
h353_ssd_lad14_occ
Entries 0
Mean 0
RMS 0


Occupancy Ladder 15
h354_ssd_lad15_occ
Entries 0
Mean 0
RMS 0


Occupancy Ladder 16
h355_ssd_lad16_occ
Entries 0
Mean 0
RMS 0


Occupancy Ladder 17
h356_ssd_lad17_occ
Entries 0
Mean 0
RMS 0

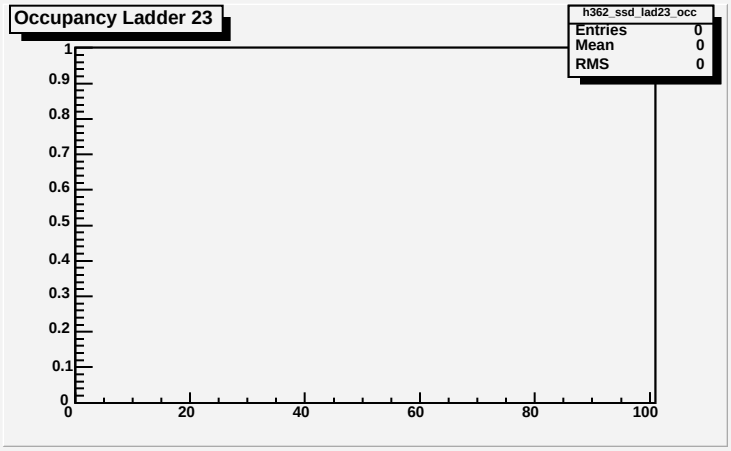

Occupancy Ladder 18
h357_ssd_lad18_occ
Entries 0
Mean 0
RMS 0

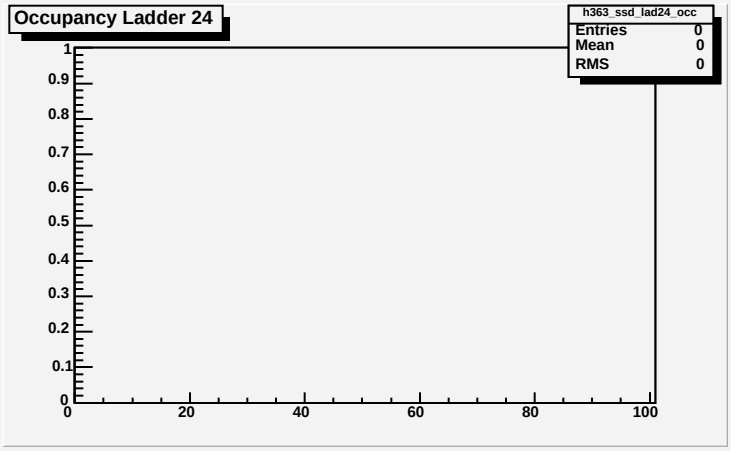
Occupancy Ladder 19
h358_ssd_lad19_occ
Entries 0
Mean 0
RMS 0

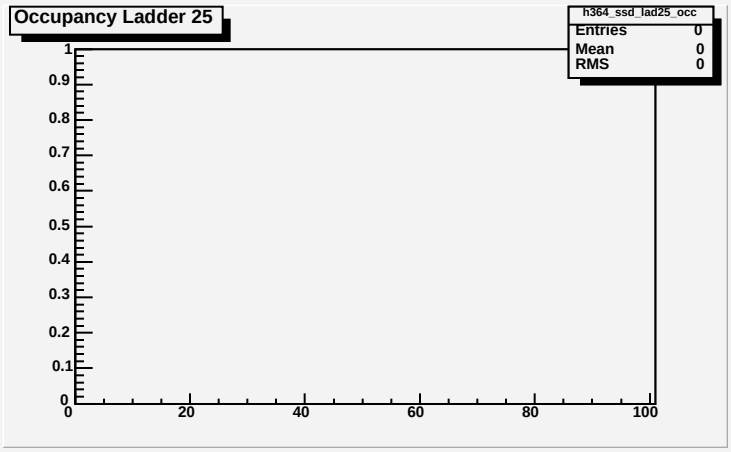
Occupancy Ladder 20
h359_ssd_lad20_occ
Entries 0
Mean 0
RMS 0

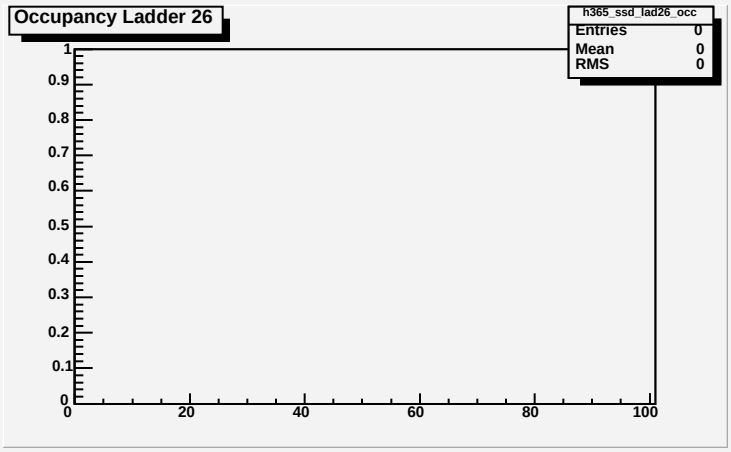
Occupancy Ladder 21
h360_ssd_lad21_occ
Entries 0
Mean 0
RMS 0

Occupancy Ladder 22
h361_ssd_lad22_occ
Entries 0
Mean 0
RMS 0


Occupancy Ladder 23
h362_ssd_lad23_occ
Entries 0
Mean 0
RMS 0


Occupancy Ladder 24
h363_ssd_lad24_occ
Entries 0
Mean 0
RMS 0


Occupancy Ladder 25
h364_ssd_lad25_occ
Entries 0
Mean 0
RMS 0


Occupancy Ladder 26
h365_ssd_lad26_occ
Entries 0
Mean 0
RMS 0

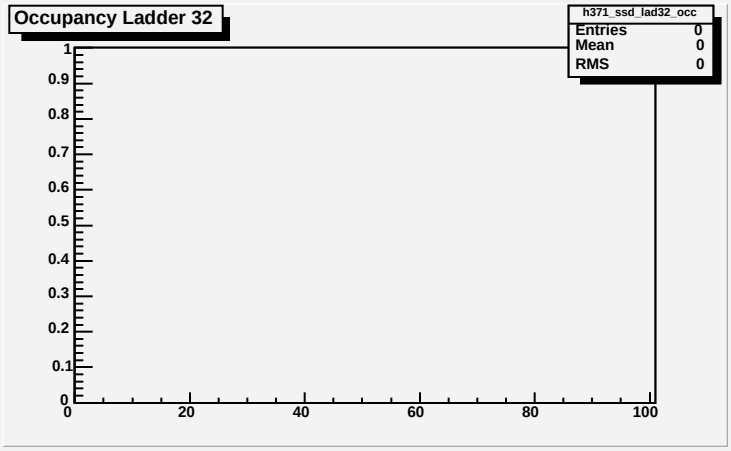
Occupancy Ladder 27
h366_ssd_lad27_occ
Entries 0
Mean 0
RMS 0

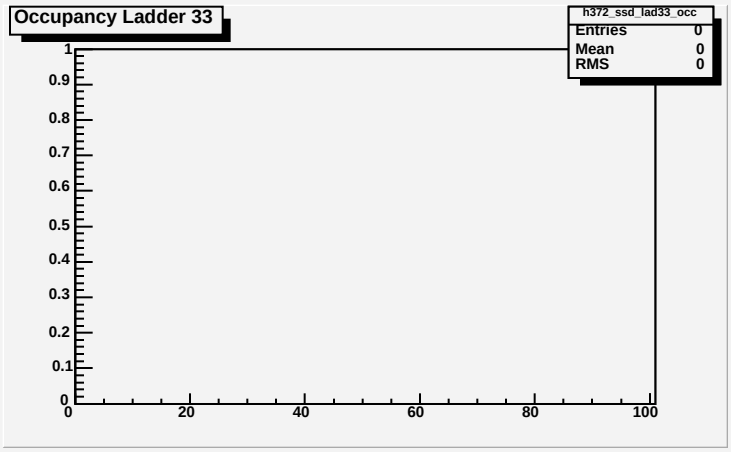
Occupancy Ladder 28
h367_ssd_lad28_occ
Entries 0
Mean 0
RMS 0

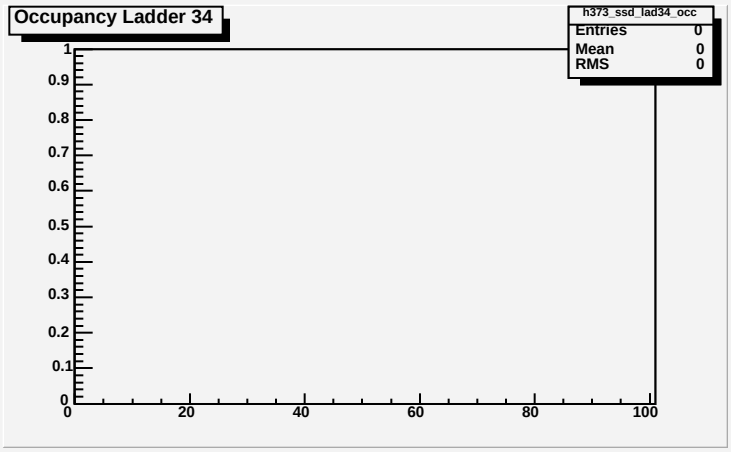
Occupancy Ladder 29
h368_ssd_lad29_occ
Entries 0
Mean 0
RMS 0

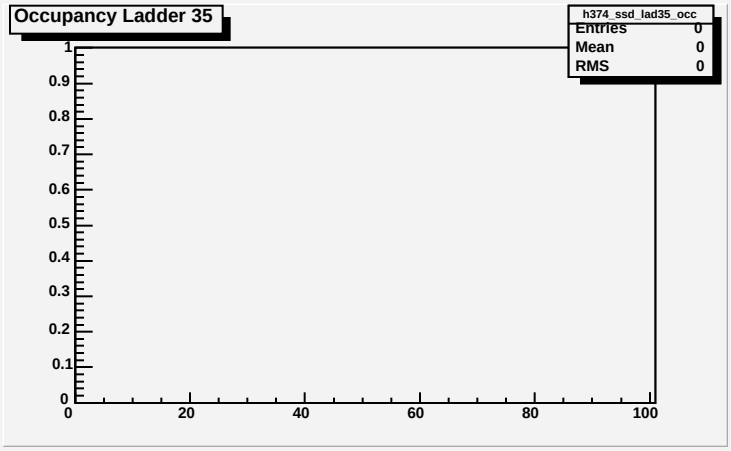
Occupancy Ladder 30
h369_ssd_lad30_occ
Entries 0
Mean 0
RMS 0

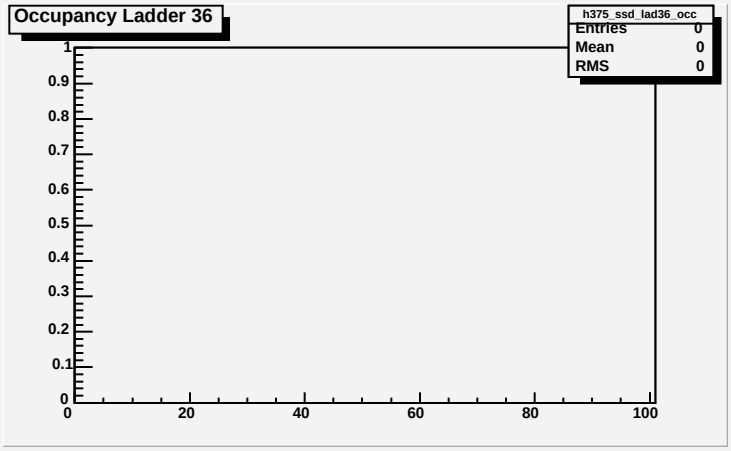
Occupancy Ladder 31
h370_ssd_lad31_occ
Entries 0
Mean 0
RMS 0

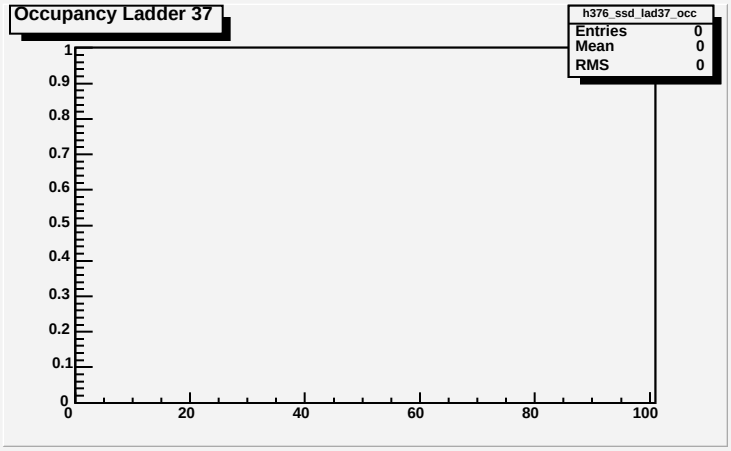

Occupancy Ladder 32
h371_ssd_lad32_occ
Entries 0
Mean 0
RMS 0


Occupancy Ladder 33
h372_ssd_lad33_occ
Entries 0
Mean 0
RMS 0


Occupancy Ladder 34
h373_ssd_lad34_occ
Entries 0
Mean 0
RMS 0


Occupancy Ladder 35
h374_ssd_lad35_occ
Entries 0
Mean 0
RMS 0

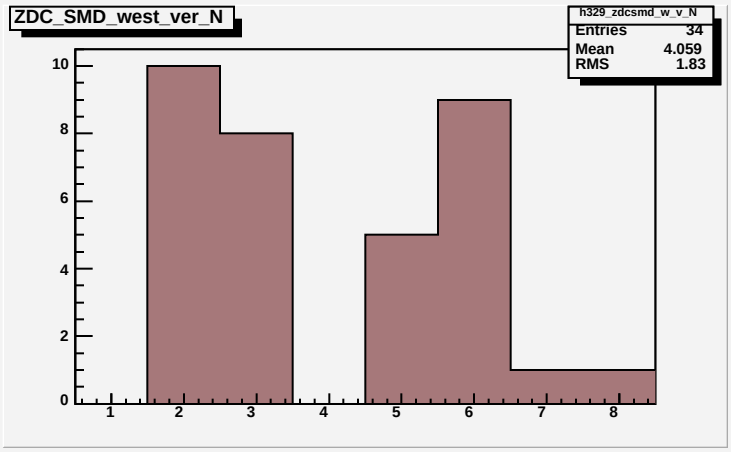

Occupancy Ladder 36
h375_ssd_lad36_occ
Entries 0
Mean 0
RMS 0

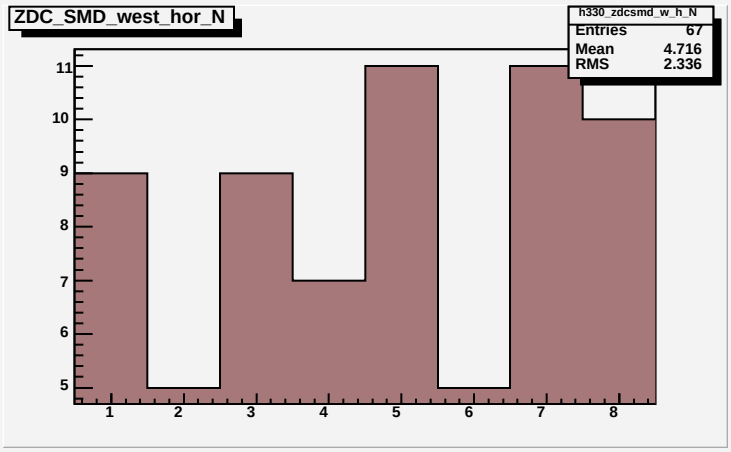

Occupancy Ladder 37
h376_ssd_lad37_occ
Entries 0
Mean 0
RMS 0

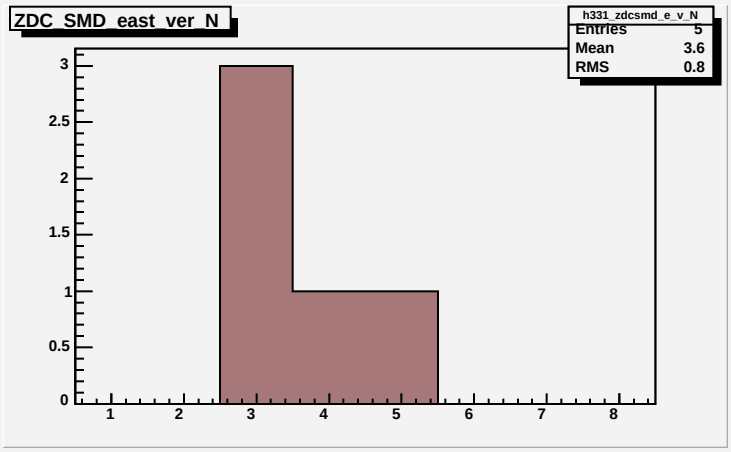
Occupancy Ladder 38
h377_ssd_lad38_occ
Entries 0
Mean 0
RMS 0

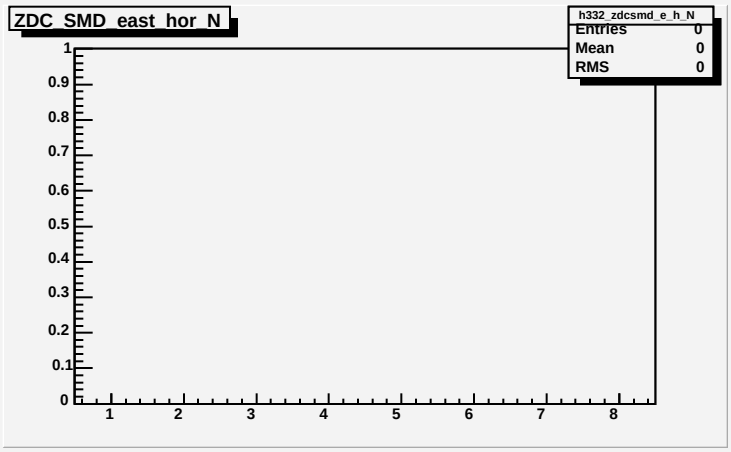
Occupancy Ladder 39
h378_ssd_lad39_occ
Entries 0
Mean 0
RMS 0

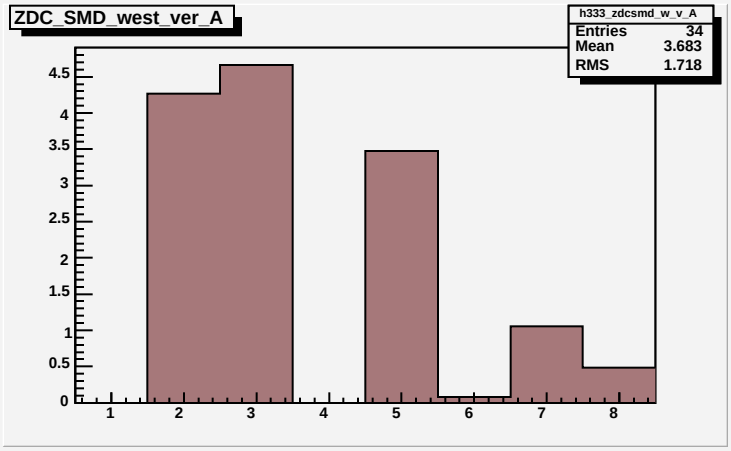
Occupancy Ladder 40
h379_ssd_lad40_occ
Entries 0
Mean 0
RMS 0

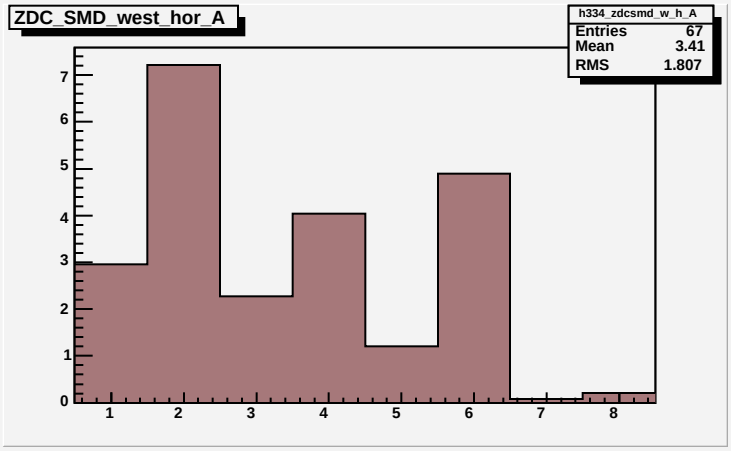

ZDC_SMD_west_ver_N
h329_zdcsmd_w_v_N
Entries 34
Mean 4.059
RMS 1.83

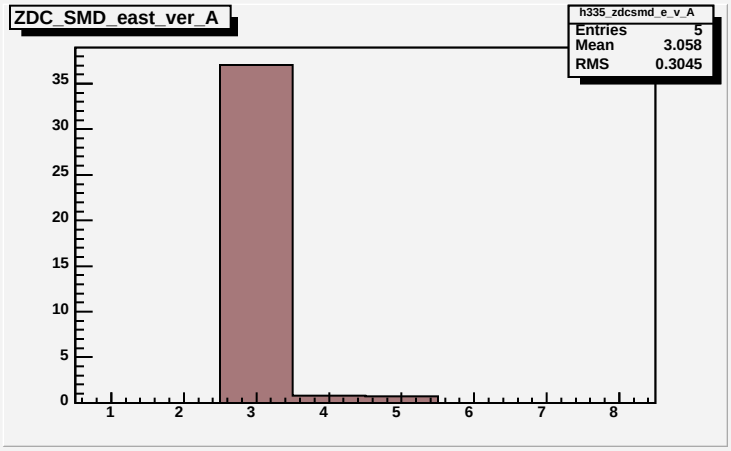

ZDC_SMD_west_hor_N
h330_zdcsmd_w_h_N
Entries 67
Mean 4.716
RMS 2.336

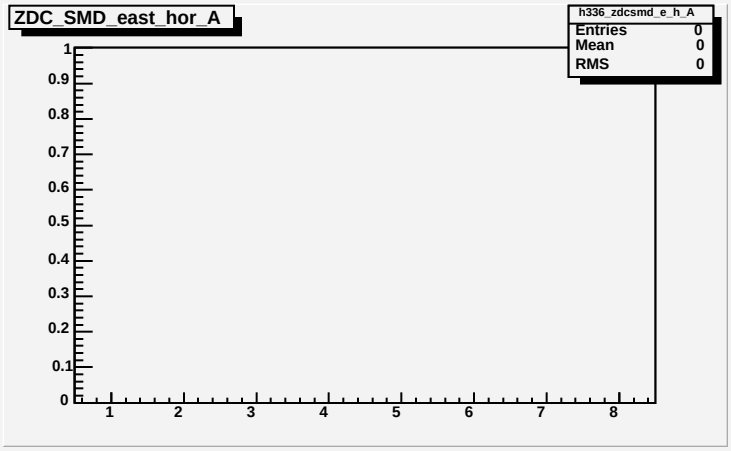

ZDC_SMD_east_ver_N
h331_zdcsmd_e_v_N
Entries 5
Mean 3.6
RMS 0.8

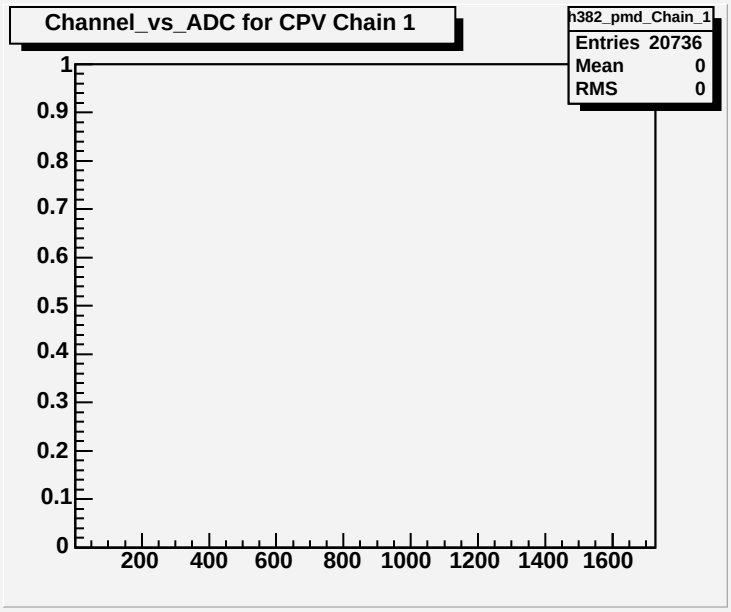

ZDC_SMD_east_hor_N
h332_zdcsmd_e_h_N
Entries 0
Mean 0
RMS 0

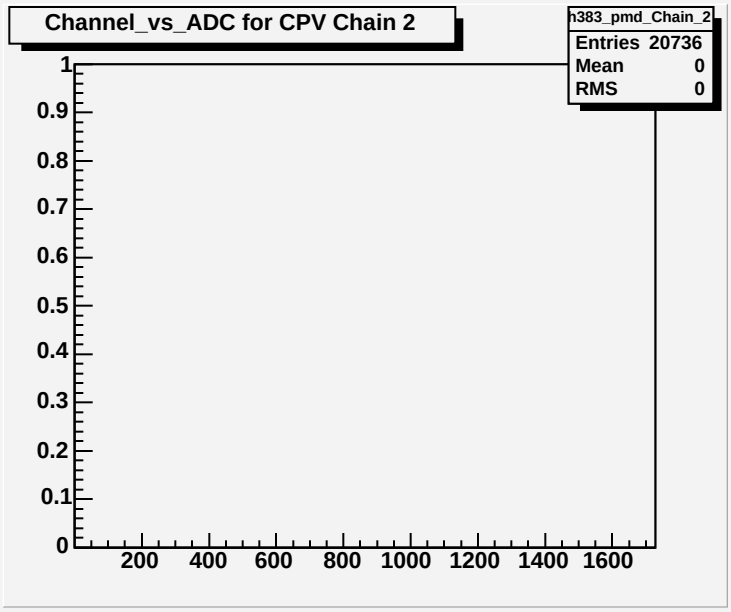

ZDC_SMD_west_ver_A
h333_zdcsmd_w_v_A
Entries 34
Mean 3.683
RMS 1.718

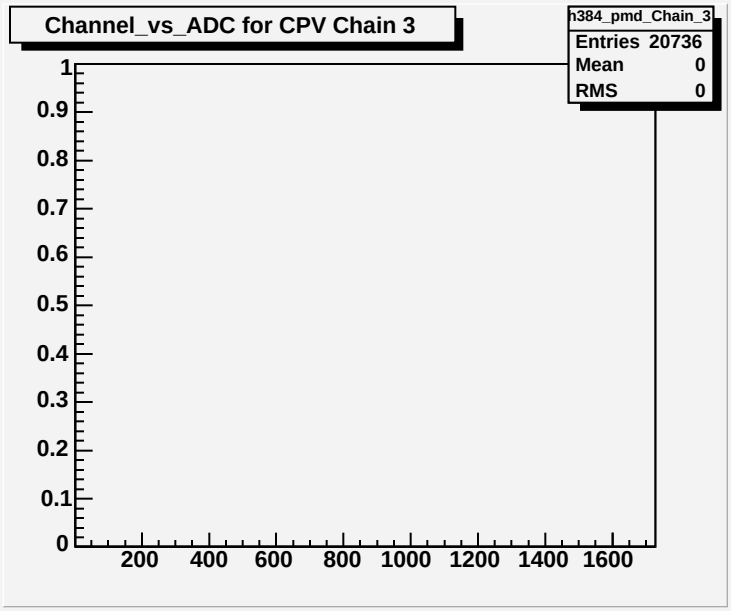

ZDC_SMD_west_hor_A
h334_zdcsmd_w_h_A
Entries 67
Mean 3.41
RMS 1.807

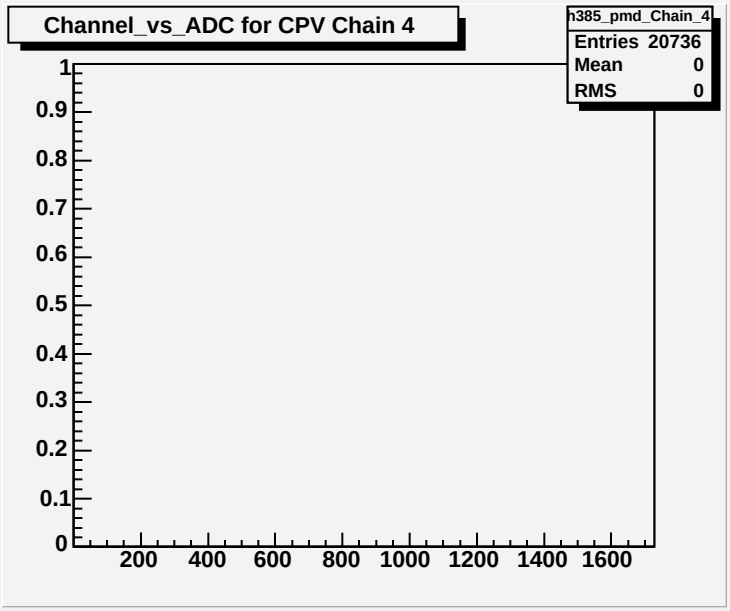

ZDC_SMD_east_ver_A
h335_zdcsmd_e_v_A
Entries 5
Mean 3.058
RMS 0.3045

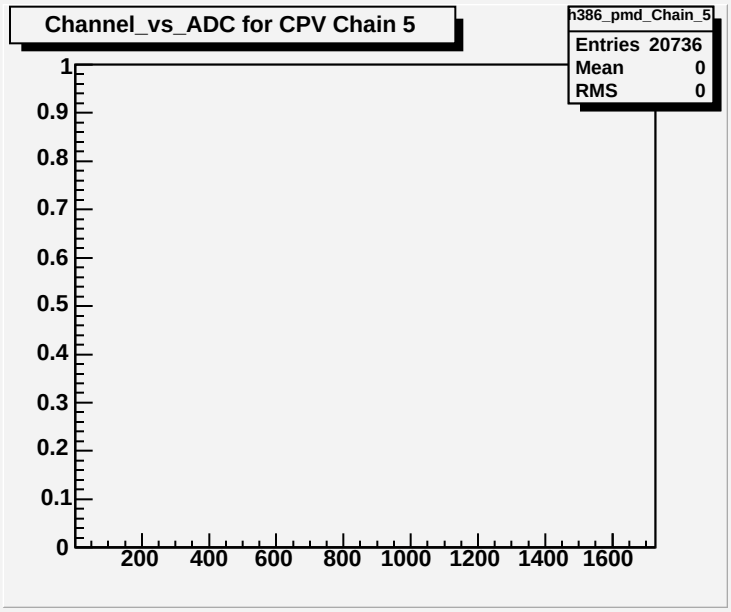

ZDC_SMD_east_hor_A
h336_zdcsmd_e_h_A
Entries 0
Mean 0
RMS 0

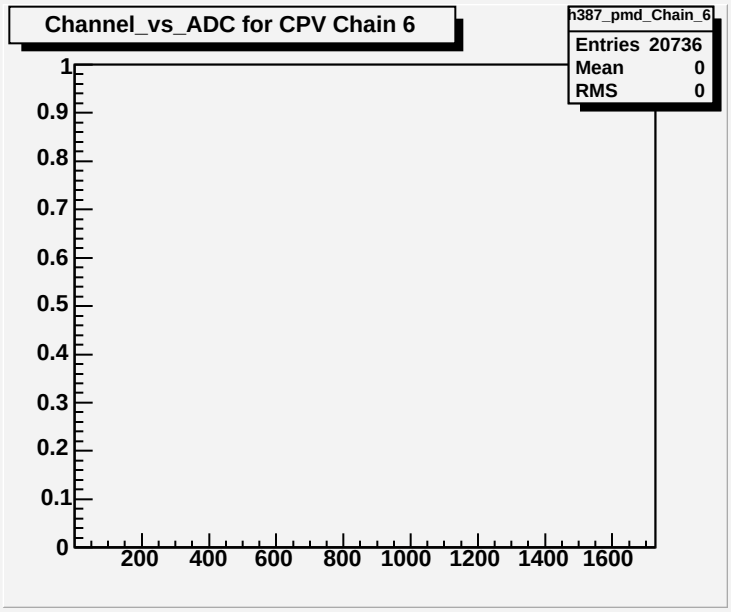

Channel_vs_ADC for CPV Chain 1
h382_pmd_Chain_1
Entries 20736
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
200 400 600 800 1000 1200 1400 1600

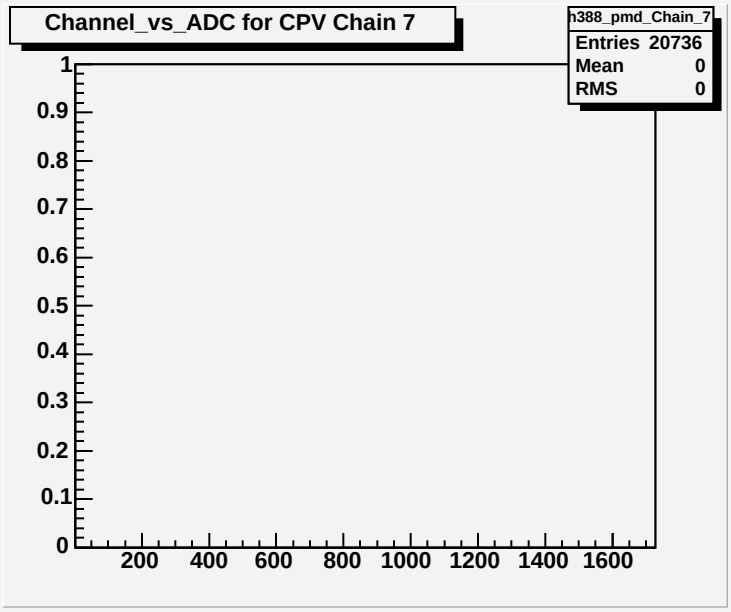

Channel_vs_ADC for CPV Chain 2
h383_pmd_Chain_2
Entries 20736
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
200 400 600 800 1000 1200 1400 1600

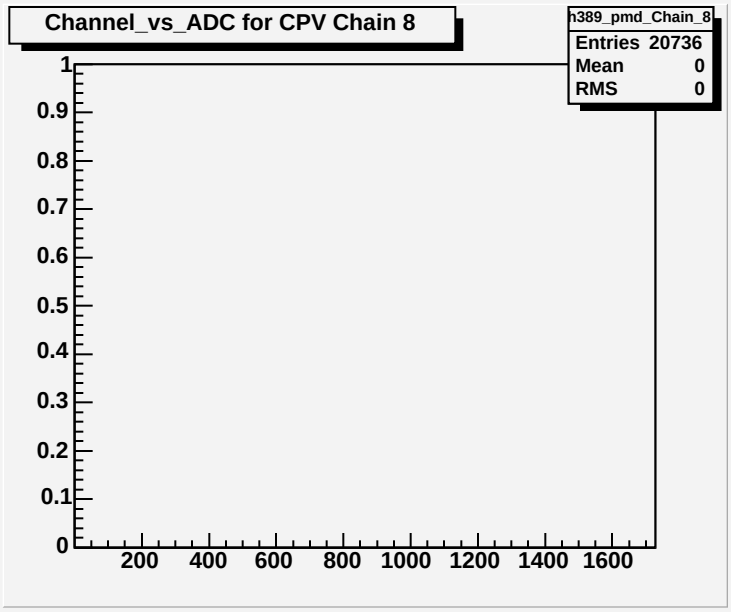

Channel_vs_ADC for CPV Chain 3
h384_pmd_Chain_3
Entries 20736
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
200 400 600 800 1000 1200 1400 1600

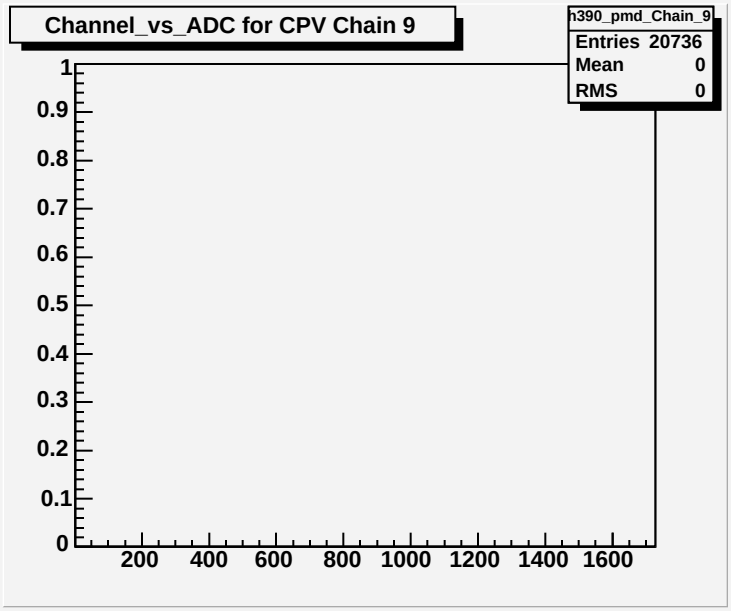

Channel_vs_ADC for CPV Chain 4
h385_pmd_Chain_4
Entries 20736
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
200 400 600 800 1000 1200 1400 1600

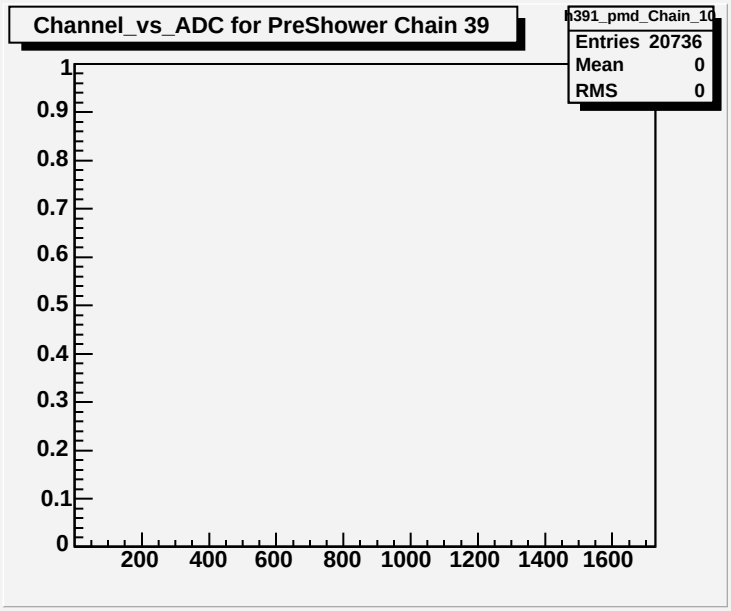

Channel_vs_ADC for CPV Chain 5
h386_pmd_Chain_5
Entries 20736
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
200 400 600 800 1000 1200 1400 1600

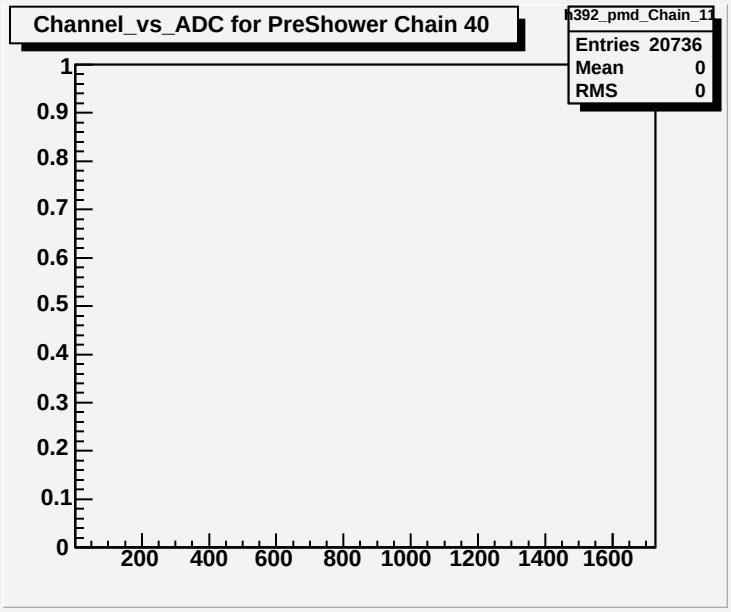

Channel_vs_ADC for CPV Chain 6
h387_pmd_Chain_6
Entries 20736
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
200 400 600 800 1000 1200 1400 1600

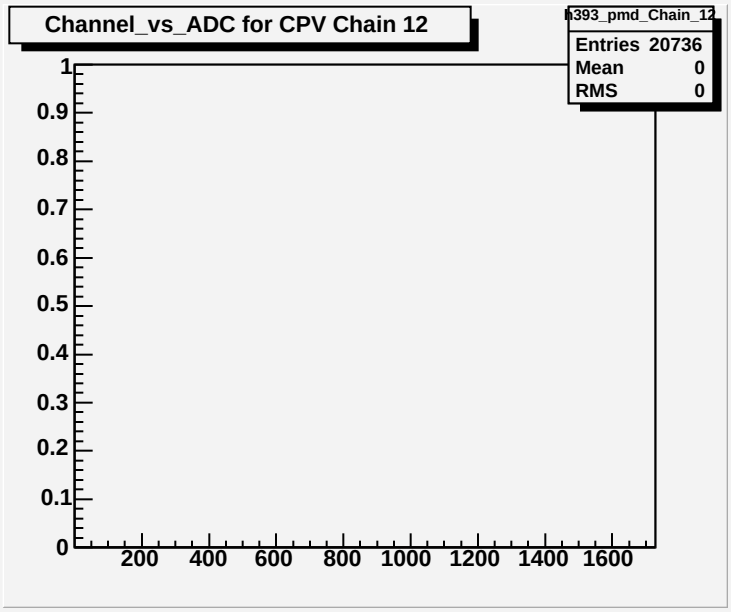

Channel_vs_ADC for CPV Chain 7
h388_pmd_Chain_7
Entries 20736
Mean 0
RMS 0

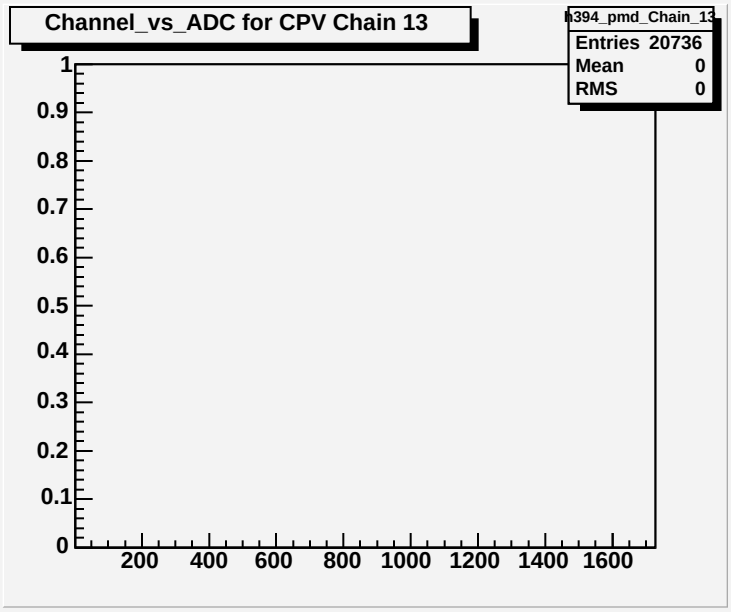

Channel_vs_ADC for CPV Chain 8
h389_pmd_Chain_8
Entries 20736
Mean 0
RMS 0

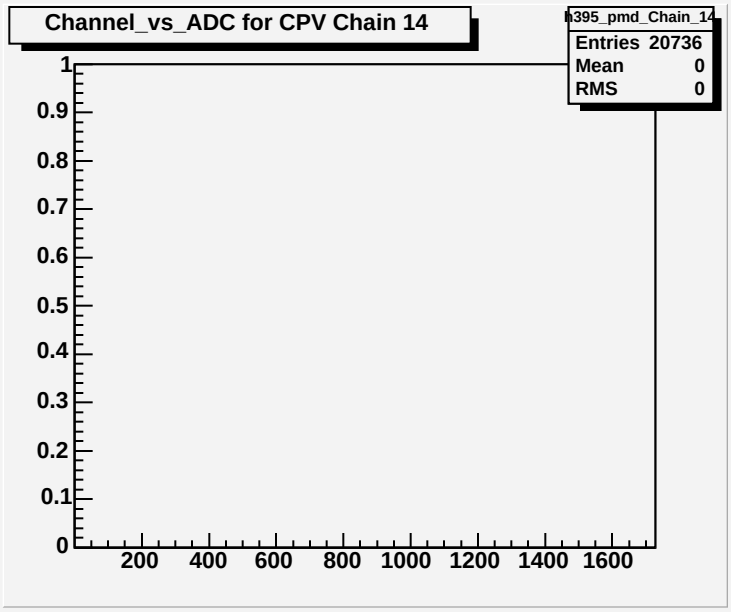

Channel_vs_ADC for CPV Chain 9
h390_pmd_Chain_9
Entries 20736
Mean 0
RMS 0

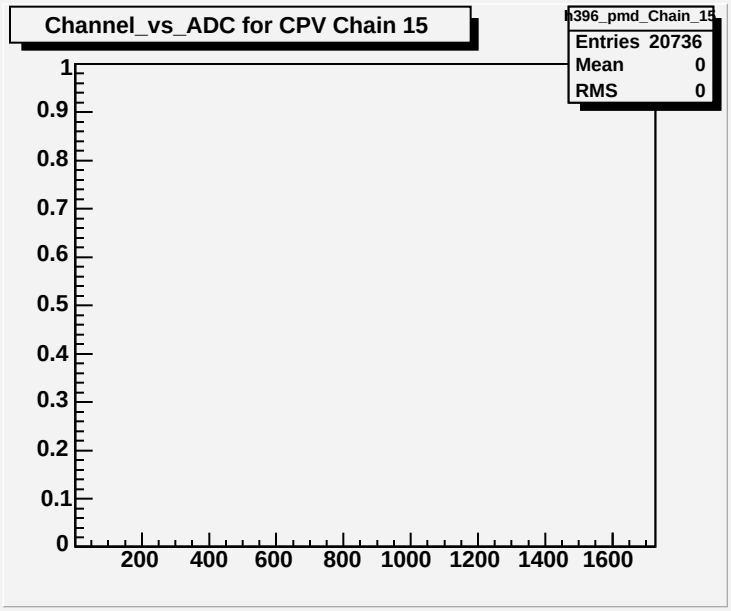

Channel_vs_ADC for PreShower Chain 39
h391_pmd_Chain_10
Entries 20736
Mean 0
RMS 0

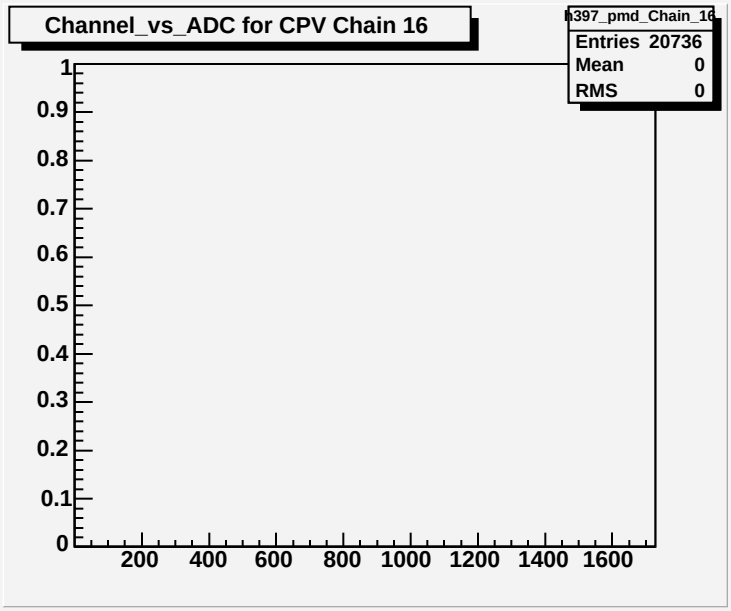

Channel_vs_ADC for PreShower Chain 40
h392_pmd_Chain_11
Entries 20736
Mean 0
RMS 0

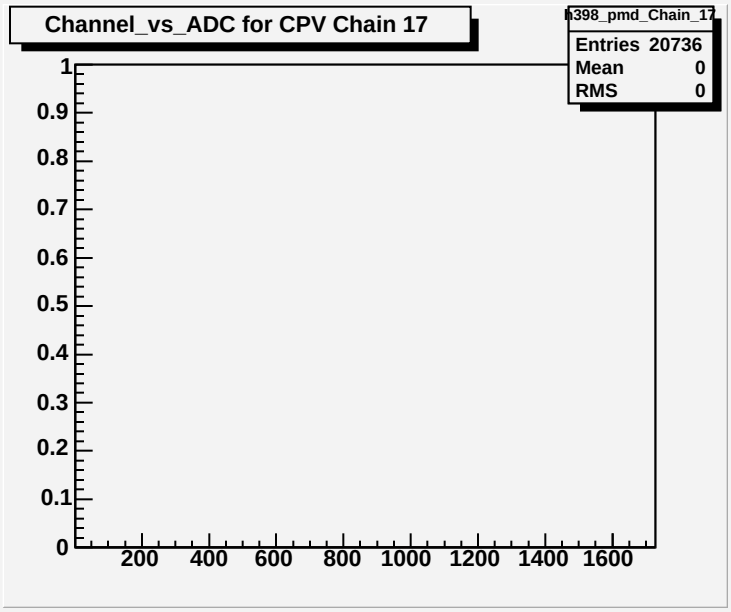

Channel_vs_ADC for CPV Chain 12
h393_pmd_Chain_12
Entries 20736
Mean 0
RMS 0

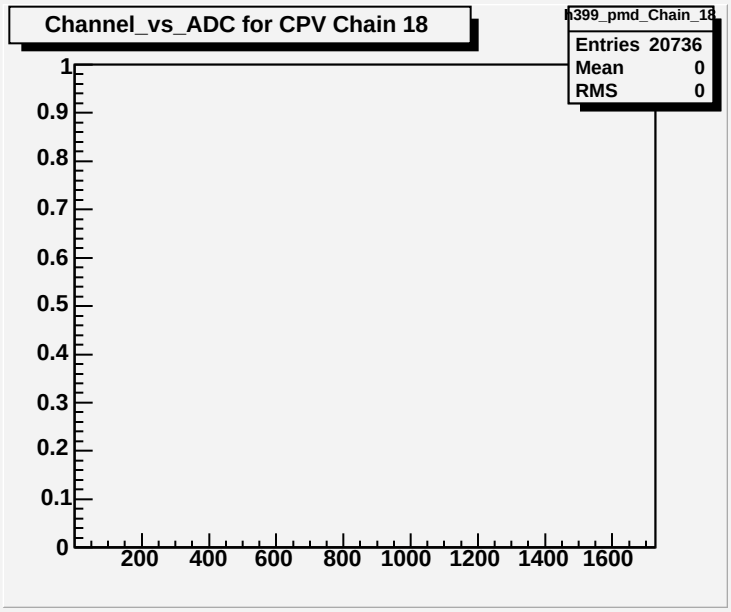

Channel_vs_ADC for CPV Chain 13
h394_pmd_Chain_13
Entries 20736
Mean 0
RMS 0

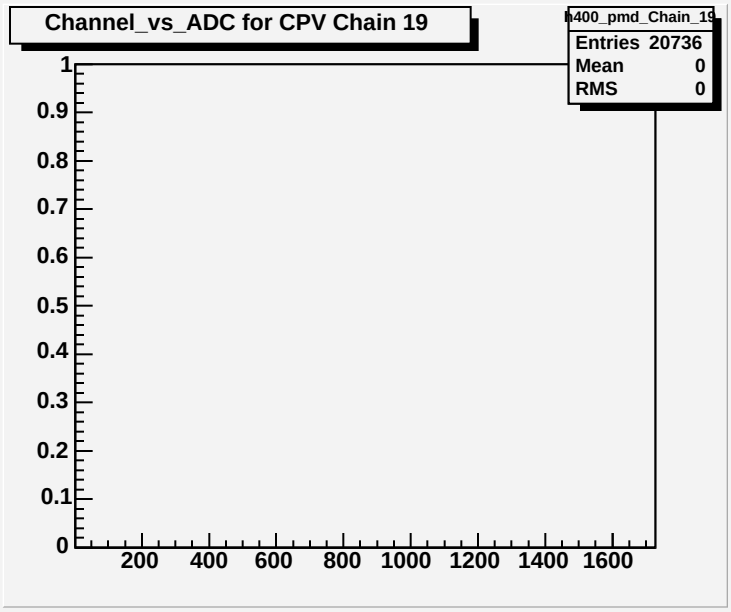

Channel_vs_ADC for CPV Chain 14
h395_pmd_Chain_14
Entries 20736
Mean 0
RMS 0

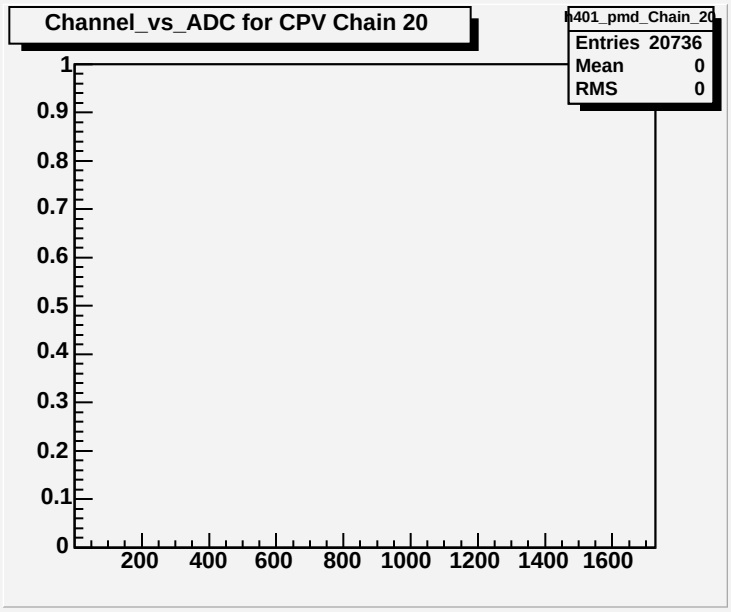

Channel_vs_ADC for CPV Chain 15
h396_pmd_Chain_15
Entries 20736
Mean 0
RMS 0

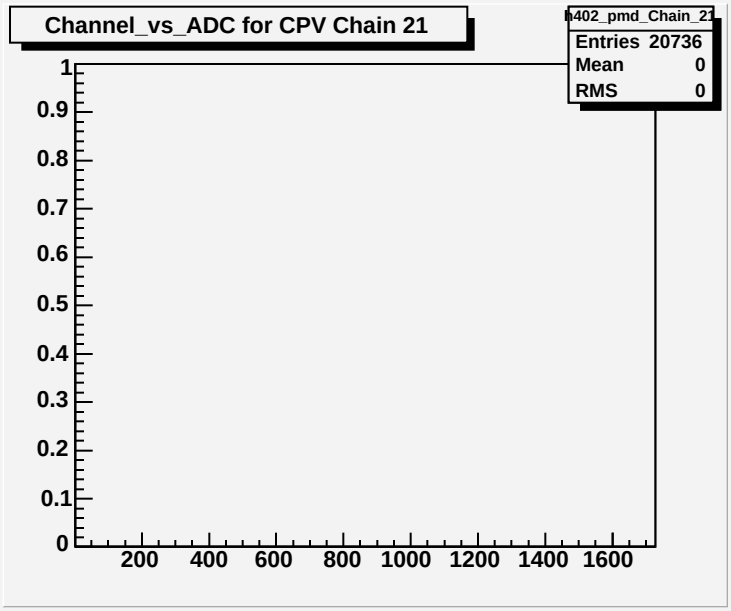

Channel_vs_ADC for CPV Chain 16
h397_pmd_Chain_16
Entries 20736
Mean 0
RMS 0

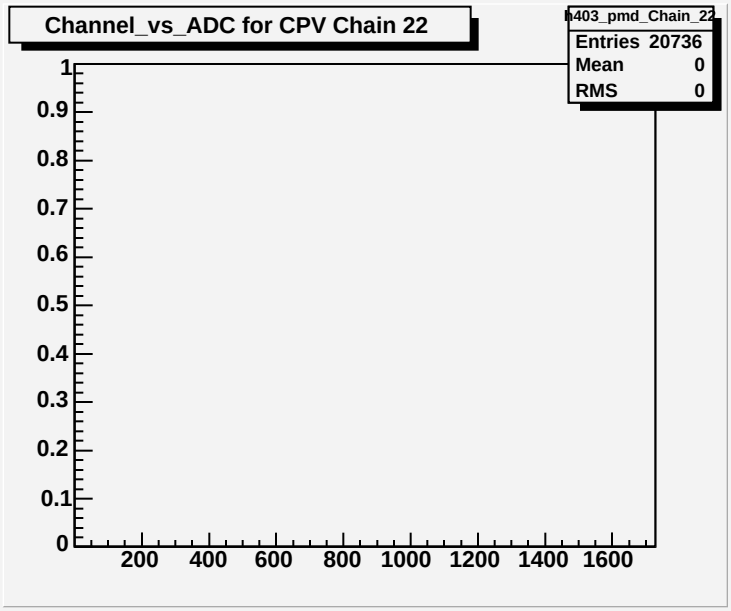

Channel_vs_ADC for CPV Chain 17
h398_pmd_Chain_17
Entries 20736
Mean 0
RMS 0

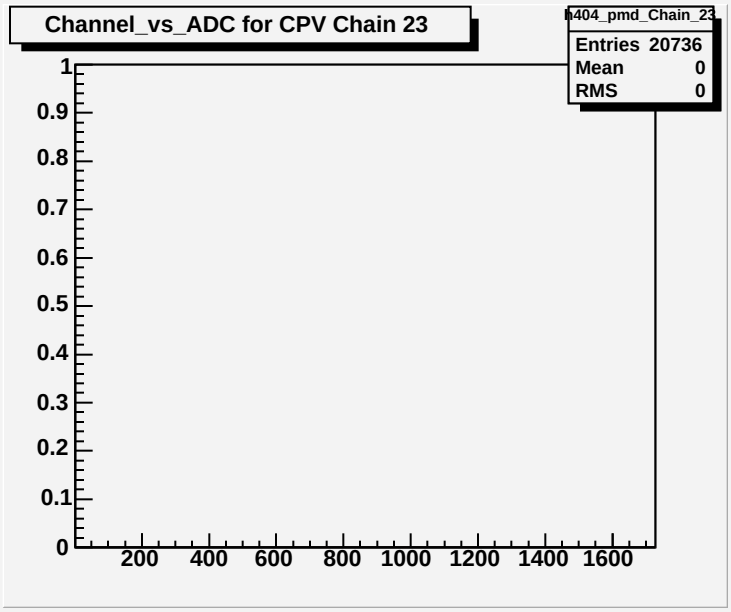

Channel_vs_ADC for CPV Chain 18
h399_pmd_Chain_18
Entries 20736
Mean 0
RMS 0

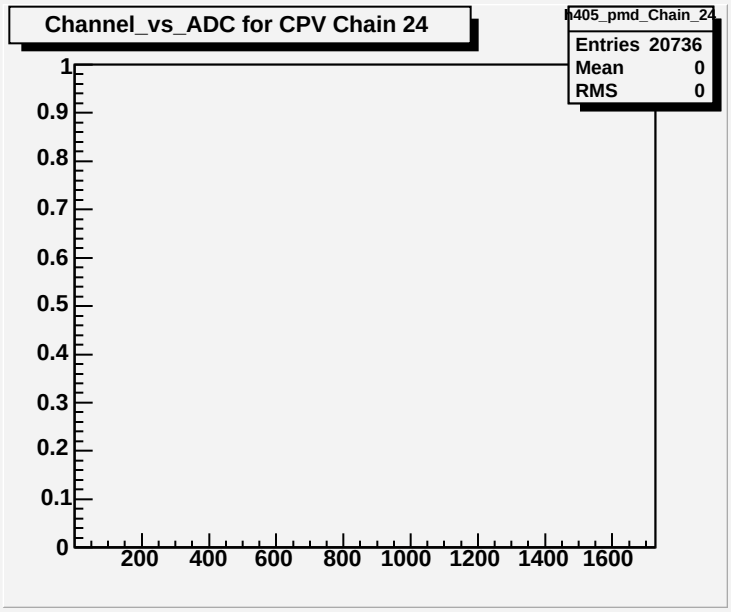

Channel_vs_ADC for CPV Chain 19
h400_pmd_Chain_19
Entries 20736
Mean 0
RMS 0

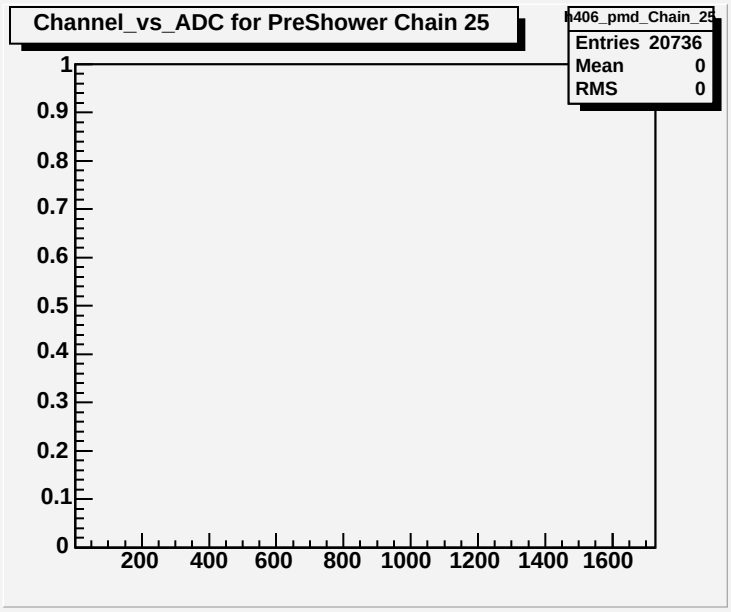

Channel_vs_ADC for CPV Chain 20
h401_pmd_Chain_20
Entries 20736
Mean 0
RMS 0

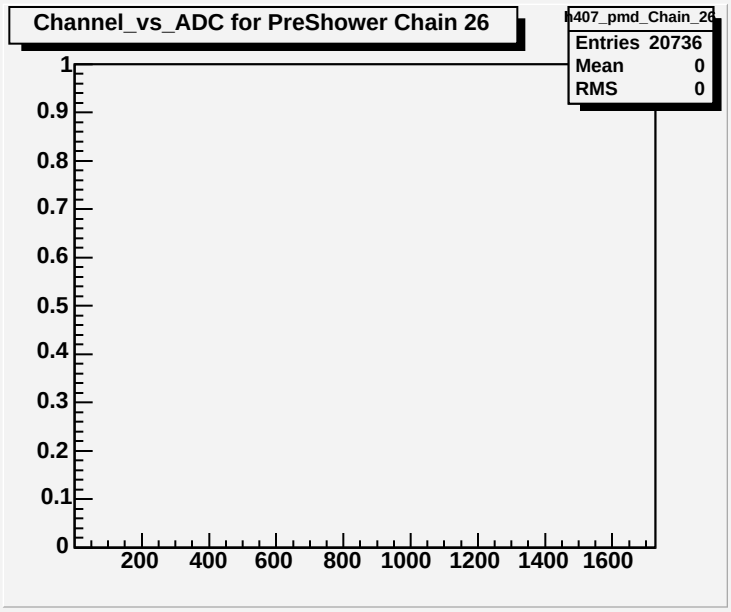

Channel_vs_ADC for CPV Chain 21
h402_pmd_Chain_21
Entries 20736
Mean 0
RMS 0

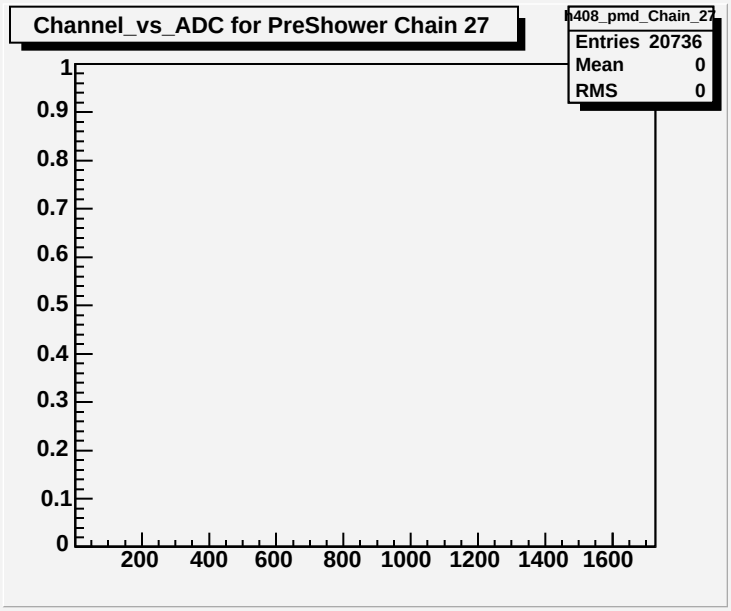

Channel_vs_ADC for CPV Chain 22
h403_pmd_Chain_22
Entries 20736
Mean 0
RMS 0

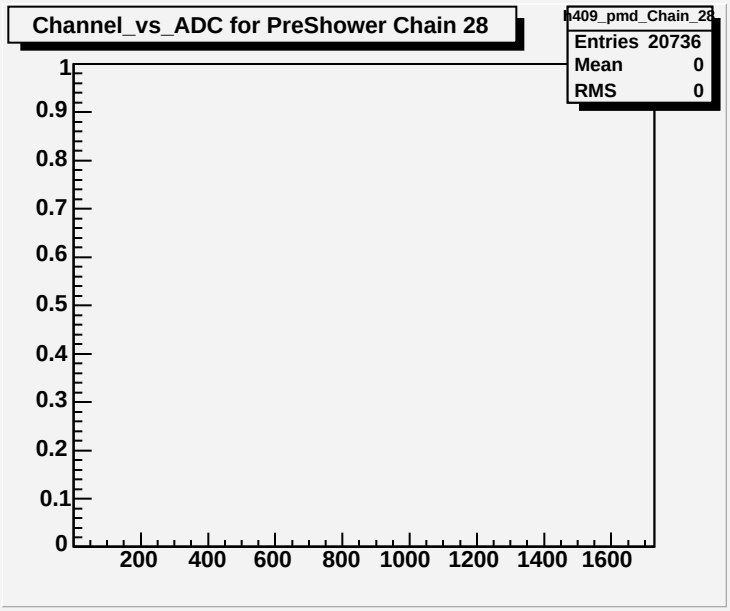

Channel_vs_ADC for CPV Chain 23
h404_pmd_Chain_23
Entries 20736
Mean 0
RMS 0

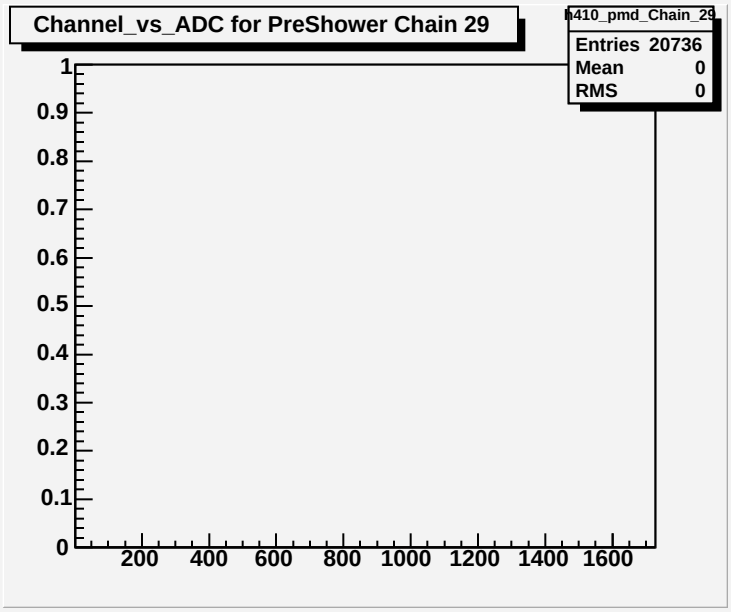

Channel_vs_ADC for CPV Chain 24
h405_pmd_Chain_24
Entries 20736
Mean 0
RMS 0

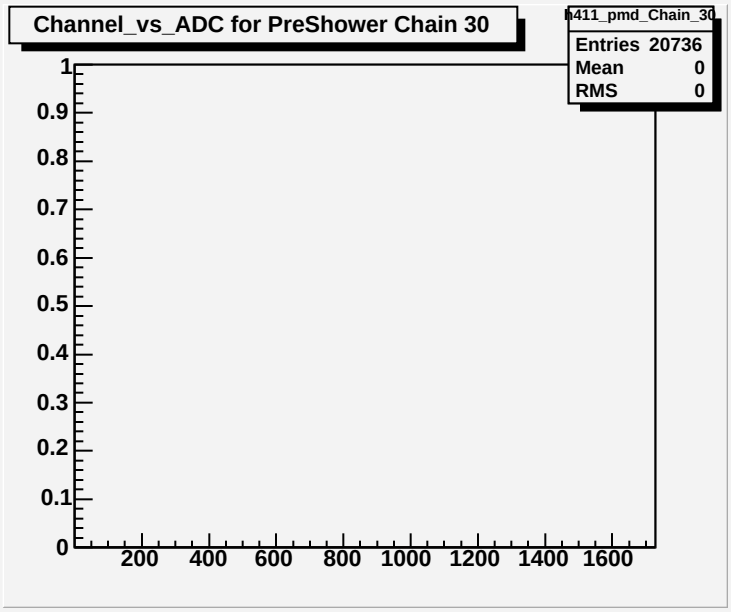

Channel_vs_ADC for PreShower Chain 25
h406_pmd_Chain_25
Entries 20736
Mean 0
RMS 0

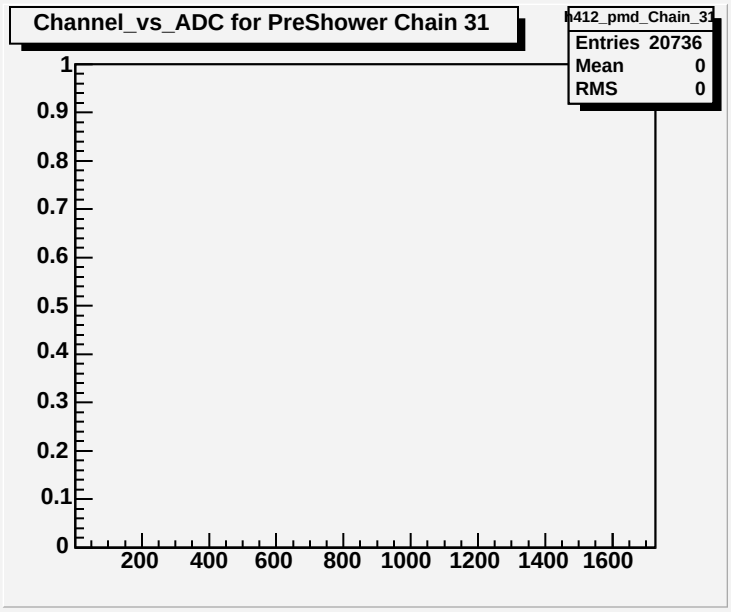

Channel_vs_ADC for PreShower Chain 26
h407_pmd_Chain_26
Entries 20736
Mean 0
RMS 0

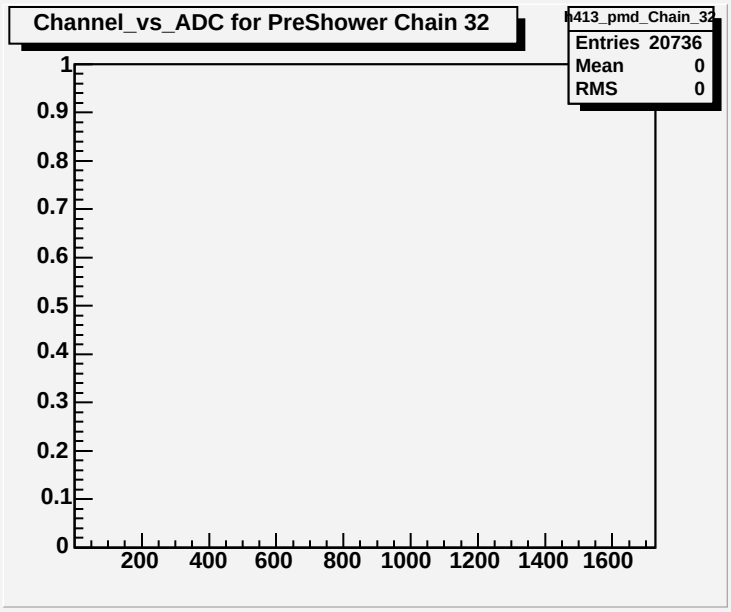

Channel_vs_ADC for PreShower Chain 27
h408_pmd_Chain_27
Entries 20736
Mean 0
RMS 0

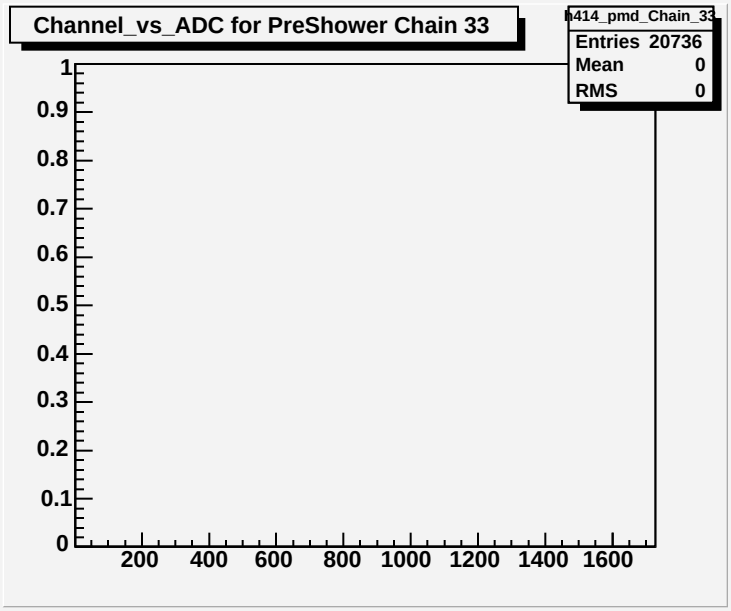

Channel_vs_ADC for PreShower Chain 28
h409_pmd_Chain_28
Entries 20736
Mean 0
RMS 0

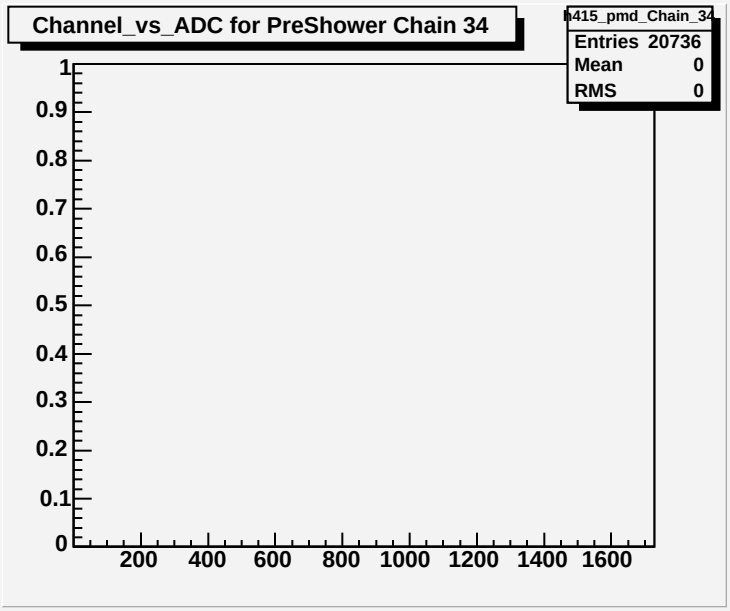

Channel_vs_ADC for PreShower Chain 29
h410_pmd_Chain_29
Entries 20736
Mean 0
RMS 0

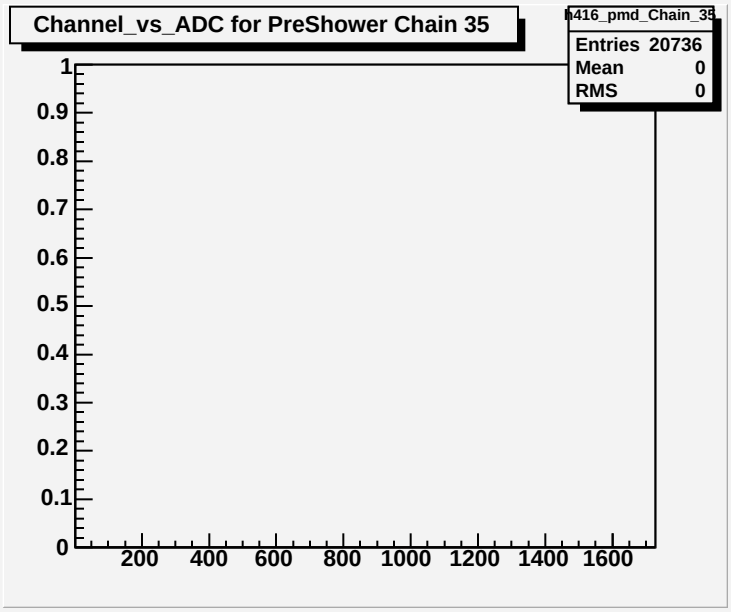

Channel_vs_ADC for PreShower Chain 30
h411_pmd_Chain_30
Entries 20736
Mean 0
RMS 0

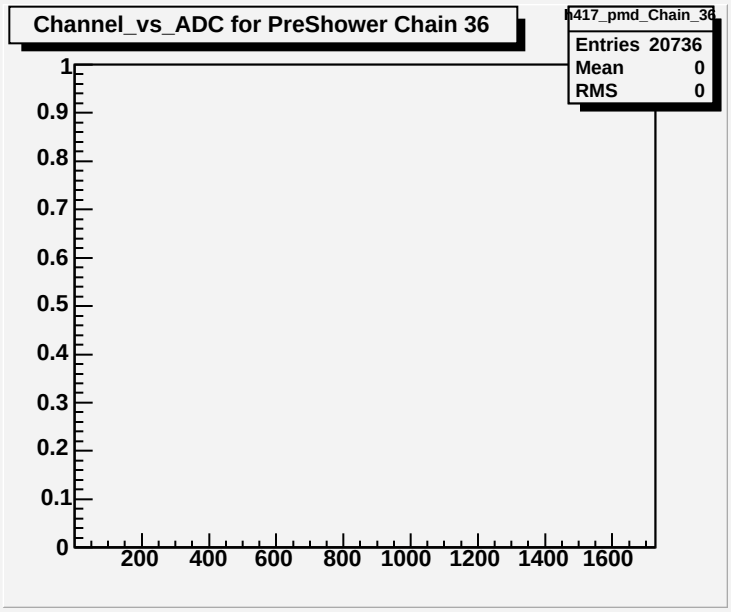

Channel_vs_ADC for PreShower Chain 31
h412_pmd_Chain_31
Entries 20736
Mean 0
RMS 0

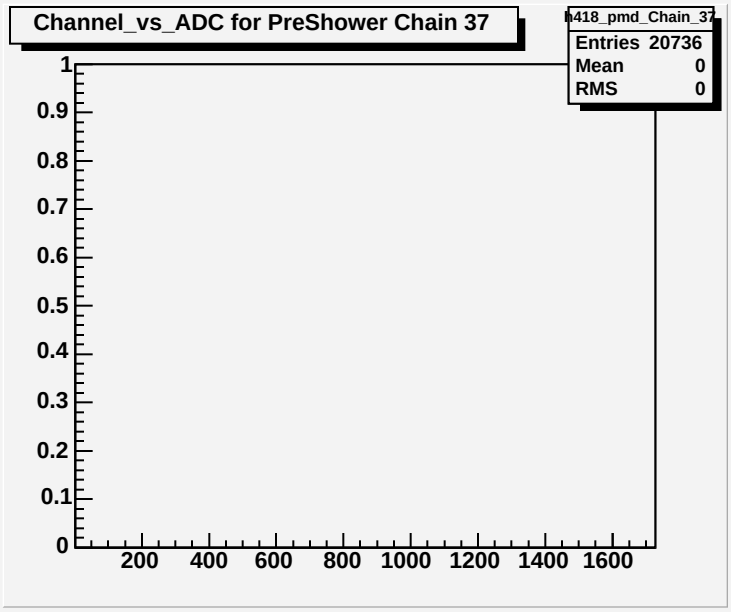

Channel_vs_ADC for PreShower Chain 32
h413_pmd_Chain_32
Entries 20736
Mean 0
RMS 0

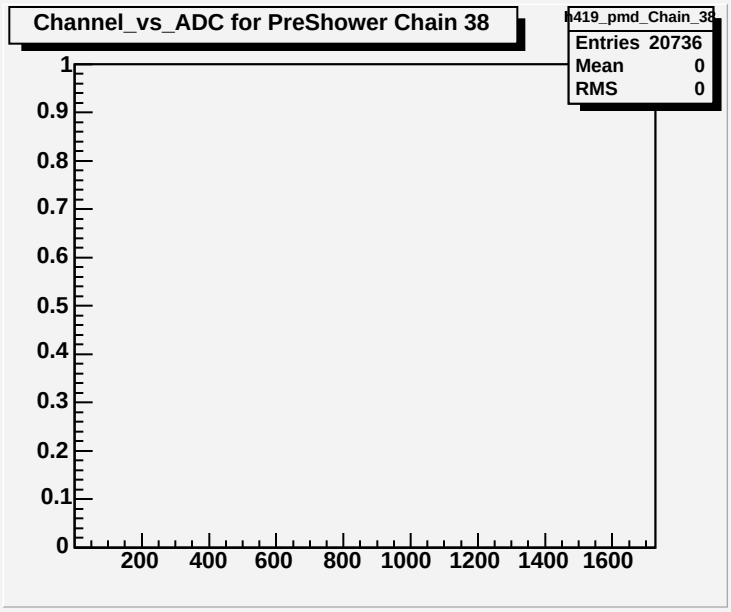

Channel_vs_ADC for PreShower Chain 33
h414_pmd_Chain_33
Entries 20736
Mean 0
RMS 0

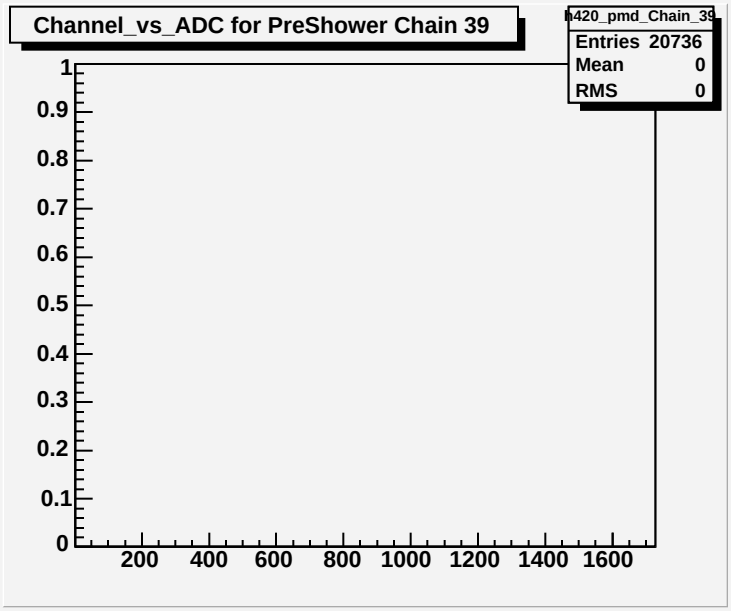

Channel_vs_ADC for PreShower Chain 34
h415_pmd_Chain_34
Entries 20736
Mean 0
RMS 0

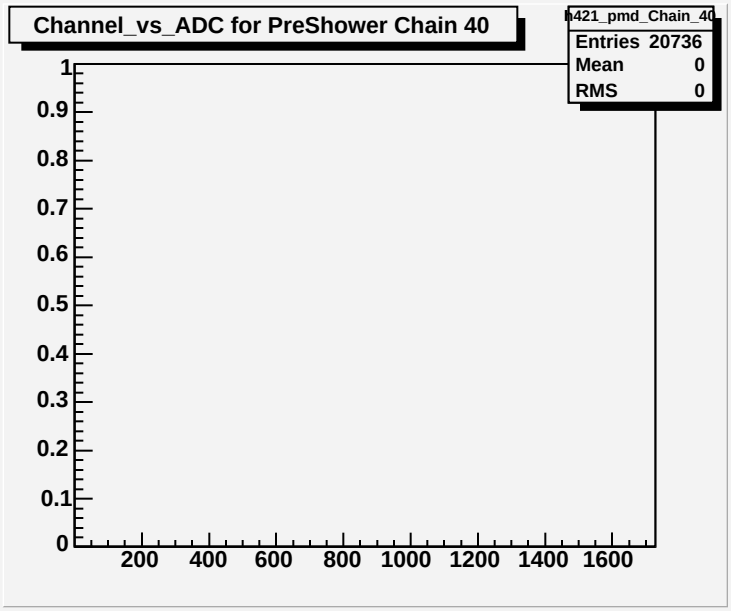

Channel_vs_ADC for PreShower Chain 35
h416_pmd_Chain_35
Entries 20736
Mean 0
RMS 0

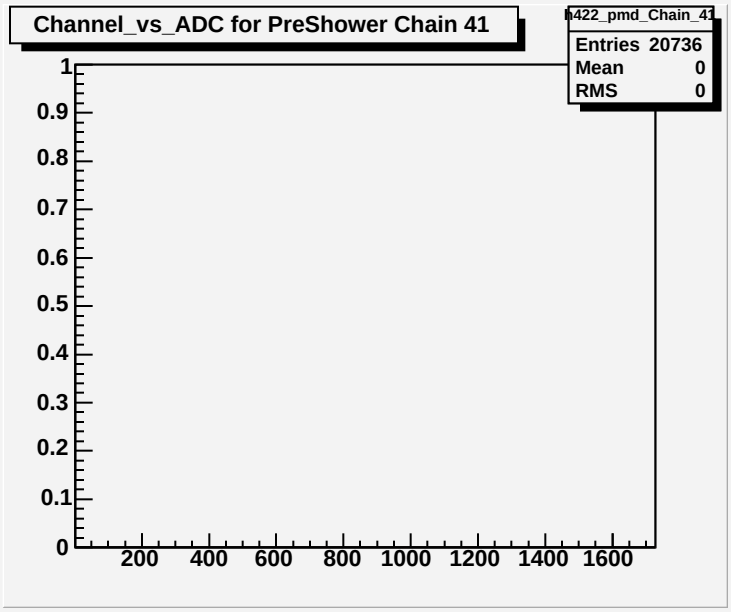

Channel_vs_ADC for PreShower Chain 36
h417_pmd_Chain_36
Entries 20736
Mean 0
RMS 0

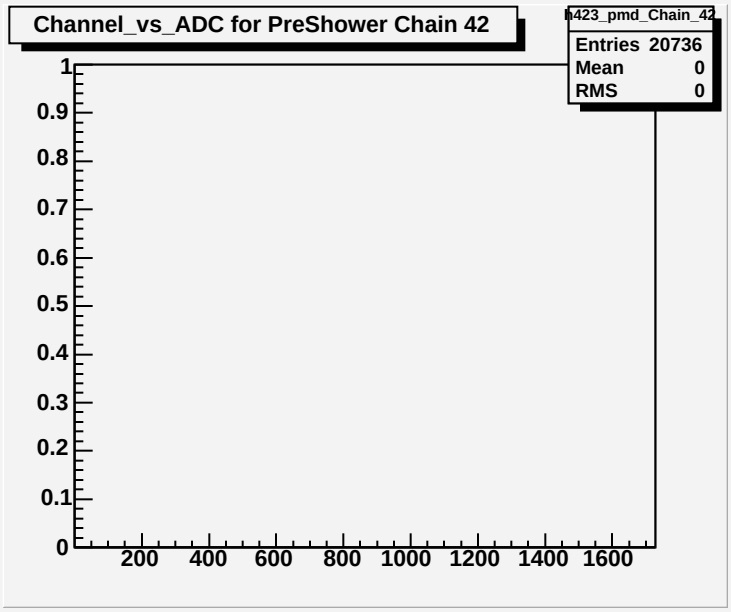

Channel_vs_ADC for PreShower Chain 37
h418_pmd_Chain_37
Entries 20736
Mean 0
RMS 0

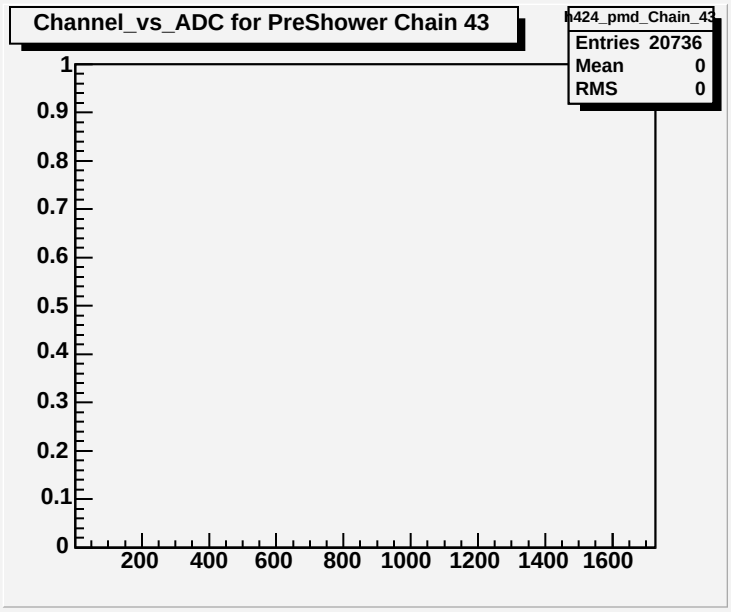

Channel_vs_ADC for PreShower Chain 38
h419_pmd_Chain_38
Entries 20736
Mean 0
RMS 0

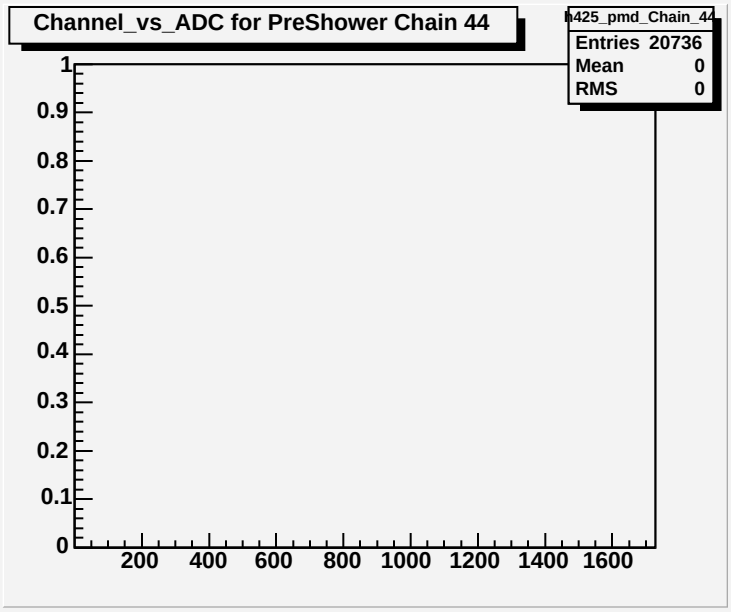

Channel_vs_ADC for PreShower Chain 39
h420_pmd_Chain_39
Entries 20736
Mean 0
RMS 0

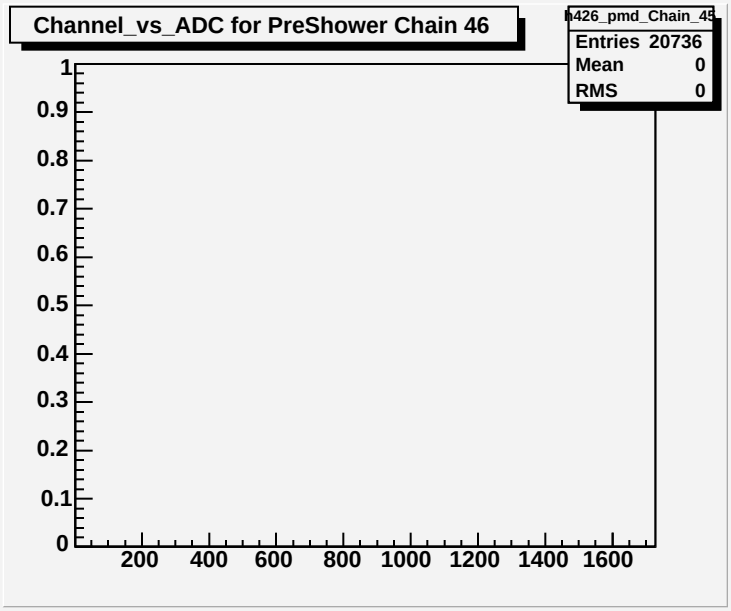

Channel_vs_ADC for PreShower Chain 40
h421_pmd_Chain_40
Entries 20736
Mean 0
RMS 0

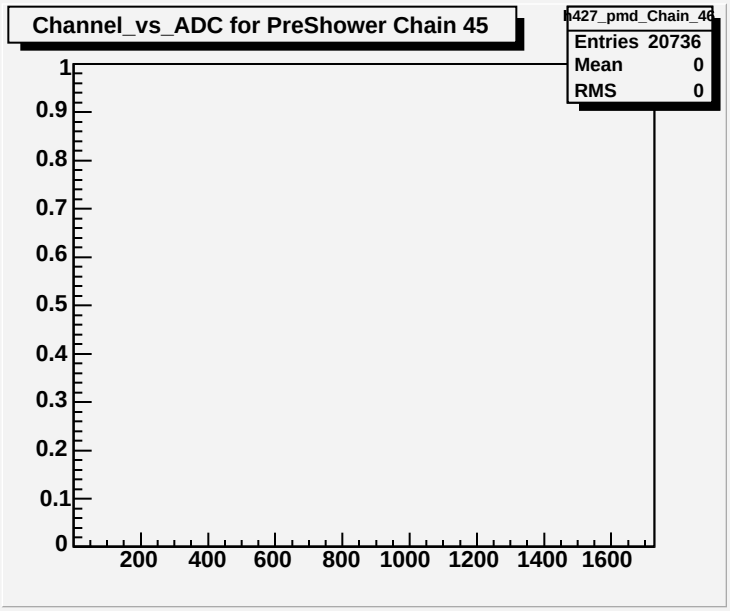

Channel_vs_ADC for PreShower Chain 41
h422_pmd_Chain_41
Entries 20736
Mean 0
RMS 0

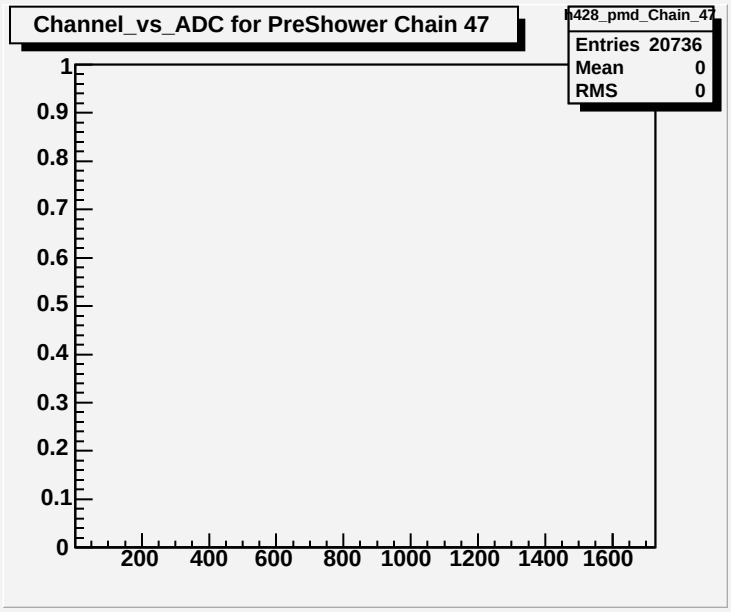

Channel_vs_ADC for PreShower Chain 42
h423_pmd_Chain_42
Entries 20736
Mean 0
RMS 0

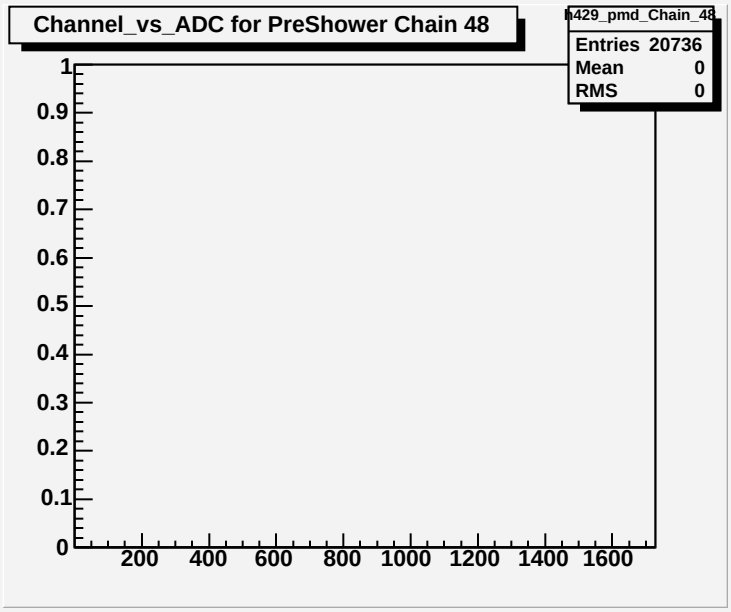

Channel_vs_ADC for PreShower Chain 43
h424_pmd_Chain_43
Entries 20736
Mean 0
RMS 0


Channel_vs_ADC for PreShower Chain 44
h425_pmd_Chain_44
Entries 20736
Mean 0
RMS 0

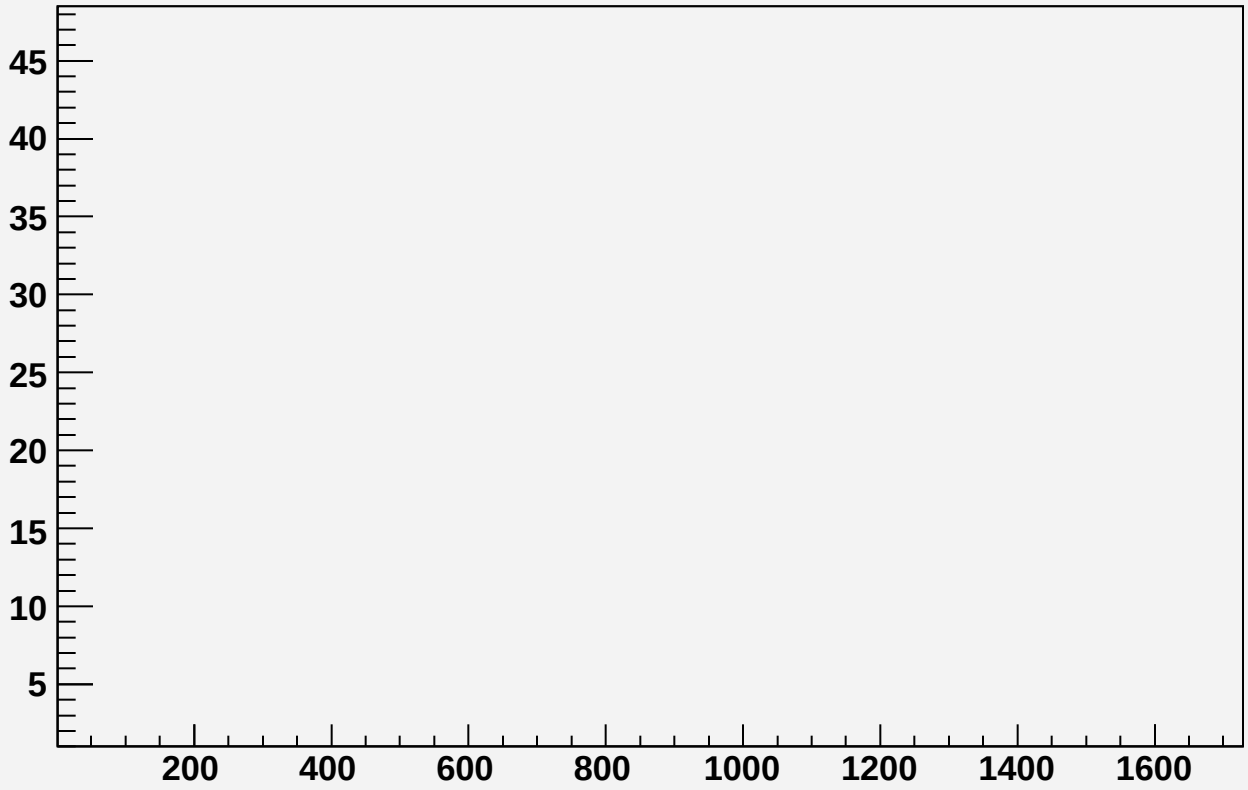

Channel_vs_ADC for PreShower Chain 46
h426_pmd_Chain_45
Entries 20736
Mean 0
RMS 0


Channel_vs_ADC for PreShower Chain 45
h427_pmd_Chain_46
Entries 20736
Mean 0
RMS 0

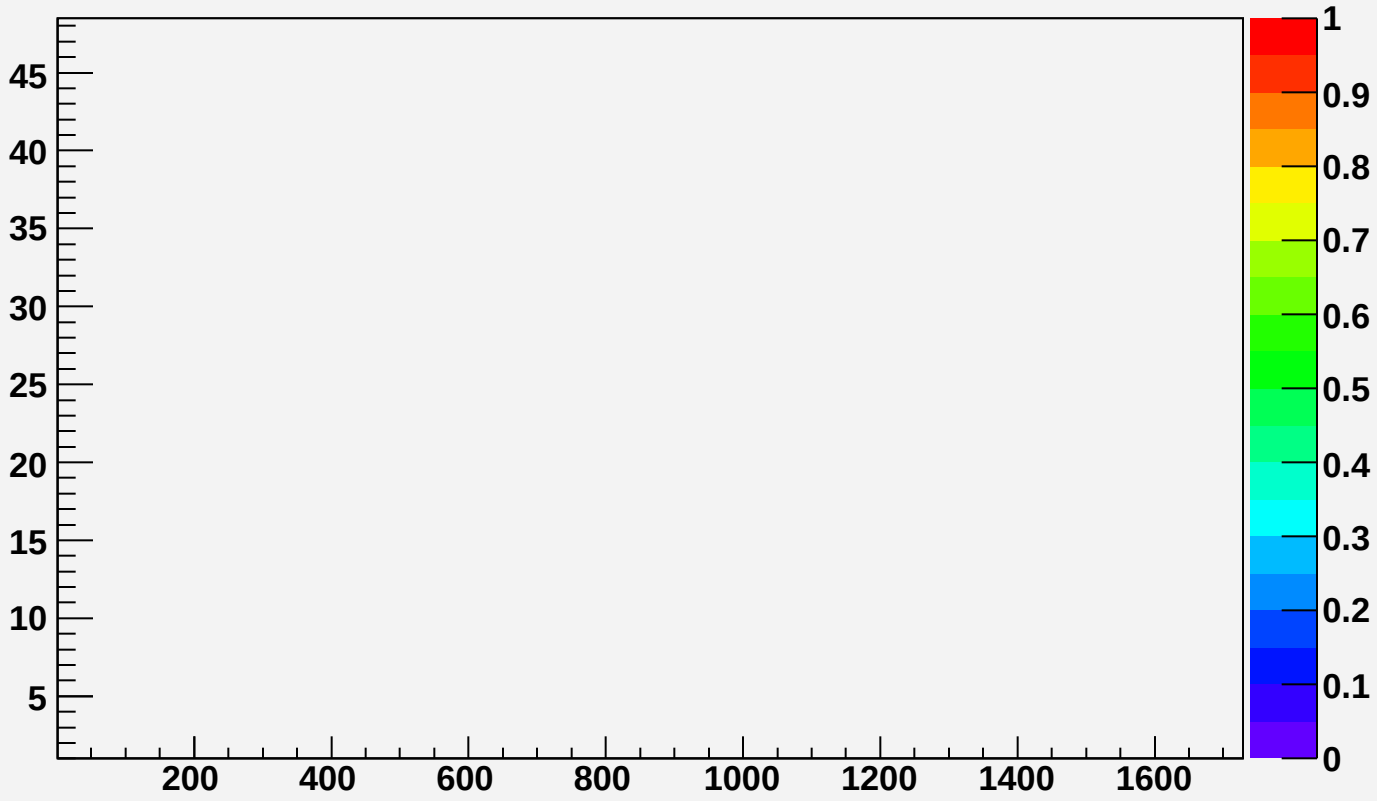

Channel_vs_ADC for PreShower Chain 47
h428_pmd_Chain_47
Entries 20736
Mean 0
RMS 0

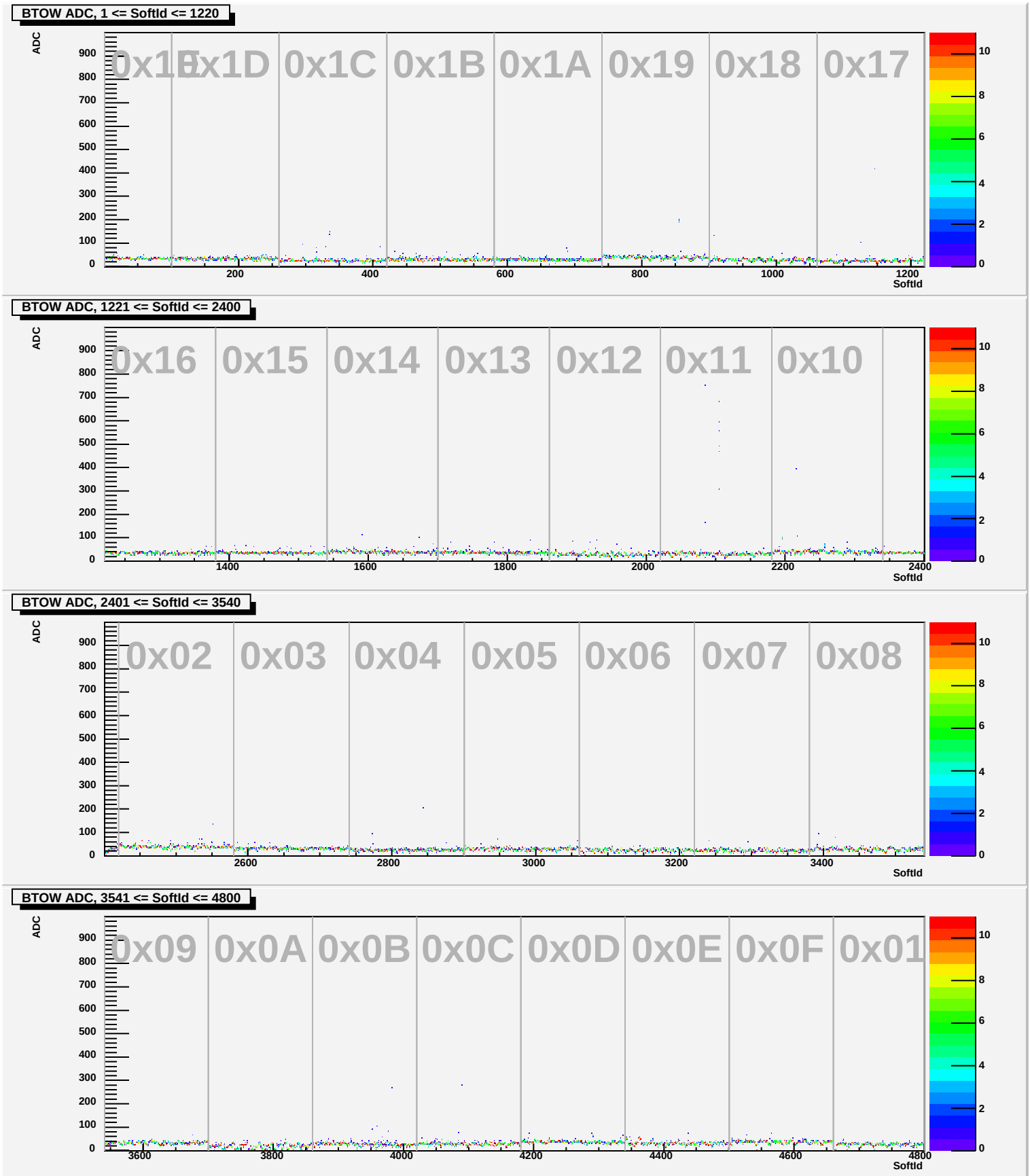

Channel_vs_ADC for PreShower Chain 48
h429_pmd_Chain_48
Entries 20736
Mean 0
RMS 0

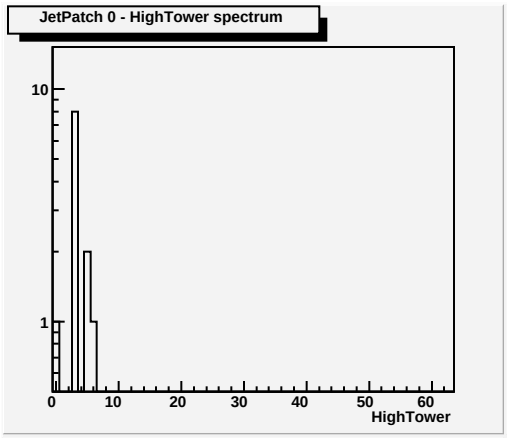
Chain vs Channel 2D

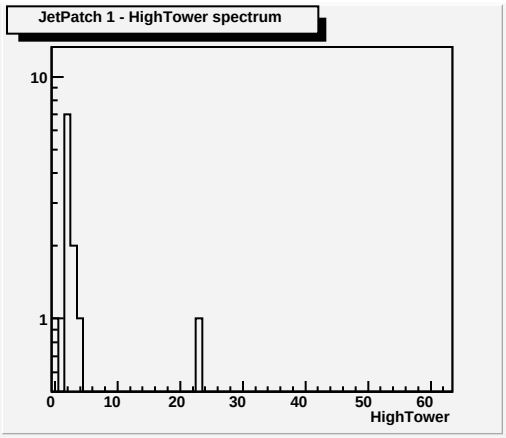


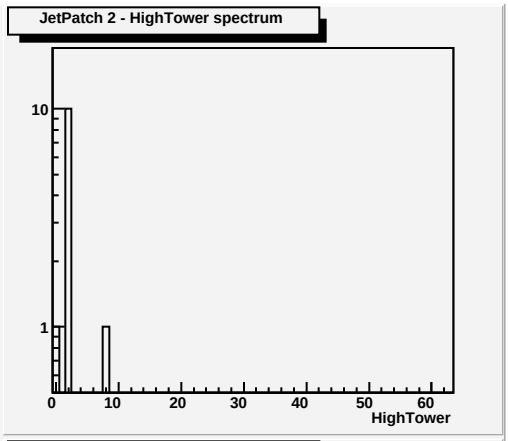
Chain vs Channel 2D (ADC weighted)

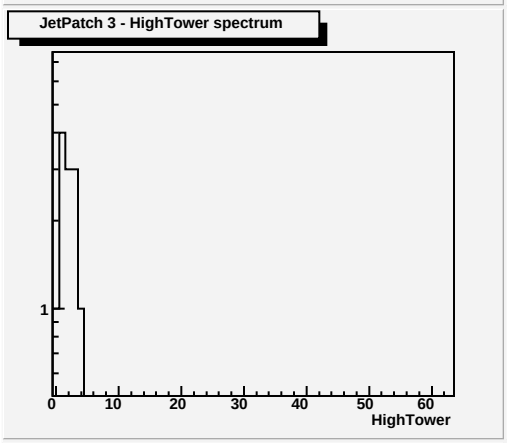


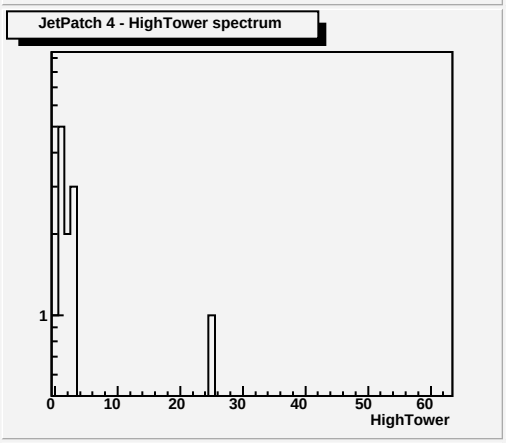

BTOW ADC, 1 <= SoftId <= 1220
0x1E 0x1D 0x1C 0x1B 0x1A 0x19 0x18 0x17
ADC
SoftId
BTOW ADC, 1221 <= SoftId <= 2400
0x16 0x15 0x14 0x13 0x12 0x11 0x10
ADC
SoftId
BTOW ADC, 2401 <= SoftId <= 3540
0x02 0x03 0x04 0x05 0x06 0x07 0x08
ADC
SoftId
BTOW ADC, 3541 <= SoftId <= 4800
0x09 0x0A 0x0B 0x0C 0x0D 0x0E 0x0F 0x01
ADC
SoftId

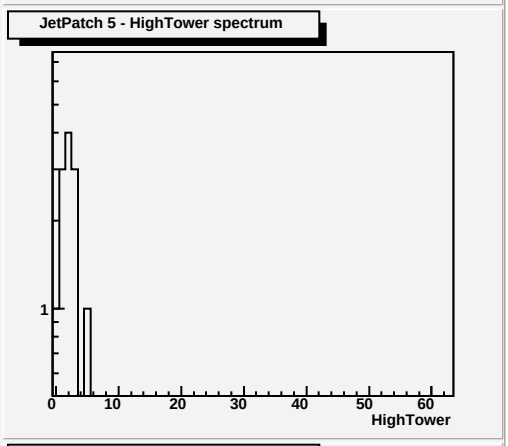

JetPatch 0 - HighTower spectrum
10
1
0 10 20 30 40 50 60
HighTower

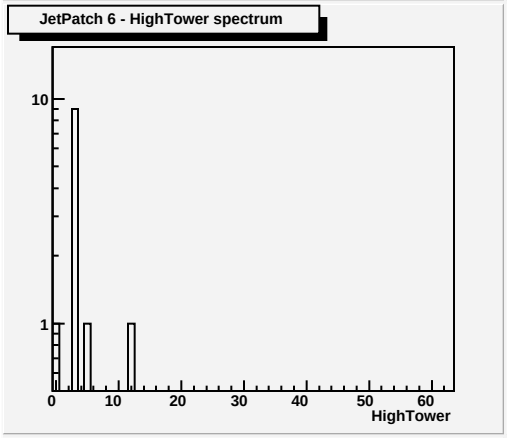

JetPatch 1 - HighTower spectrum
10
1
0 10 20 30 40 50 60
HighTower

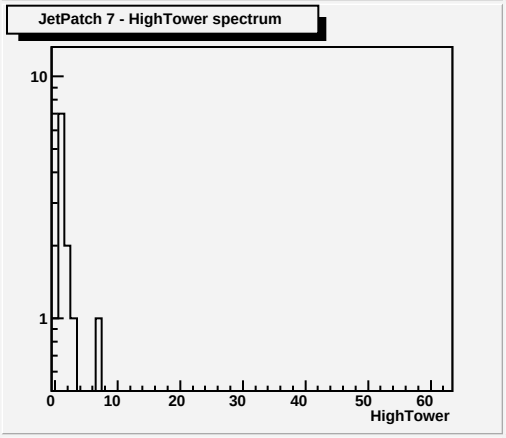

JetPatch 2 - HighTower spectrum
10
1
0 10 20 30 40 50 60
HighTower

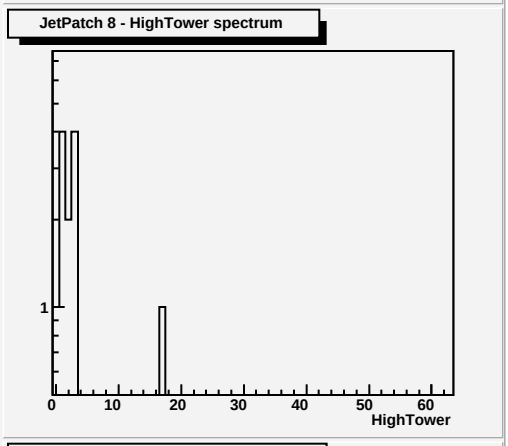

JetPatch 3 - HighTower spectrum
1
0 10 20 30 40 50 60
HighTower

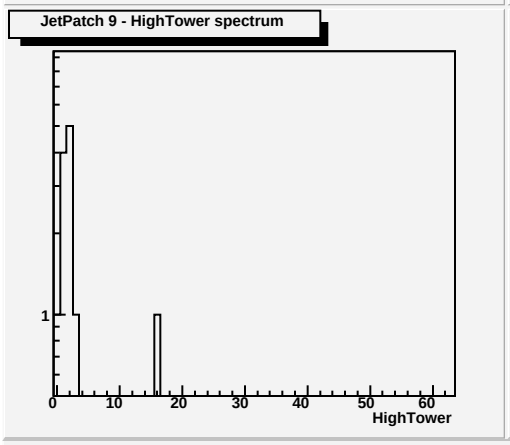

JetPatch 4 - HighTower spectrum
1
0 10 20 30 40 50 60
HighTower

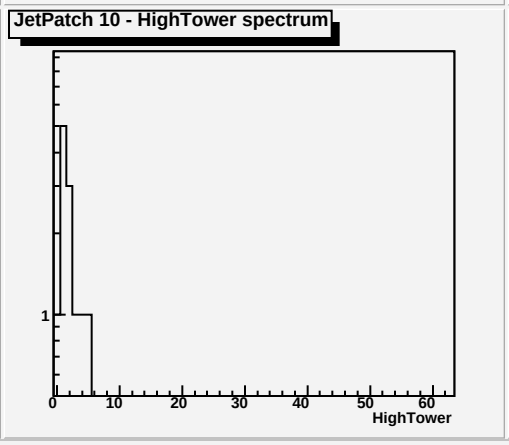

JetPatch 5 - HighTower spectrum
1
0 10 20 30 40 50 60
HighTower

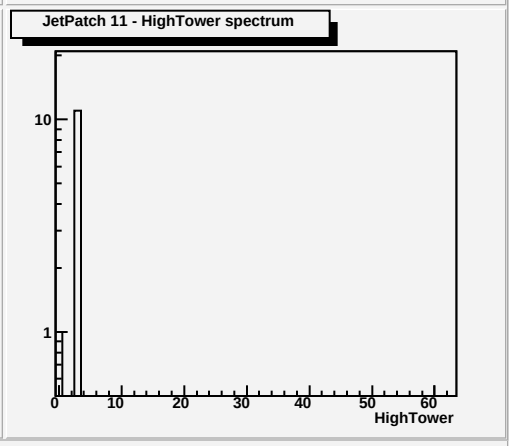

JetPatch 6 - HighTower spectrum
10
1
0 10 20 30 40 50 60
HighTower

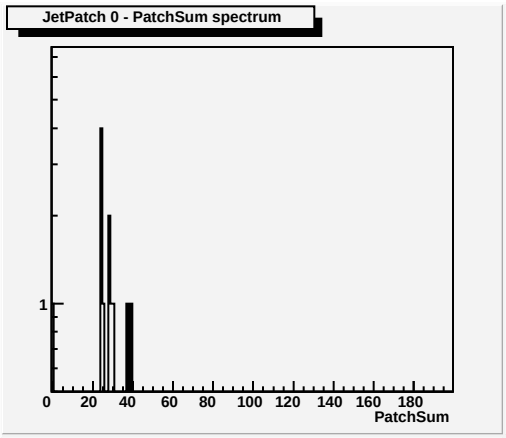

JetPatch 7 - HighTower spectrum
10
1
0 10 20 30 40 50 60
HighTower

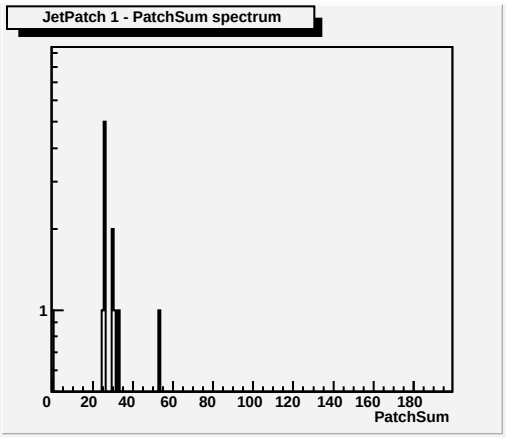

JetPatch 8 - HighTower spectrum
1
0 10 20 30 40 50 60
HighTower

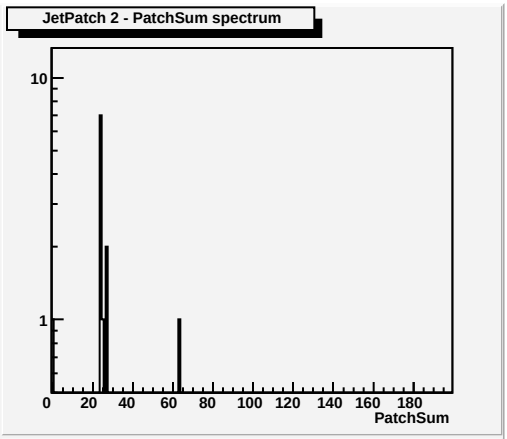

JetPatch 9 - HighTower spectrum
1
0 10 20 30 40 50 60
HighTower

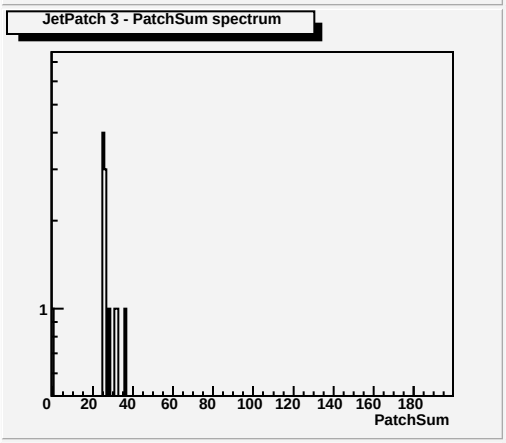

JetPatch 10 - HighTower spectrum
1
0 10 20 30 40 50 60
HighTower

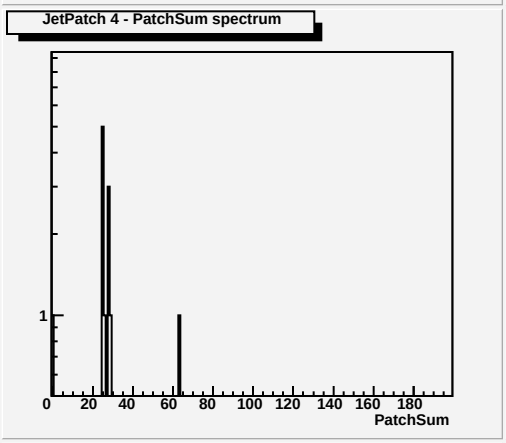

JetPatch 11 - HighTower spectrum
10
1
0 10 20 30 40 50 60
HighTower

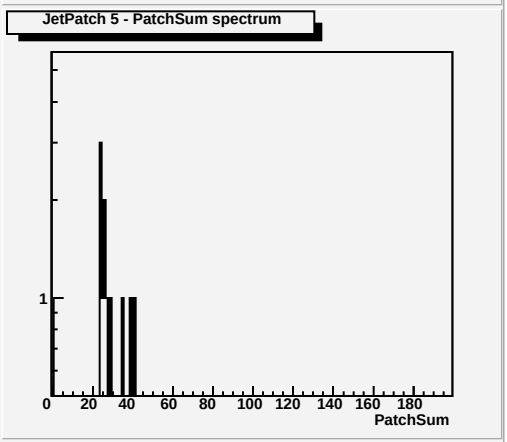

JetPatch 0 - PatchSum spectrum
1
0 20 40 60 80 100 120 140 160 180
PatchSum

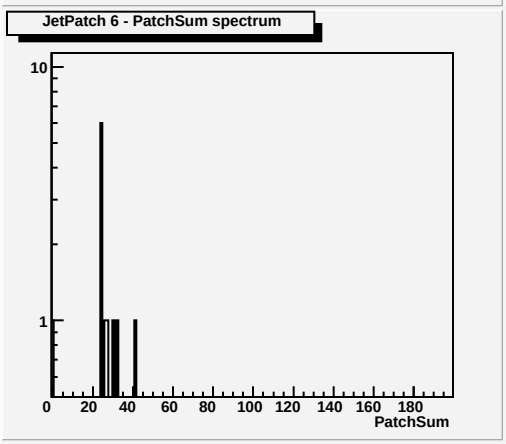

JetPatch 1 - PatchSum spectrum
1
0 20 40 60 80 100 120 140 160 180
PatchSum

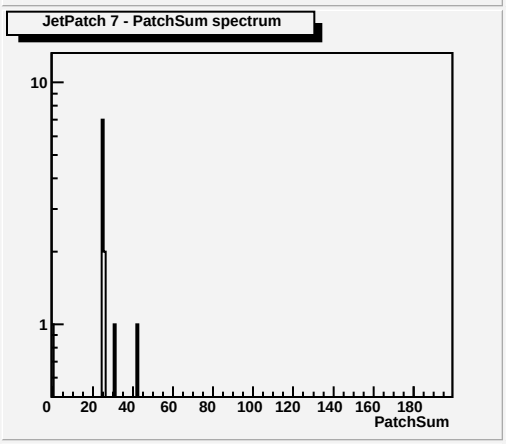

JetPatch 2 - PatchSum spectrum
10
1
0 20 40 60 80 100 120 140 160 180
PatchSum

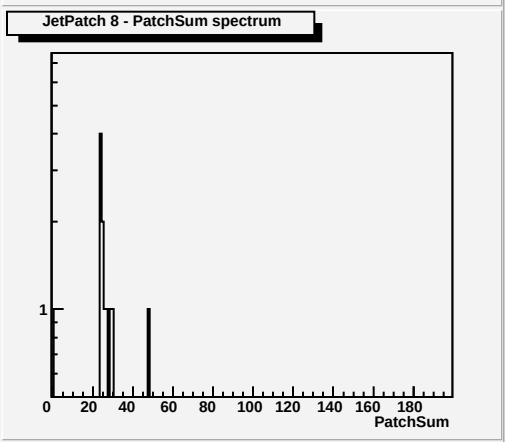

JetPatch 3 - PatchSum spectrum
1
0 20 40 60 80 100 120 140 160 180
PatchSum

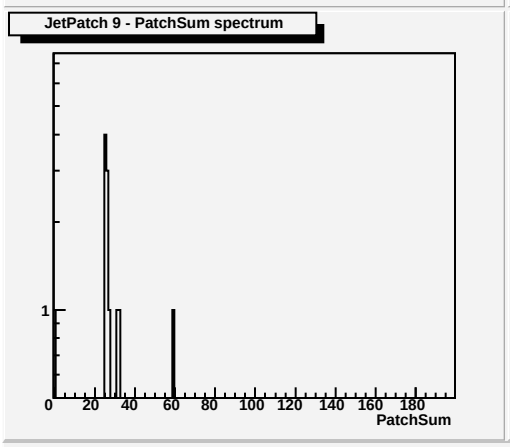

JetPatch 4 - PatchSum spectrum
1
0 20 40 60 80 100 120 140 160 180
PatchSum

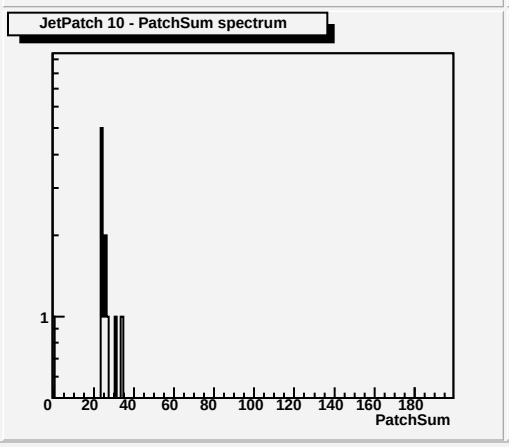

JetPatch 5 - PatchSum spectrum
1
0 20 40 60 80 100 120 140 160 180
PatchSum

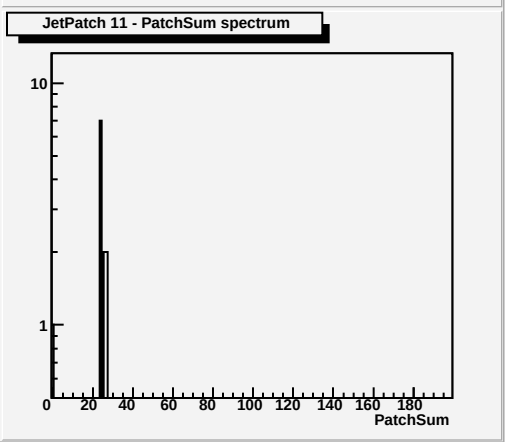

JetPatch 6 - PatchSum spectrum
10
1
0 20 40 60 80 100 120 140 160 180
PatchSum


JetPatch 7 - PatchSum spectrum
10
1
0 20 40 60 80 100 120 140 160 180
PatchSum

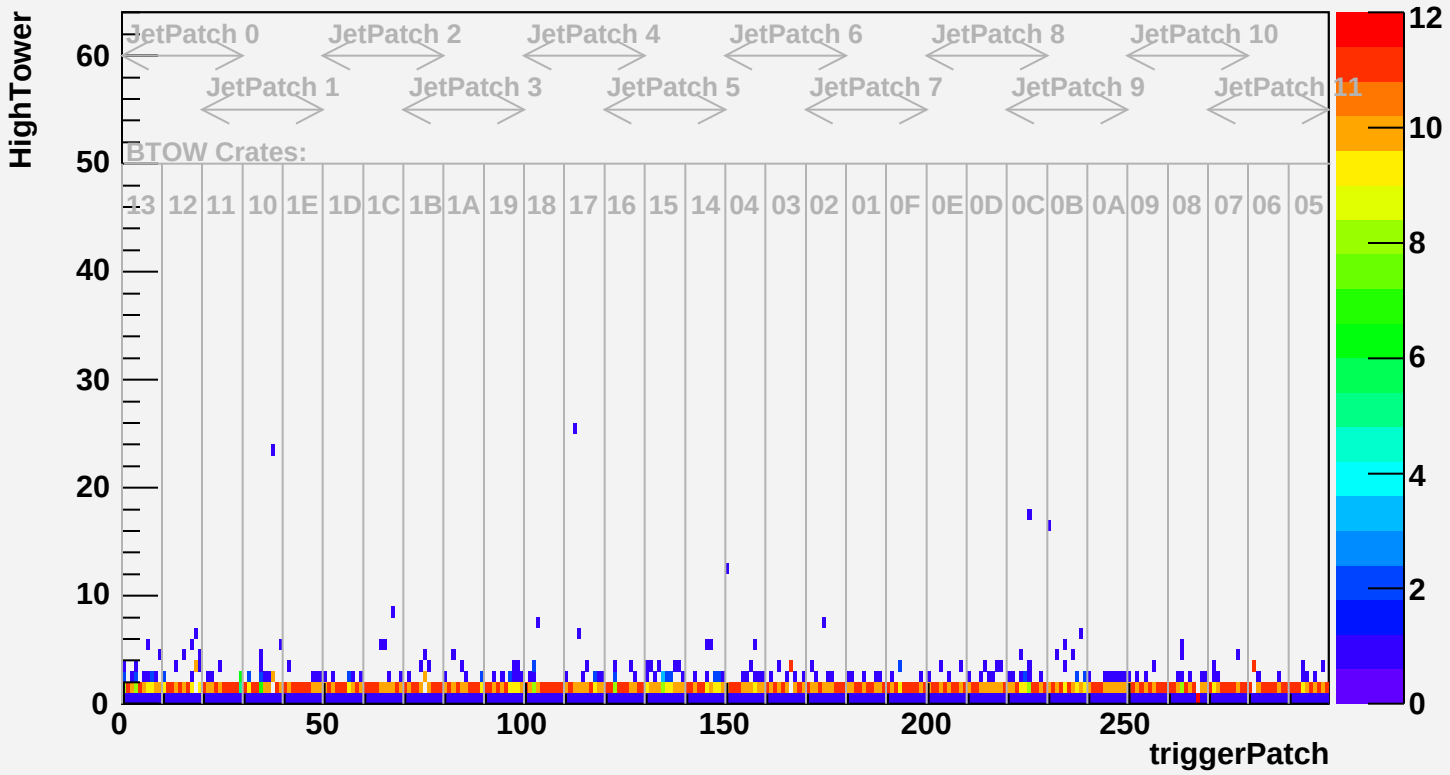

JetPatch 8 - PatchSum spectrum
1
0 20 40 60 80 100 120 140 160 180
PatchSum


JetPatch 9 - PatchSum spectrum
1
0 20 40 60 80 100 120 140 160 180
PatchSum

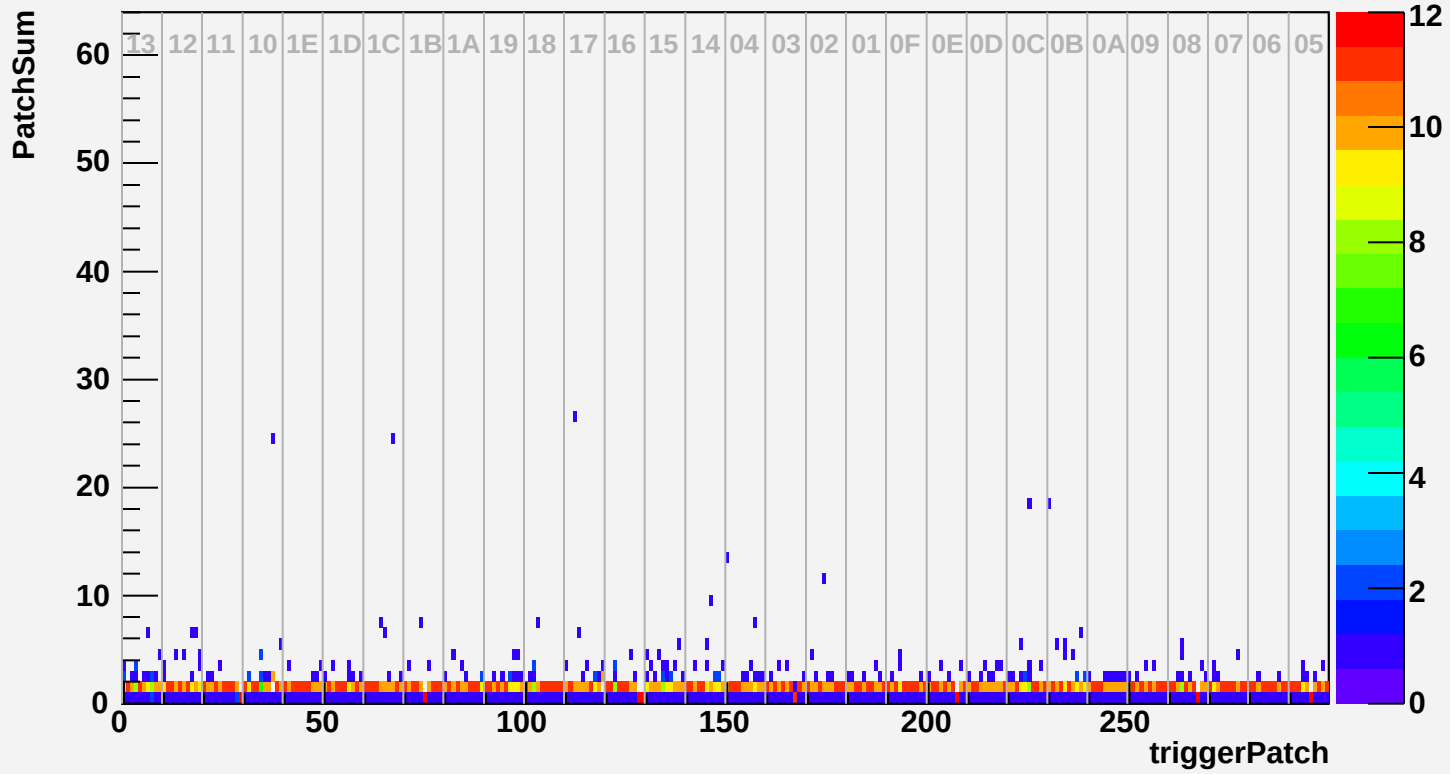

JetPatch 10 - PatchSum spectrum
1
0 20 40 60 80 100 120 140 160 180
PatchSum


JetPatch 11 - PatchSum spectrum
10
1
0 20 40 60 80 100 120 140 160 180
PatchSum

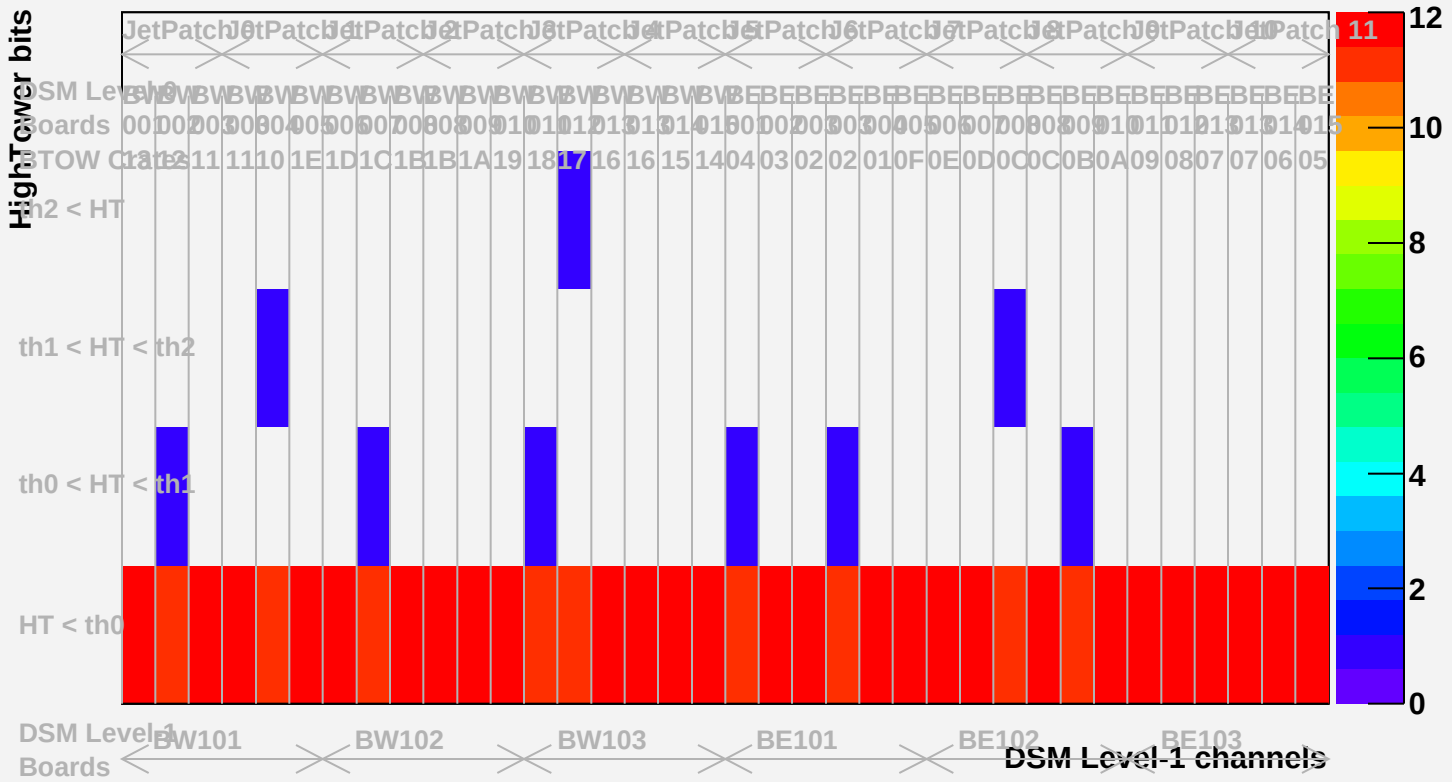
BEMC DSM L0 Input - HighTower



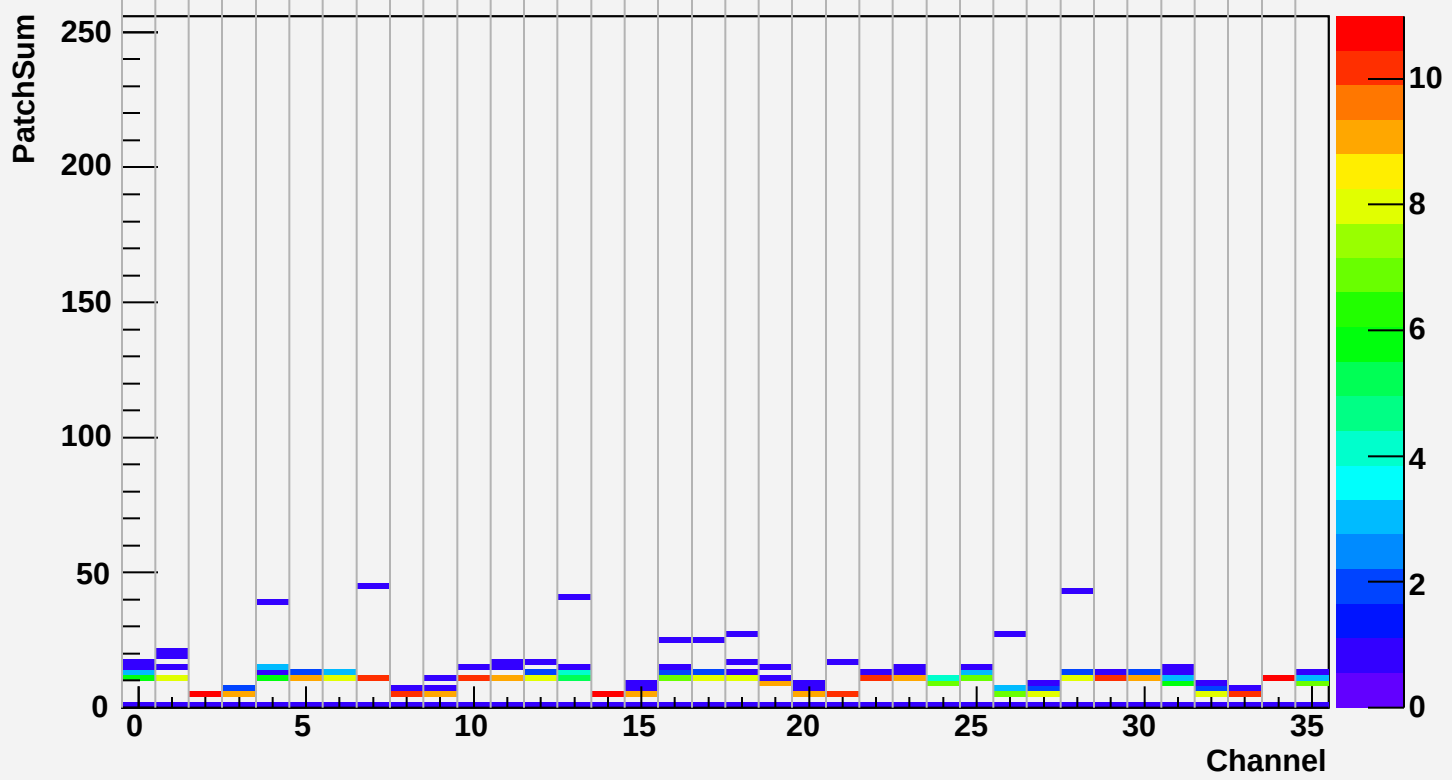
BEMC DSM L0 Input - PatchSum

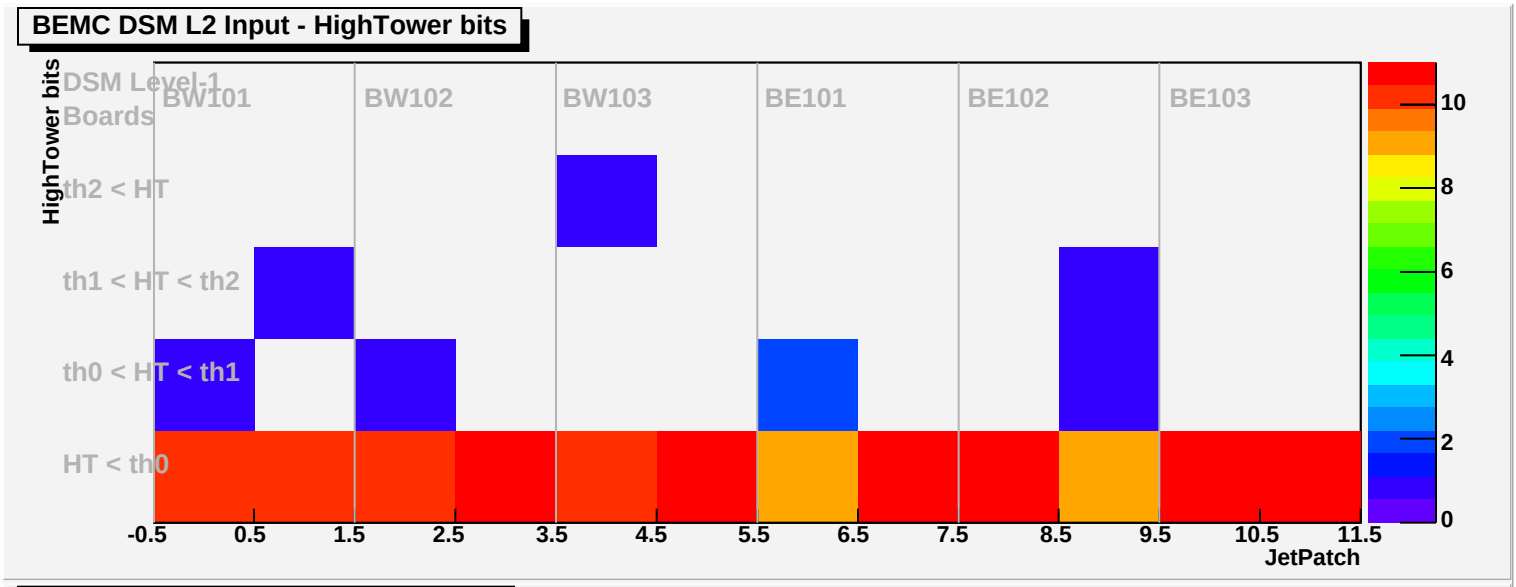


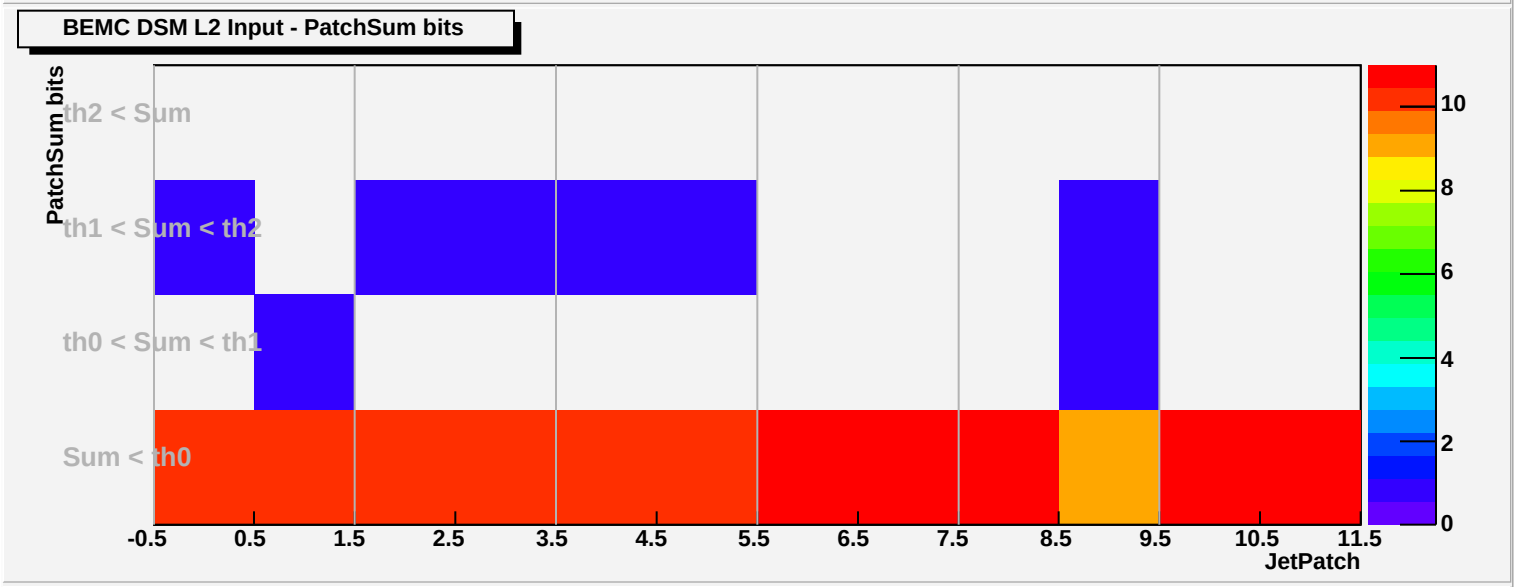
BEMC DSM L1 Input - HighTower bits

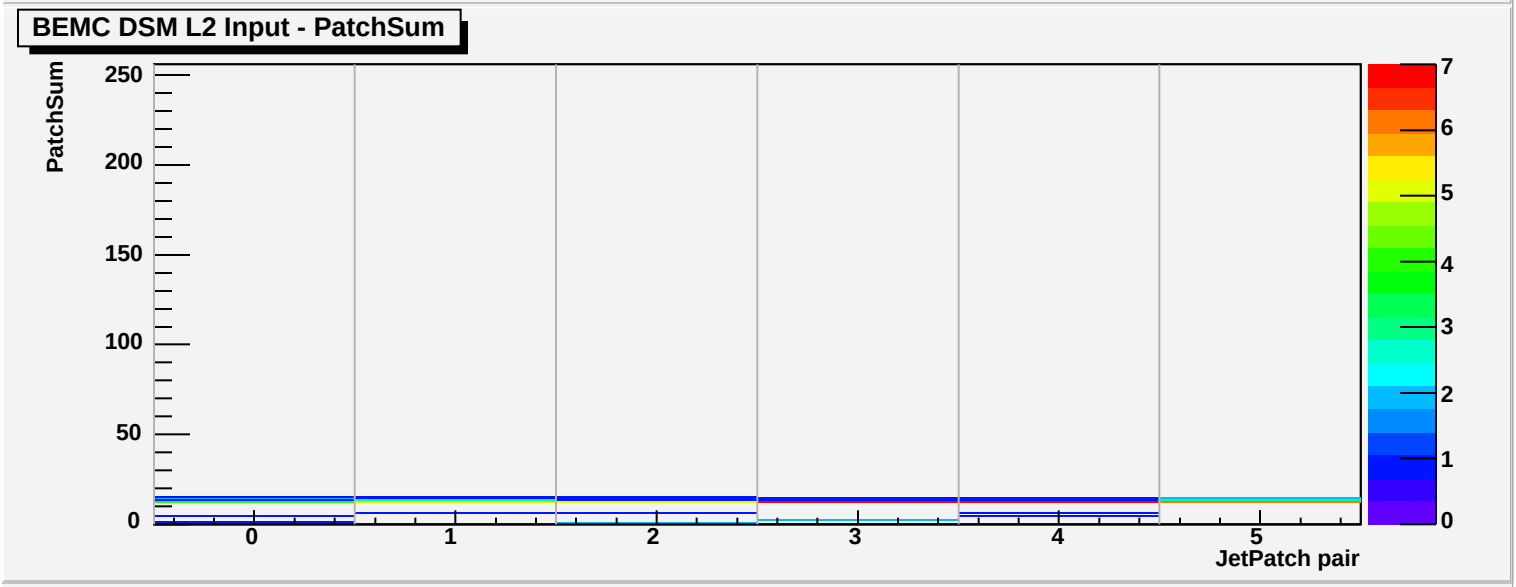


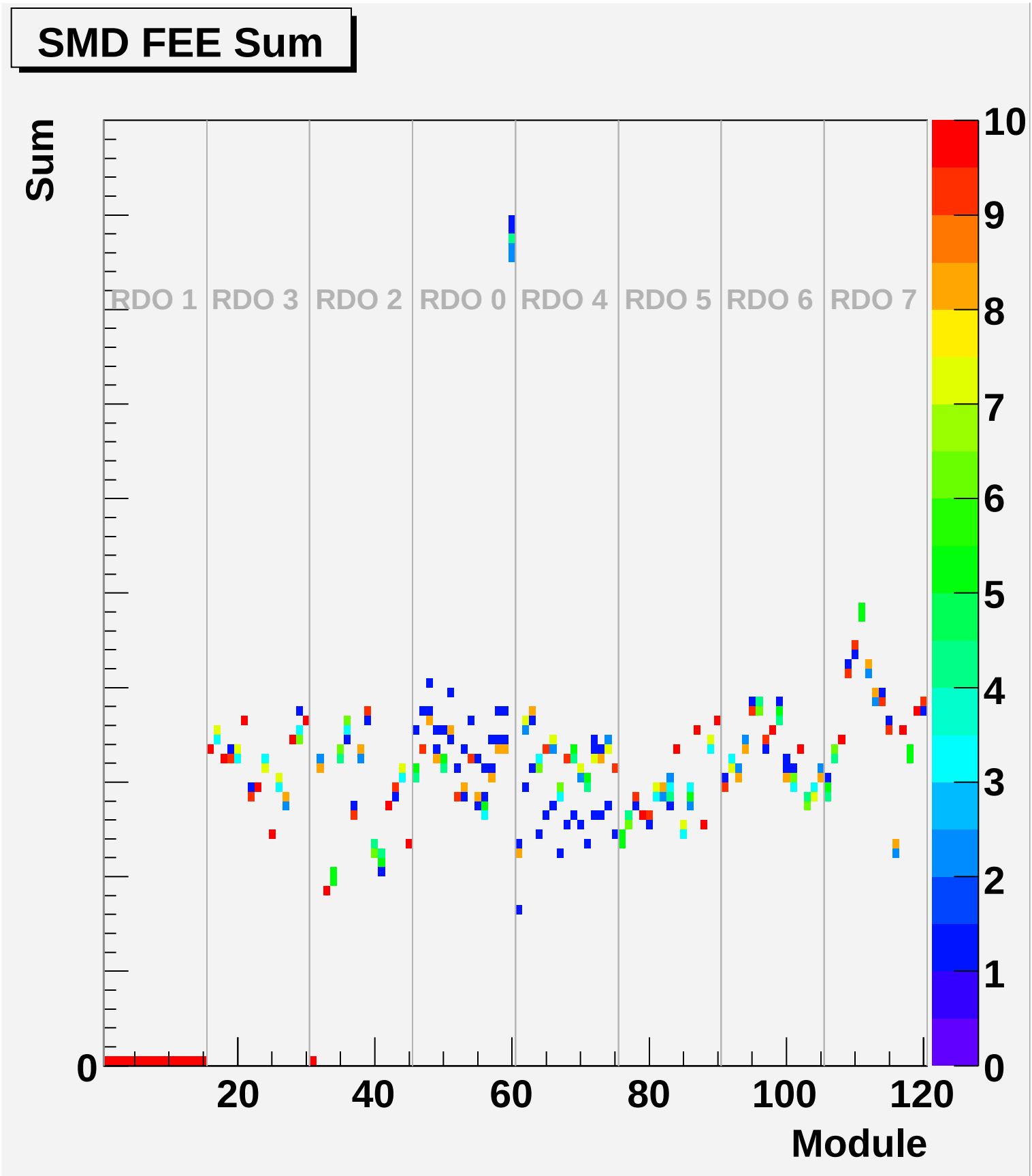
BEMC DSM L1 Input - PatchSum

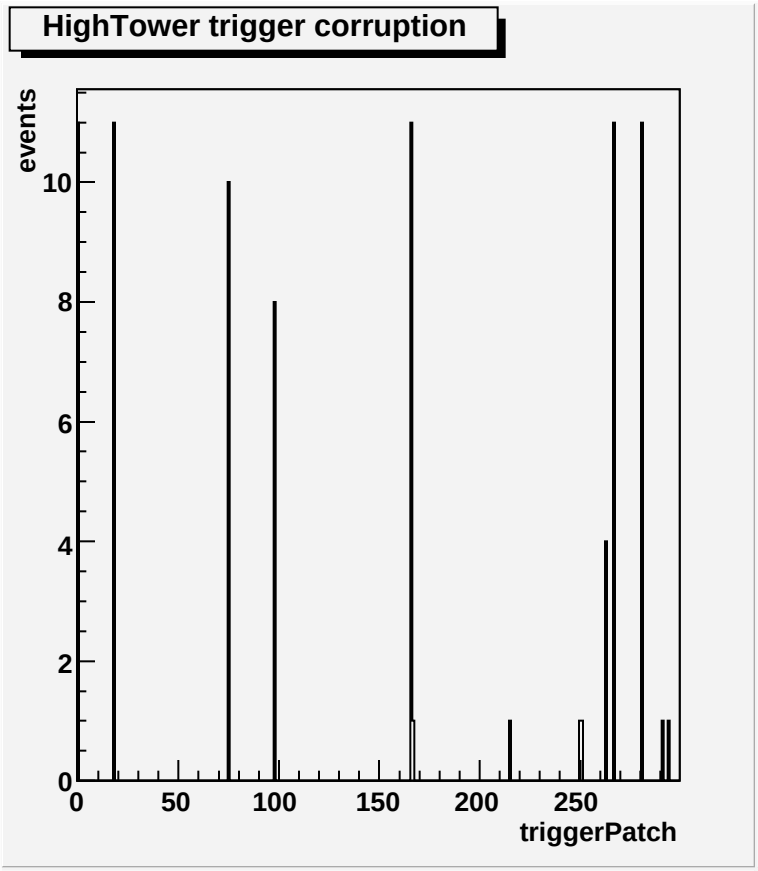


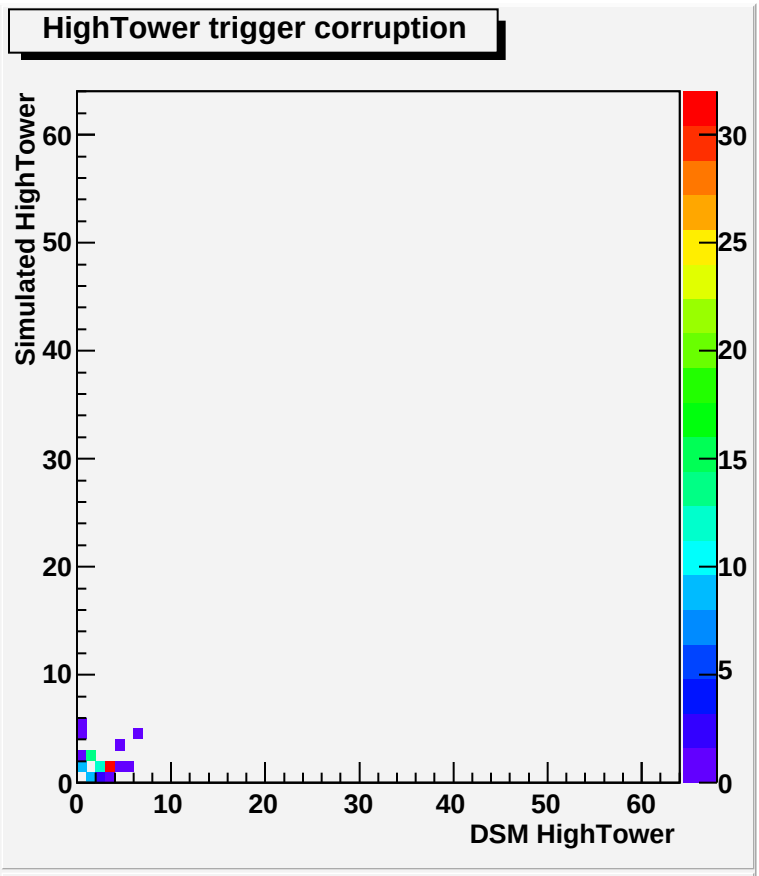

BEMC DSM L2 Input - HighTower bits
HighTower bits
DSM Level-1 Boards
BW101
BW102
BW103
BE101
BE102
BE103
th2 < HT
th1 < HT < th2
th0 < HT < th1
HT < th0
-0.5
0.5
1.5
2.5
3.5
4.5
5.5
6.5
7.5
8.5
9.5
10.5
11.5
JetPatch
0
2
4
6
8
10

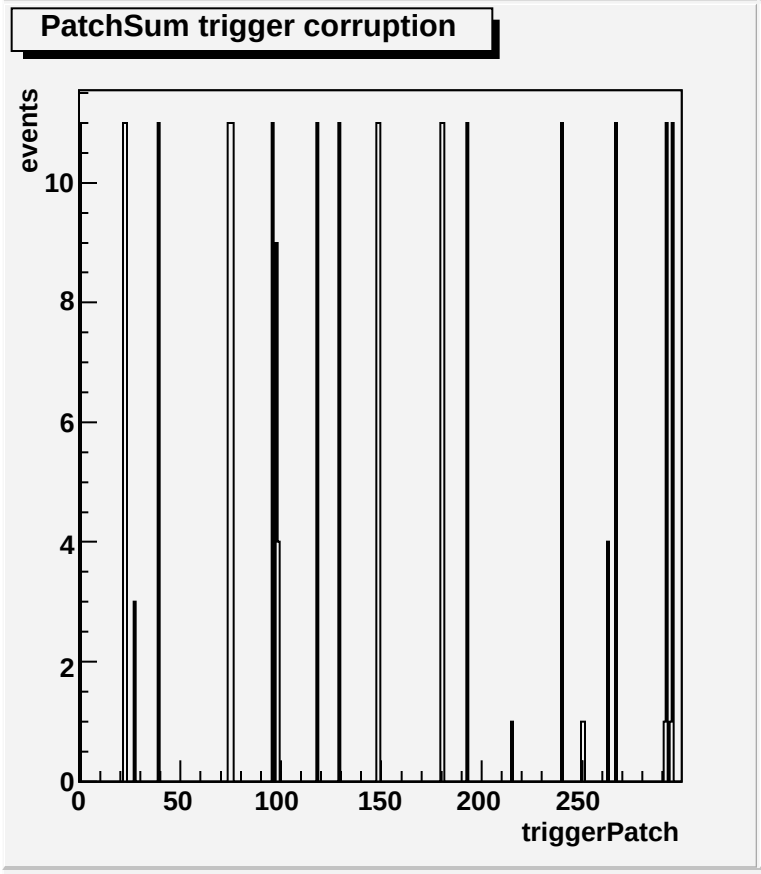

BEMC DSM L2 Input - PatchSum bits
PatchSum bits
th2 < Sum
th1 < Sum < th2
th0 < Sum < th1
Sum < th0
-0.5
0.5
1.5
2.5
3.5
4.5
5.5
6.5
7.5
8.5
9.5
10.5
11.5
JetPatch
0
2
4
6
8
10

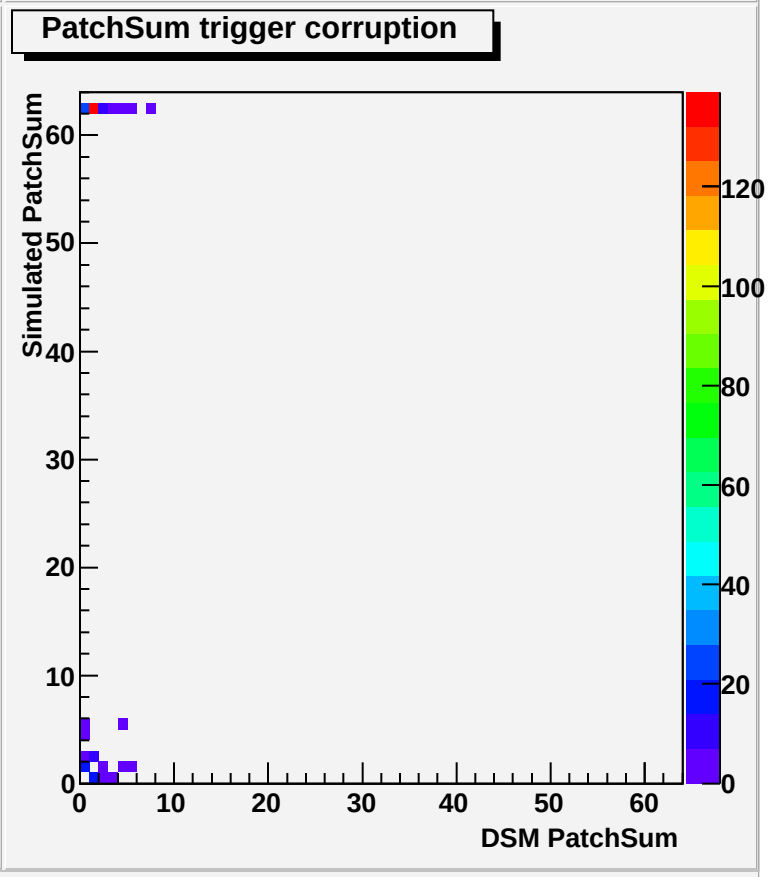

BEMC DSM L2 Input - PatchSum
PatchSum
250
200
150
100
50
0
0
1
2
3
4
5
JetPatch pair
0
1
2
3
4
5
6
7

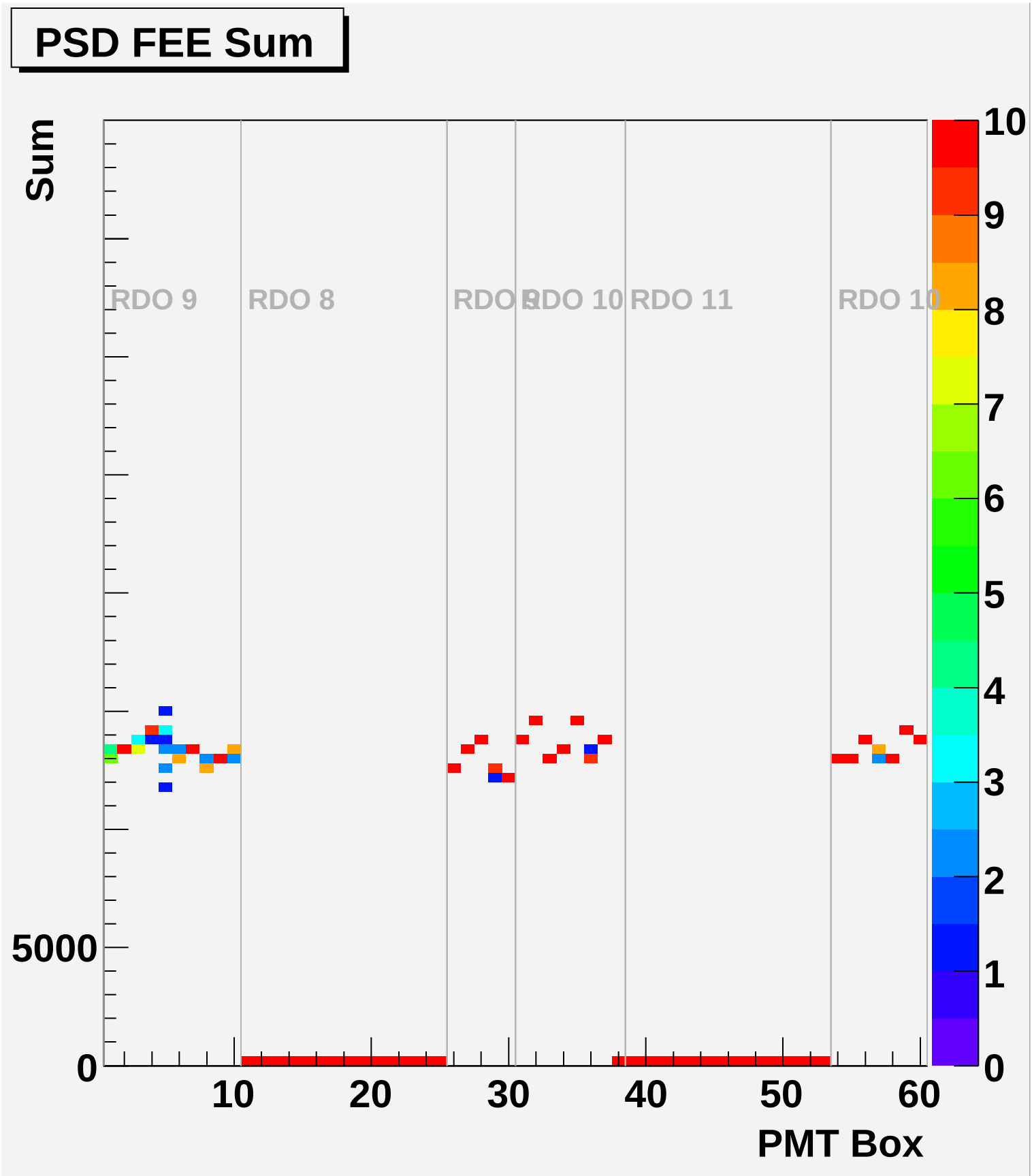

SMD FEE Sum
Sum
RDO 1
RDO 3
RDO 2
RDO 0
RDO 4
RDO 5
RDO 6
RDO 7
0
20
40
60
80
100
120
Module
0
1
2
3
4
5
6
7
8
9
10


HighTower trigger corruption
events
triggerPatch


HighTower trigger corruption
Simulated HighTower
DSM HighTower


PatchSum trigger corruption
events
triggerPatch


PatchSum trigger corruption
Simulated PatchSum
DSM PatchSum


PSD FEE Sum
Sum
5000
0
RDO 9
RDO 8
RDO 10
RDO 11
10
20
30
40
50
60
PMT Box
10
9
8
7
6
5
4
3
2
1
0