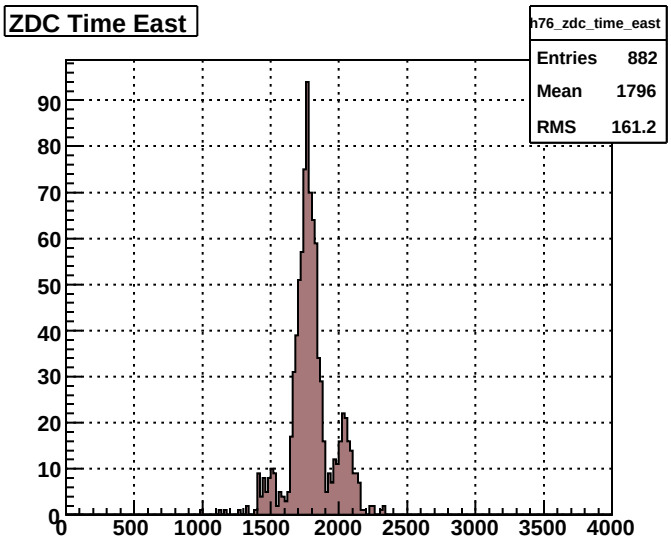
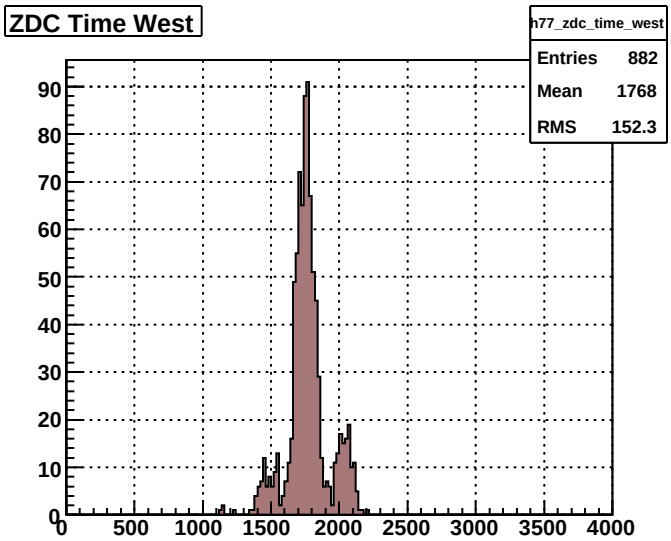
Run: #12147027

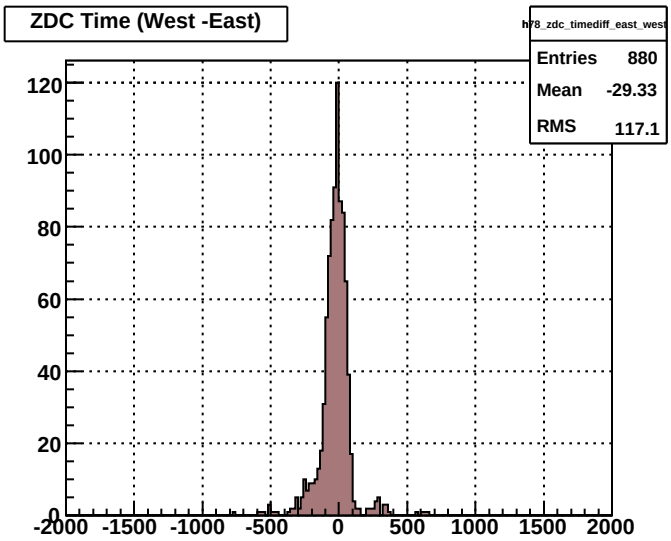
TriggerBits: 11111111 11111111 11111111 11111111

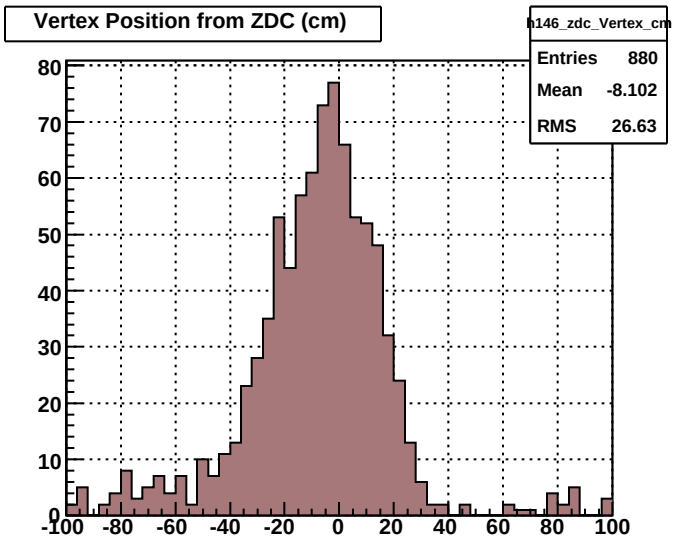
DetectorBits: 00000000 01010100 00000011 00101100

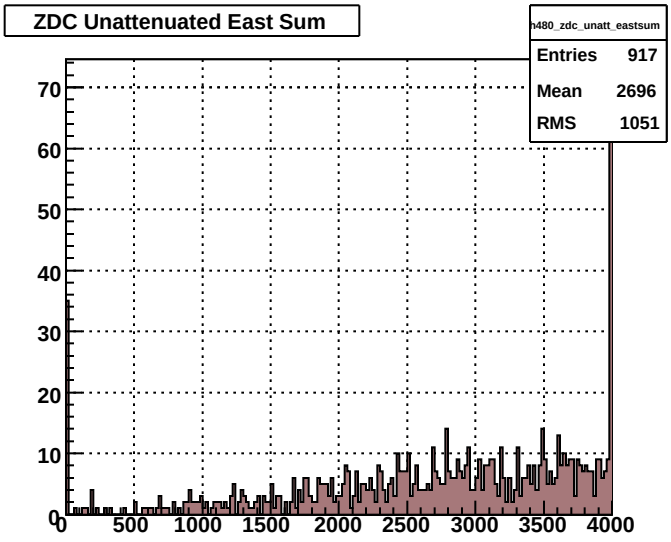
Fri May 27 10:16:38 2011

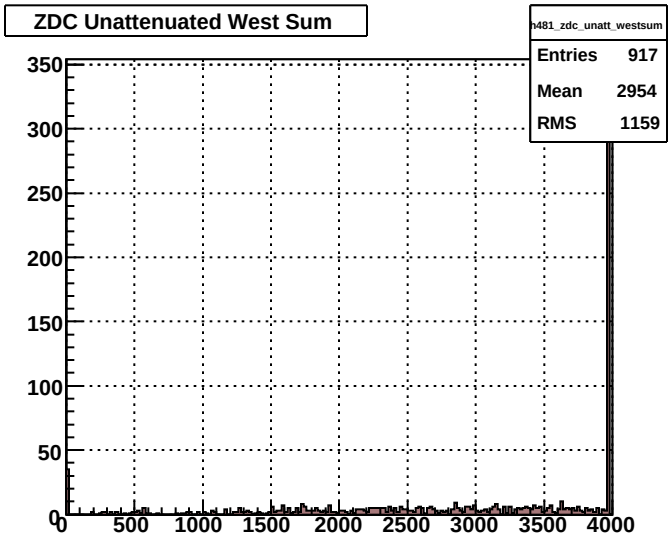

ZDC Time East
h76_zdc_time_east
Entries 882
Mean 1796
RMS 161.2

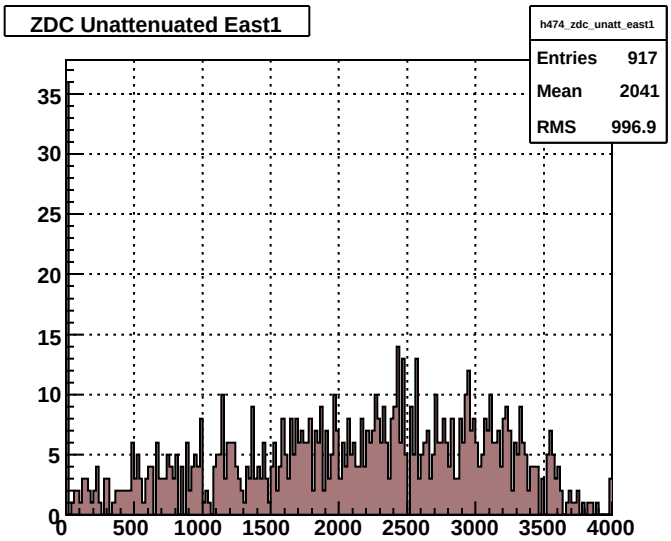

ZDC Time West
h77_zdc_time_west
Entries 882
Mean 1768
RMS 152.3

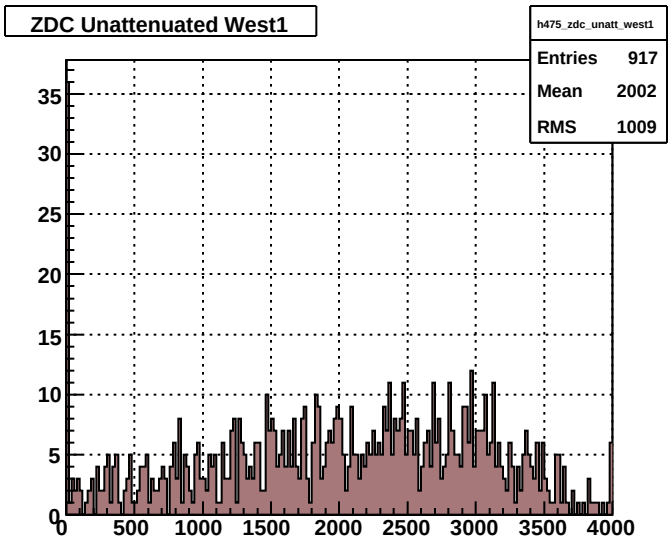

ZDC Time (West -East)
h78_zdc_timediff_east_west
Entries 880
Mean -29.33
RMS 117.1

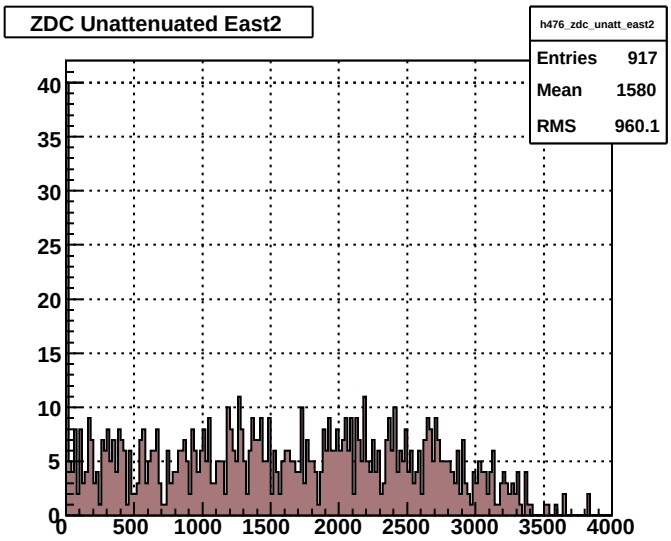

Vertex Position from ZDC (cm)
h146_zdc_Vertex_cm
Entries 880
Mean -8.102
RMS 26.63

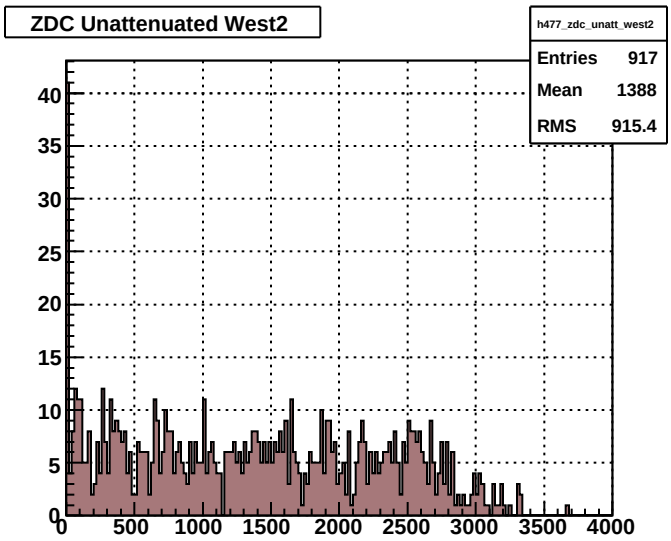

ZDC Unattenuated East Sum
h480_zdc_unatt_eastsum
Entries 917
Mean 2696
RMS 1051

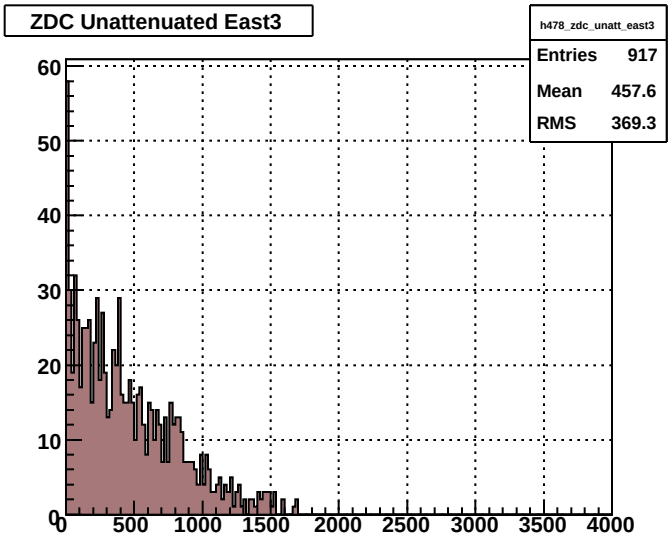

ZDC Unattenuated West Sum
h481_zdc_unatt_westsum
Entries 917
Mean 2954
RMS 1159

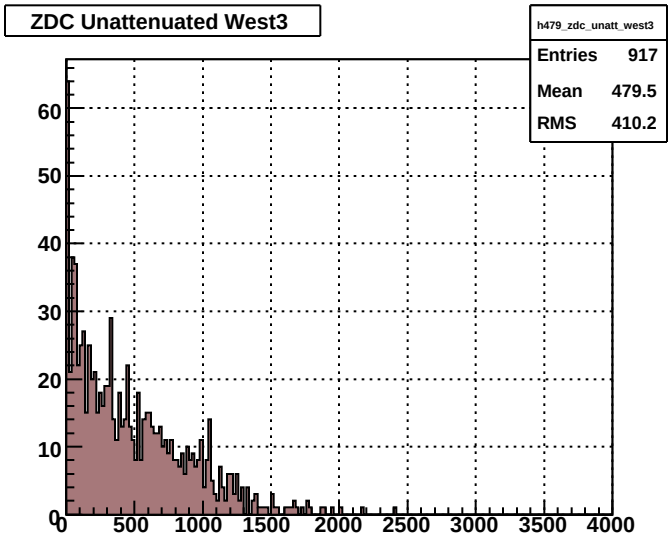

ZDC Unattenuated East1
h474_zdc_unatt_east1
Entries 917
Mean 2041
RMS 996.9

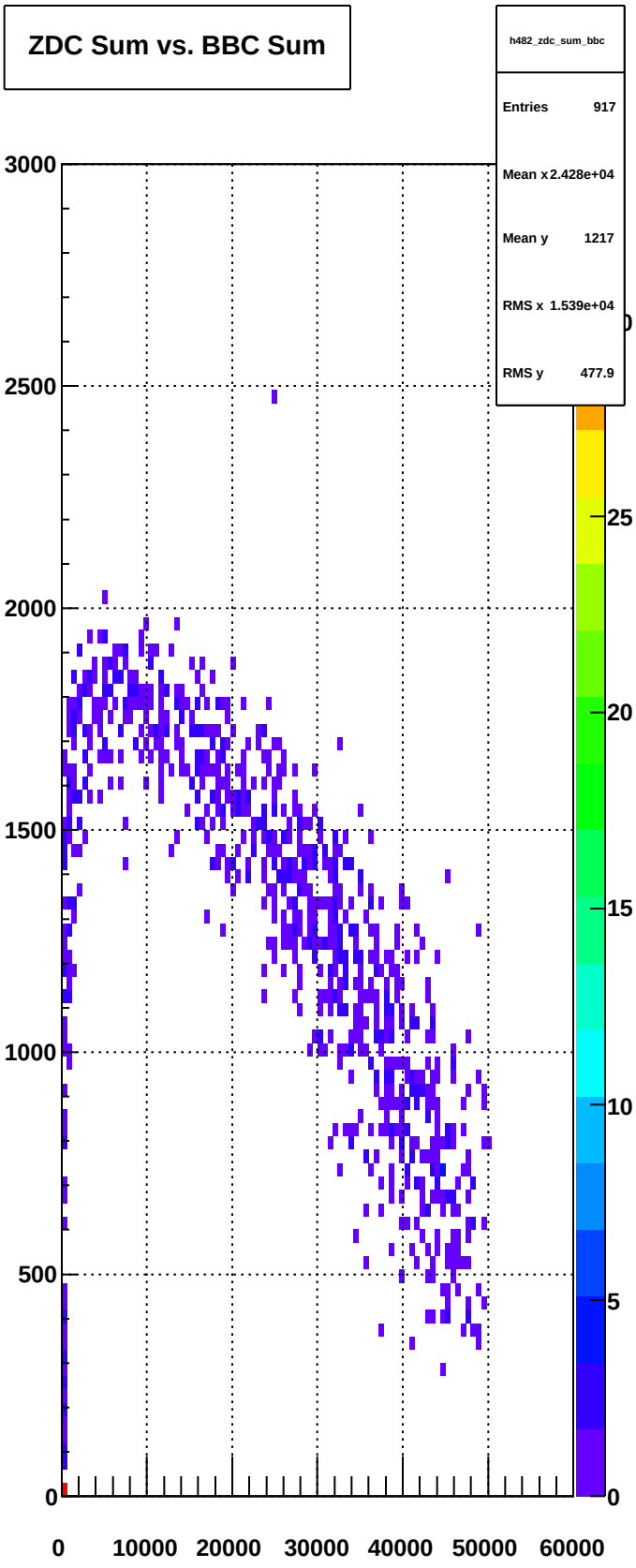

ZDC Unattenuated West1
h475_zdc_unatt_west1
Entries 917
Mean 2002
RMS 1009

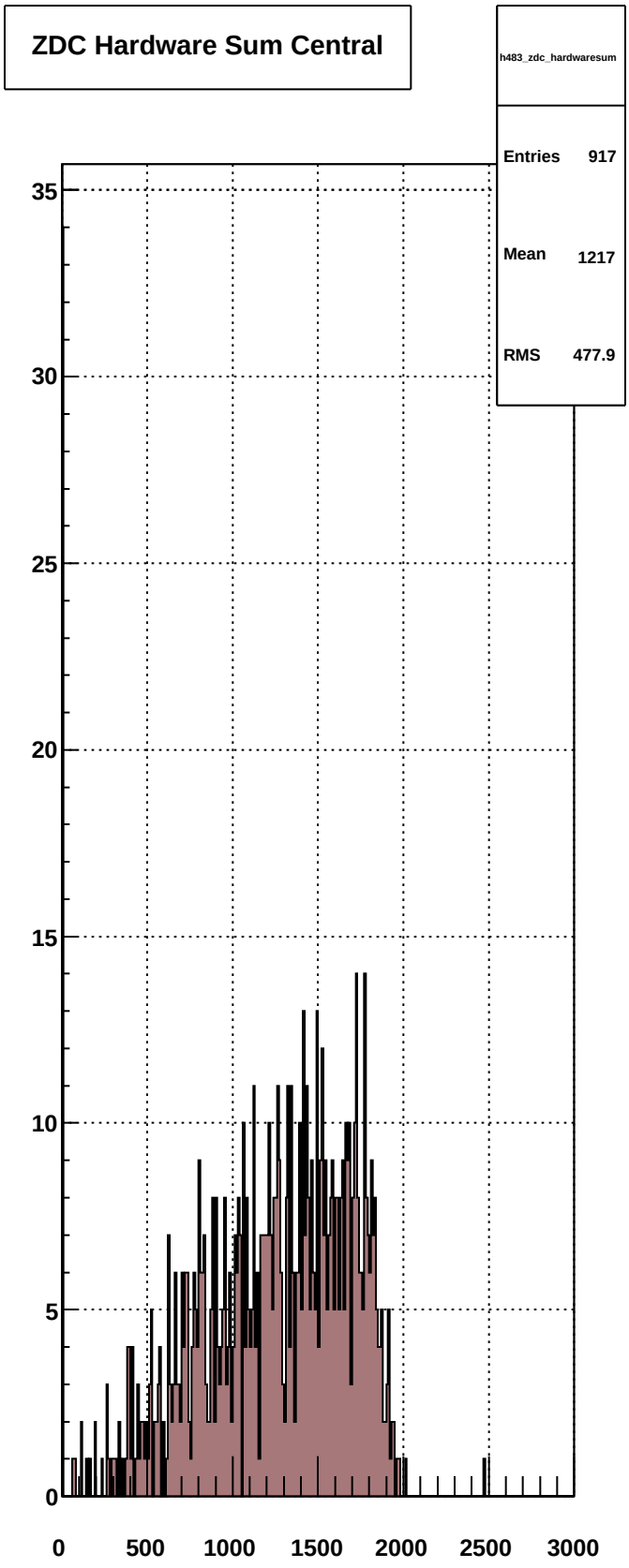

ZDC Unattenuated East2
h476_zdc_unatt_east2
Entries 917
Mean 1580
RMS 960.1

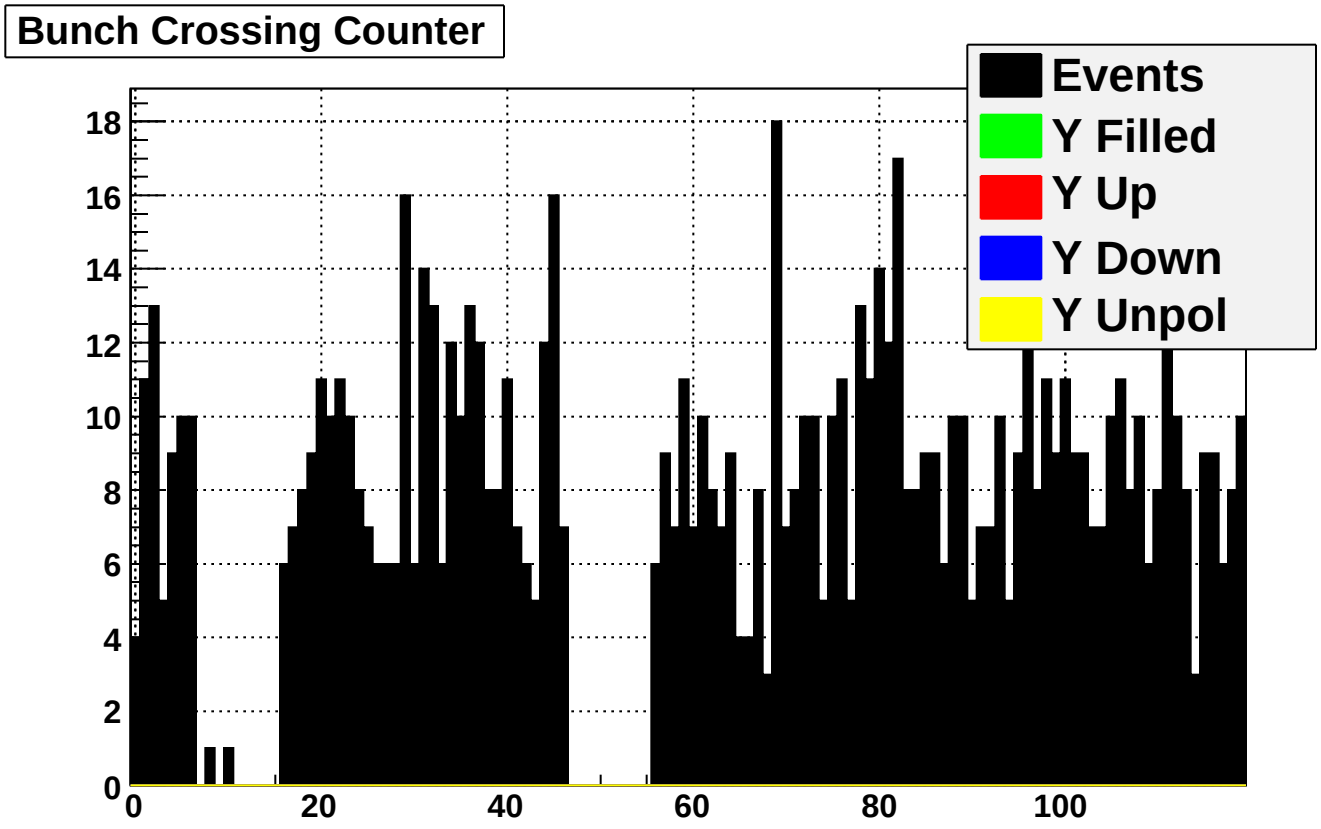

ZDC Unattenuated West2
h477_zdc_unatt_west2
Entries 917
Mean 1388
RMS 915.4

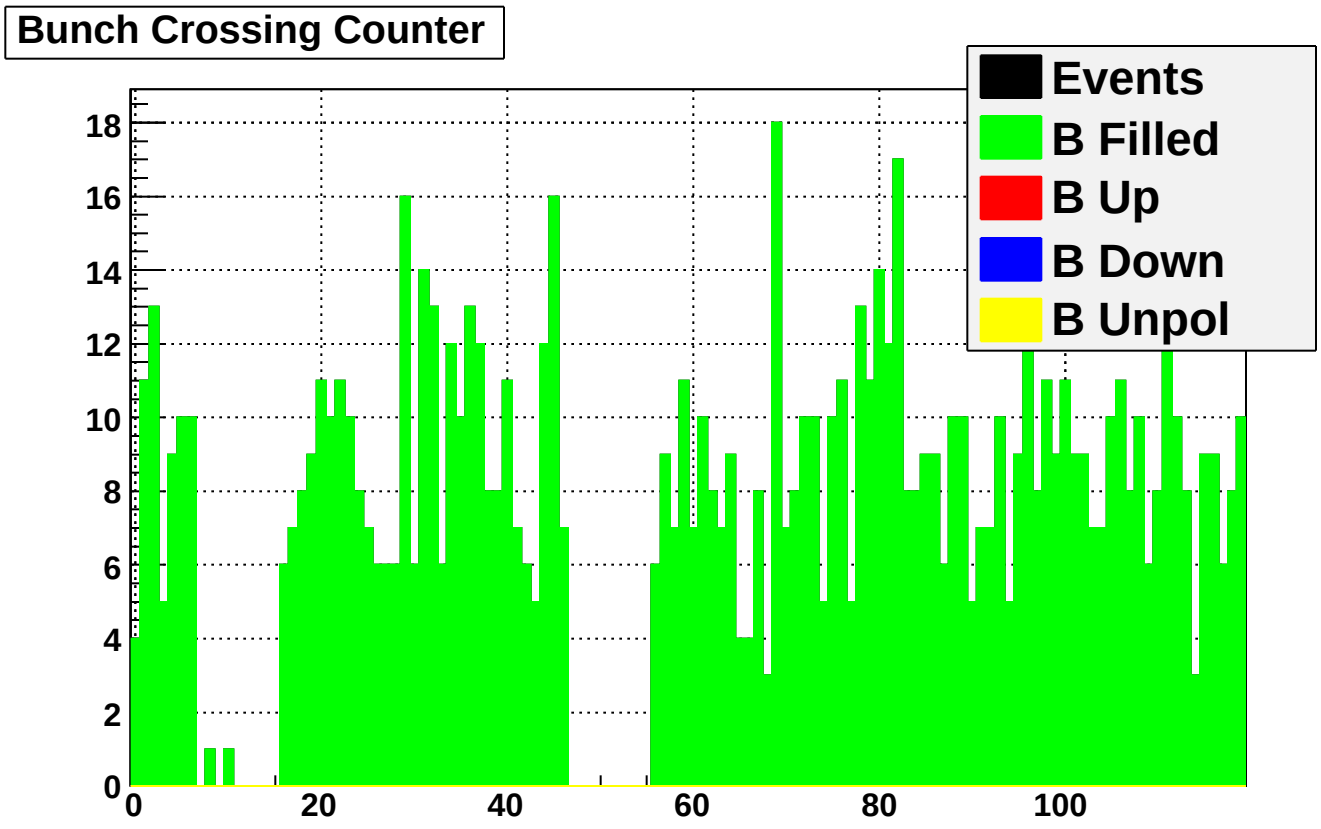

ZDC Unattenuated East3
h478_zdc_unatt_east3
Entries 917
Mean 457.6
RMS 369.3

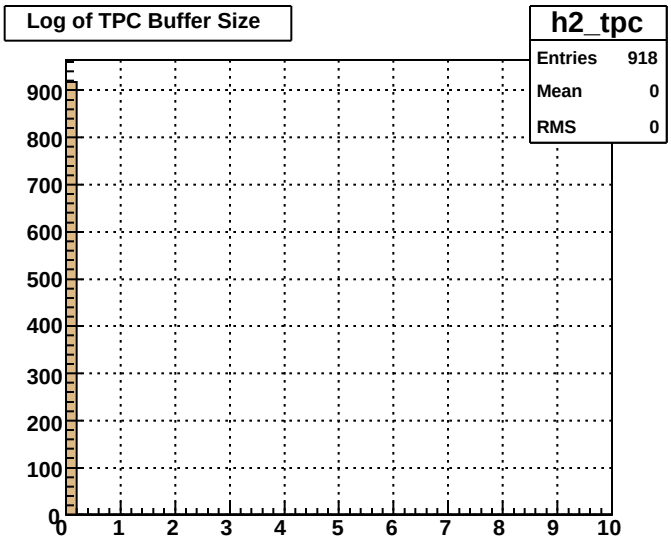

ZDC Unattenuated West3
h479_zdc_unatt_west3
Entries 917
Mean 479.5
RMS 410.2

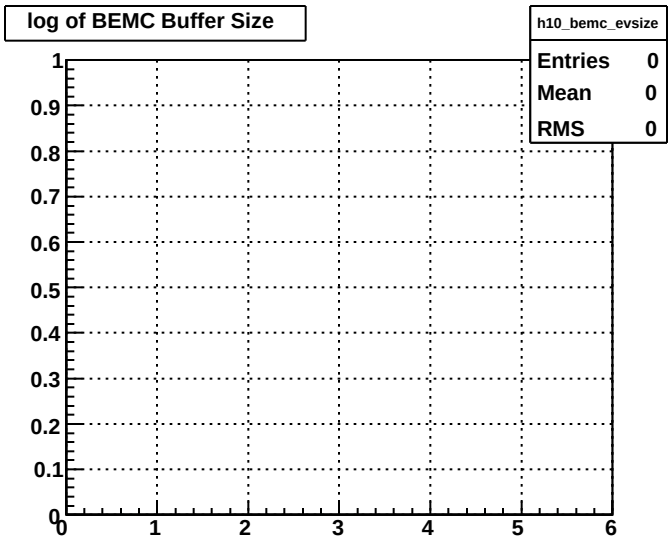

ZDC Sum vs. BBC Sum
h482_zdc_sum_bbc
Entries 917
Mean x 2.428e+04
Mean y 1217
RMS x 1.539e+04
RMS y 477.9
3000
2500
2000
1500
1000
500
0
0
10000
20000
30000
40000
50000
60000
25
20
15
10
5
0

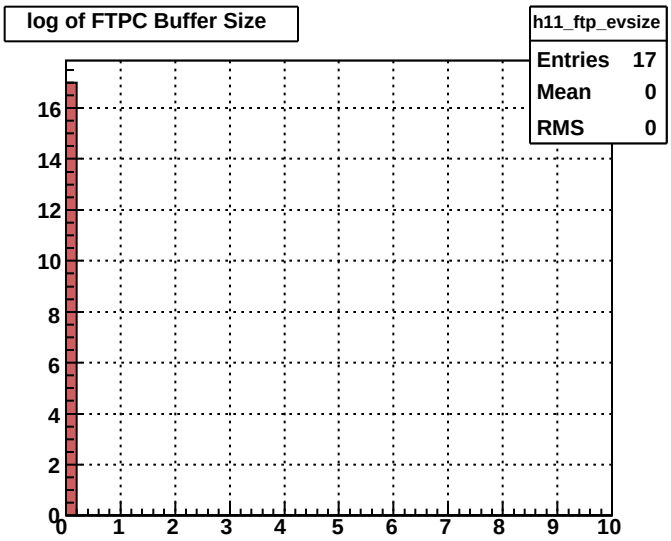

ZDC Hardware Sum Central
h483_zdc_hardwaresum
Entries 917
Mean 1217
RMS 477.9
35
30
25
20
15
10
5
0
0
500
1000
1500
2000
2500
3000

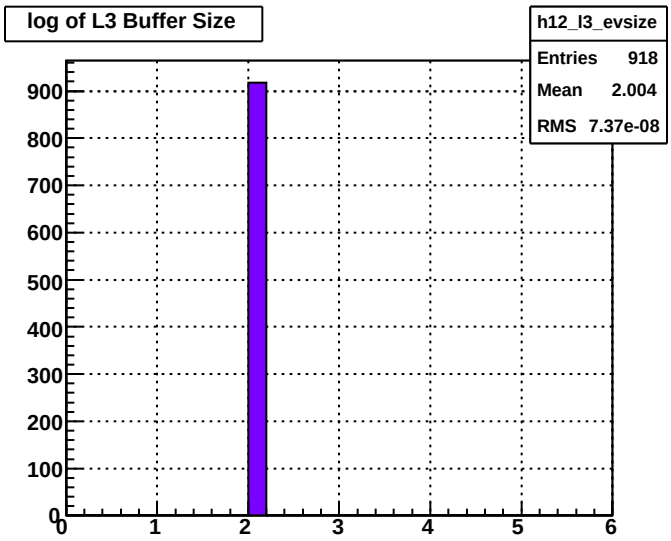

Bunch Crossing Counter
Events
Y Filled
Y Up
Y Down
Y Unpol
0
2
4
6
8
10
12
14
16
18
0
20
40
60
80
100

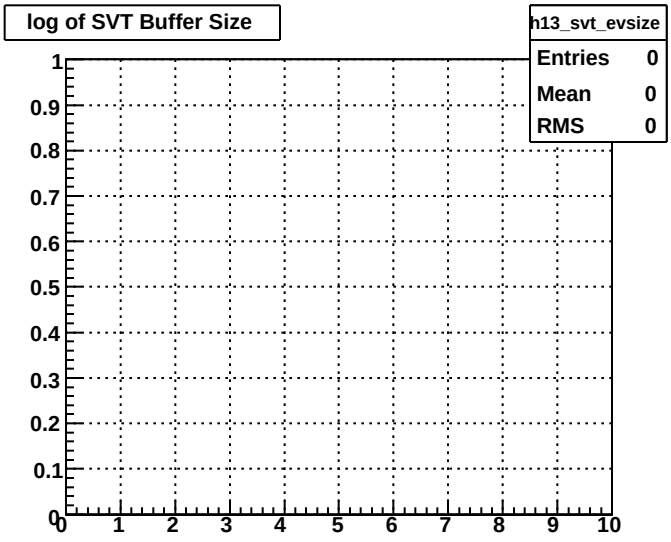

Bunch Crossing Counter
Events
B Filled
B Up
B Down
B Unpol
0
2
4
6
8
10
12
14
16
18
0
20
40
60
80
100

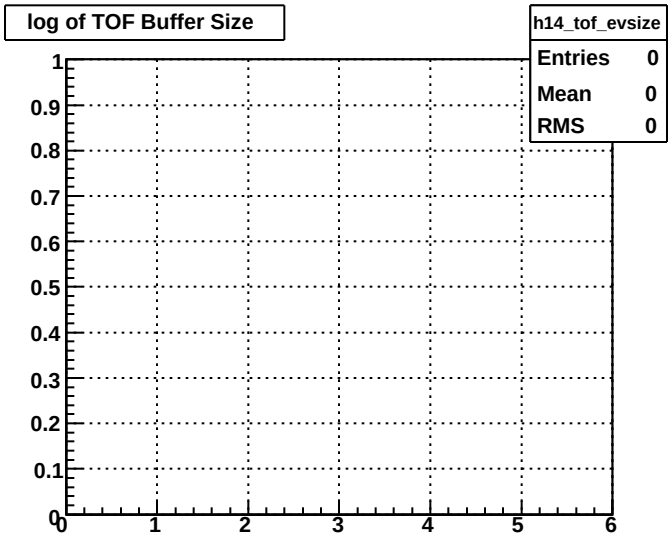

Log of TPC Buffer Size
h2_tpc
Entries 918
Mean 0
RMS 0

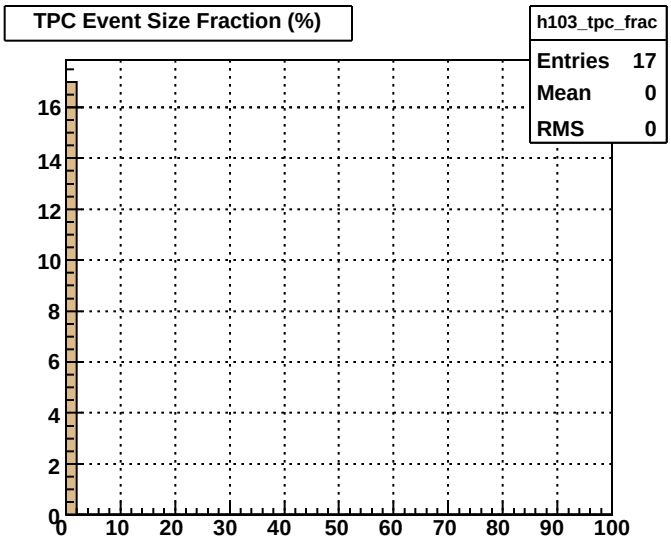

log of BEMC Buffer Size
h10_bemc_evsize
Entries 0
Mean 0
RMS 0

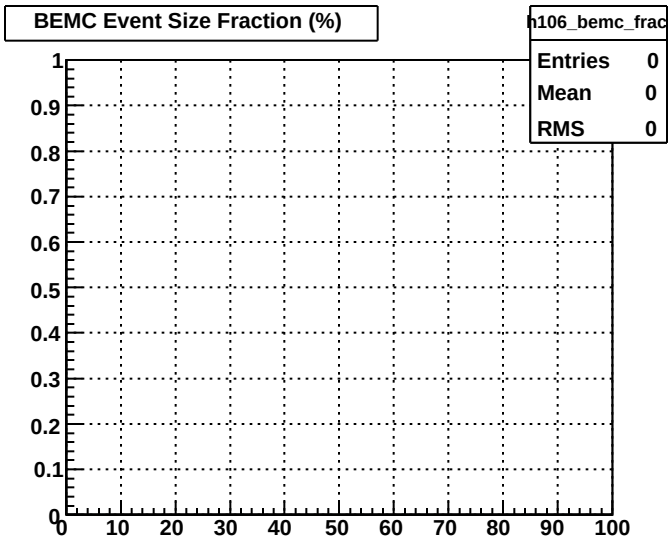

log of FTPC Buffer Size
h11_ftp_evsize
Entries 17
Mean 0
RMS 0

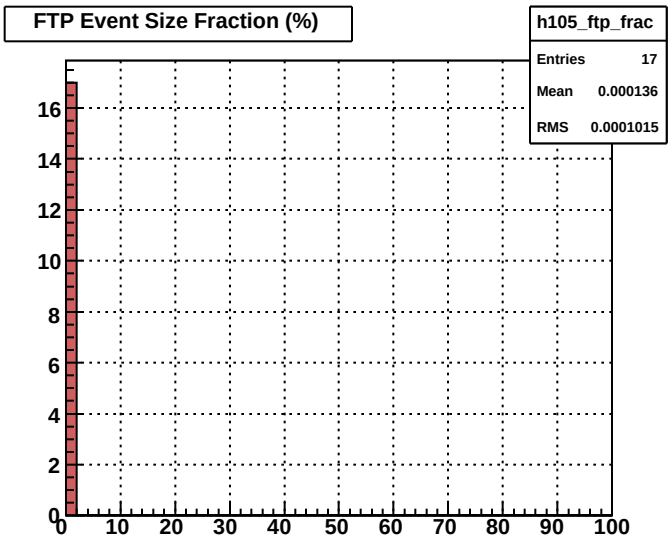

log of L3 Buffer Size
h12_l3_evsize
Entries 918
Mean 2.004
RMS 7.37e-08

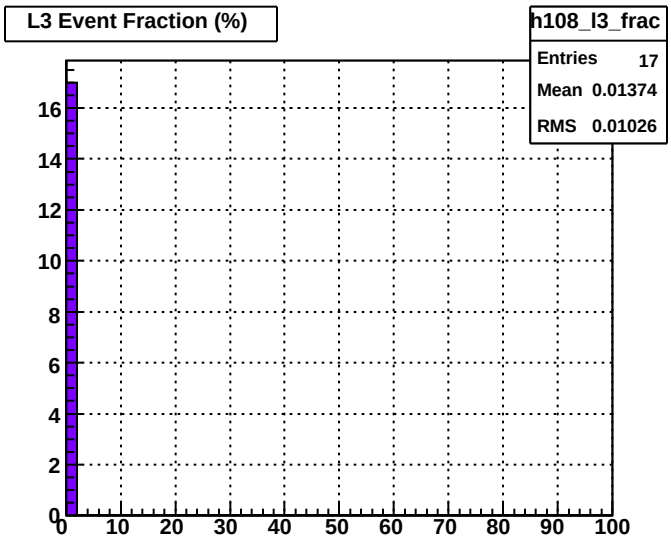

log of SVT Buffer Size
h13_svt_evsize
Entries 0
Mean 0
RMS 0

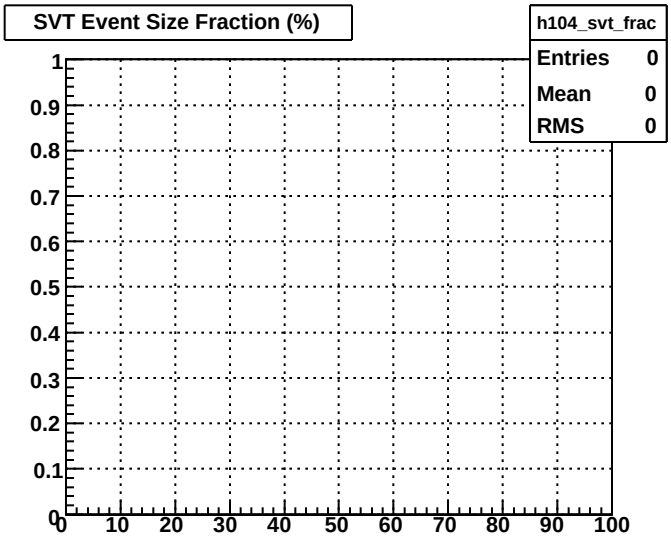

log of TOF Buffer Size
h14_tof_evsize
Entries 0
Mean 0
RMS 0

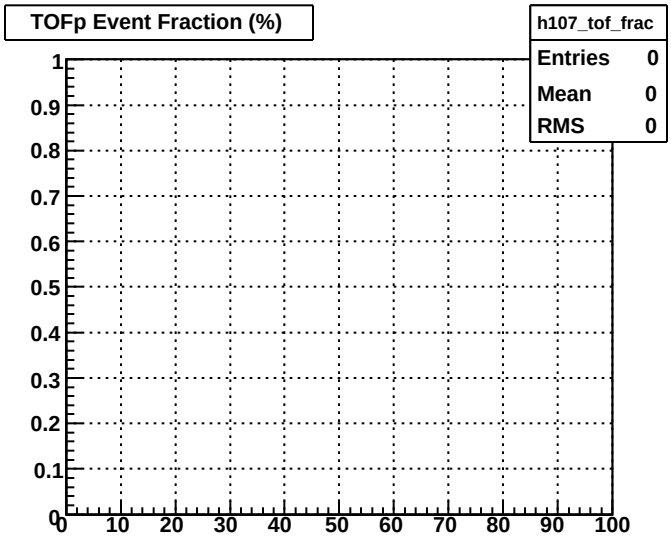

TPC Event Size Fraction (%)
h103_tpc_frac
Entries 17
Mean 0
RMS 0


BEMC Event Size Fraction (%)
h106_bemc_frac
Entries 0
Mean 0
RMS 0

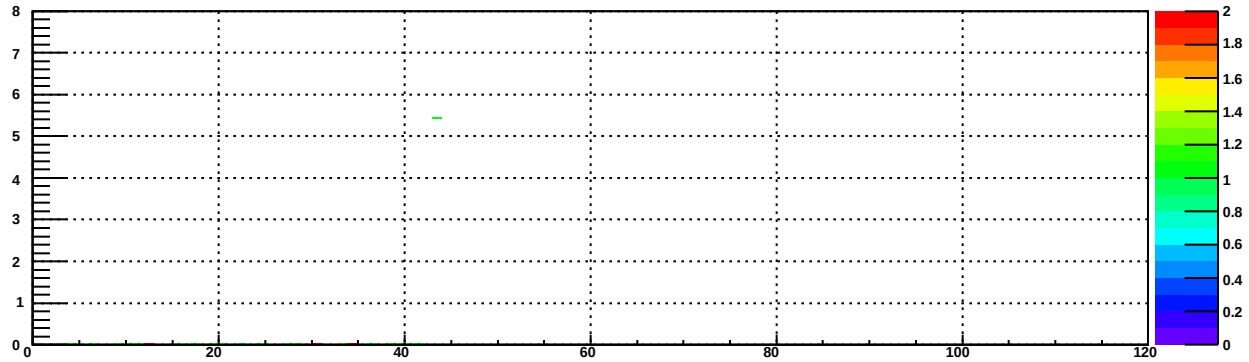

FTP Event Size Fraction (%)
h105_ftp_frac
Entries 17
Mean 0.000136
RMS 0.0001015


L3 Event Fraction (%)
h108_l3_frac
Entries 17
Mean 0.01374
RMS 0.01026

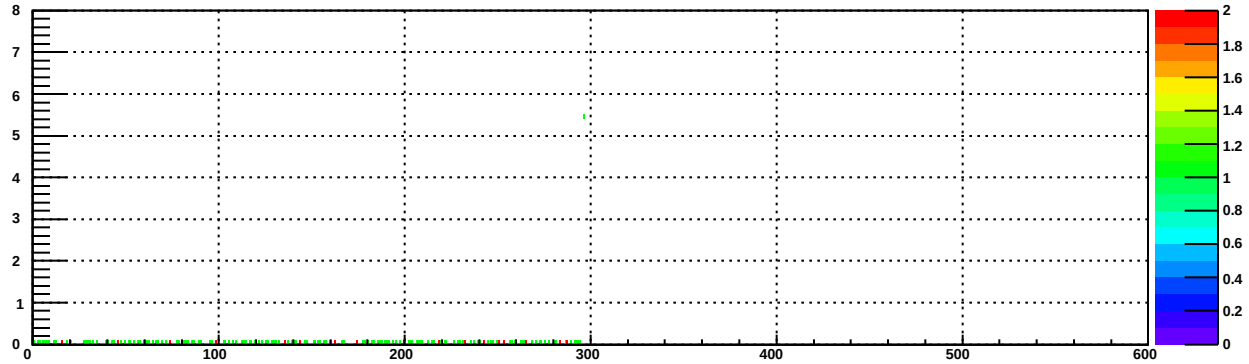

SVT Event Size Fraction (%)
h104_svt_frac
Entries 0
Mean 0
RMS 0


TOFp Event Fraction (%)
h107_tof_frac
Entries 0
Mean 0
RMS 0

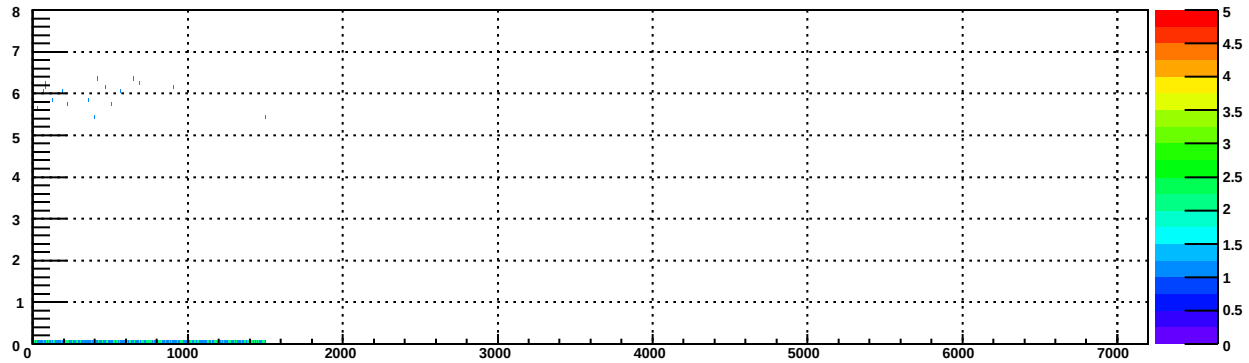
Event Size (Log10) vs time (sec)



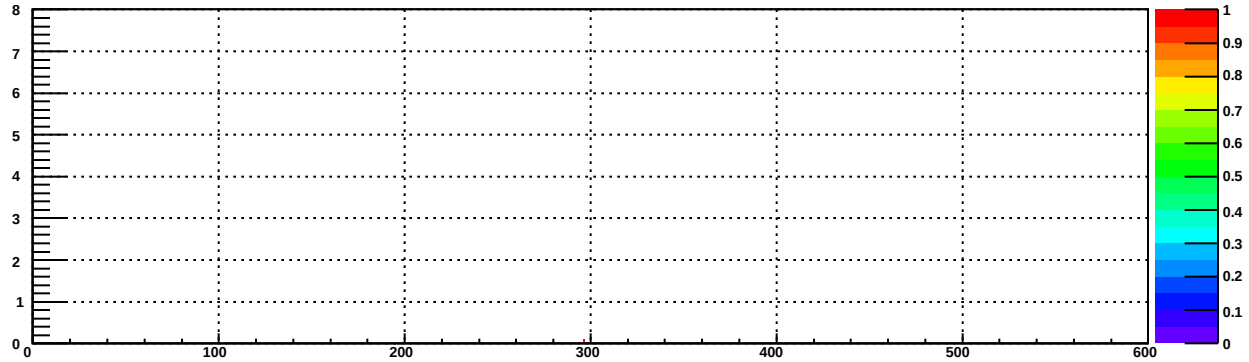
Event Size (Log10) vs time (sec)

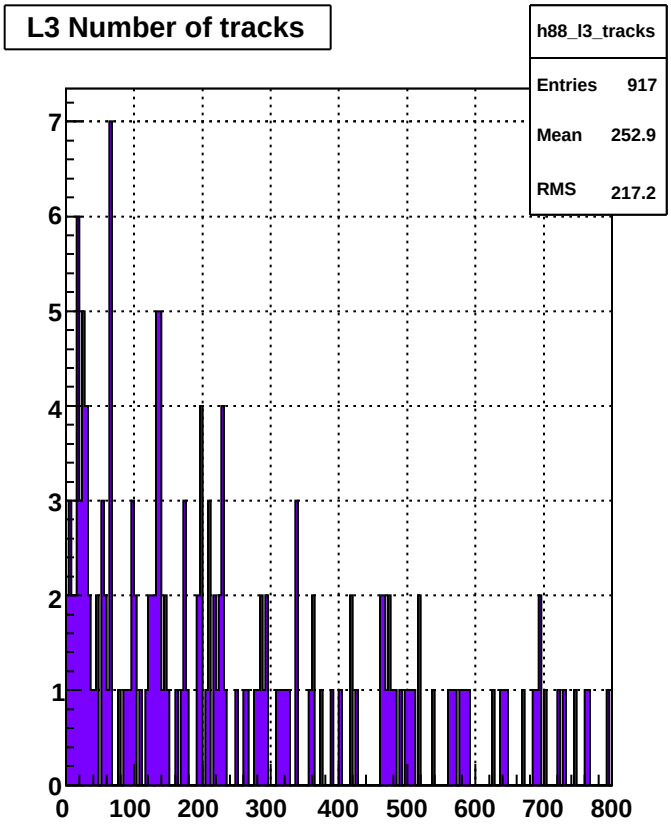


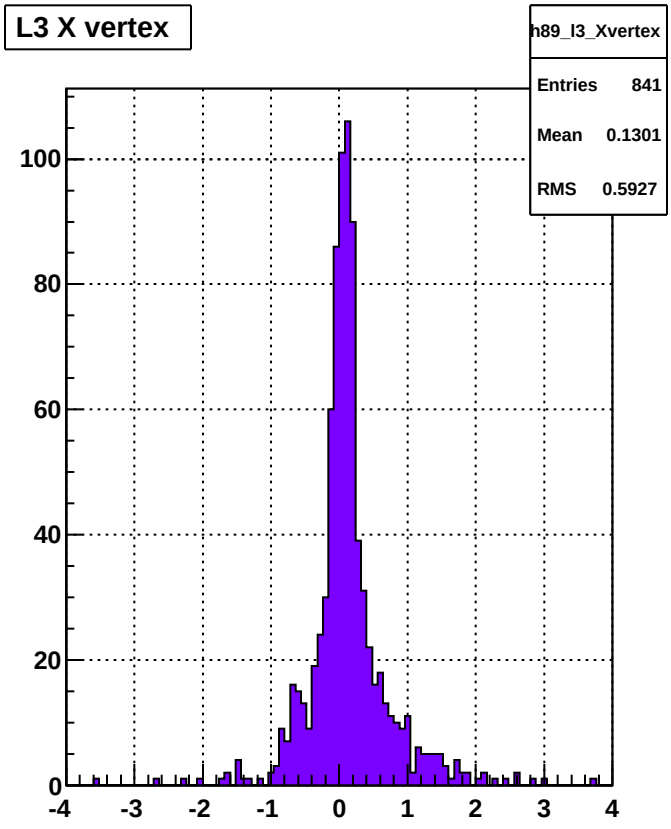
Event Size (Log10) vs time(sec)

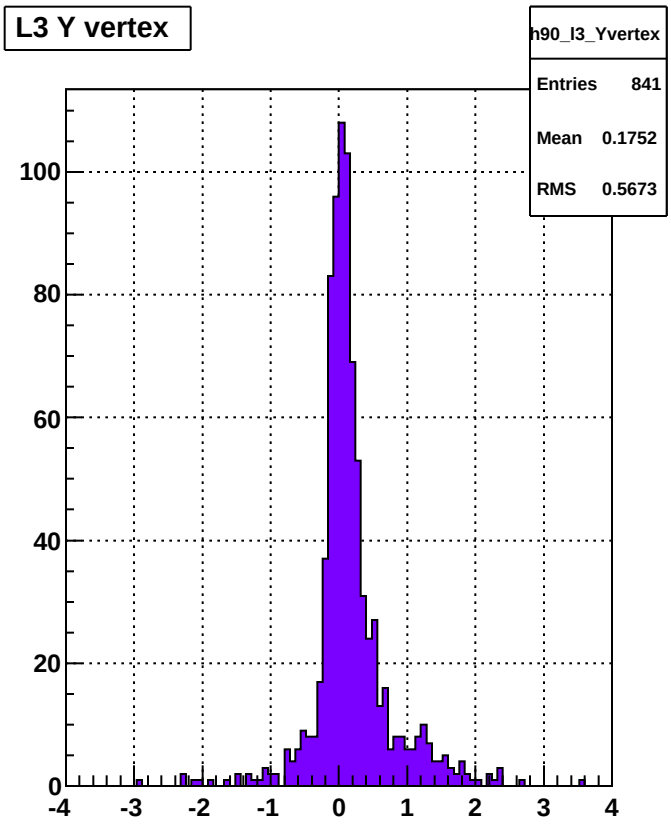


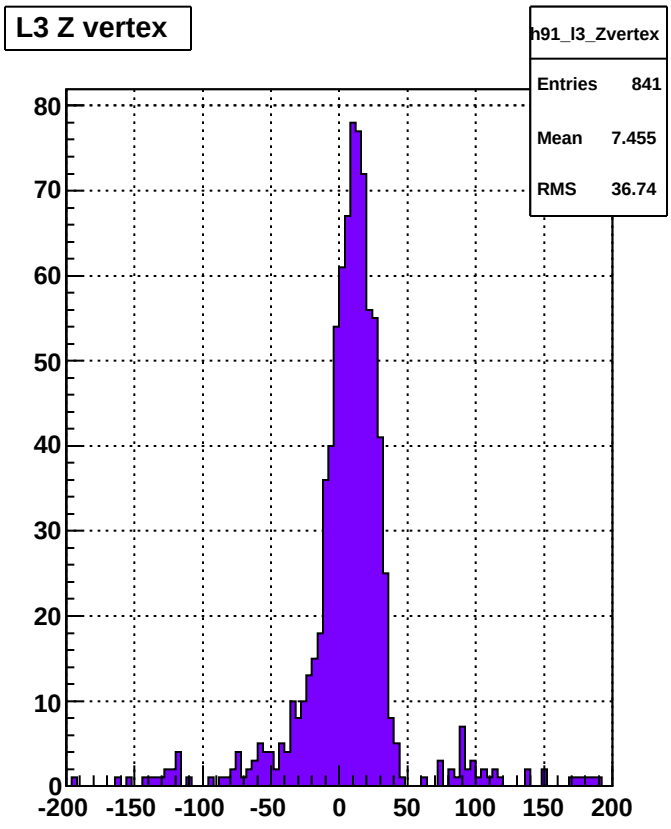
FTPC Event Size vs time(sec)



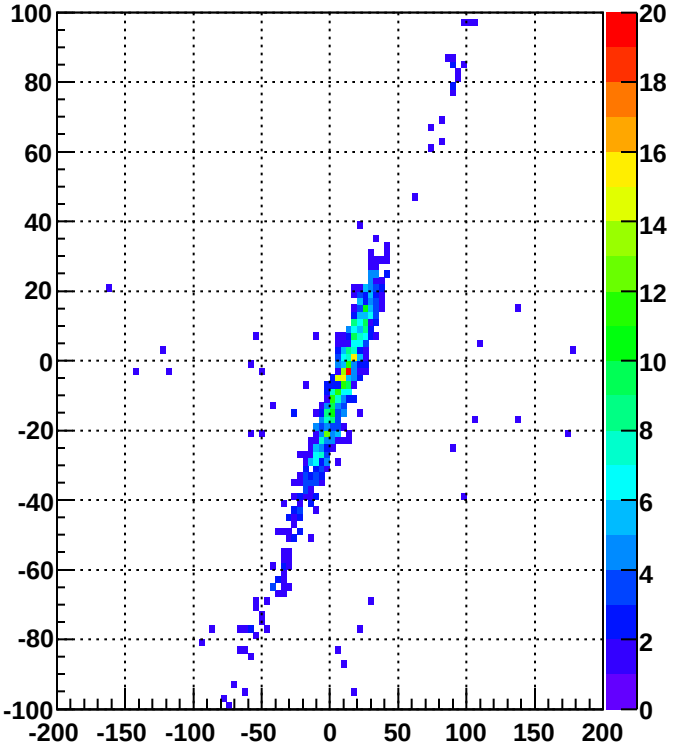

L3 Number of tracks
h88_l3_tracks
Entries 917
Mean 252.9
RMS 217.2


L3 X vertex
h89_l3_Xvertex
Entries 841
Mean 0.1301
RMS 0.5927

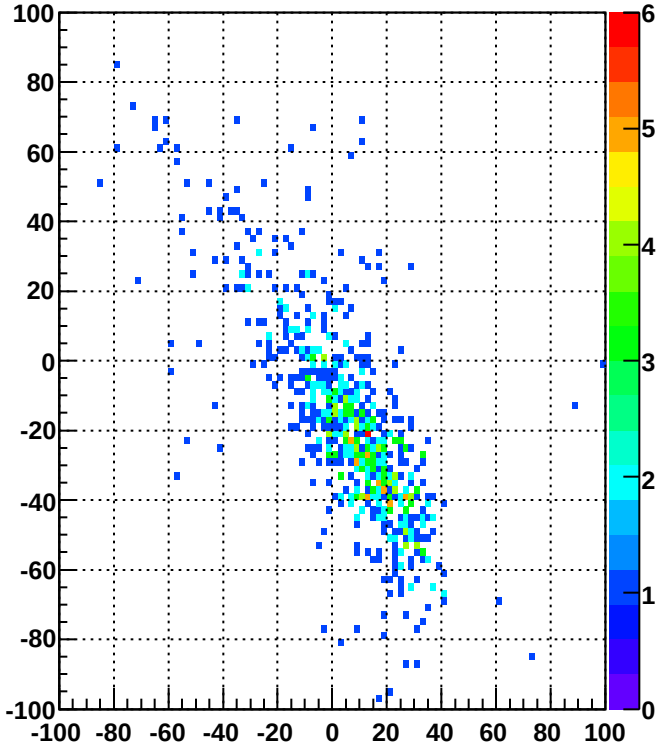

L3 Y vertex
h90_l3_Yvertex
Entries 841
Mean 0.1752
RMS 0.5673


L3 Z vertex
h91_l3_Zvertex
Entries 841
Mean 7.455
RMS 36.74

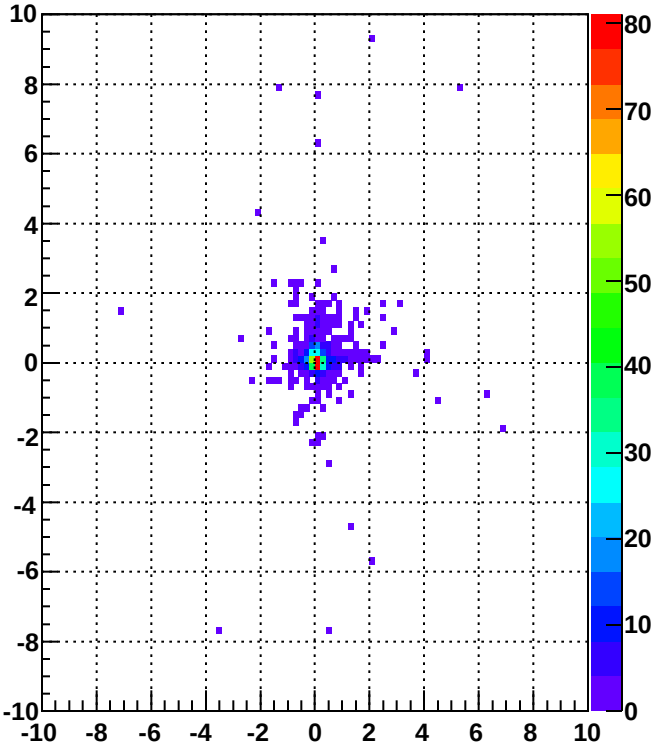
ZDC Vertex vs L3 Vertex



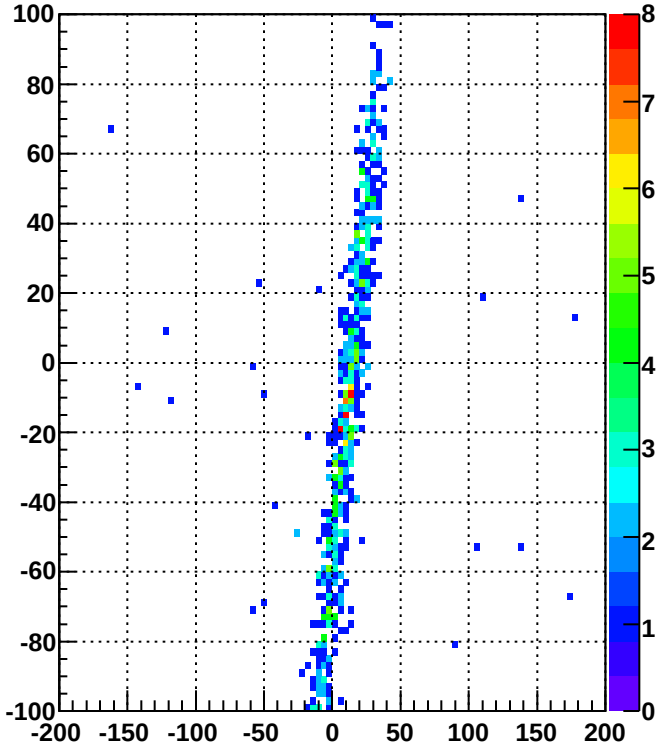
BBC Vertex vs L3 Vertex

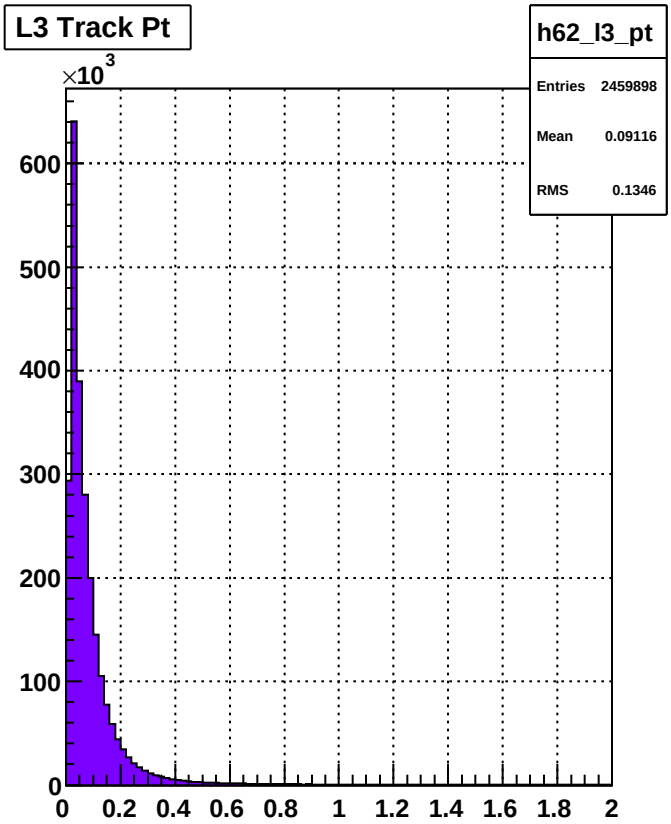


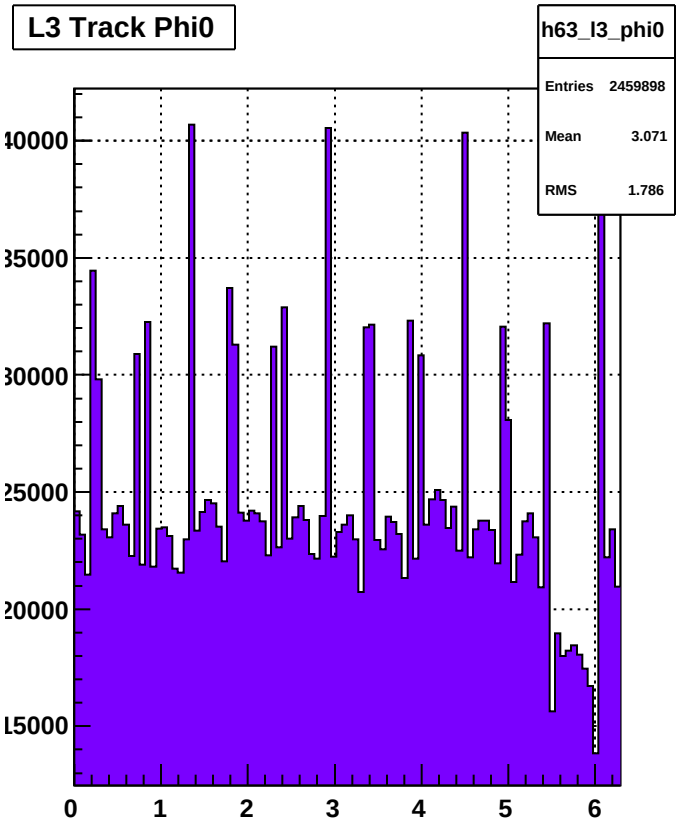
L3 Vertex Y vs X

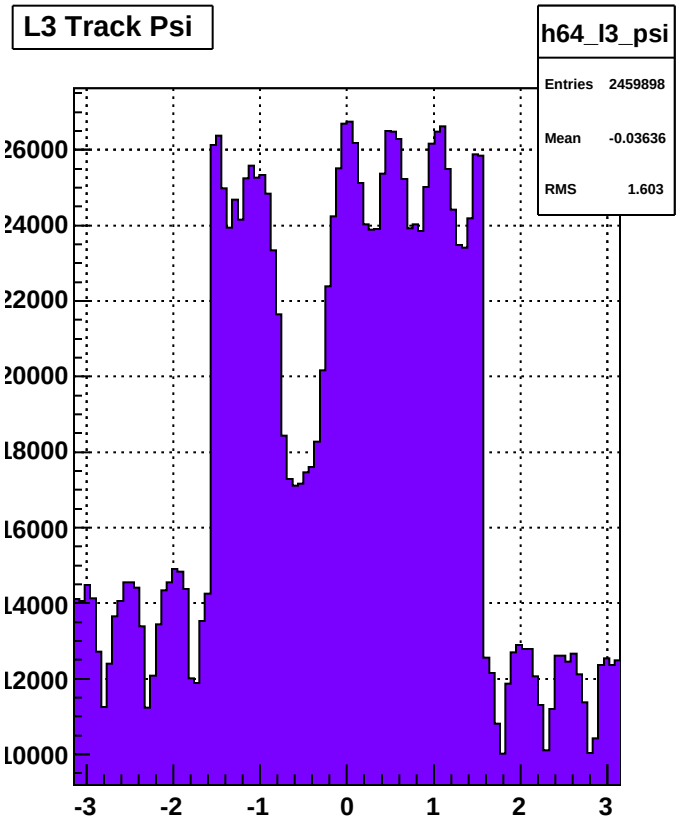


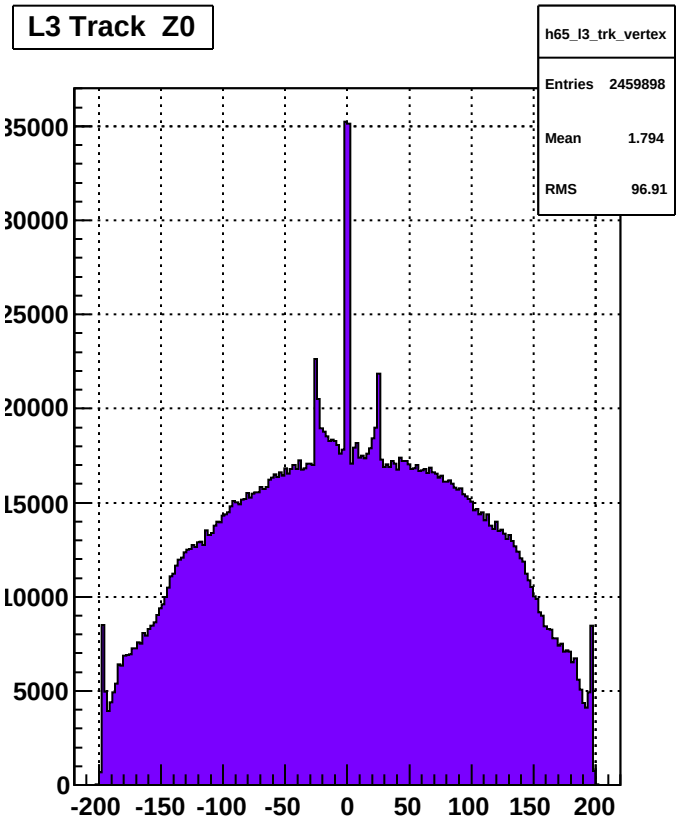
L3 Vertex-Z vs ZDC Timing difference

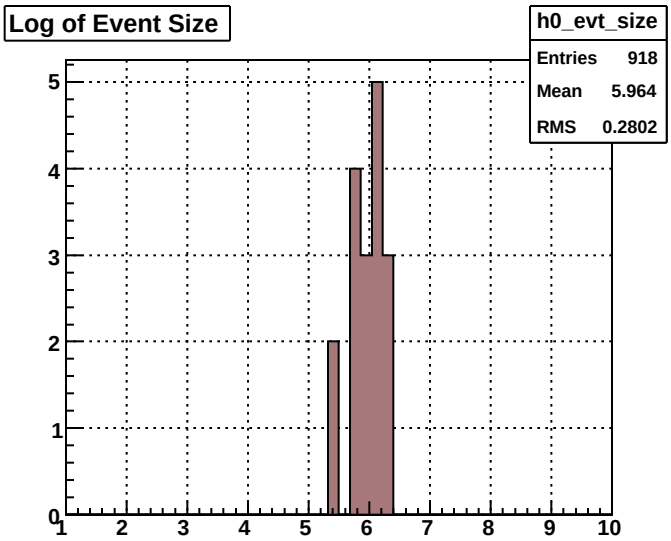


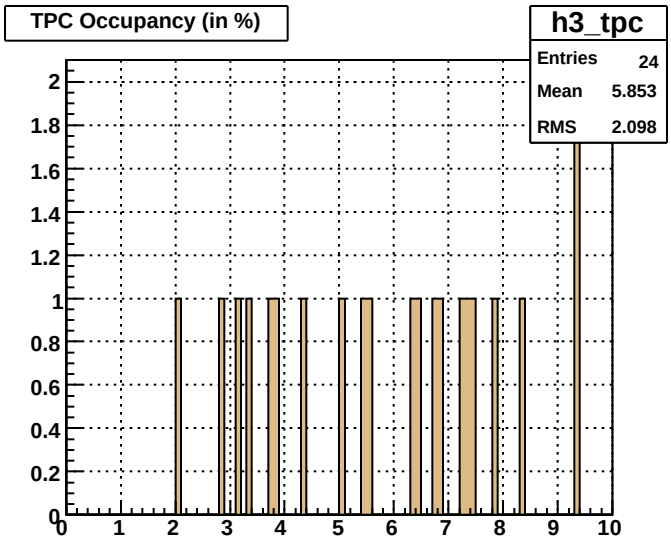

L3 Track Pt
h62_l3_pt
Entries 2459898
Mean 0.09116
RMS 0.1346
×10^3
600
500
400
300
200
100
0
0 0.2 0.4 0.6 0.8 1 1.2 1.4 1.6 1.8 2

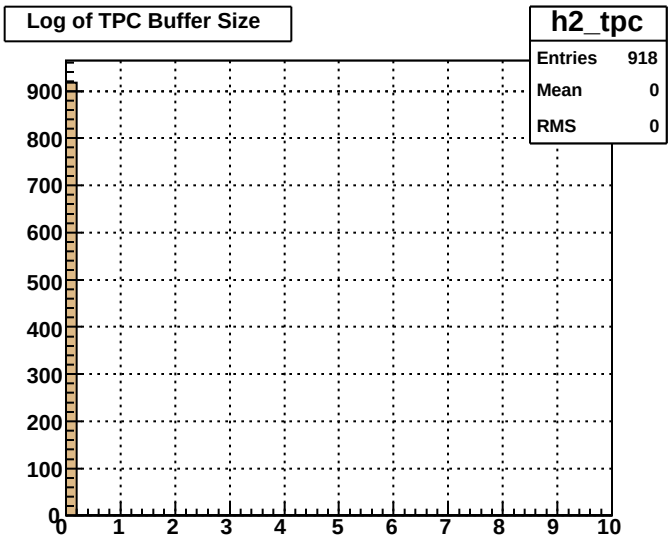

L3 Track Phi0
h63_l3_phi0
Entries 2459898
Mean 3.071
RMS 1.786
0 1 2 3 4 5 6

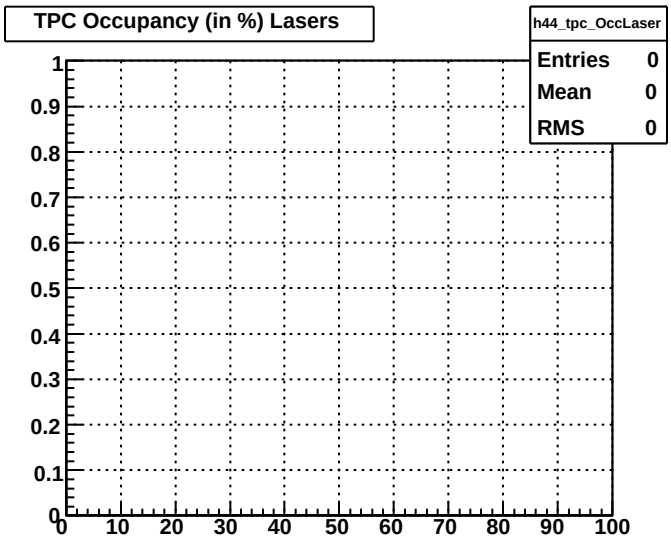

L3 Track Psi
h64_l3_psi
Entries 2459898
Mean -0.03636
RMS 1.603
-3 -2 -1 0 1 2 3

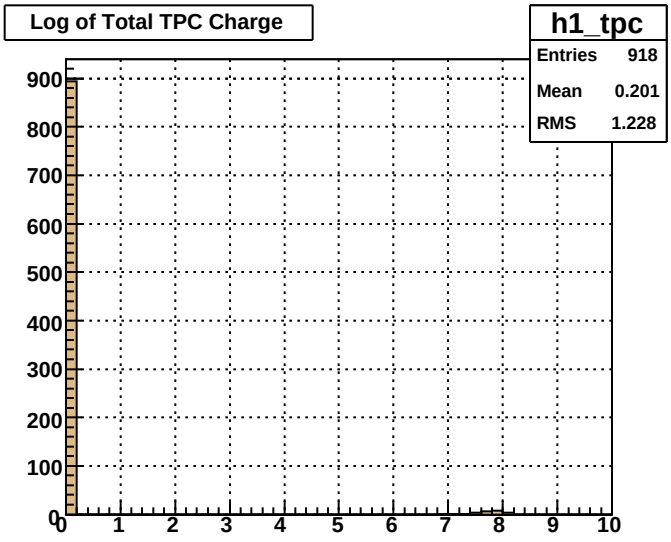

L3 Track Z0
h65_l3_trk_vertex
Entries 2459898
Mean 1.794
RMS 96.91
5000
0
-200 -150 -100 -50 0 50 100 150 200

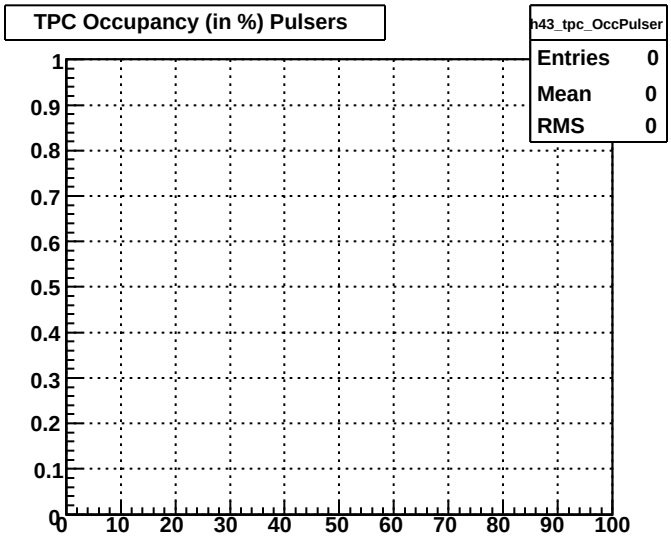

Log of Event Size
h0_evt_size
Entries 918
Mean 5.964
RMS 0.2802


TPC Occupancy (in %)
h3_tpc
Entries 24
Mean 5.853
RMS 2.098

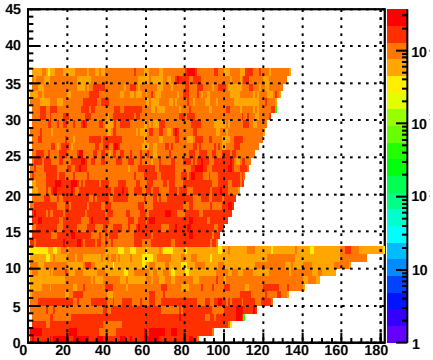

Log of TPC Buffer Size
h2_tpc
Entries 918
Mean 0
RMS 0


TPC Occupancy (in %) Lasers
h44_tpc_OccLaser
Entries 0
Mean 0
RMS 0

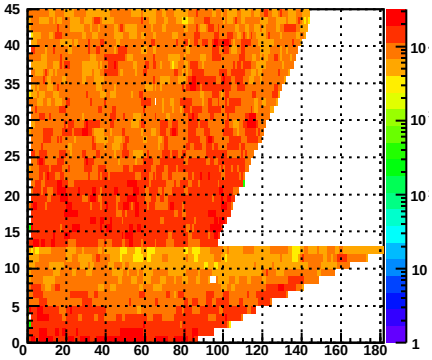

Log of Total TPC Charge
h1_tpc
Entries 918
Mean 0.201
RMS 1.228


TPC Occupancy (in %) Pulsers
h43_tpc_OccPulser
Entries 0
Mean 0
RMS 0

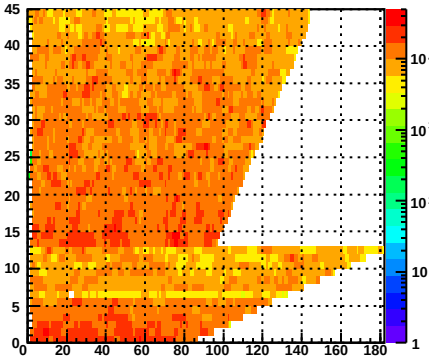
Sec. 1 charge per pad



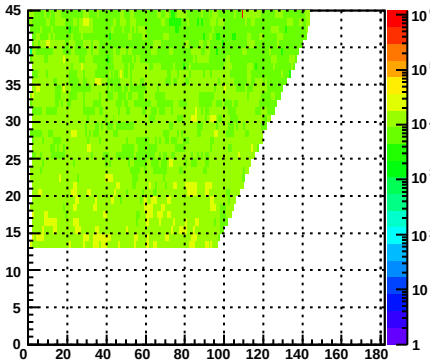
Sec. 2 charge per pad



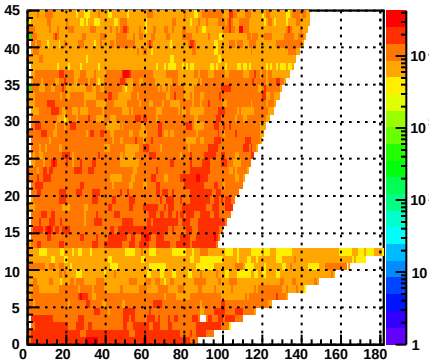
Sec. 3 charge per pad



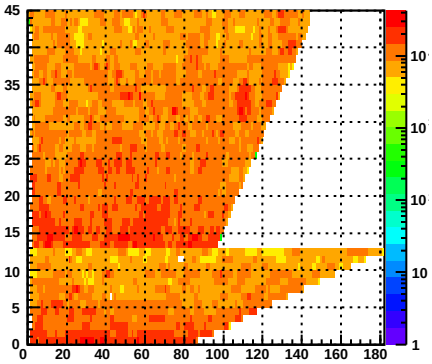
Sec. 4 charge per pad



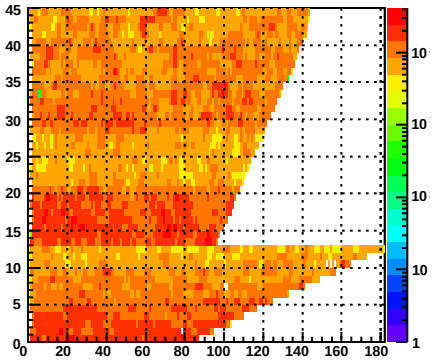
Sec. 5 charge per pad



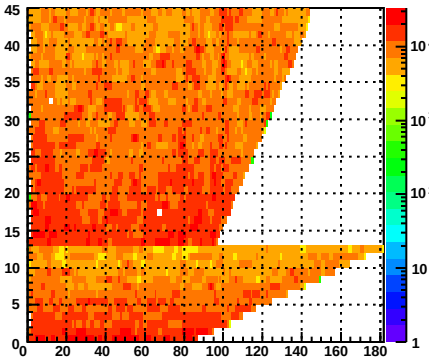
Sec. 6 charge per pad



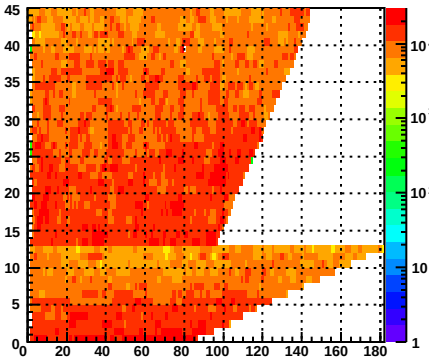
Sec. 7 charge per pad



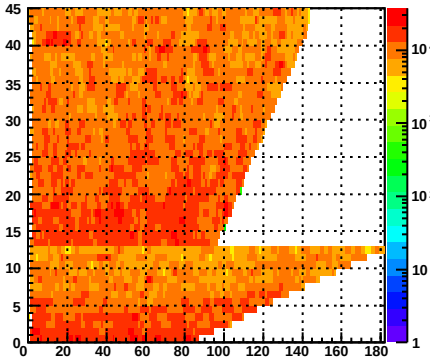
Sec. 8 charge per pad



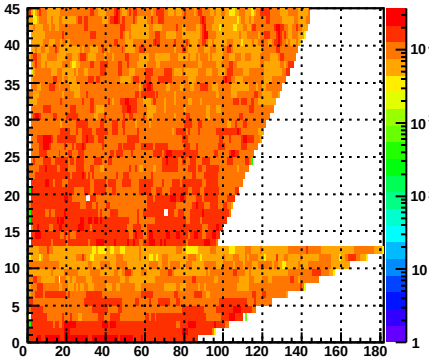
Sec. 9 charge per pad



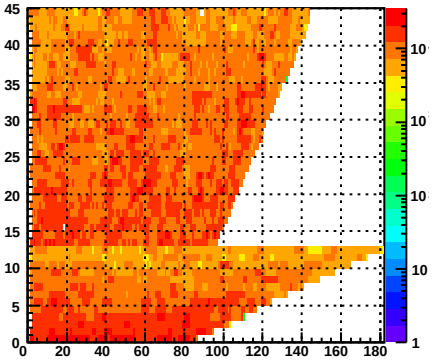
Sec. 10 charge per pad



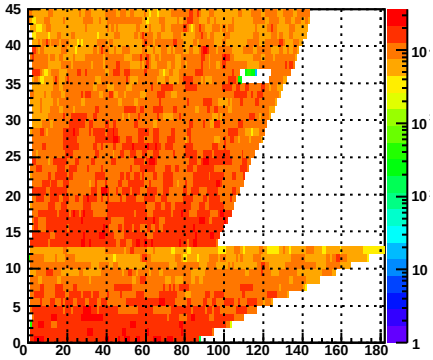
Sec. 11 charge per pad



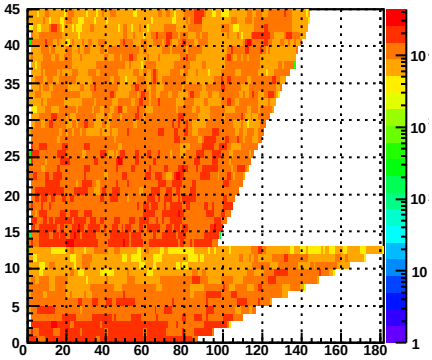
Sec. 12 charge per pad



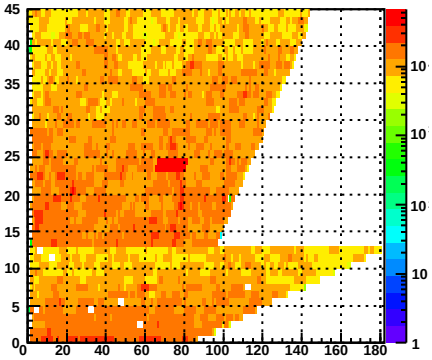
Sec. 13 charge per pad



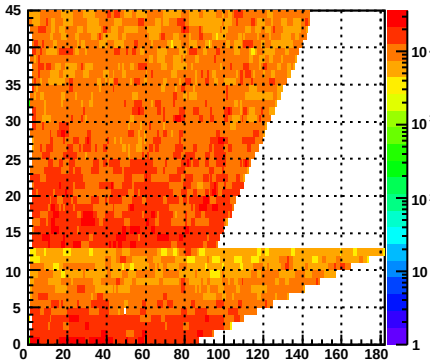
Sec. 14 charge per pad



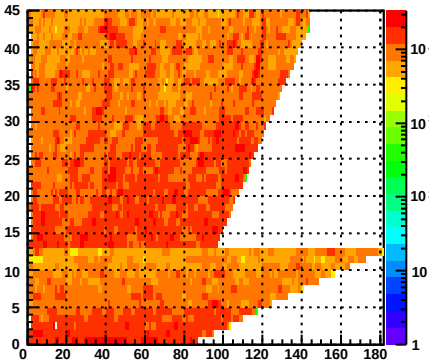
Sec. 15 charge per pad



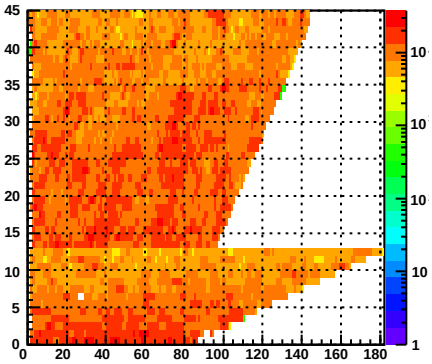
Sec. 16 charge per pad



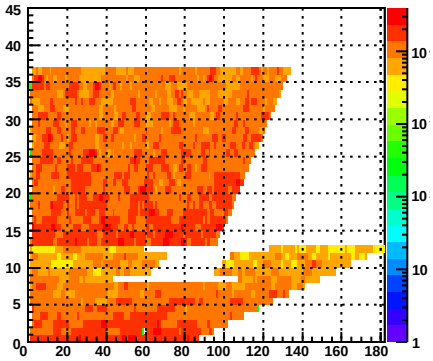
Sec. 17 charge per pad



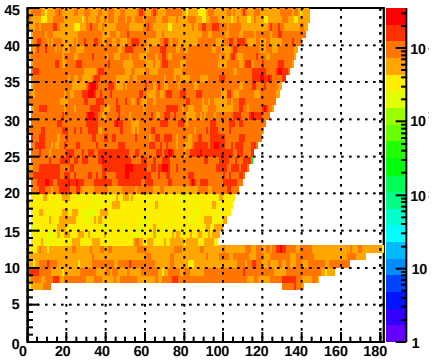
Sec. 18 charge per pad



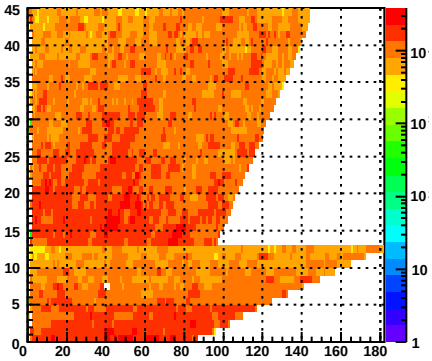
Sec. 19 charge per pad



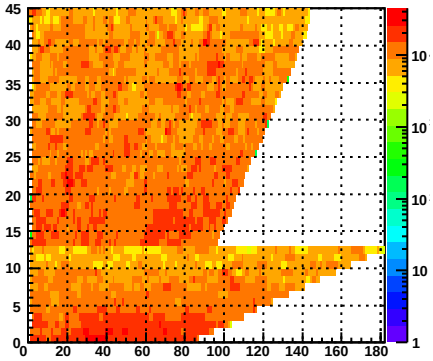
Sec. 20 charge per pad



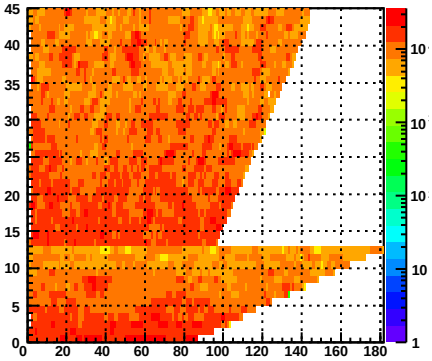
Sec. 21 charge per pad



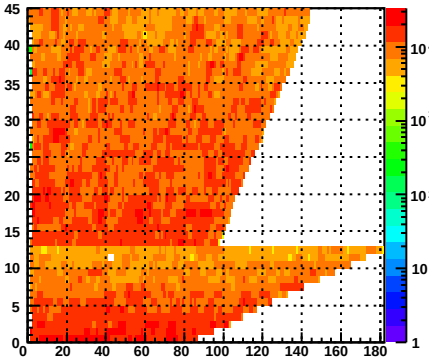
Sec. 22 charge per pad

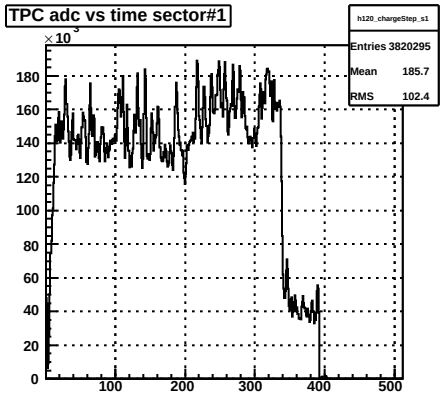


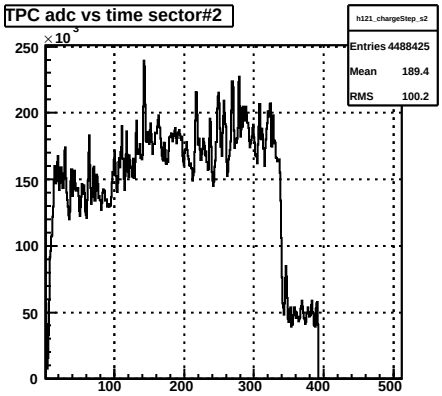
Sec. 23 charge per pad

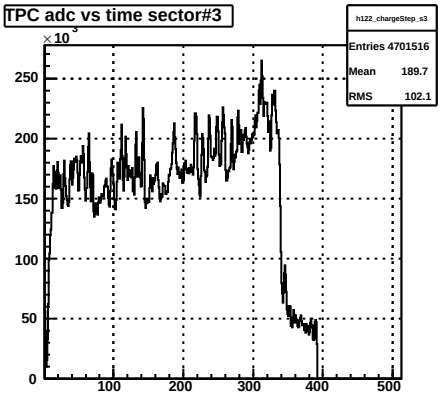


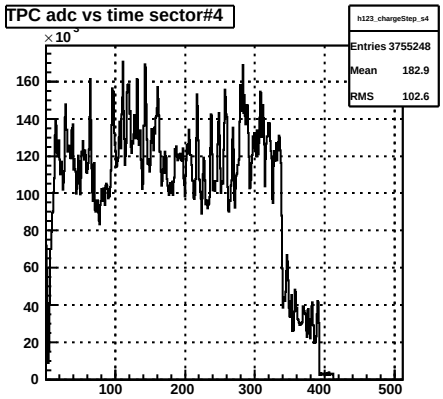
Sec. 24 charge per pad

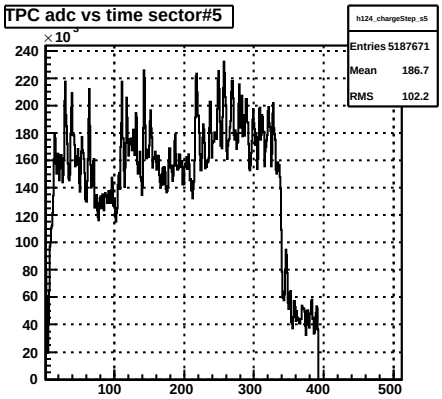


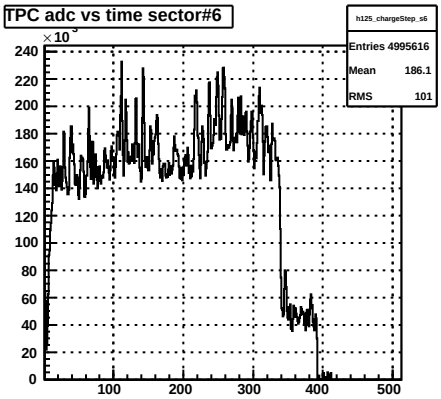

TPC adc vs time sector#1
h120_chargeStep_s1
Entries 3820295
Mean 185.7
RMS 102.4

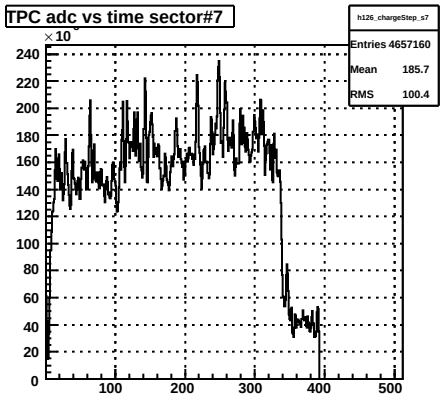

TPC adc vs time sector#2
h121_chargeStep_s2
Entries 4488425
Mean 189.4
RMS 100.2

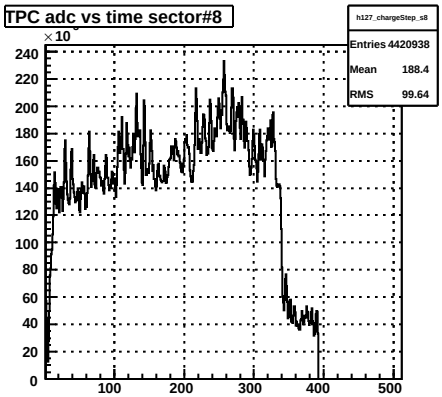

TPC adc vs time sector#3
h122_chargeStep_s3
Entries 4701516
Mean 189.7
RMS 102.1

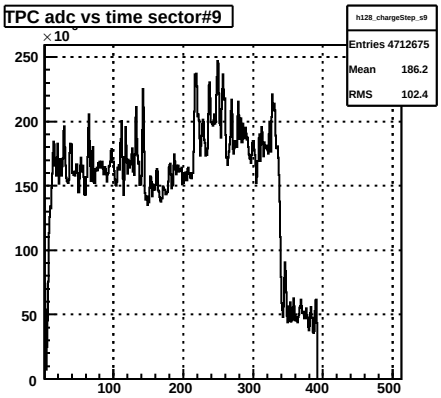

TPC adc vs time sector#4
h123_chargeStep_s4
Entries 3755248
Mean 182.9
RMS 102.6

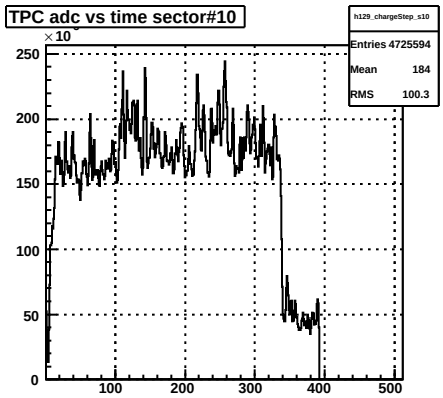

TPC adc vs time sector#5
h124_chargeStep_s5
Entries 5187671
Mean 186.7
RMS 102.2

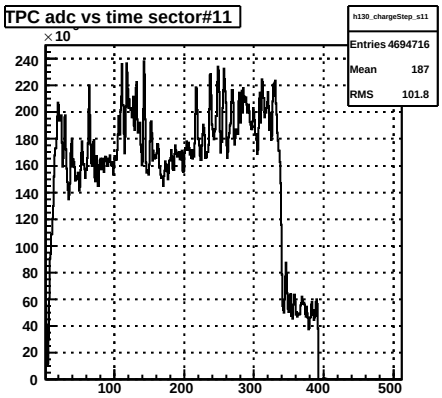

TPC adc vs time sector#6
h125_chargeStep_s6
Entries 4995616
Mean 186.1
RMS 101

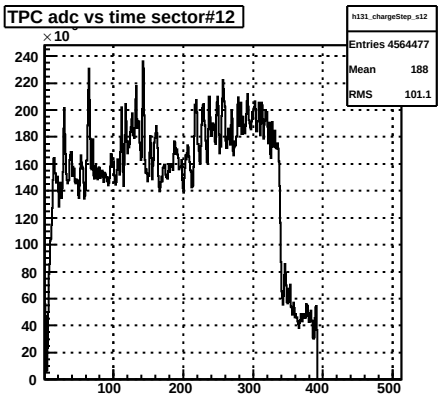

TPC adc vs time sector#7
h126_chargeStep_s7
Entries 4657160
Mean 185.7
RMS 100.4

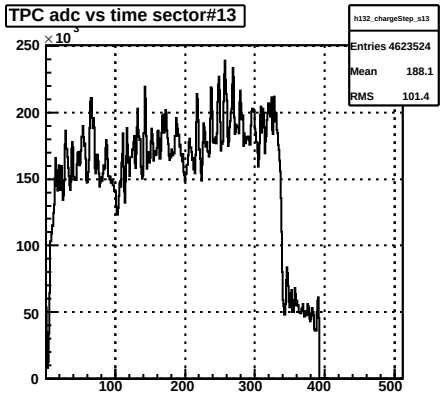

TPC adc vs time sector#8
h127_chargeStep_s8
Entries 4420938
Mean 188.4
RMS 99.64

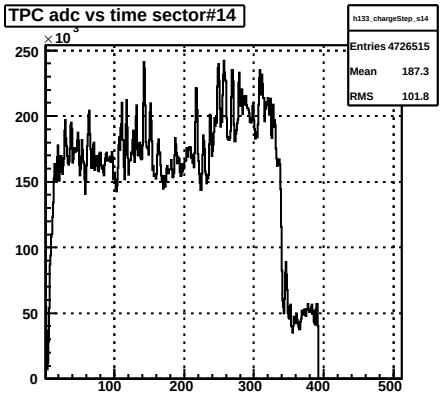

TPC adc vs time sector#9
h128_chargeStep_s9
Entries 4712675
Mean 186.2
RMS 102.4

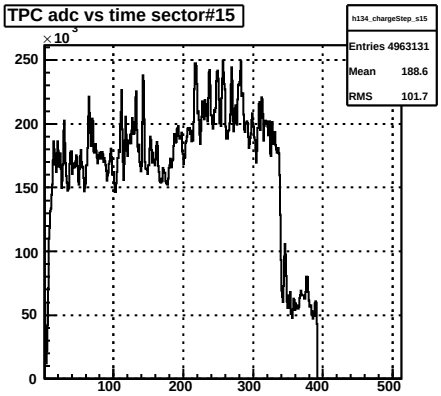

TPC adc vs time sector#10
h129_chargeStep_s10
Entries 4725594
Mean 184
RMS 100.3

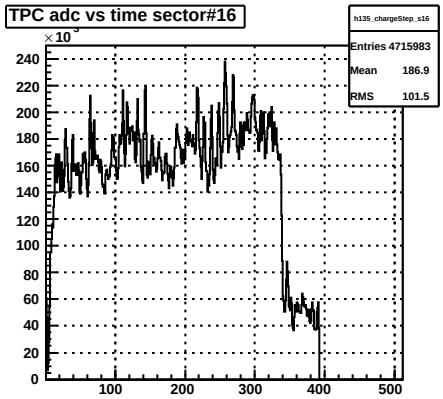

TPC adc vs time sector#11
h130_chargeStep_s11
Entries 4694716
Mean 187
RMS 101.8

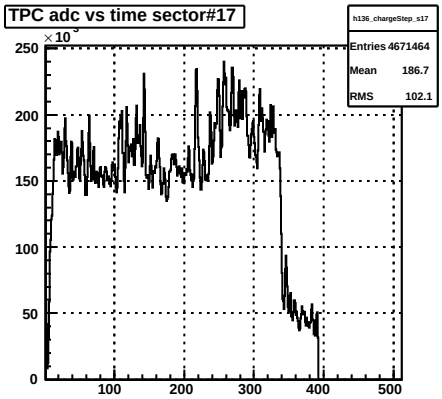

TPC adc vs time sector#12
h131_chargeStep_s12
Entries 4564477
Mean 188
RMS 101.1

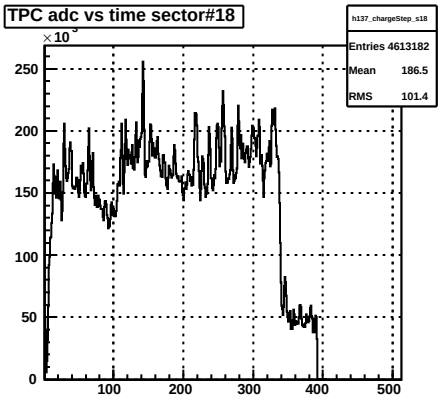

TPC adc vs time sector#13
h132_chargeStep_s13
Entries 4623524
Mean 188.1
RMS 101.4

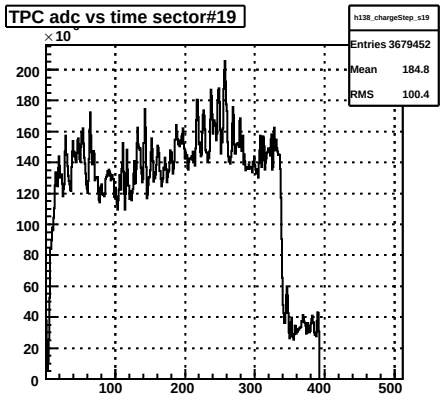

TPC adc vs time sector#14
h133_chargeStep_s14
Entries 4726515
Mean 187.3
RMS 101.8

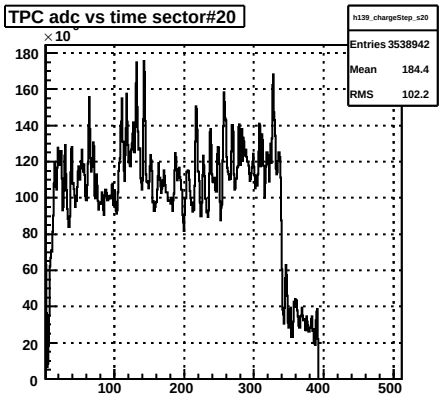

TPC adc vs time sector#15
h134_chargeStep_s15
Entries 4963131
Mean 188.6
RMS 101.7

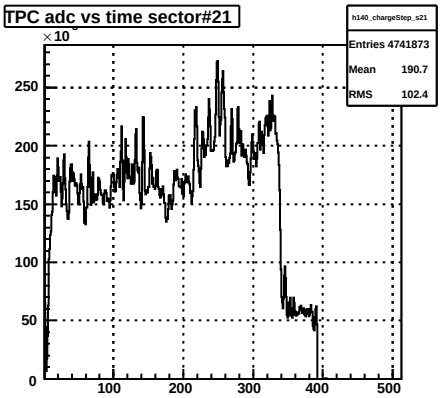

TPC adc vs time sector#16
h135_chargeStep_s16
Entries 4715983
Mean 186.9
RMS 101.5

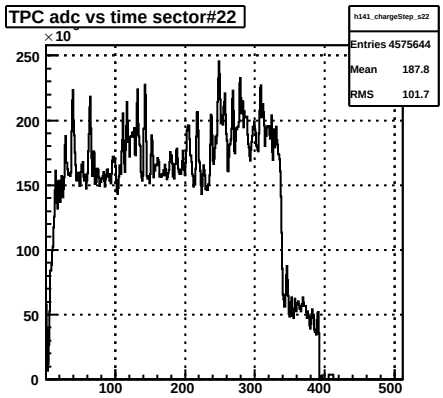

TPC adc vs time sector#17
h136_chargeStep_s17
Entries 4671464
Mean 186.7
RMS 102.1

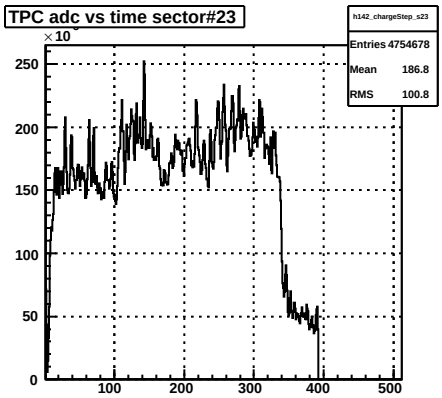

TPC adc vs time sector#18
h137_chargeStep_s18
Entries 4613182
Mean 186.5
RMS 101.4

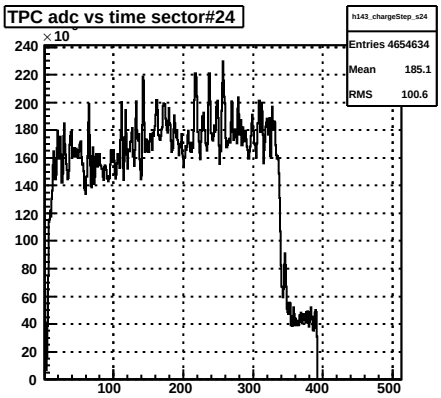

TPC adc vs time sector#19
h138_chargeStep_s19
Entries 3679452
Mean 184.8
RMS 100.4

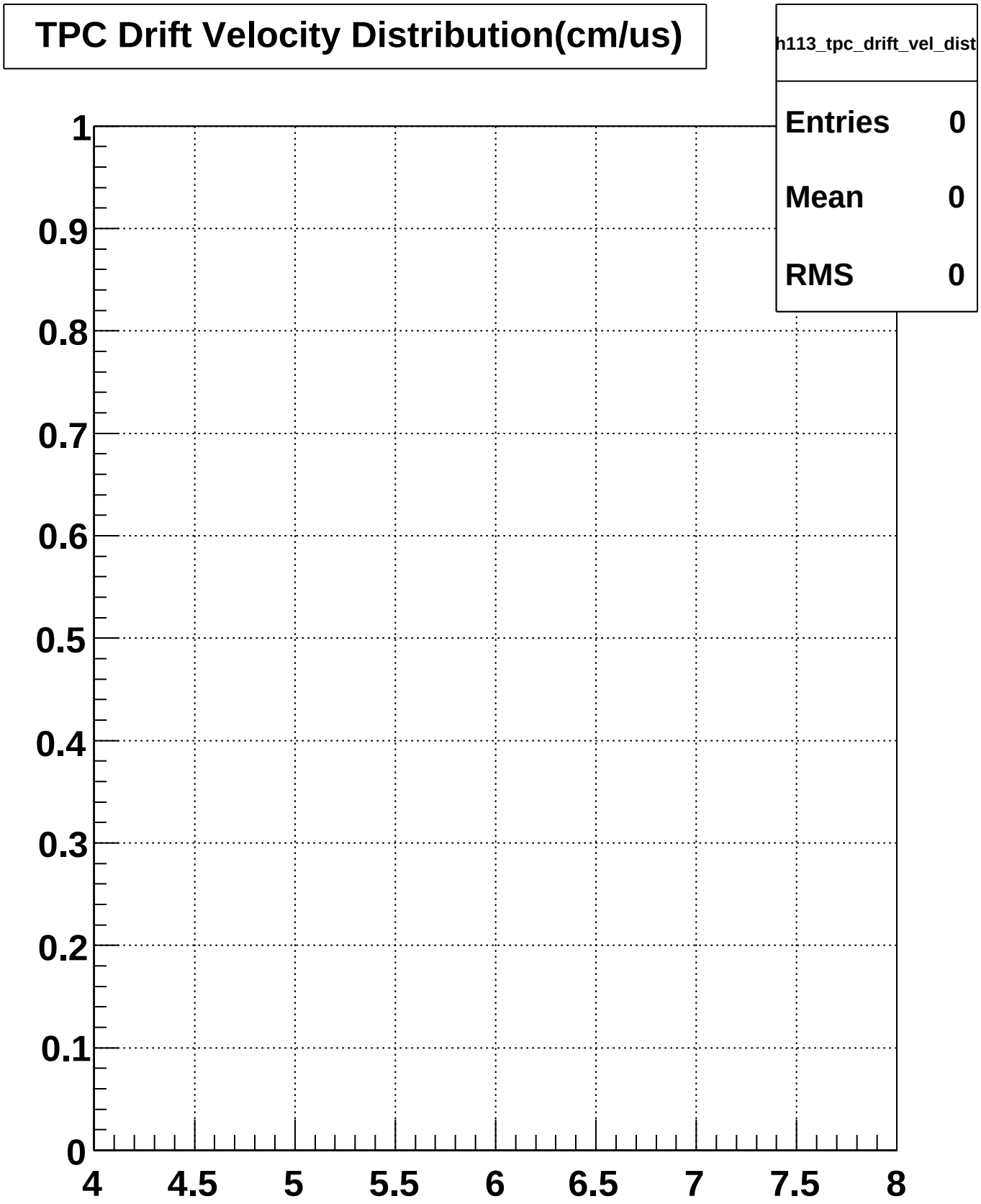

TPC adc vs time sector#20
h139_chargeStep_s20
Entries 3538942
Mean 184.4
RMS 102.2

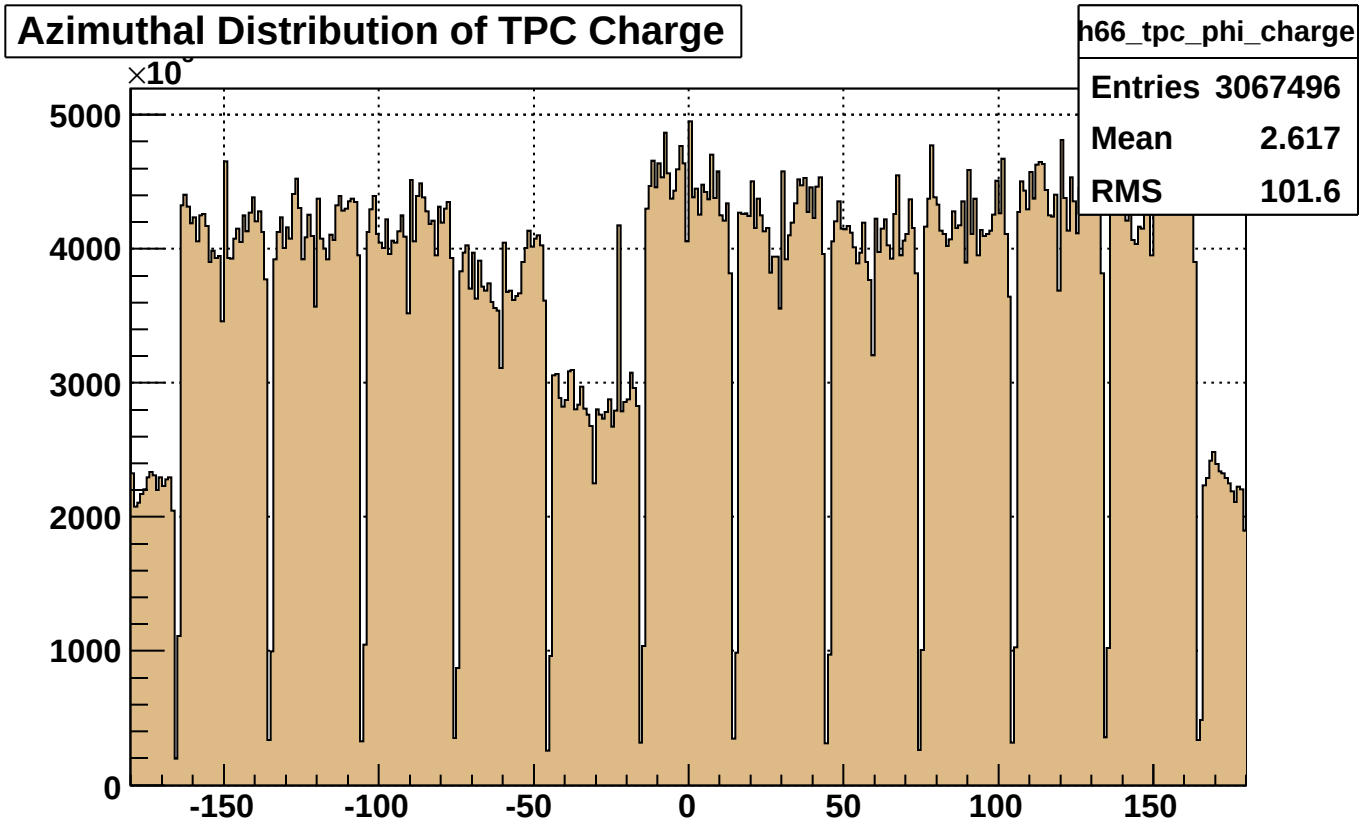

TPC adc vs time sector#21
h140_chargeStep_s21
Entries 4741873
Mean 190.7
RMS 102.4

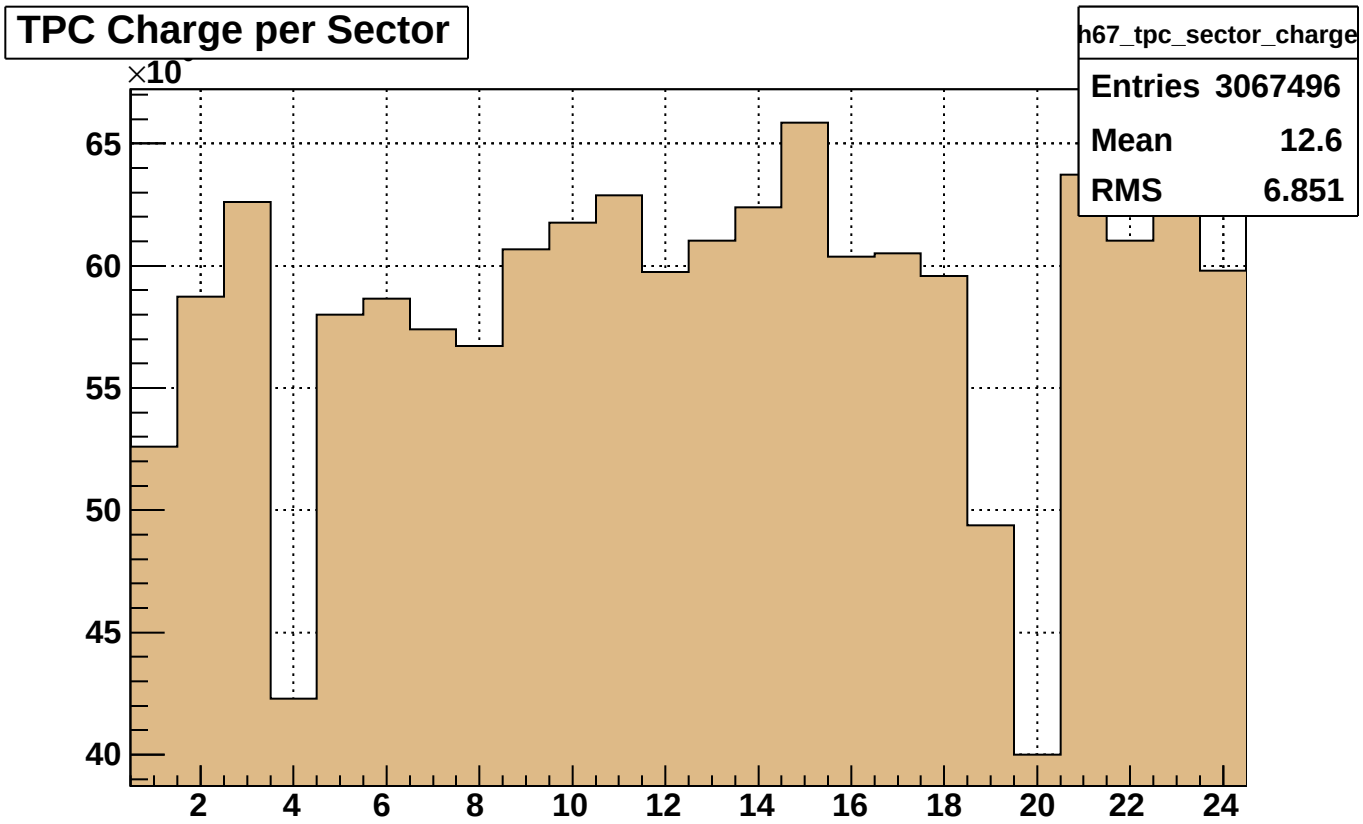

TPC adc vs time sector#22
h141_chargeStep_s22
Entries 4575644
Mean 187.8
RMS 101.7

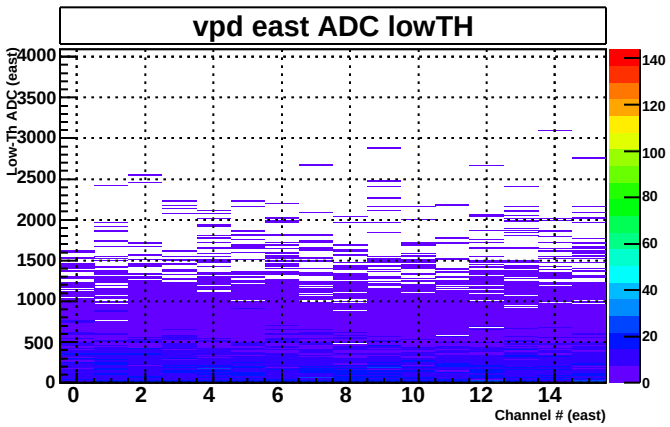

TPC adc vs time sector#23
h142_chargeStep_s23
Entries 4754678
Mean 186.8
RMS 100.8

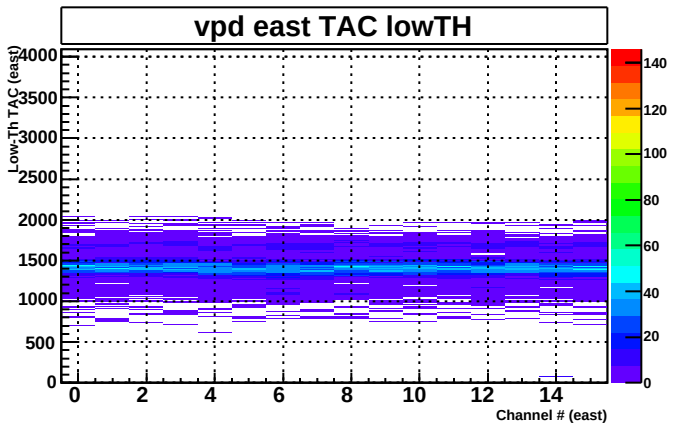

TPC adc vs time sector#24
h143_chargeStep_s24
Entries 4654634
Mean 185.1
RMS 100.6

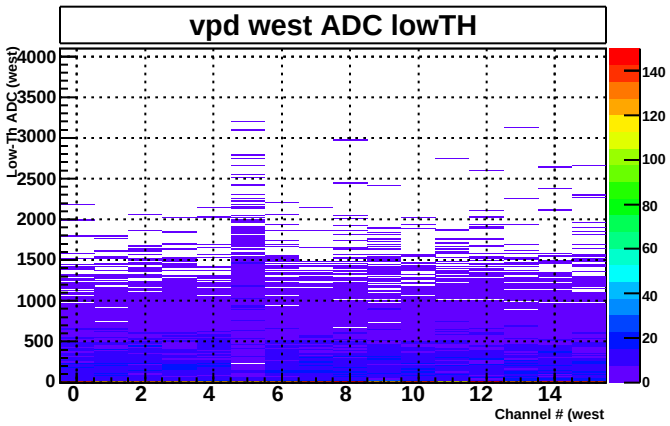

TPC Drift Velocity Distribution(cm/us)
h113_tpc_drift_vel_dist
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
4
4.5
5
5.5
6
6.5
7
7.5
8

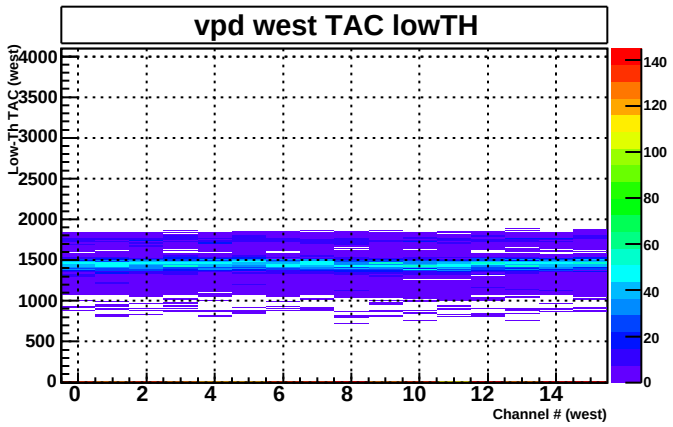

Azimuthal Distribution of TPC Charge
h66_tpc_phi_charge
Entries 3067496
Mean 2.617
RMS 101.6
×10
5000
4000
3000
2000
1000
0
-150
-100
-50
0
50
100
150

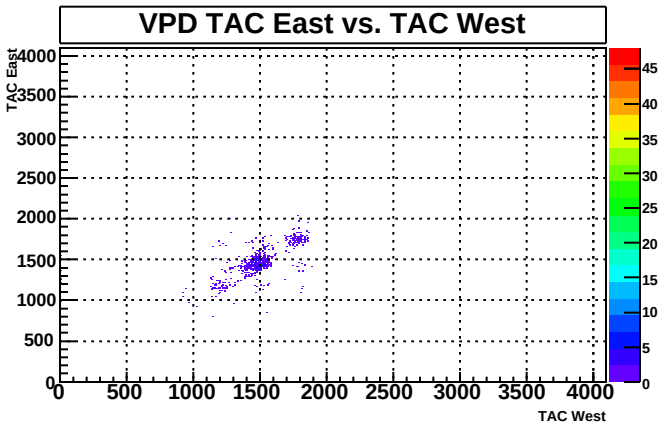

TPC Charge per Sector
h67_tpc_sector_charge
Entries 3067496
Mean 12.6
RMS 6.851
×10
65
60
55
50
45
40
2
4
6
8
10
12
14
16
18
20
22
24

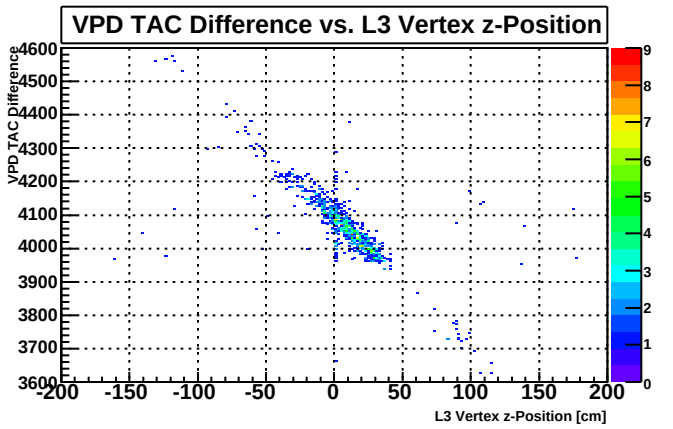

vpd east ADC lowTH
Low-Th ADC (east)
Channel # (east)

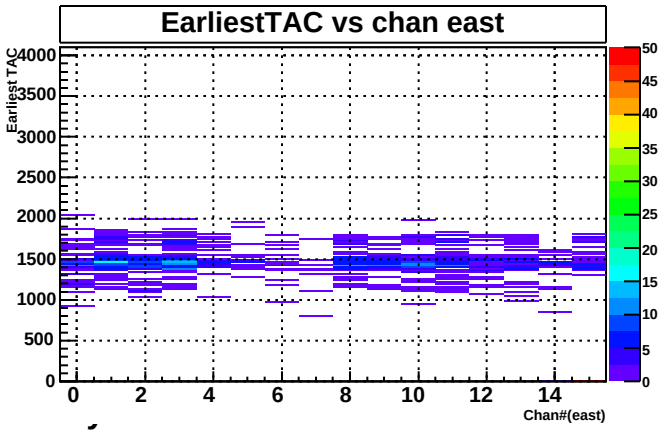

vpd east TAC lowTH
Low-Th TAC (east)
Channel # (east)

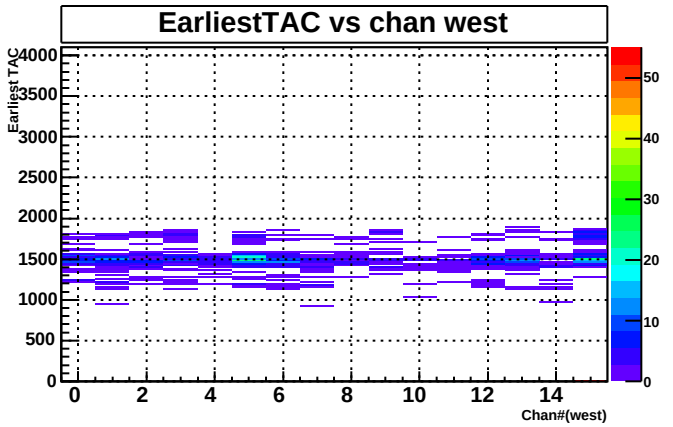

vpd west ADC lowTH
Low-Th ADC (west)
Channel # (west

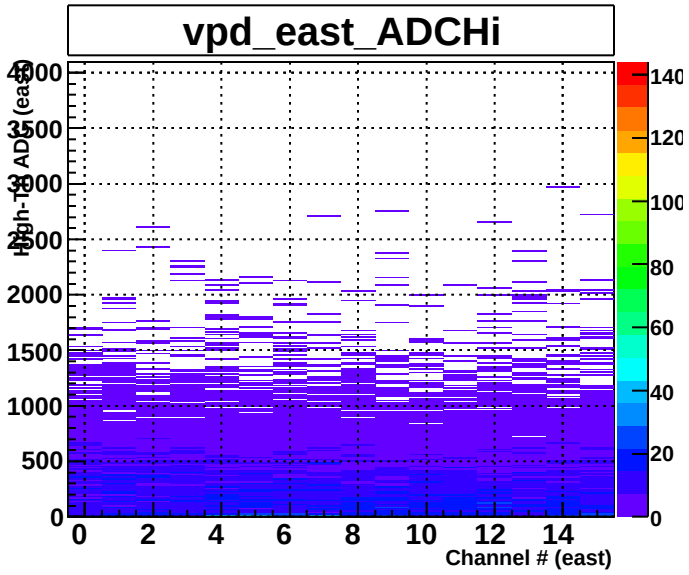

vpd west TAC lowTH
Low-Th TAC (west)
Channel # (west)

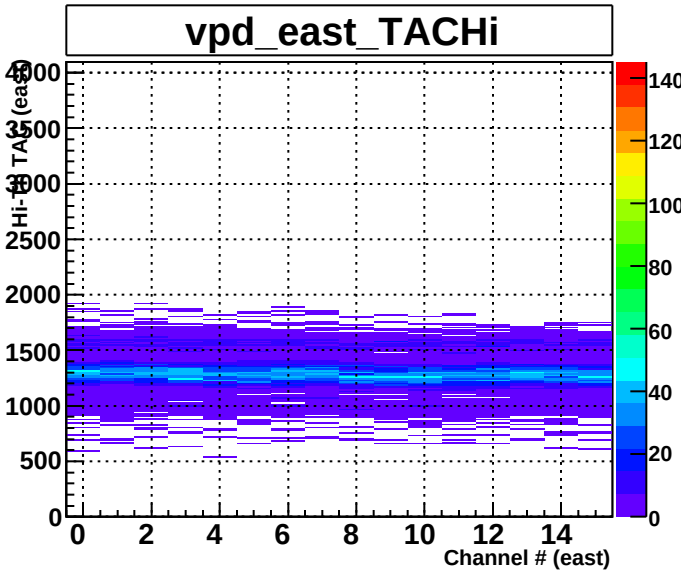

VPD TAC East vs. TAC West
TAC East
TAC West

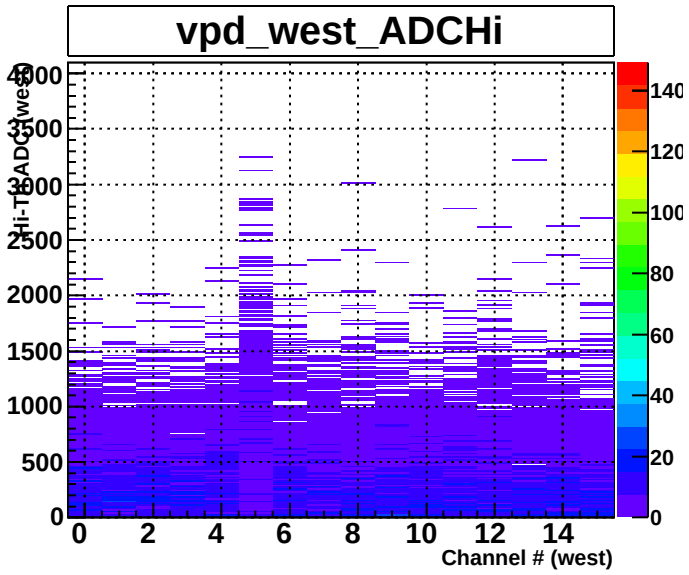

VPD TAC Difference vs. L3 Vertex z-Position
VPD TAC Difference
L3 Vertex z-Position [cm]

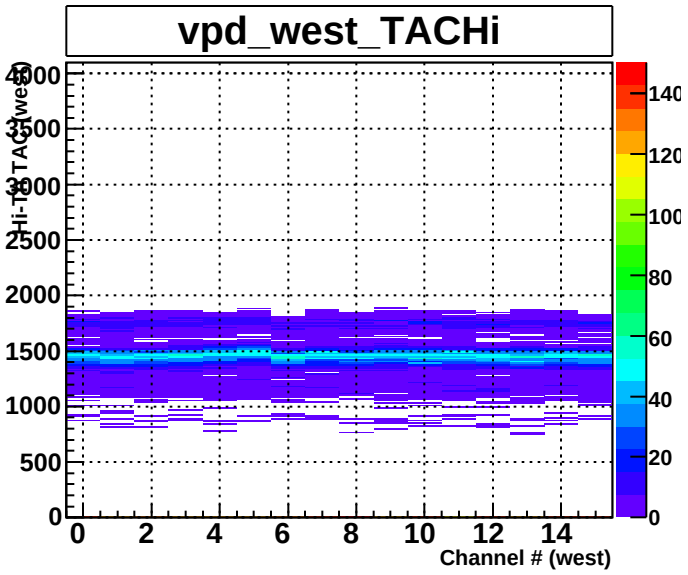

EarliestTAC vs chan east
Earliest TAC
Chan#(east)

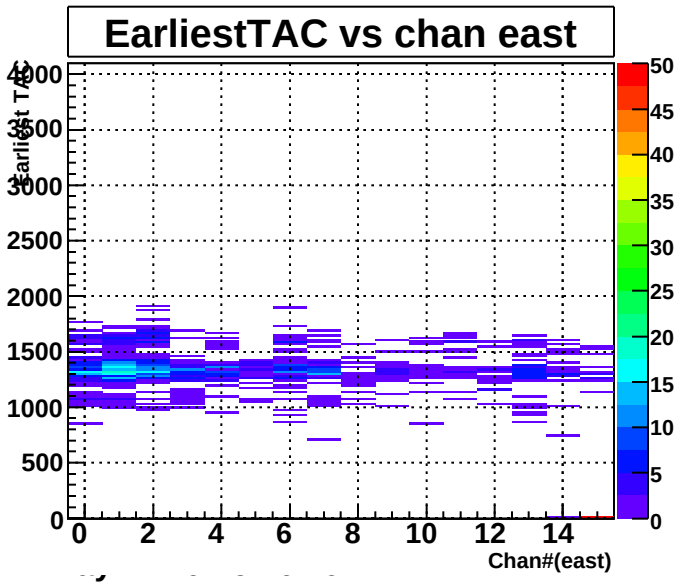

EarliestTAC vs chan west
Earliest TAC
Chan#(west)

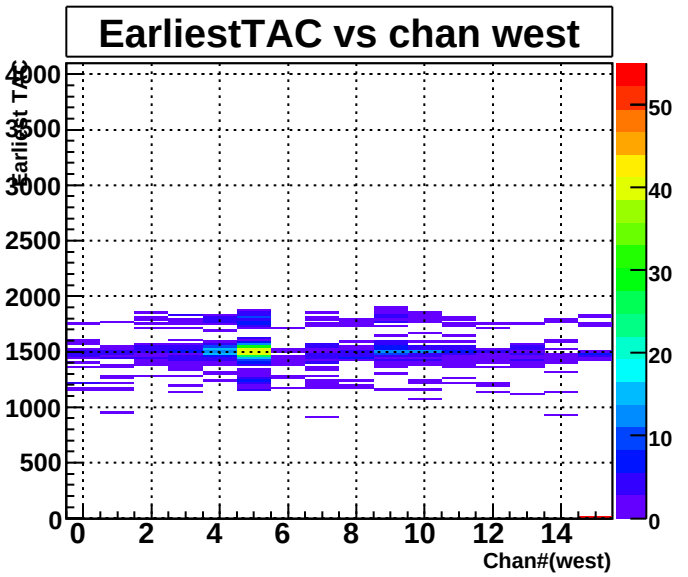

vpd_east_ADCHi
High-Hi ADC (east)
Channel # (east)

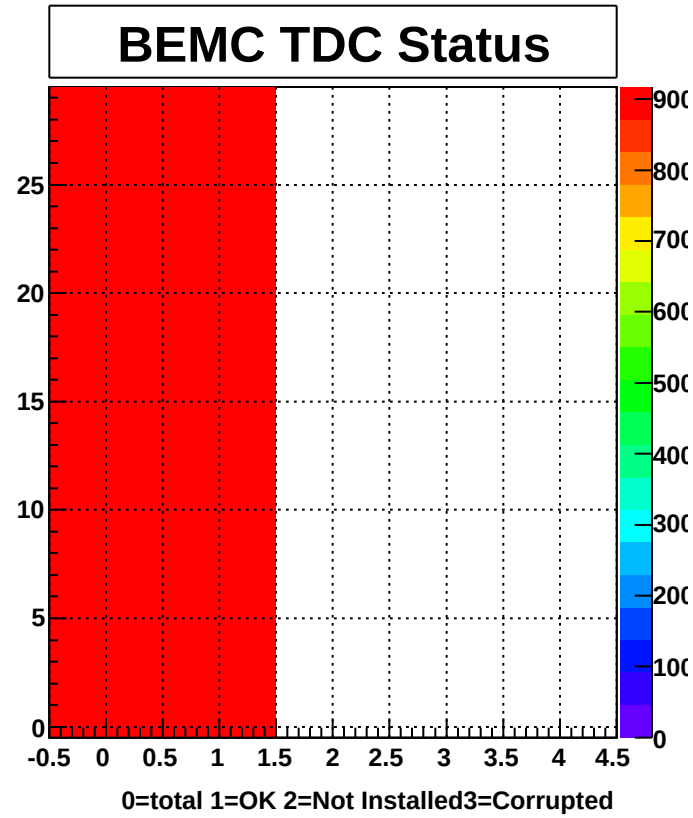

vpd_east_TACHi
Hi-Hi TAC (east)
Channel # (east)

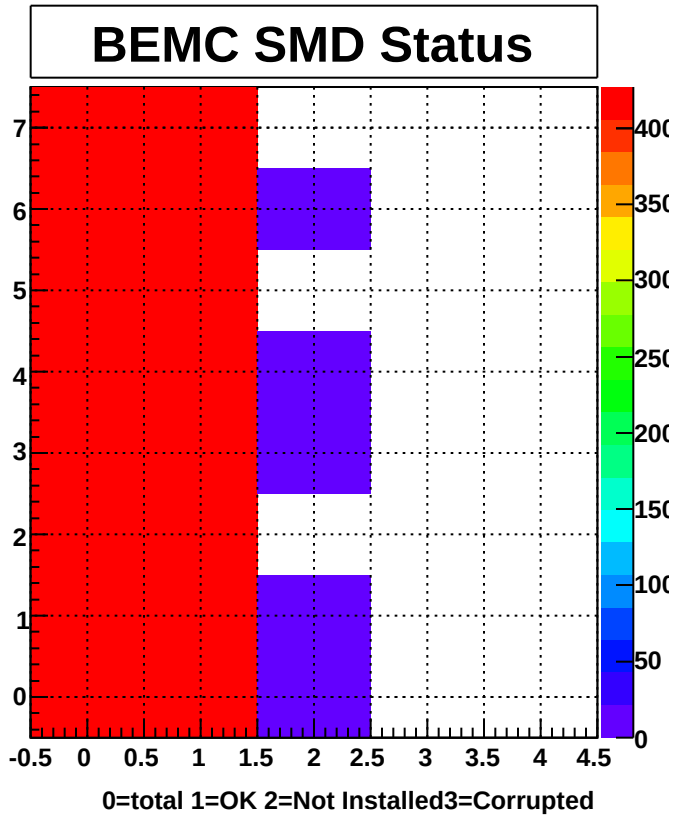

vpd_west_ADCHi
Hi-Hi ADC (west)
Channel # (west)

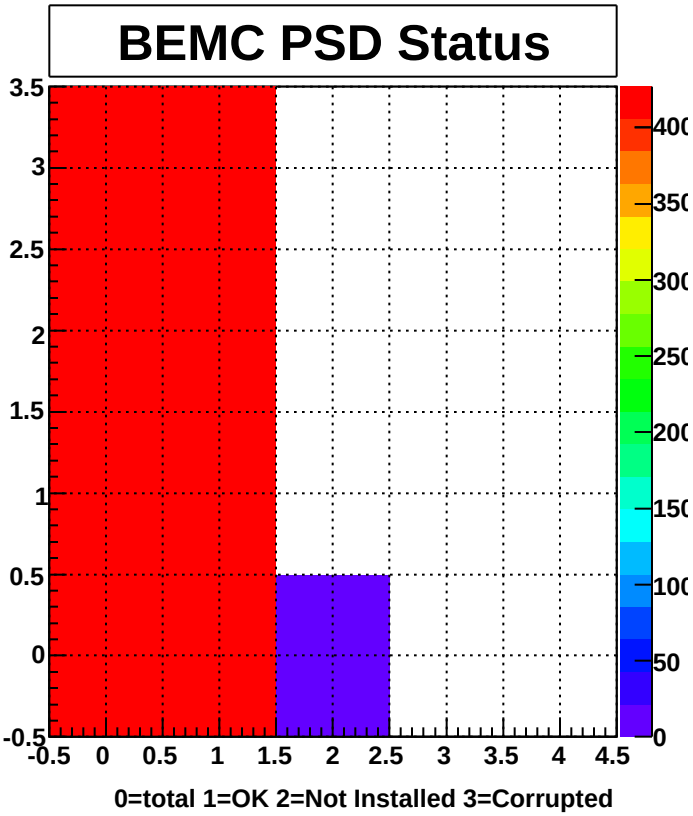

vpd_west_TACHi
Hi-Hi TAC (west)
Channel # (west)

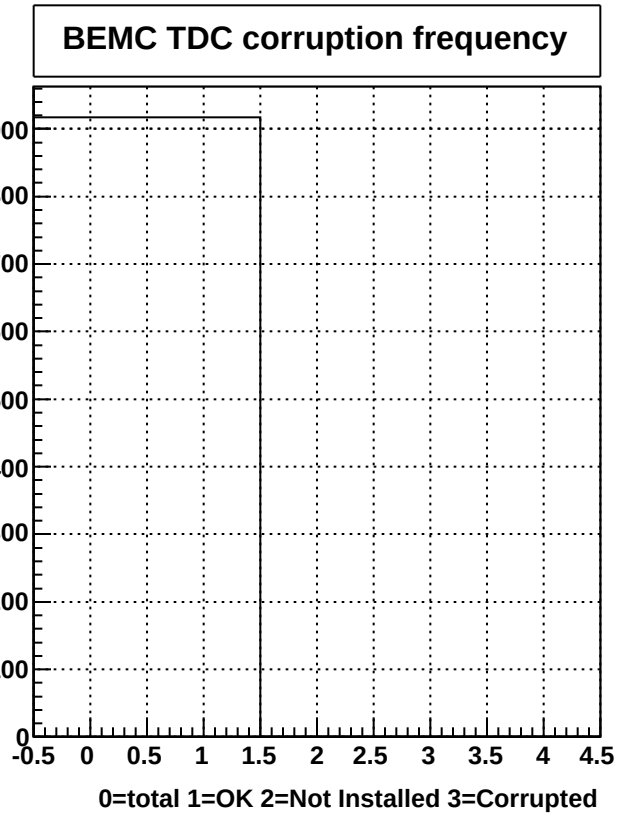

EarliestTAC vs chan east
Earliest TAC
Chan#(east)


EarliestTAC vs chan west
Earliest TAC
Chan#(west)

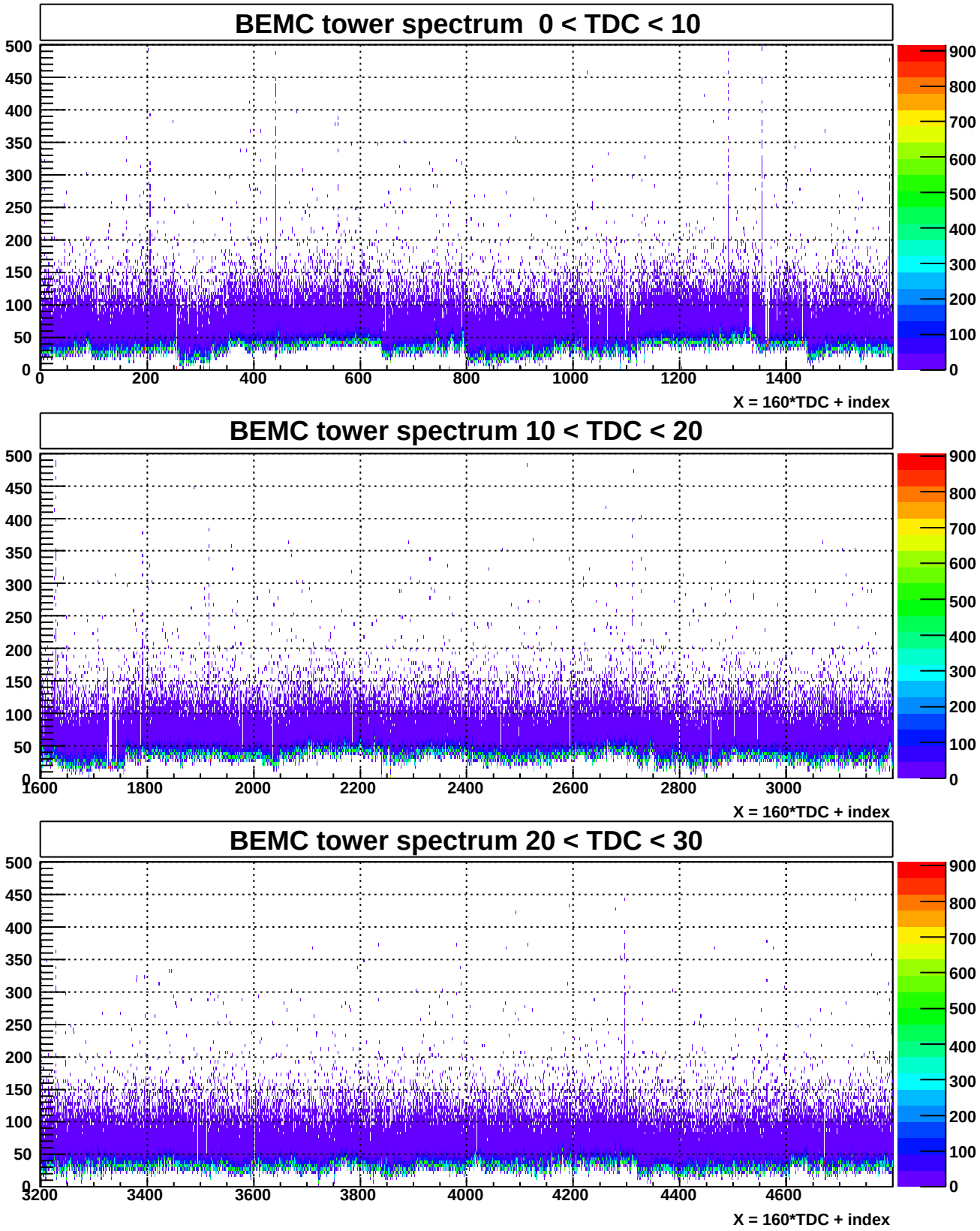




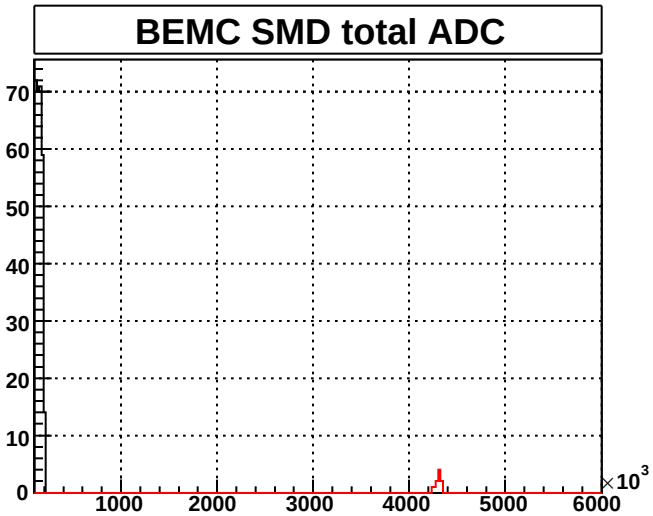


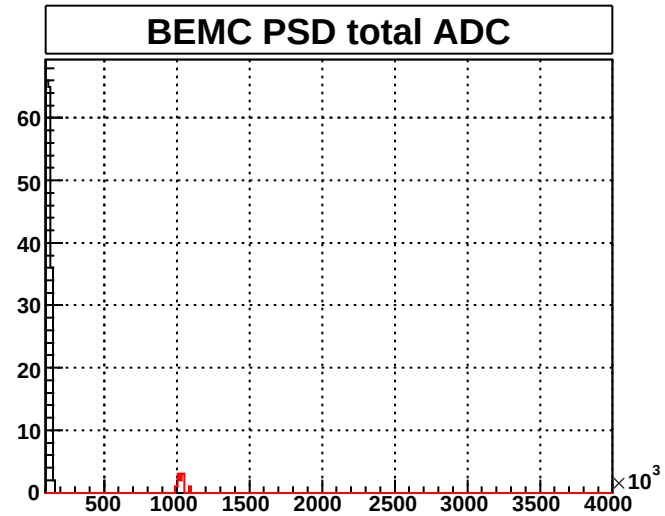


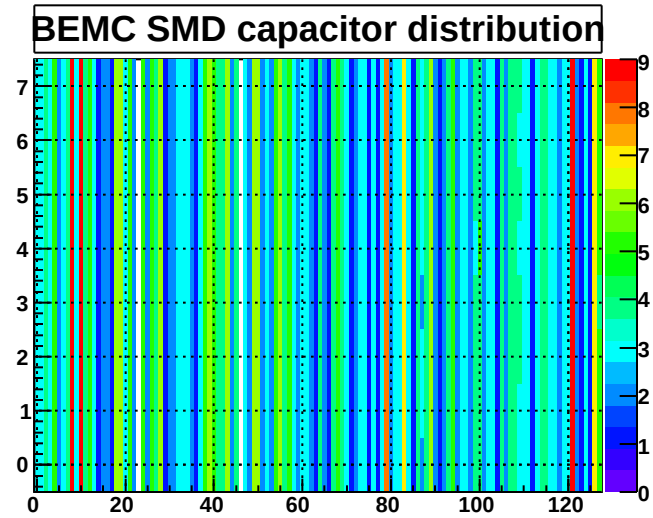
Fri May 27 10:16:41 2011

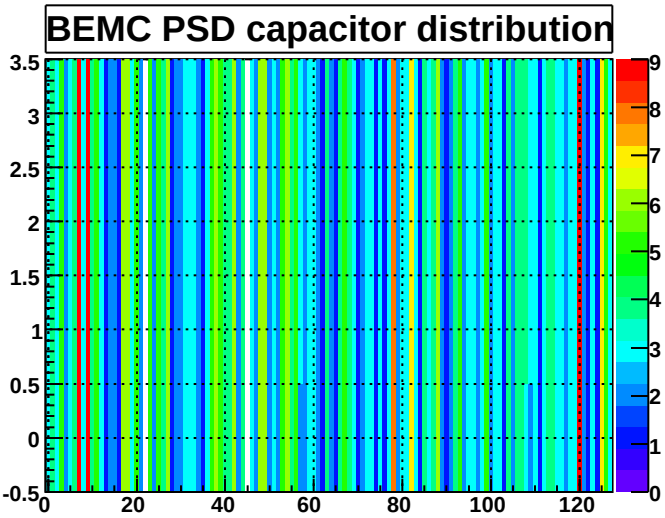


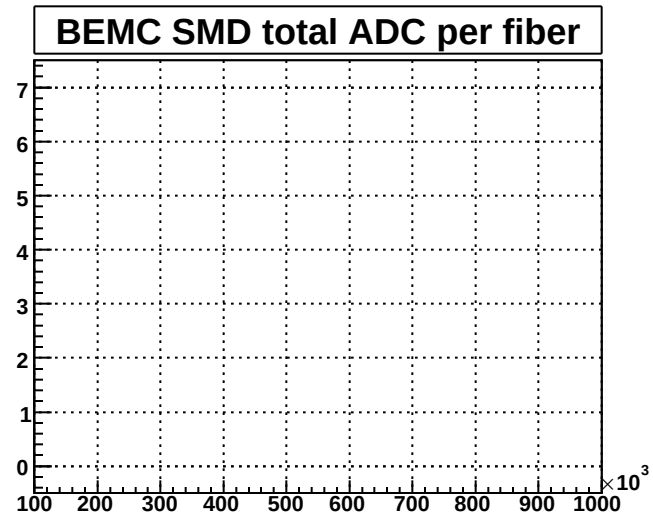
Fri May 27 10:16:41 2011

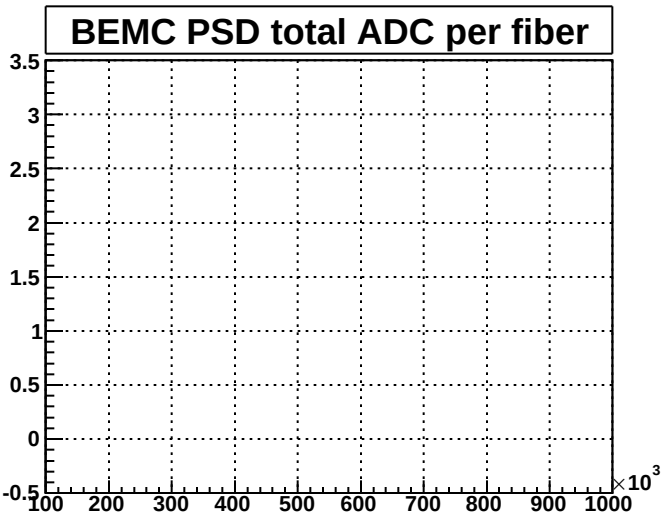




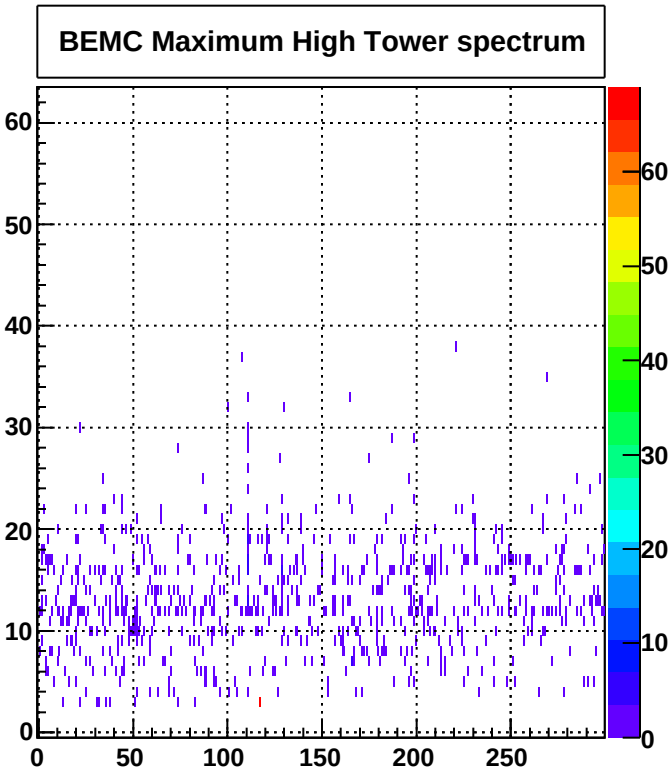


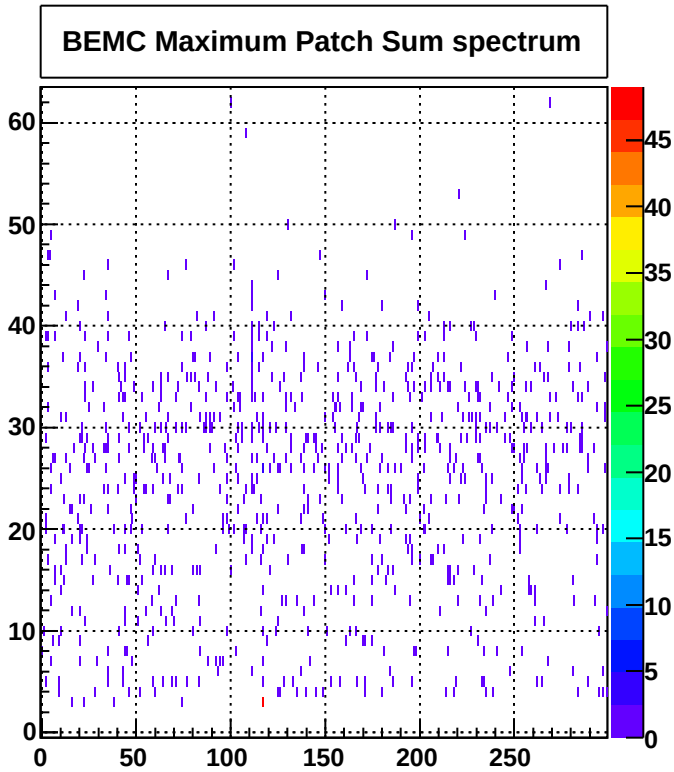


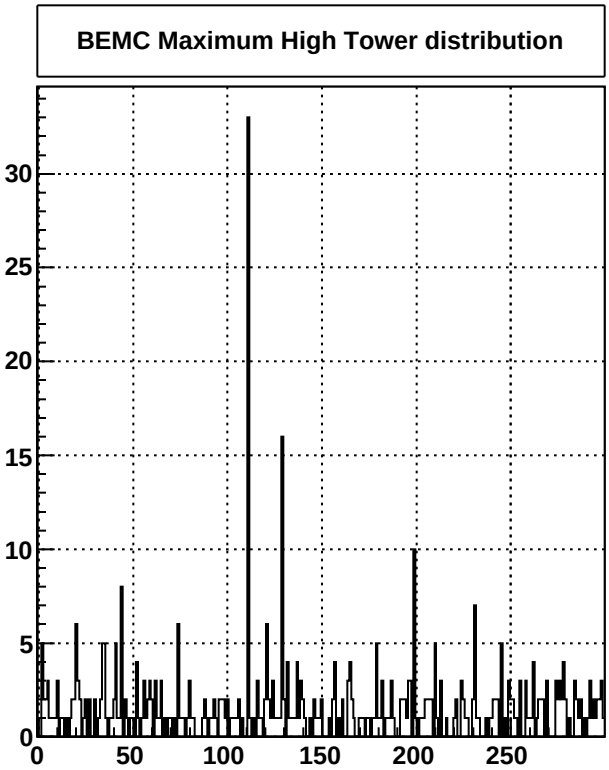


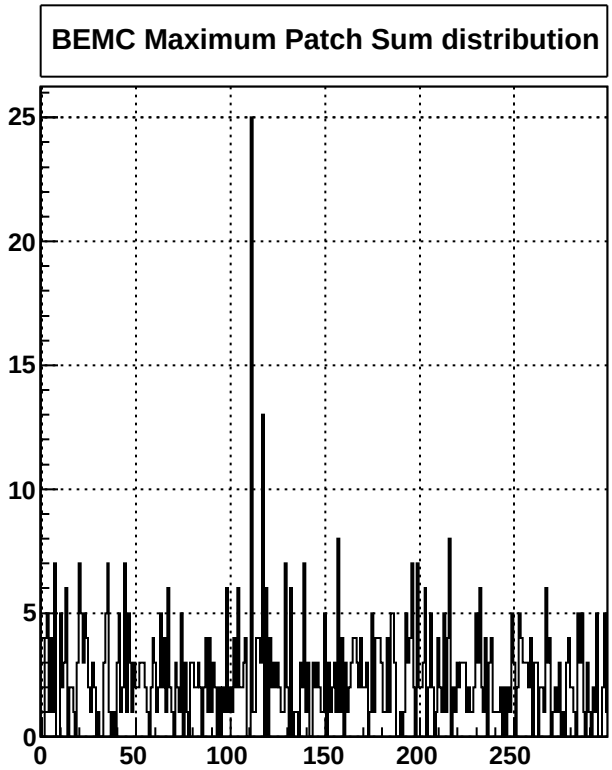


Fri May 27 10:16:41 2011

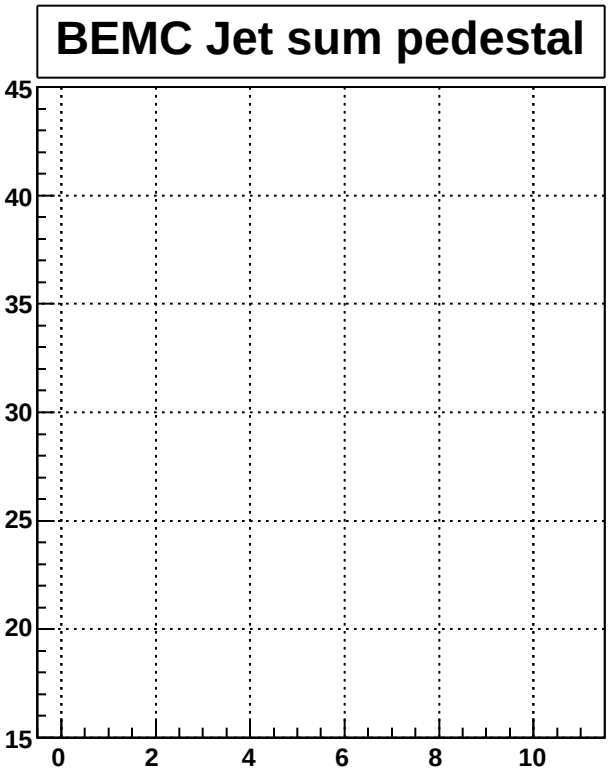


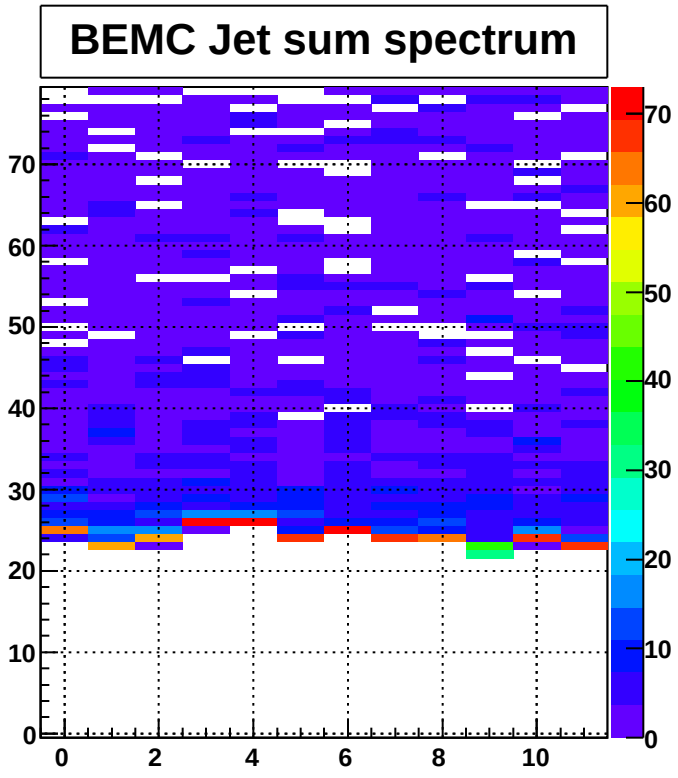


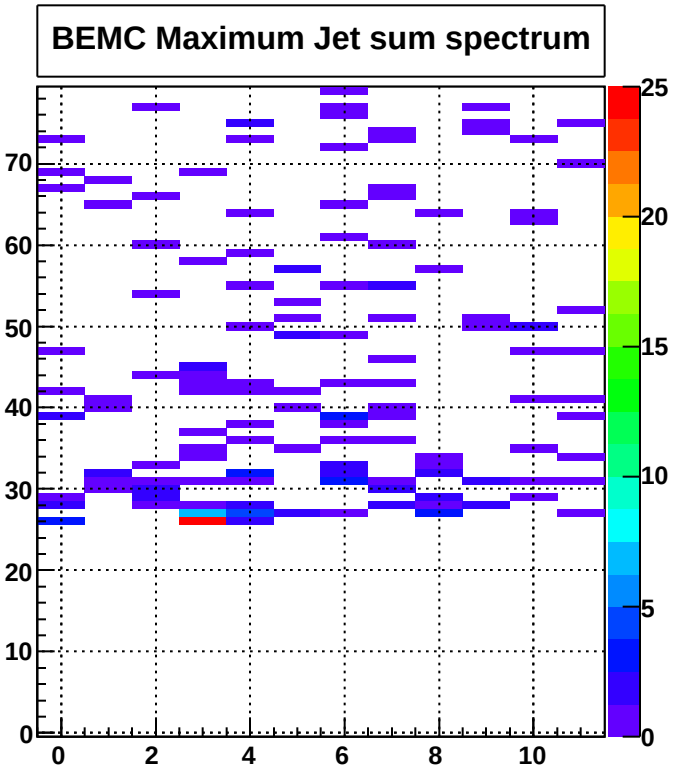


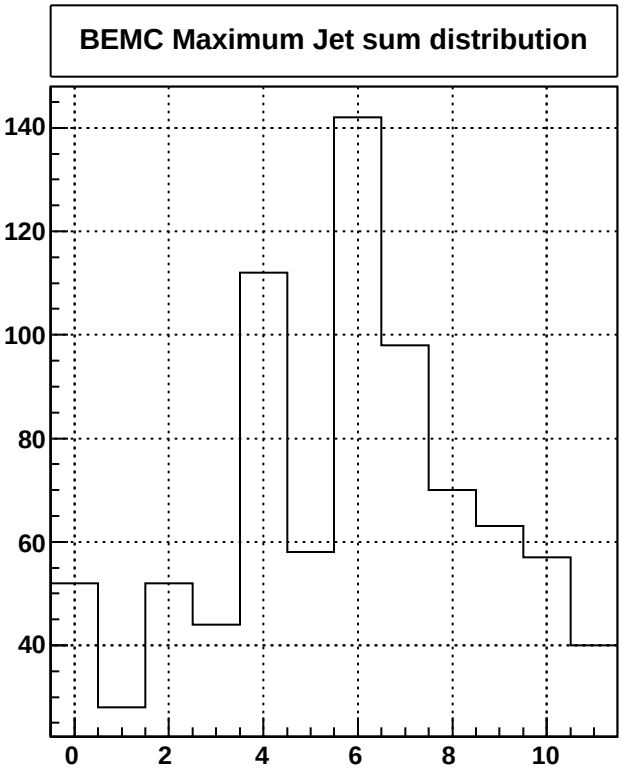


Fri May 27 10:16:41 2011

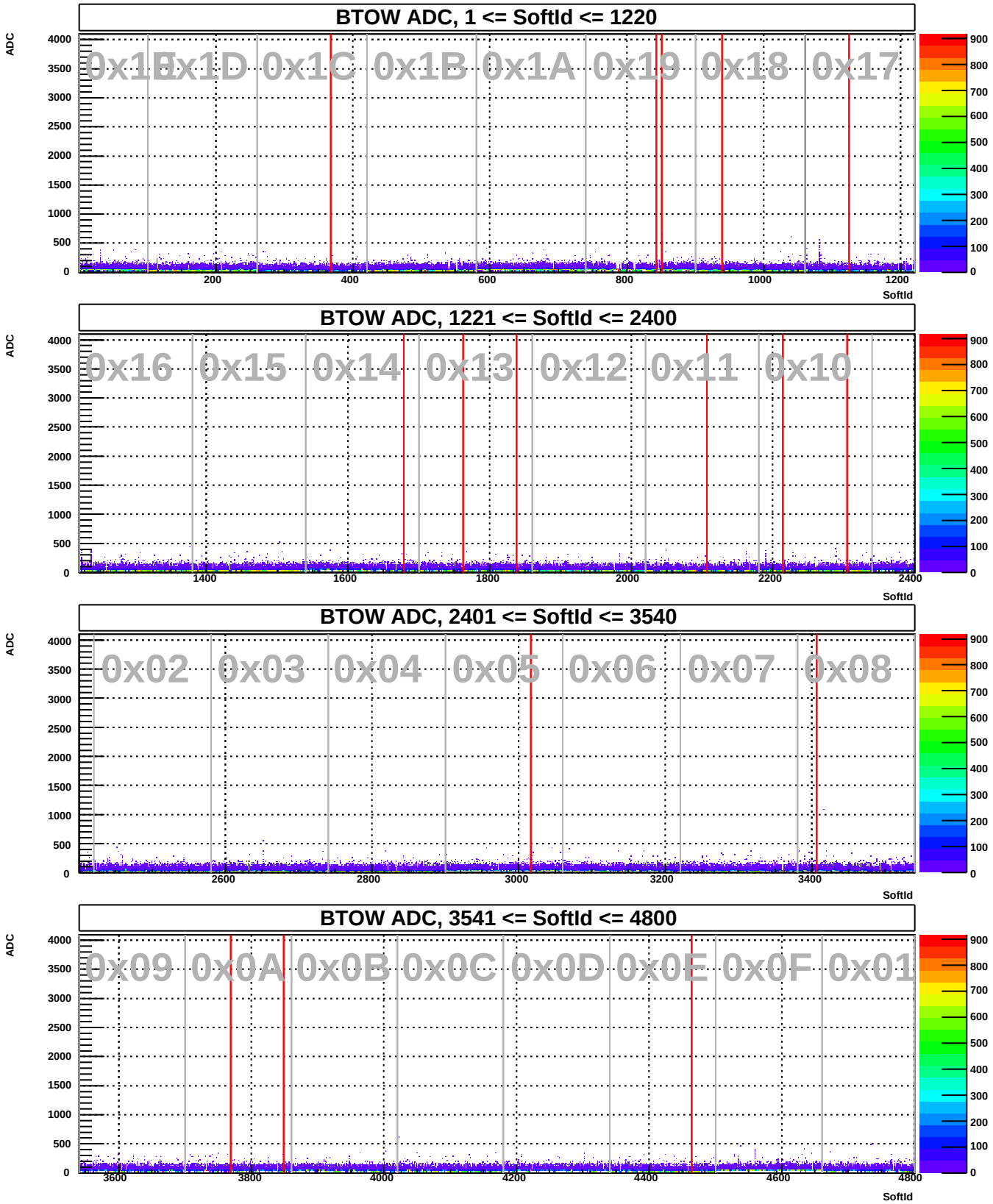




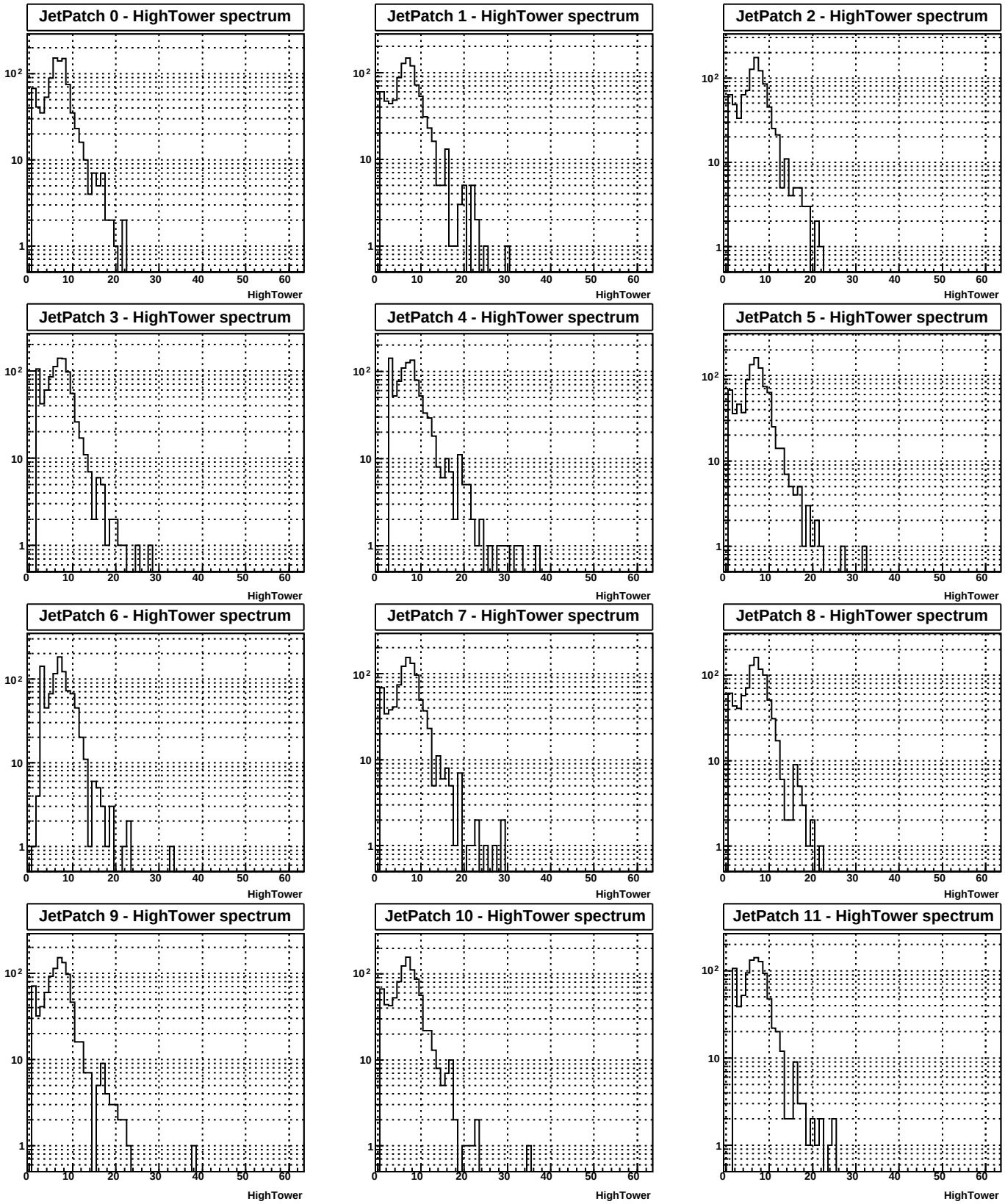




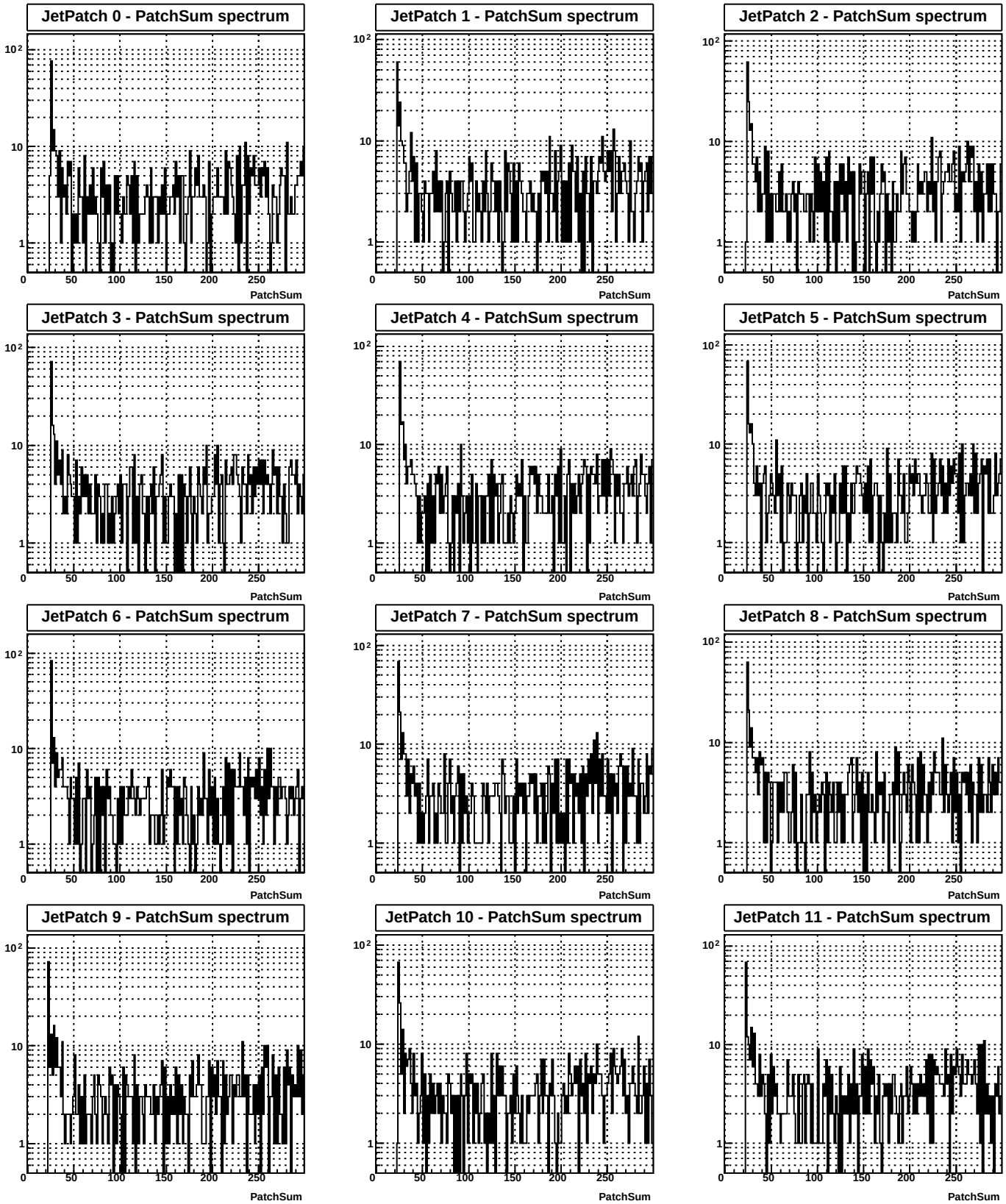
Fri May 27 10:16:41 2011



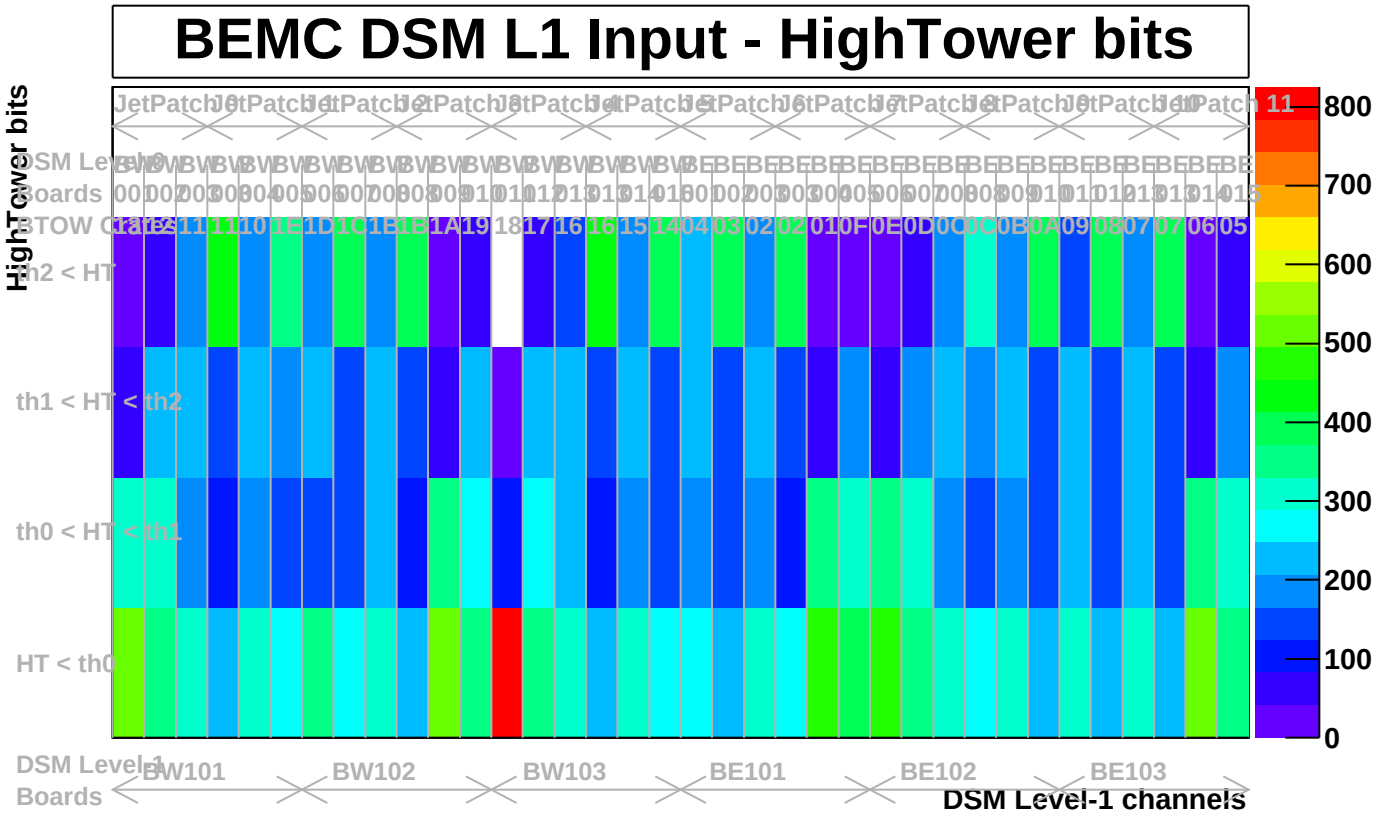
Fri May 27 10:16:42 2011

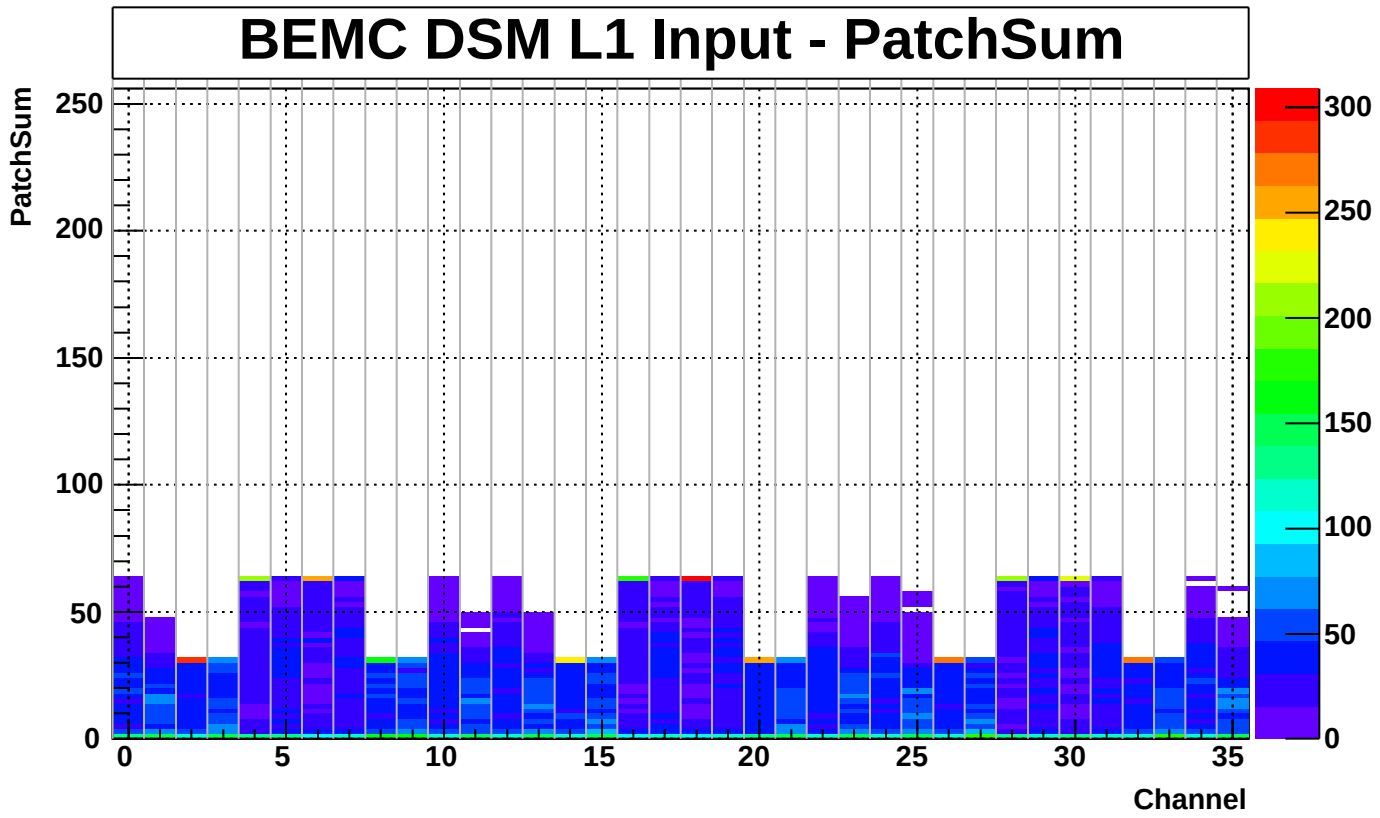


Fri May 27 10:16:43 2011

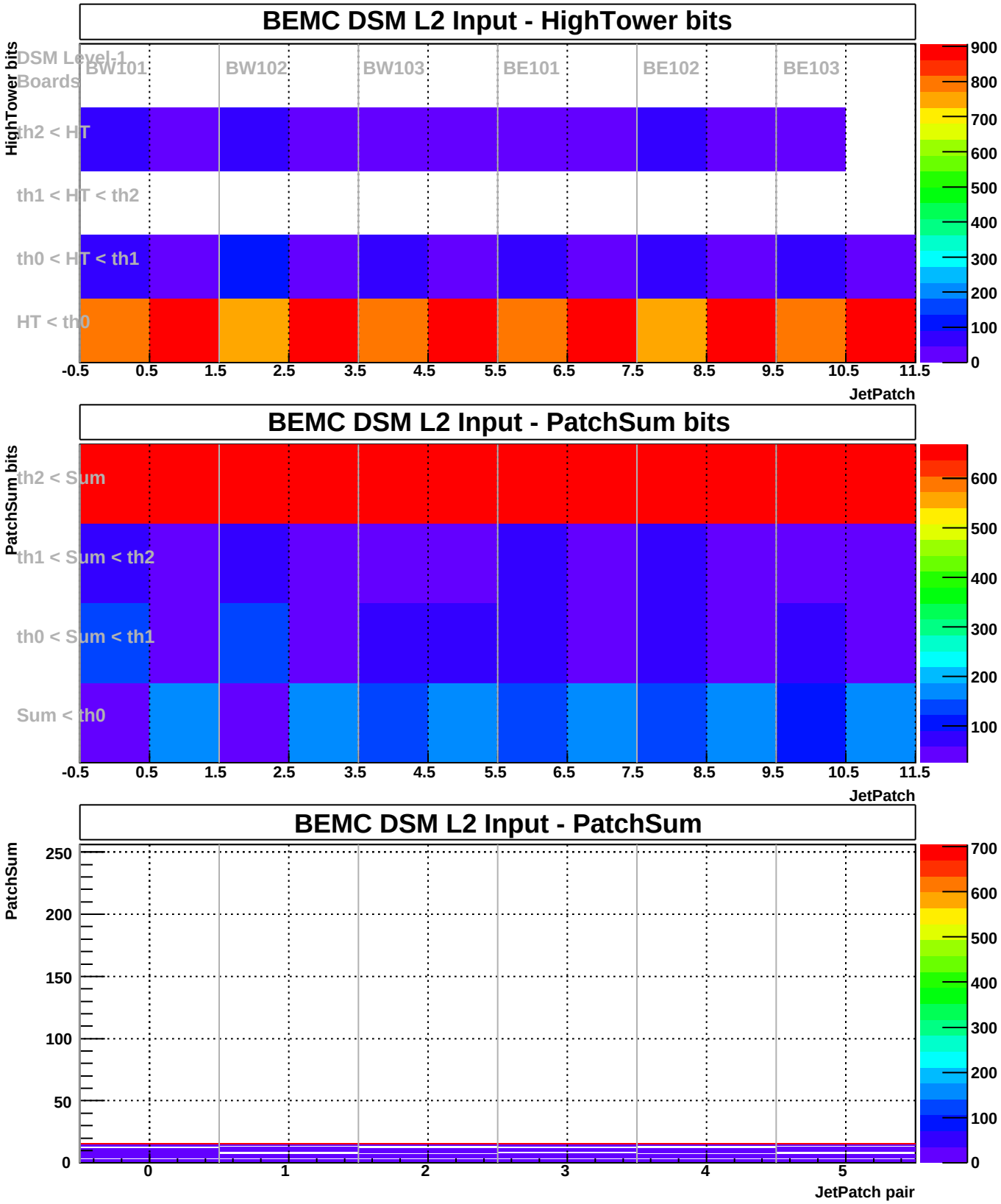


Fri May 27 10:16:43 2011

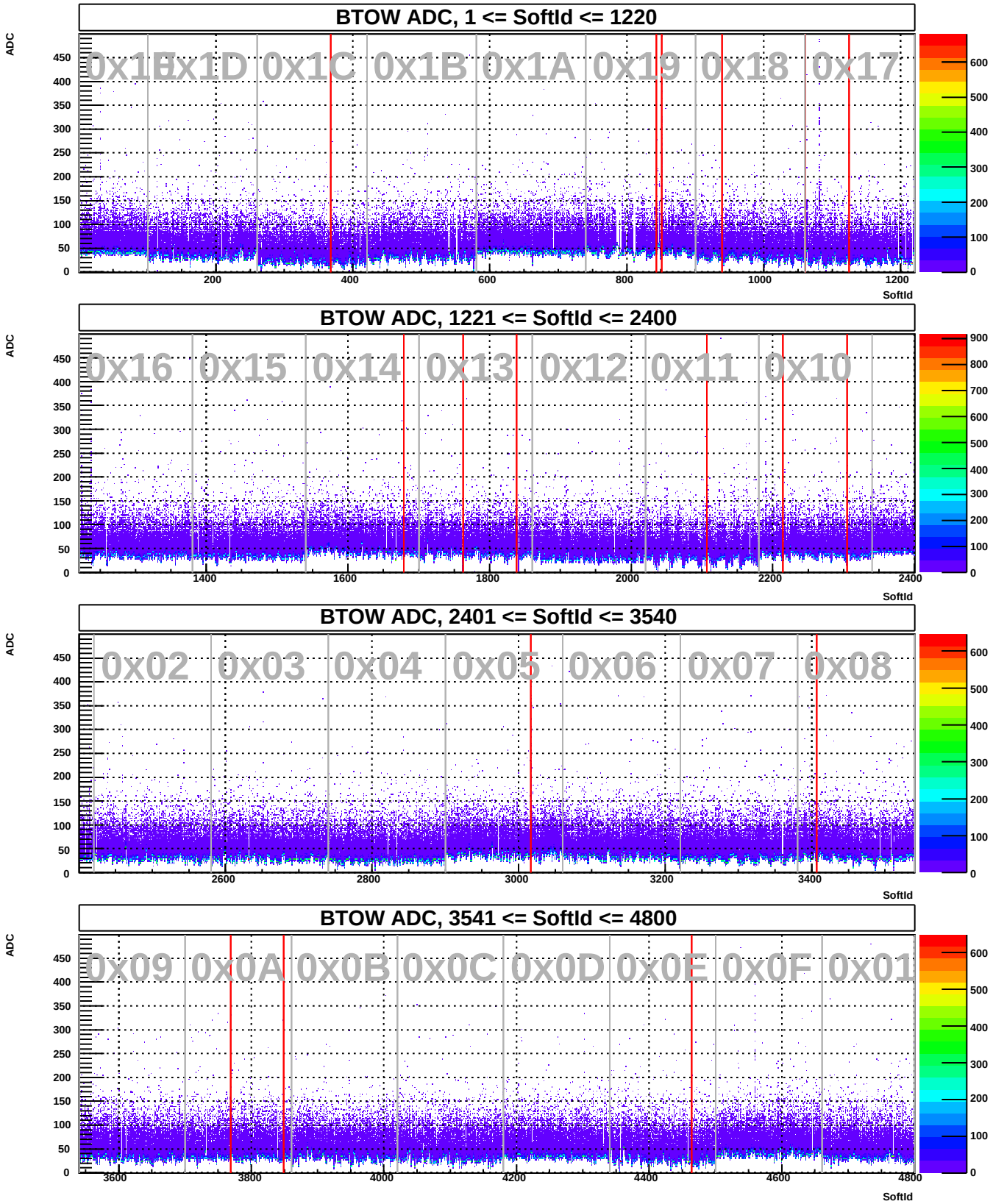




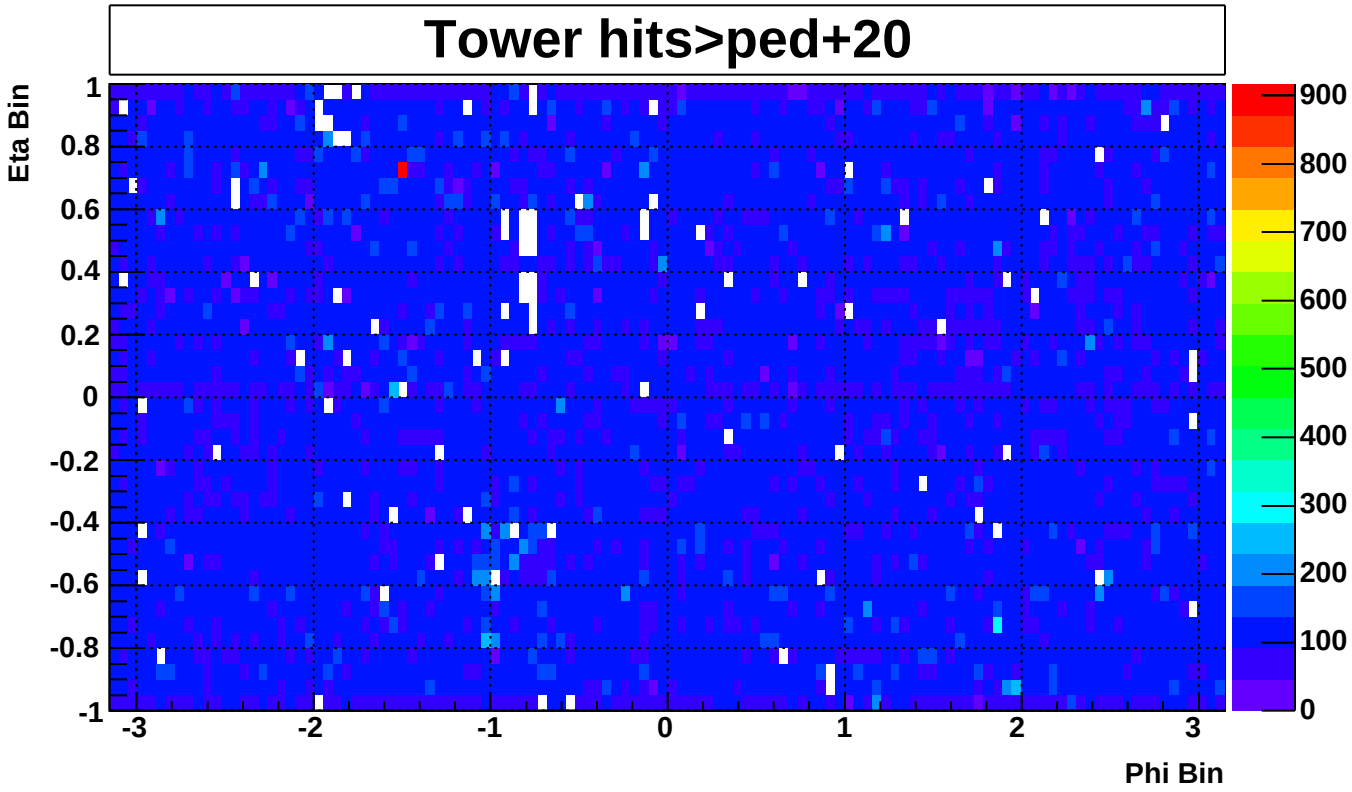
Fri May 27 10:16:43 2011

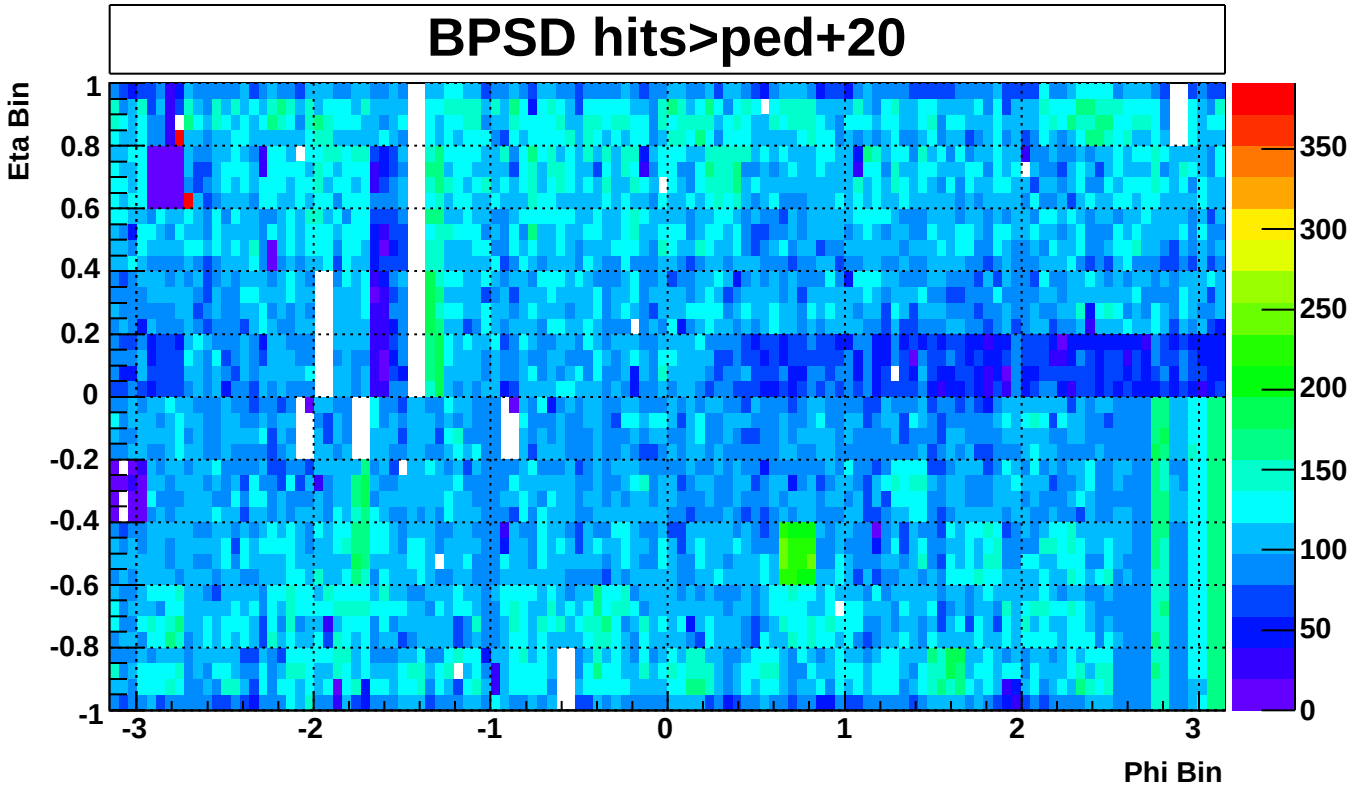


Fri May 27 10:16:43 2011

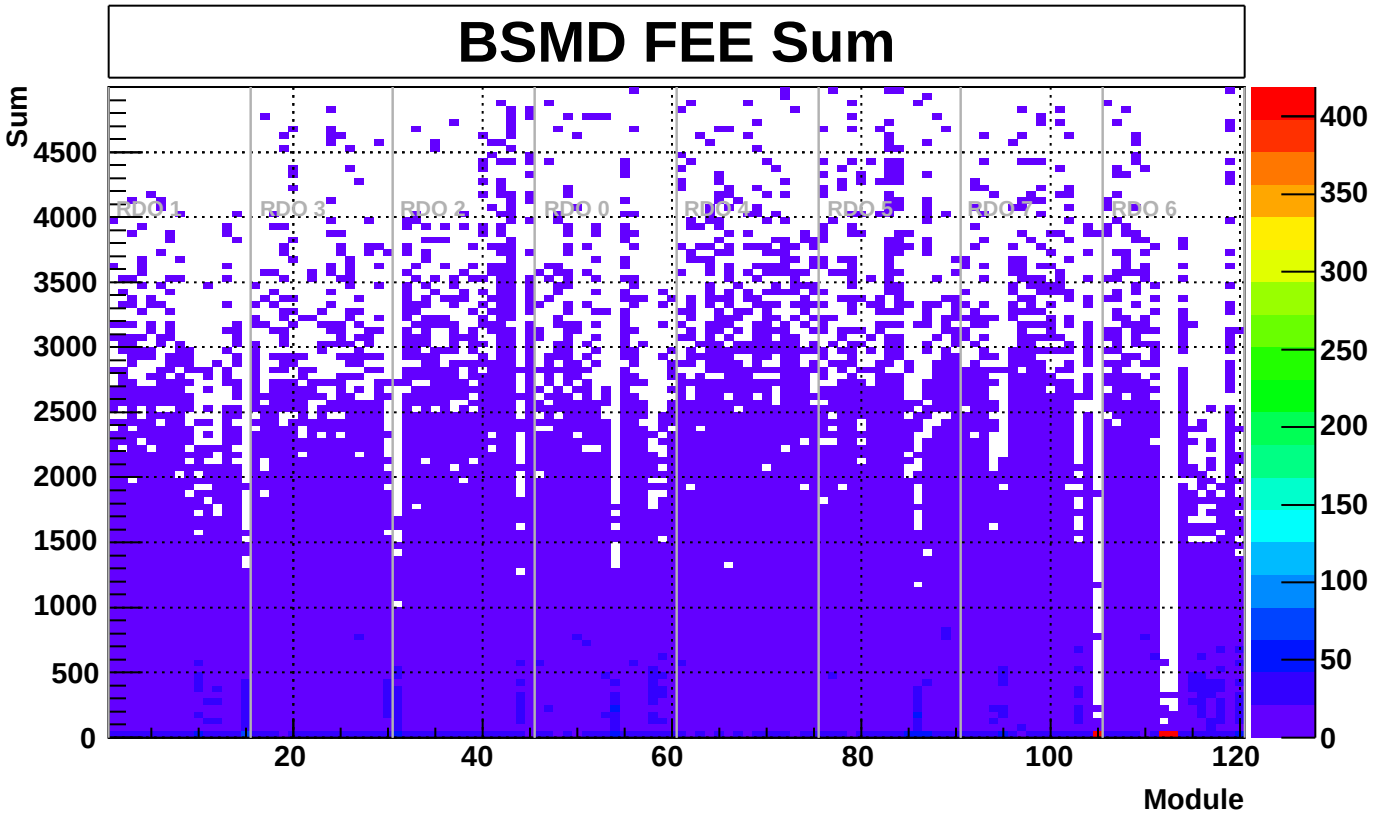


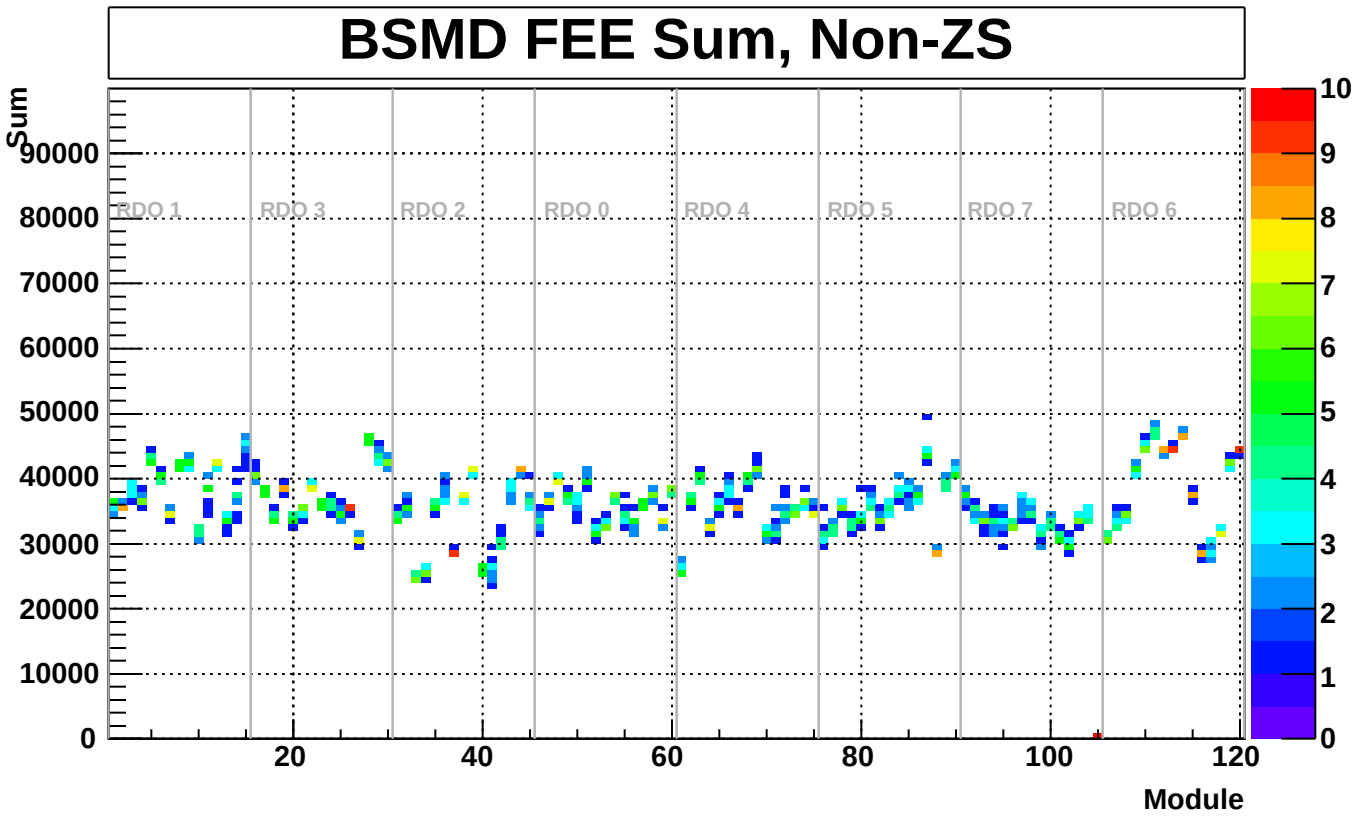
Fri May 27 10:16:44 2011



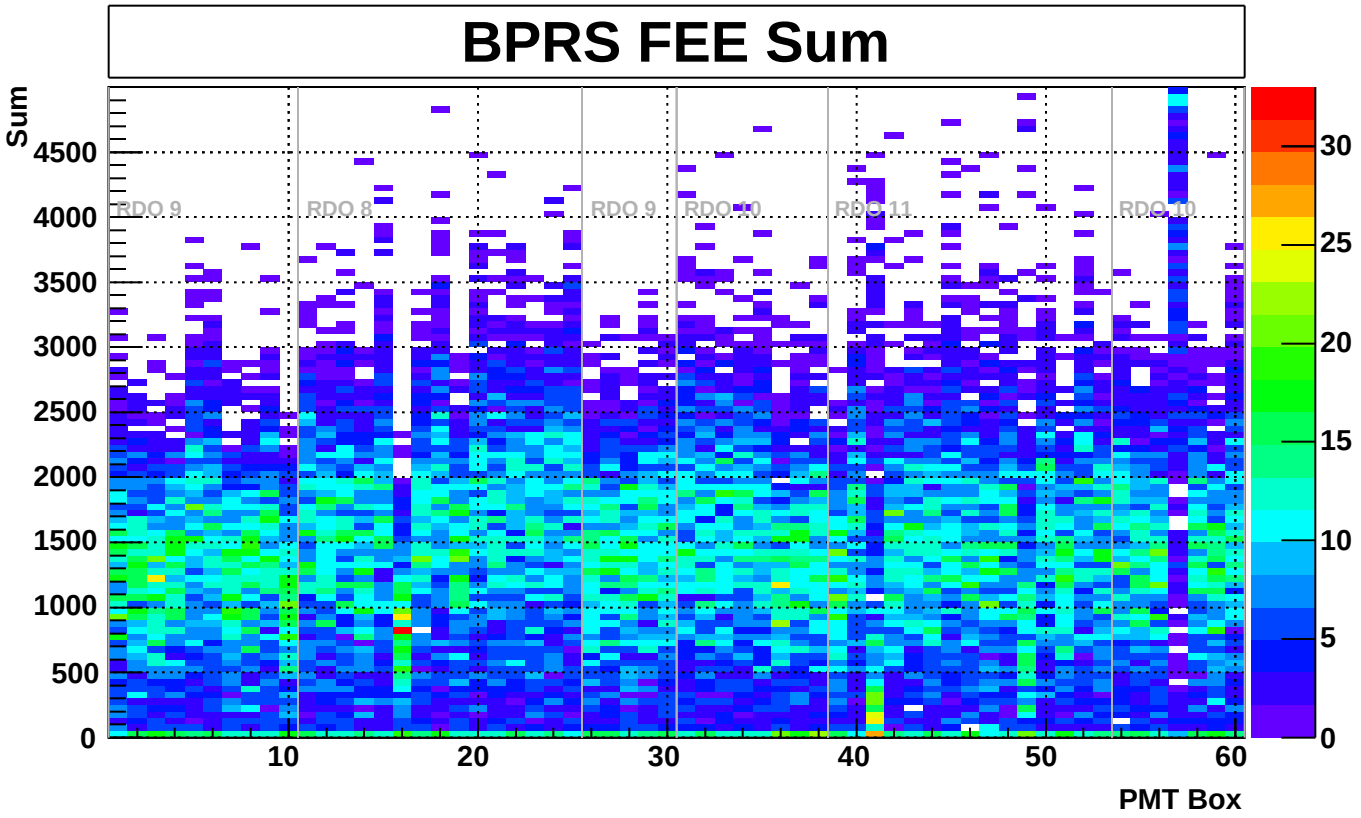


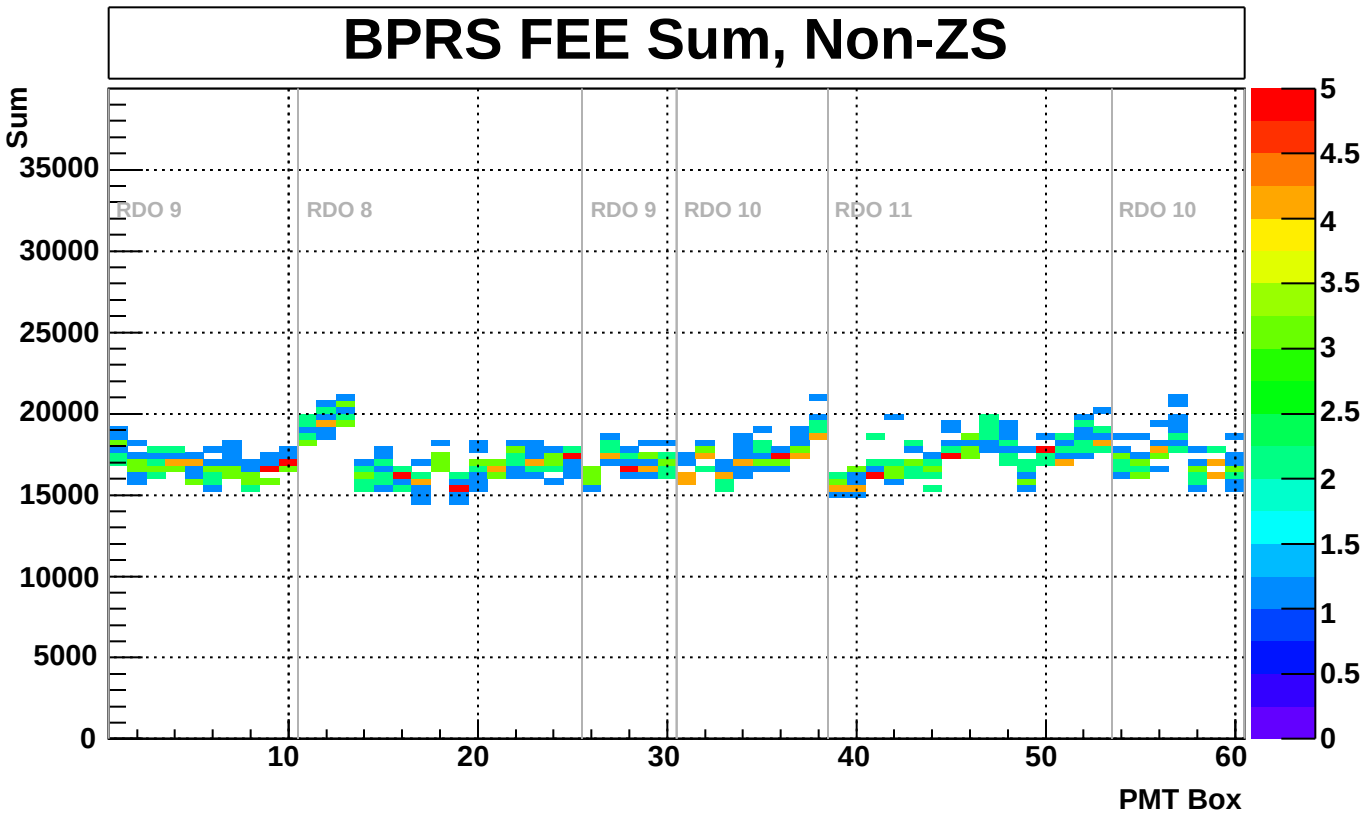
Fri May 27 10:16:45 2011



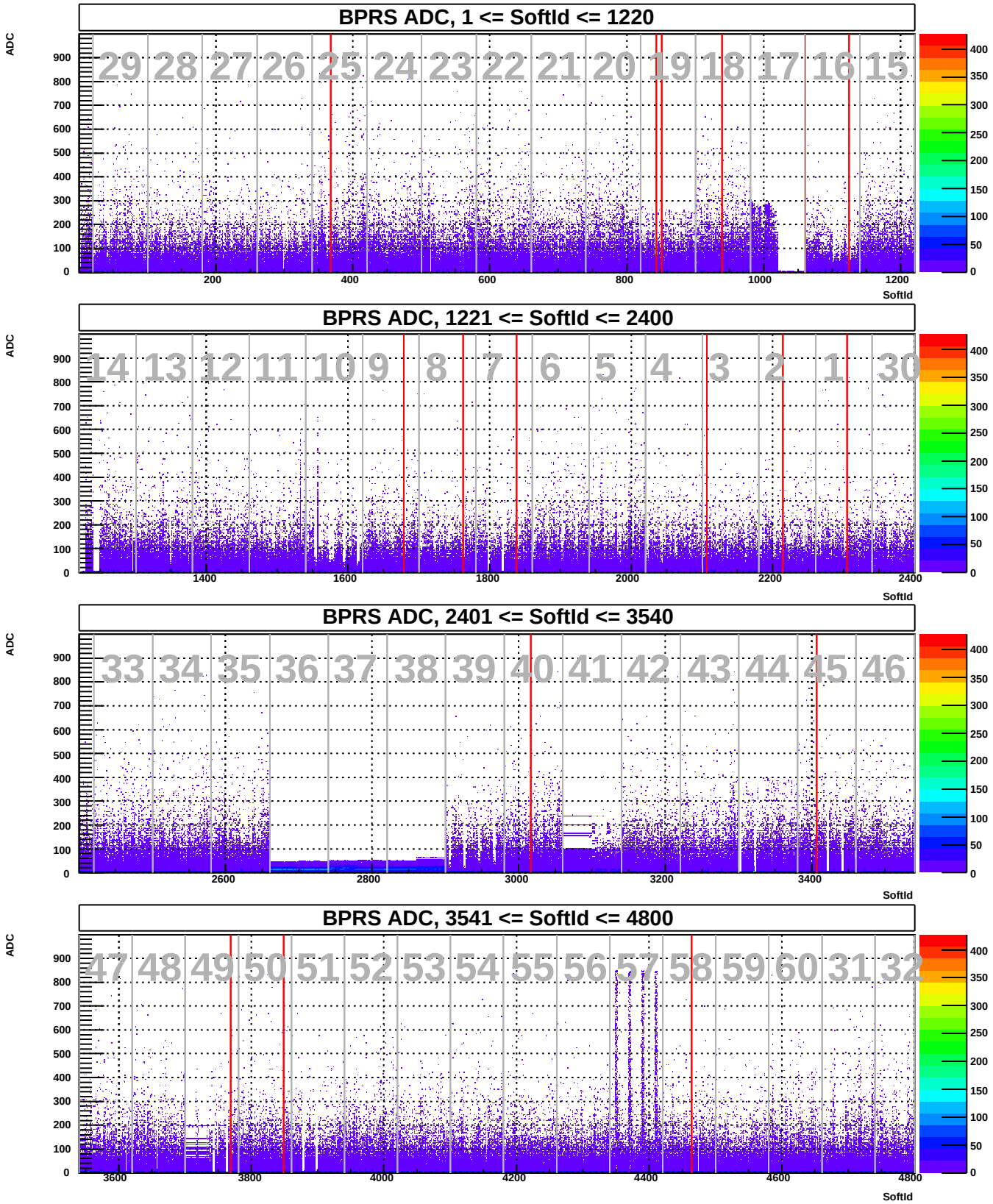


Fri May 27 10:16:47 2011

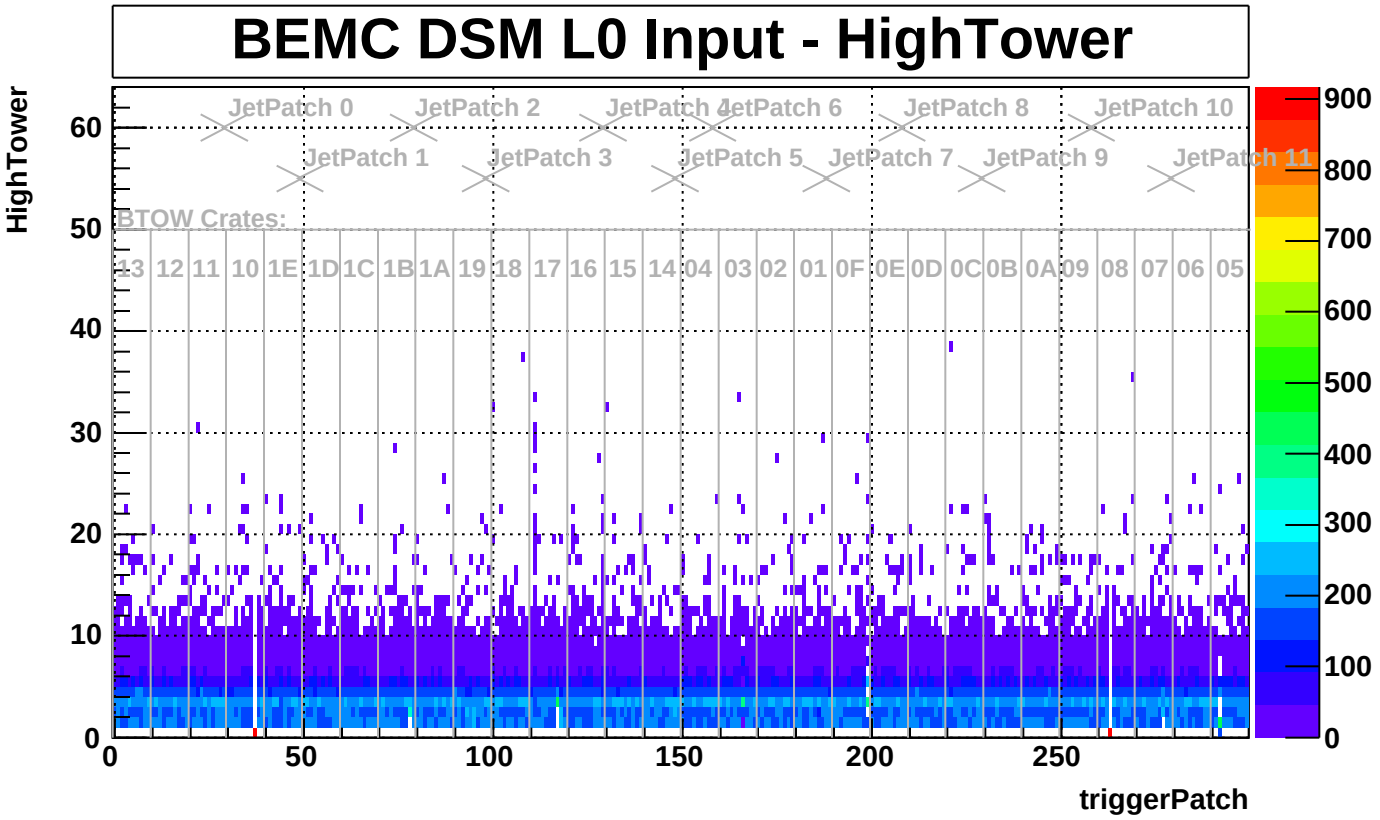


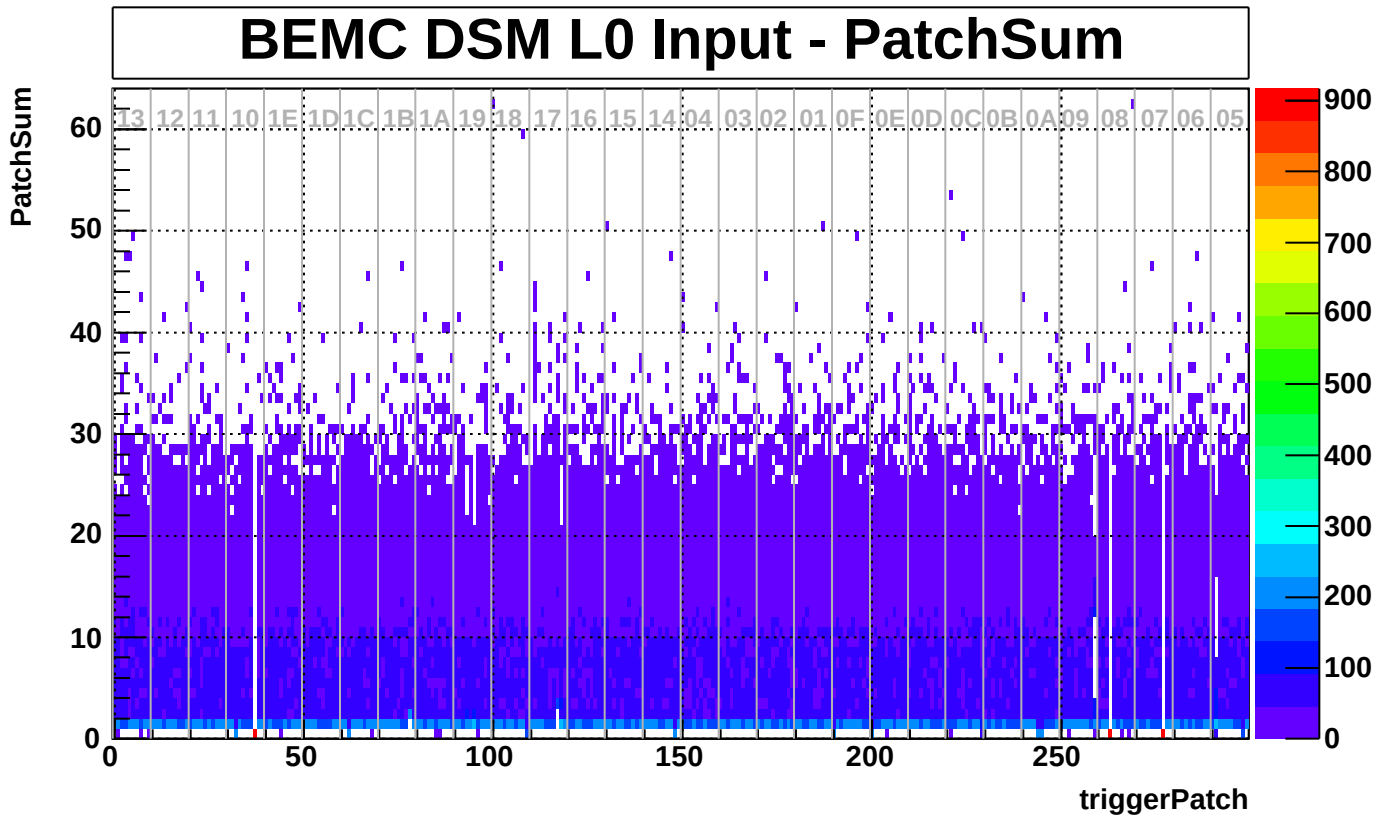


Fri May 27 10:16:48 2011

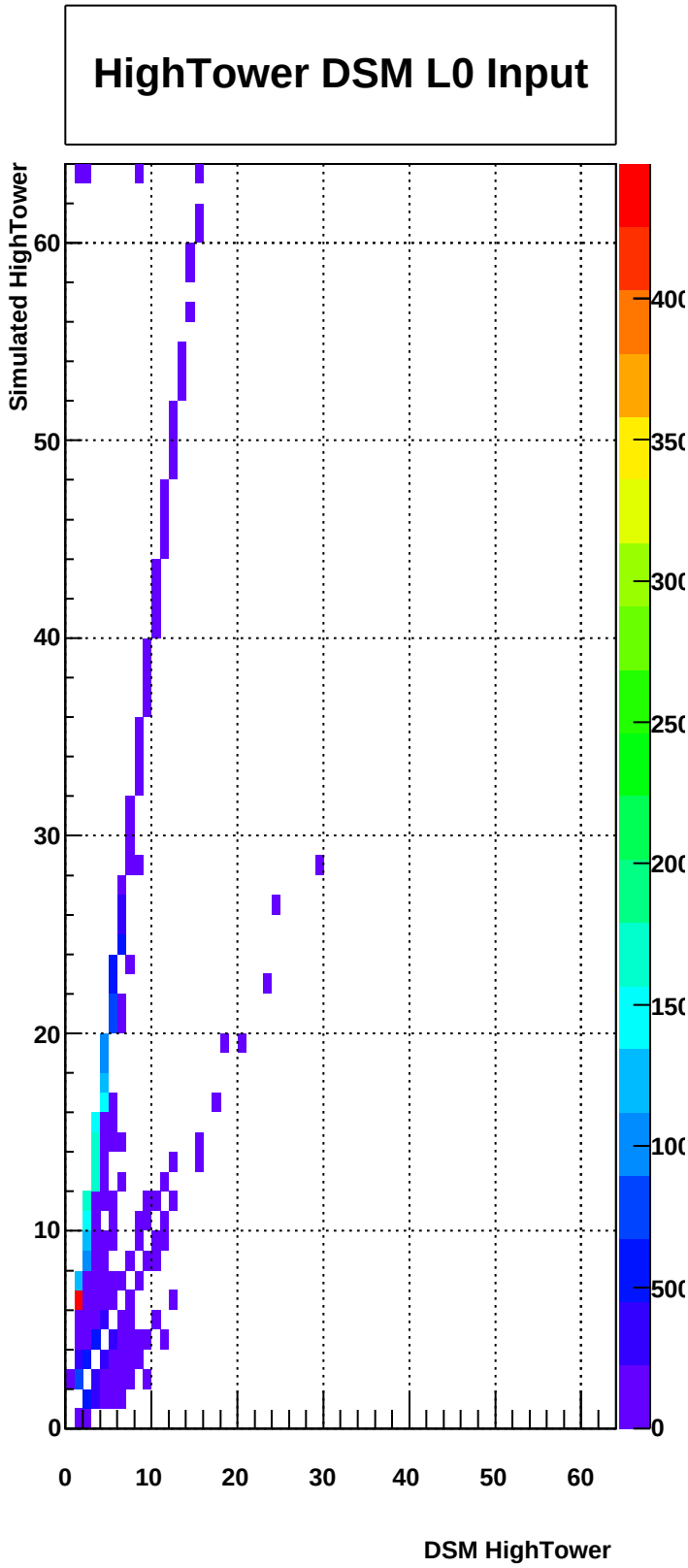


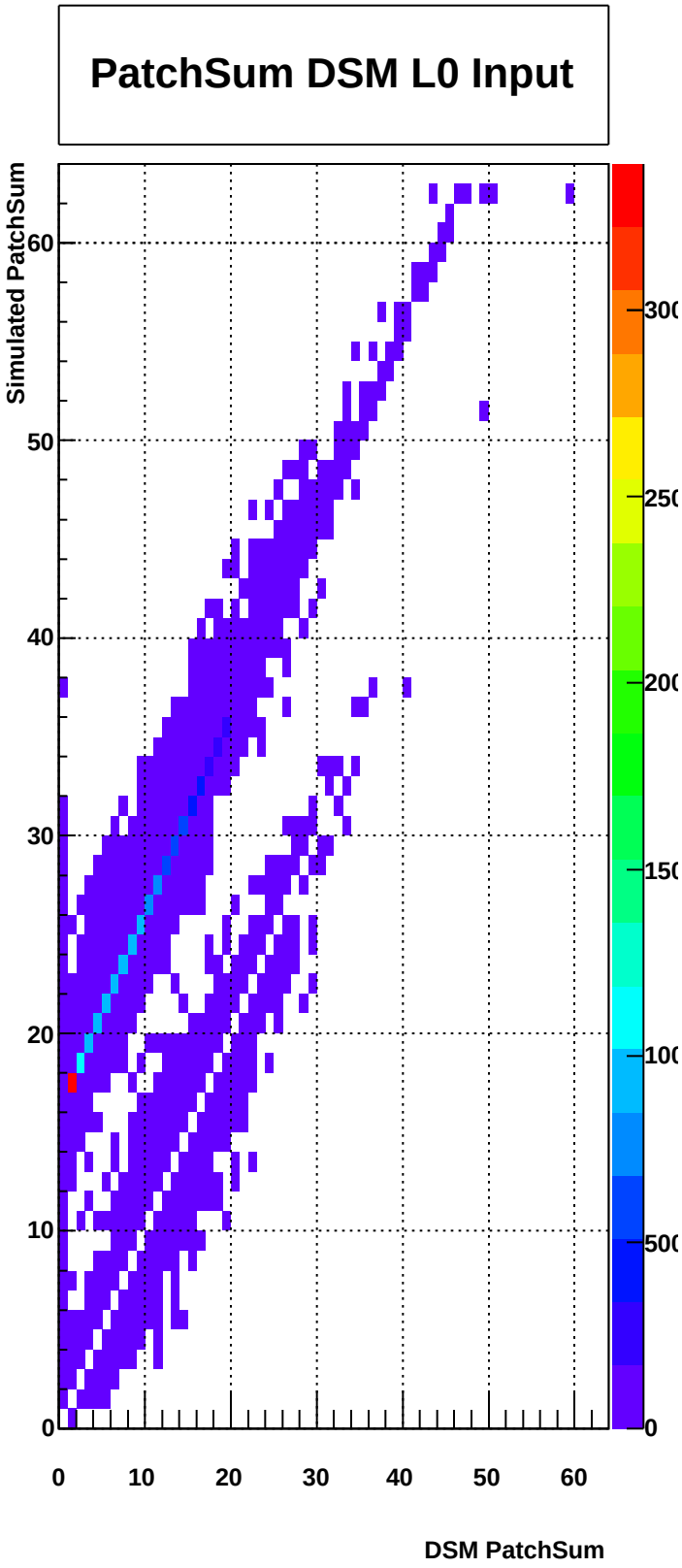
Fri May 27 10:16:48 2011



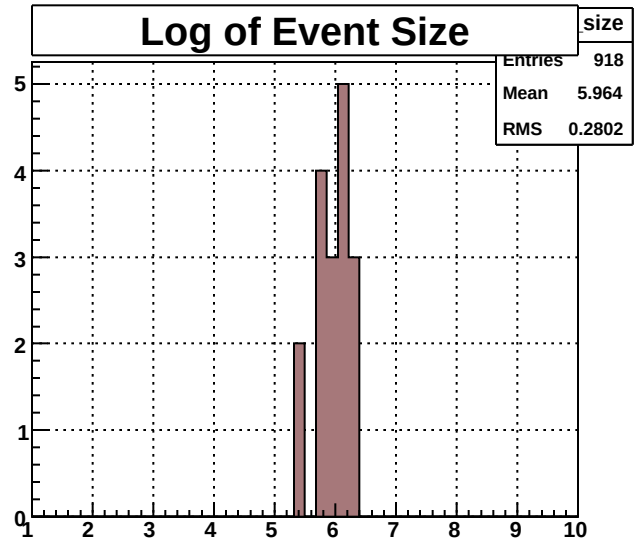


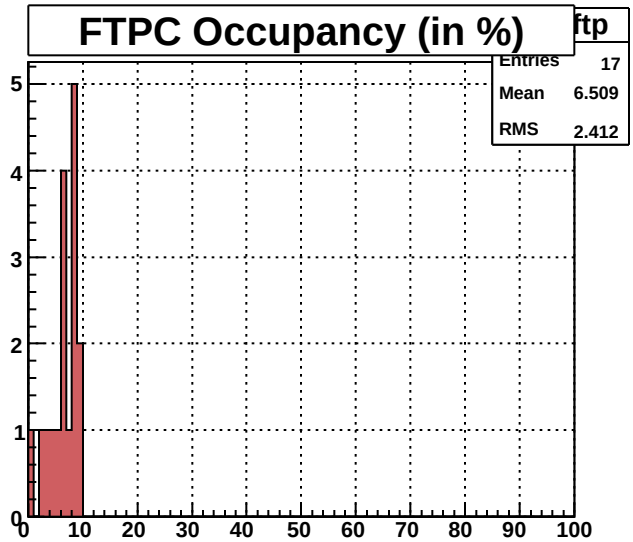
Fri May 27 10:16:49 2011

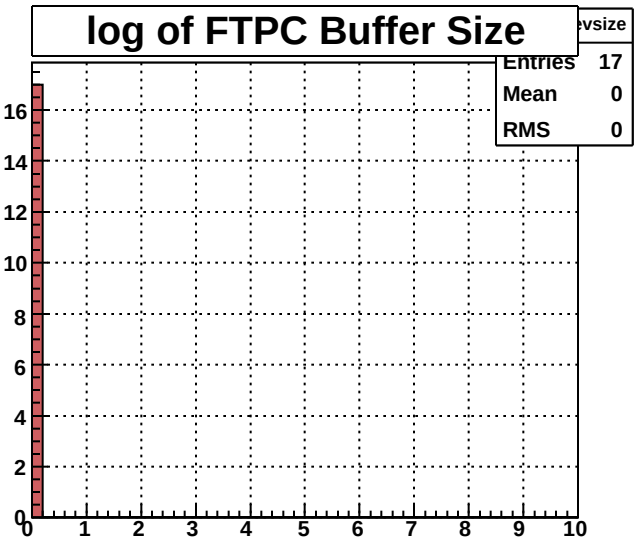


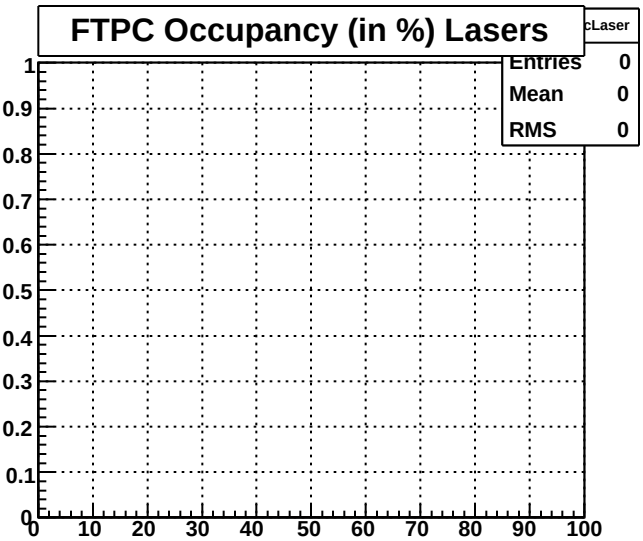


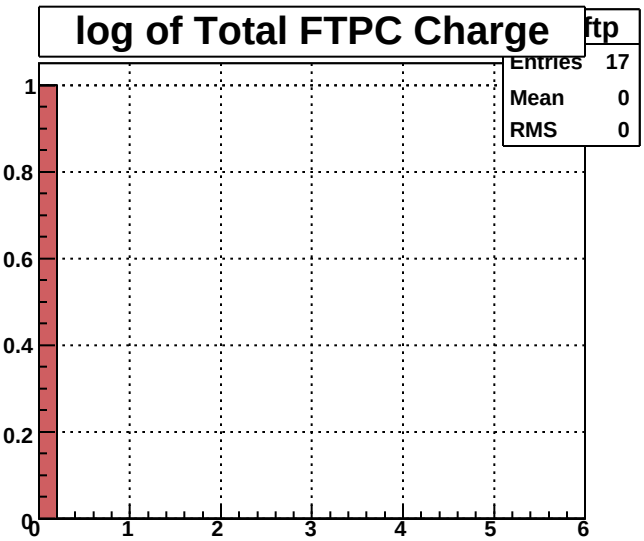
Fri May 27 10:16:49 2011

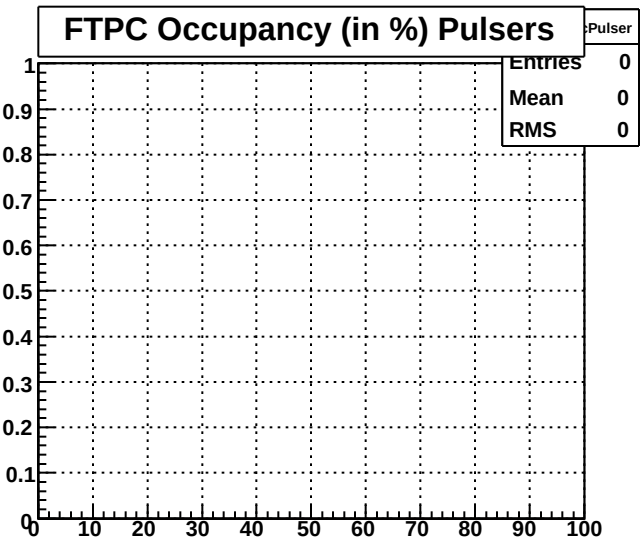

Log of Event Size
size
Entries 918
Mean 5.964
RMS 0.2802

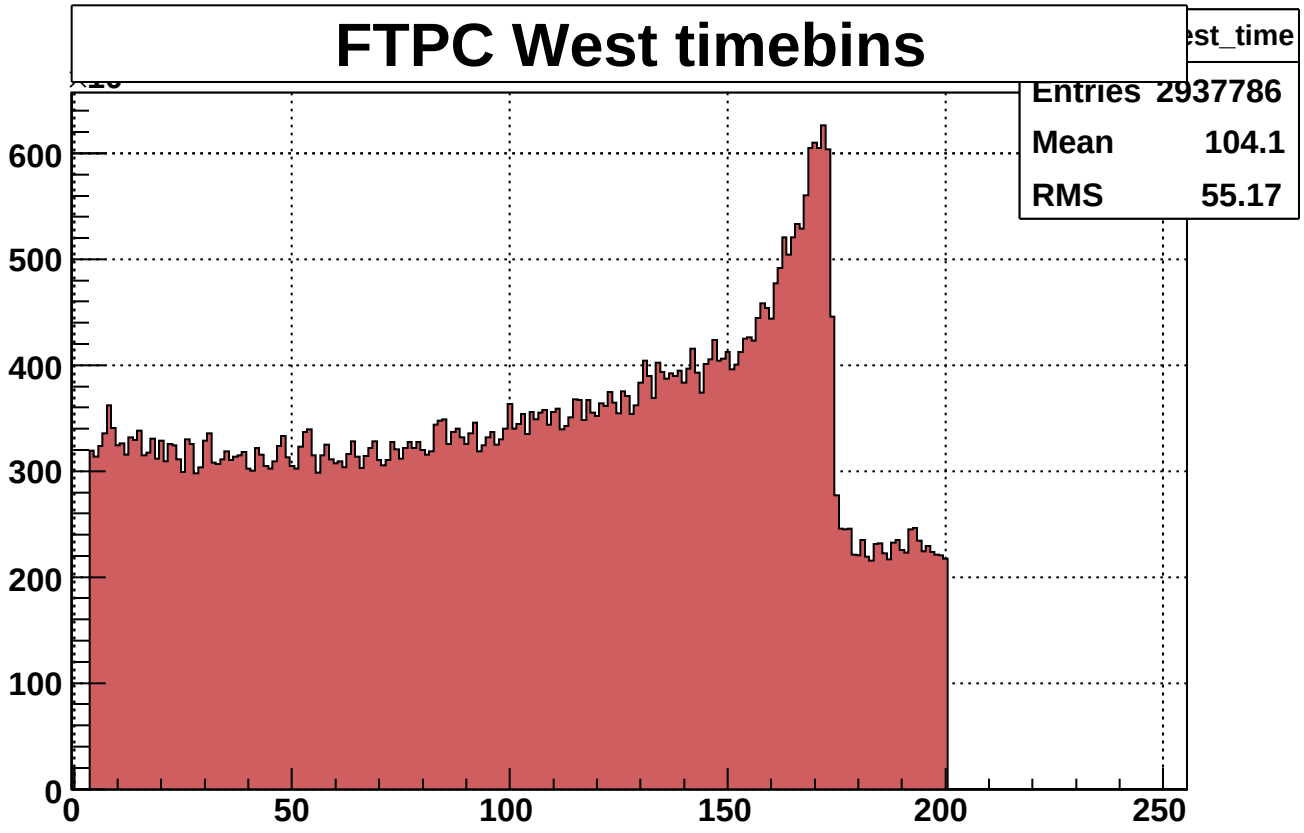

FTPC Occupancy (in %)
ftp
Entries 17
Mean 6.509
RMS 2.412

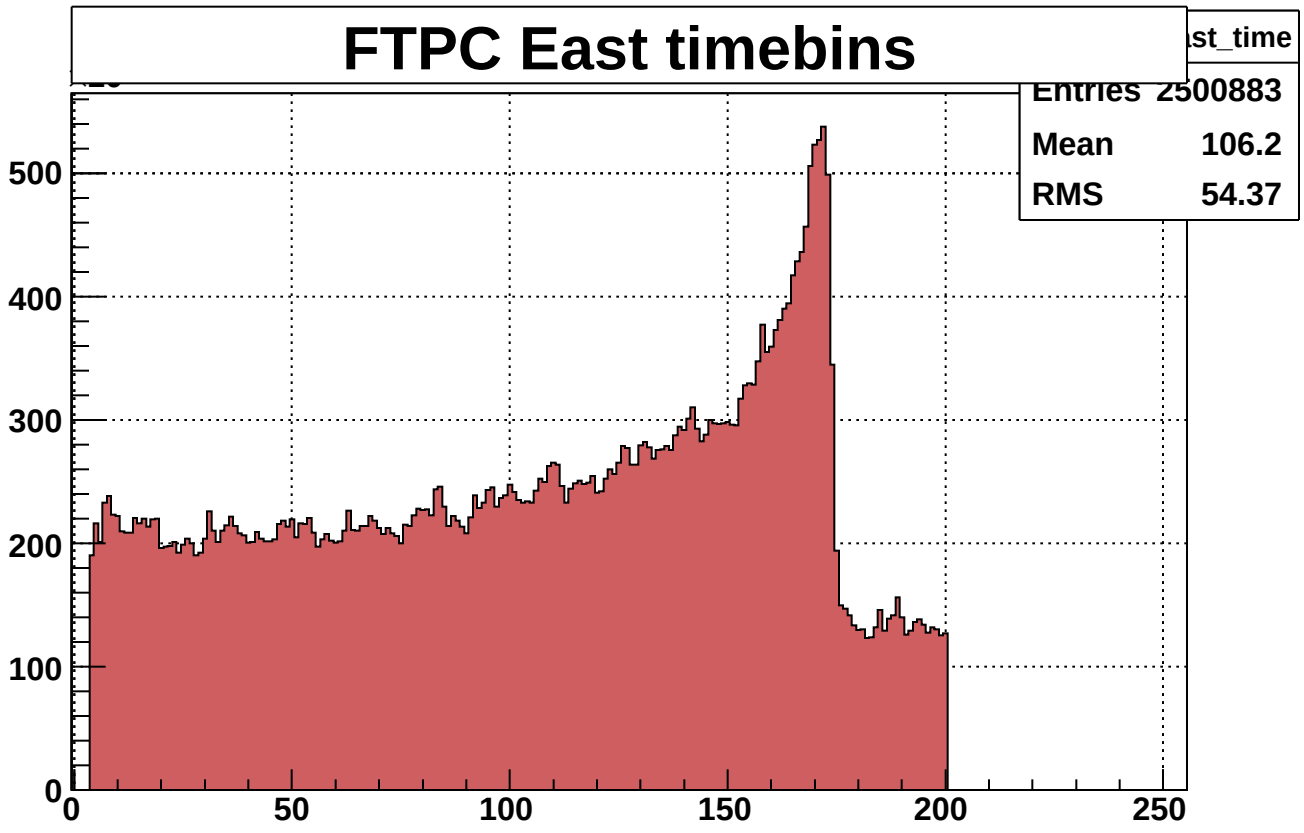

log of FTPC Buffer Size
evsize
Entries 17
Mean 0
RMS 0

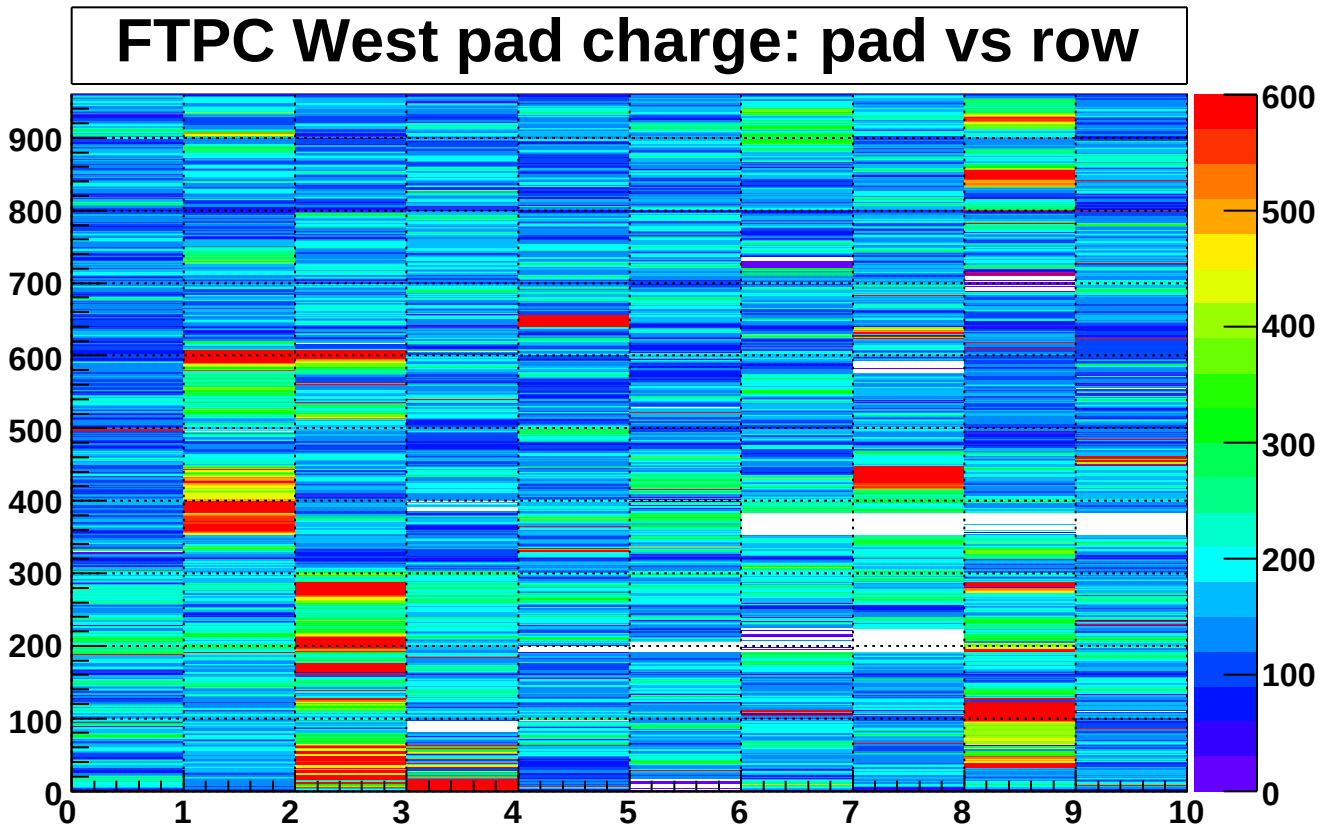

FTPC Occupancy (in %) Lasers
cLaser
Entries 0
Mean 0
RMS 0

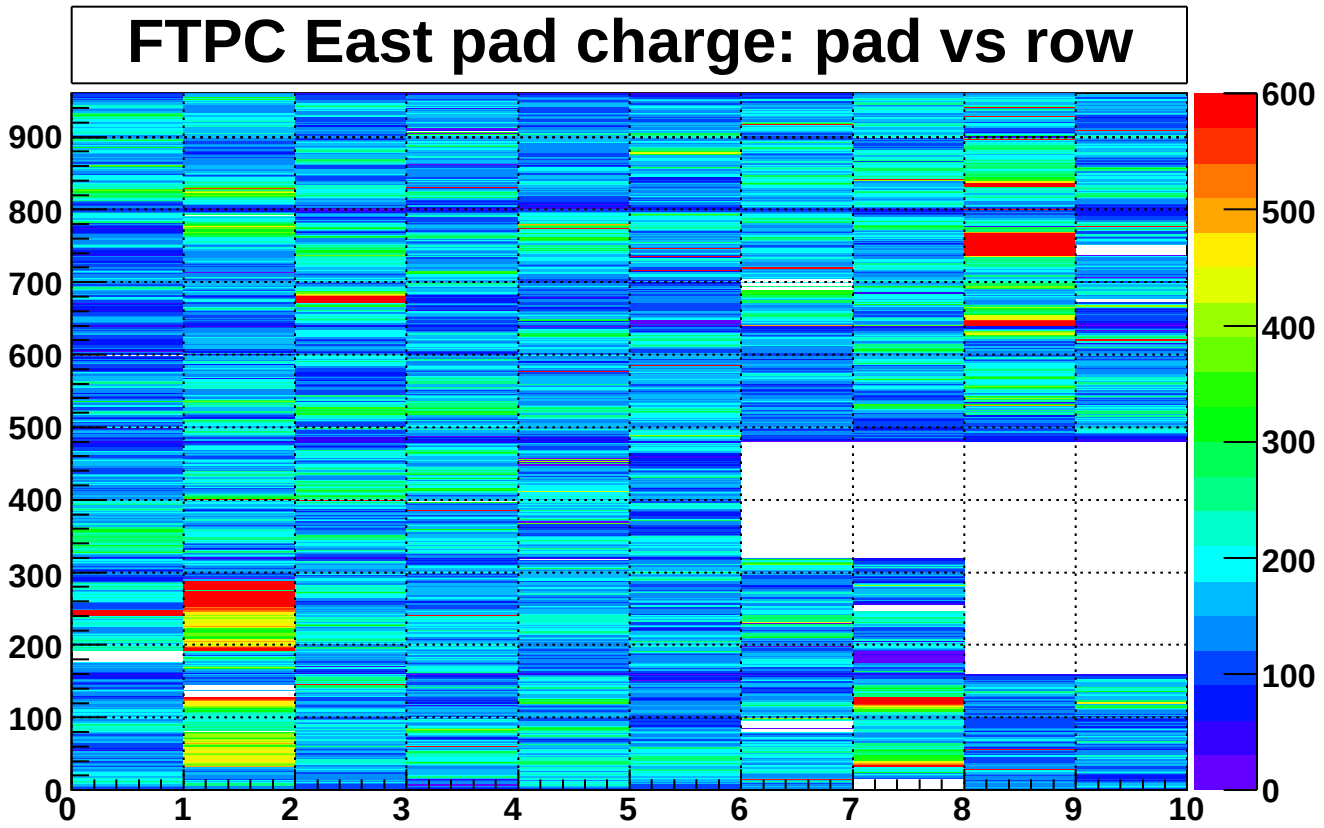

log of Total FTPC Charge
ftp
Entries 17
Mean 0
RMS 0

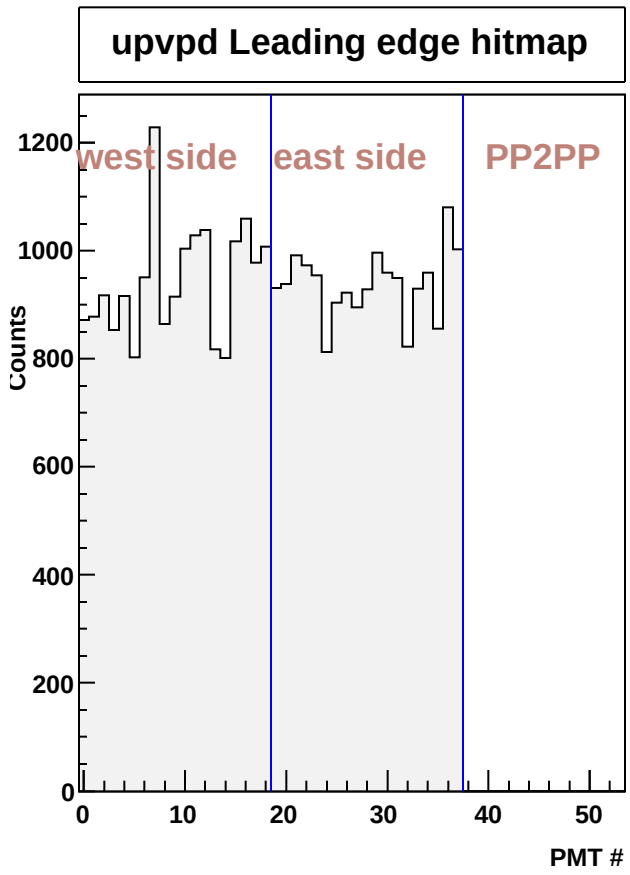

FTPC Occupancy (in %) Pulsers
cPulser
Entries 0
Mean 0
RMS 0

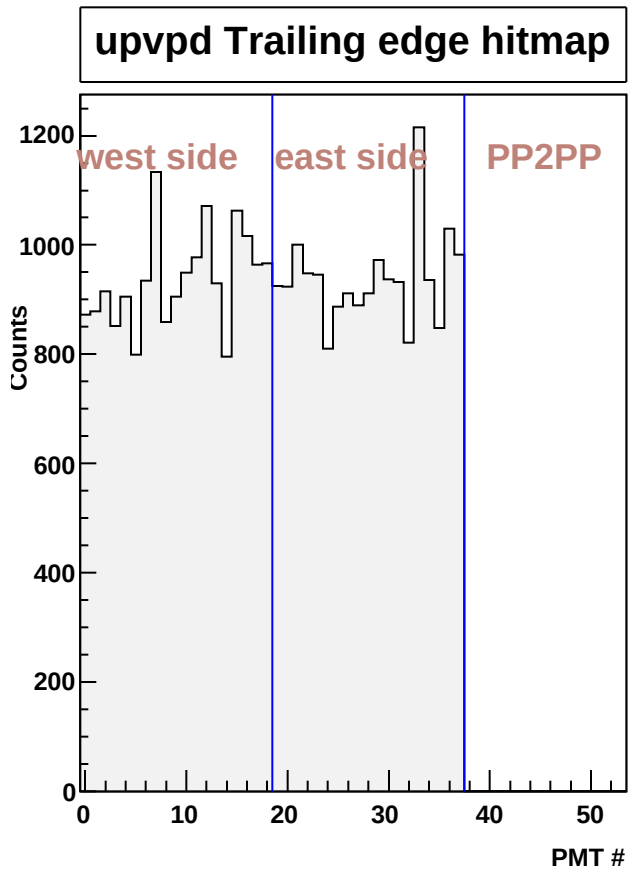

FTPC West timebins
est_time
Entries 2937786
Mean 104.1
RMS 55.17
600
500
400
300
200
100
0
0
50
100
150
200
250

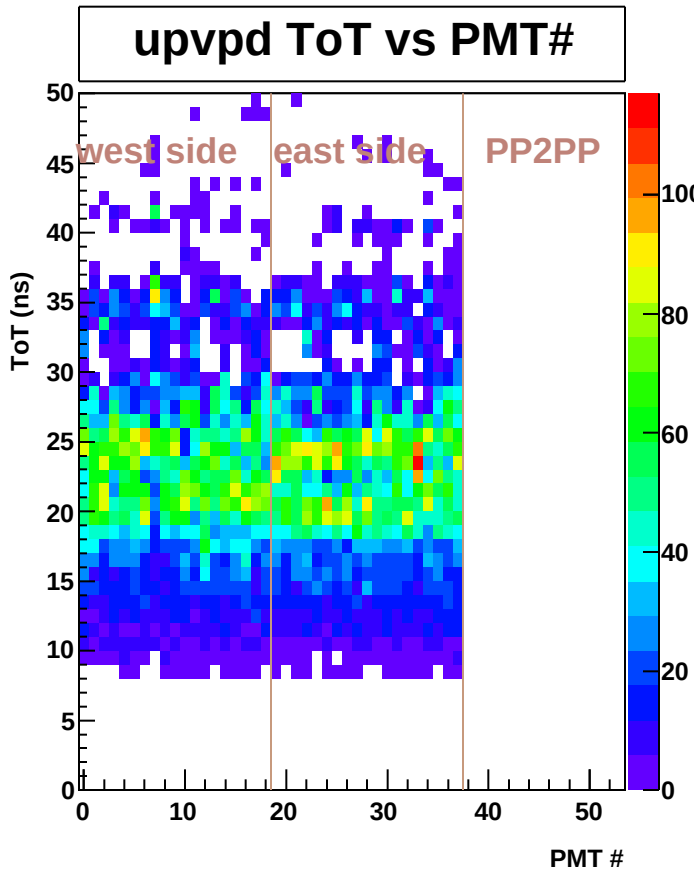

FTPC East timebins
st_time
Entries 2500883
Mean 106.2
RMS 54.37
500
400
300
200
100
0
0
50
100
150
200
250

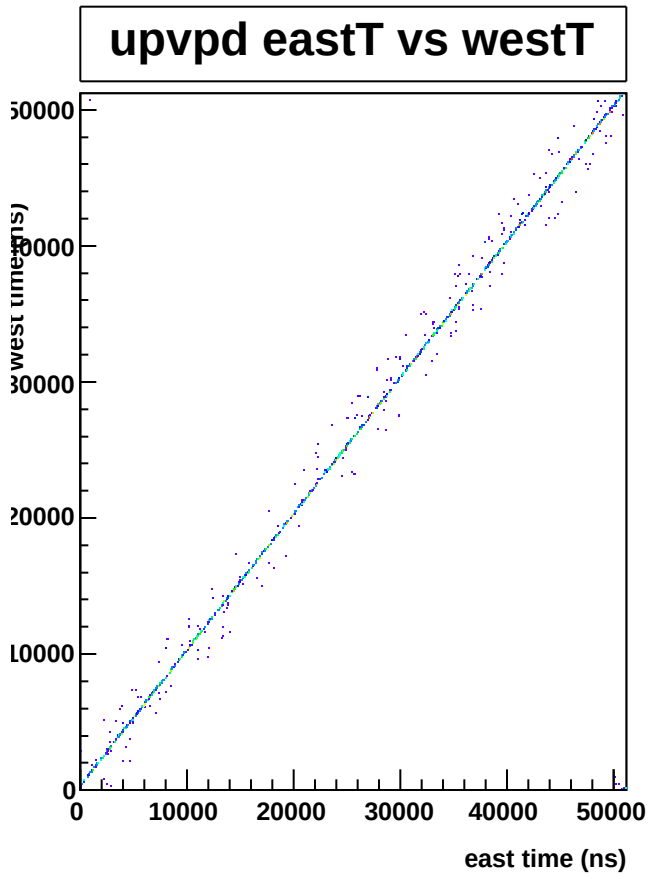

FTPC West pad charge: pad vs row
900
800
700
600
500
400
300
200
100
0
0
1
2
3
4
5
6
7
8
9
10
600
500
400
300
200
100
0

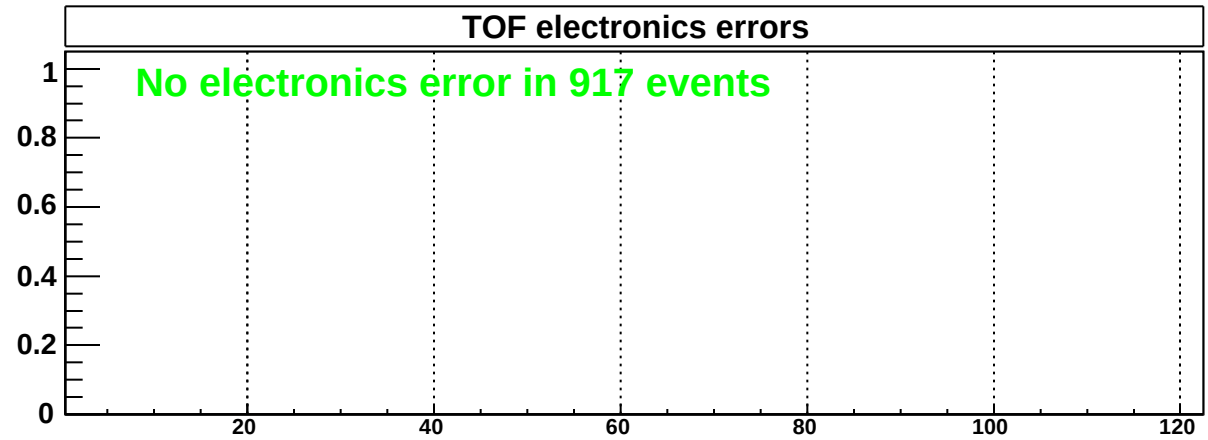

FTPC East pad charge: pad vs row
900
800
700
600
500
400
300
200
100
0
0
1
2
3
4
5
6
7
8
9
10
600
500
400
300
200
100
0

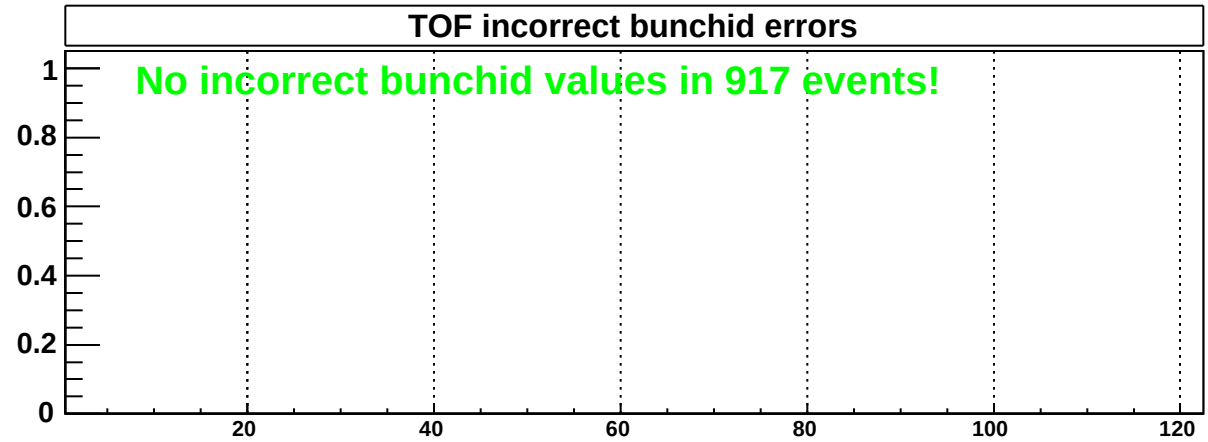

upvpd Leading edge hitmap
west side
east side
PP2PP
Counts
PMT #

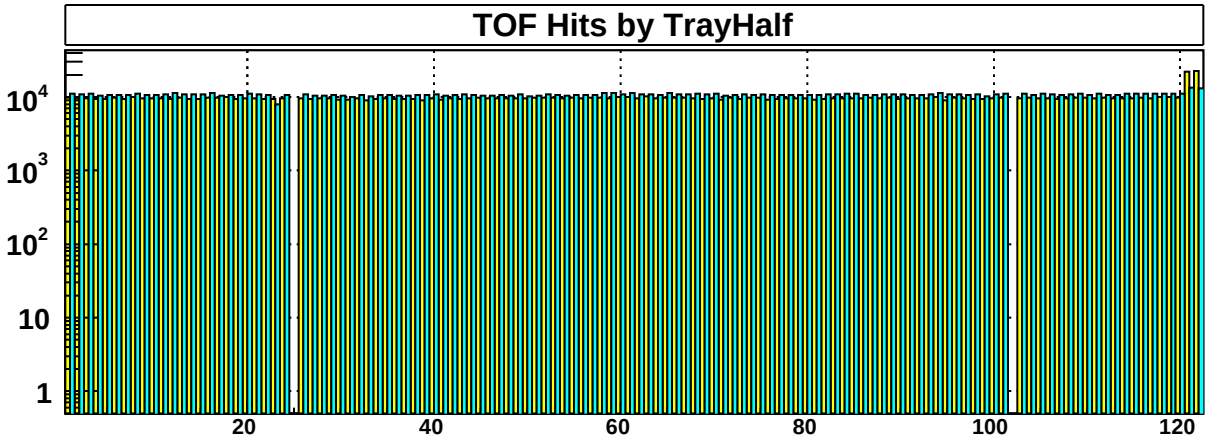

upvpd Trailing edge hitmap
west side
east side
PP2PP
Counts
PMT #

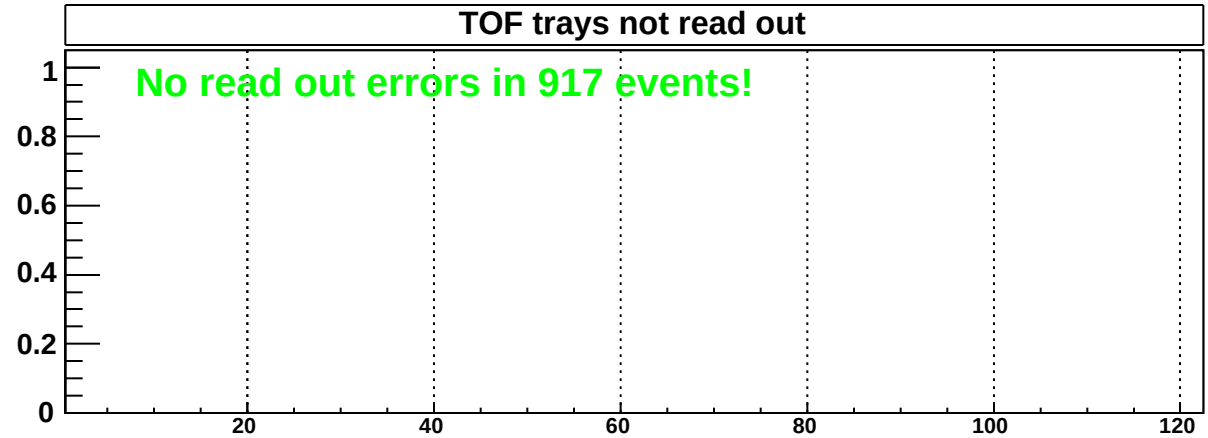

upvpd ToT vs PMT#
west side
east side
PP2PP
ToT (ns)
PMT #

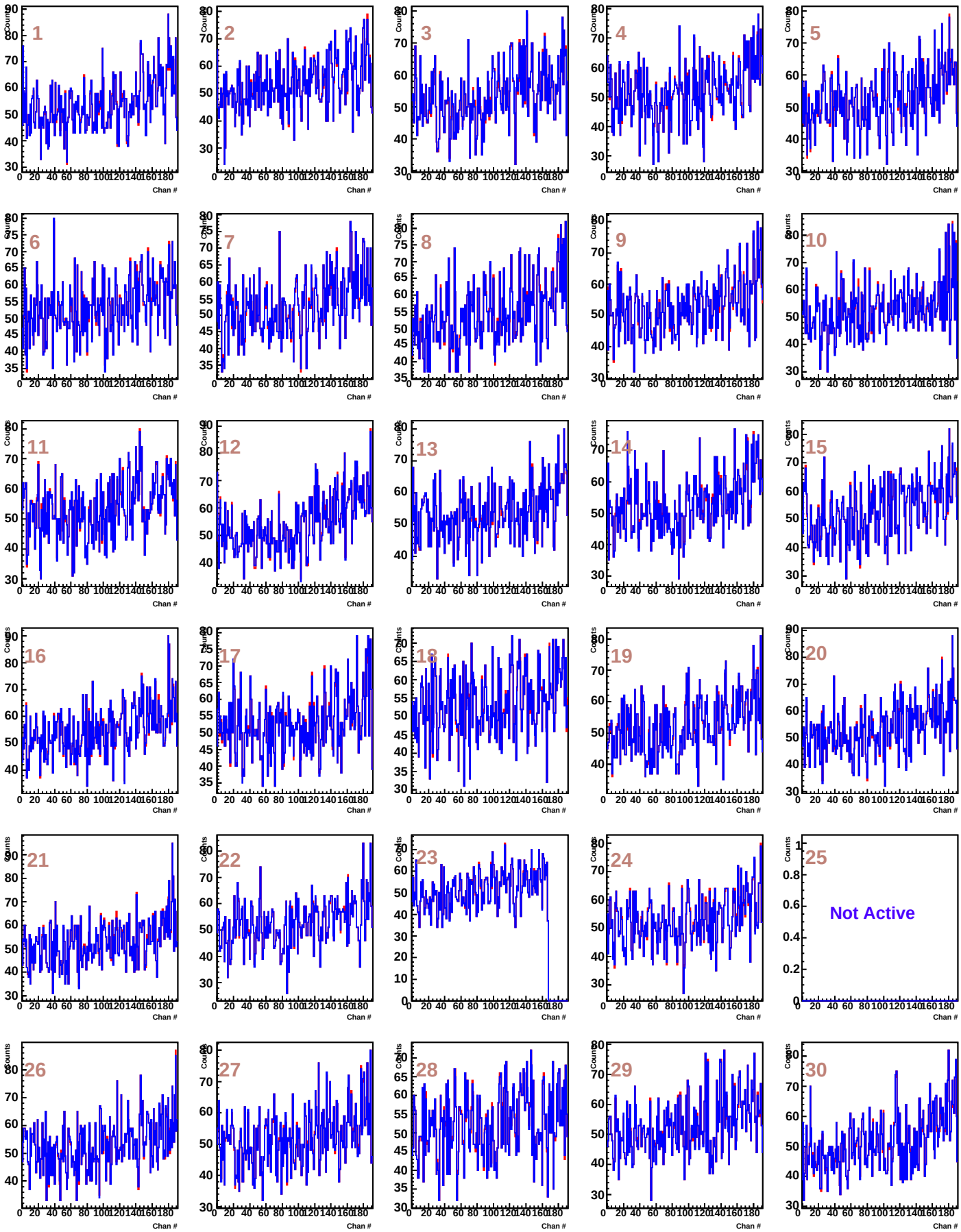

upvpd eastT vs westT
east time (ns)

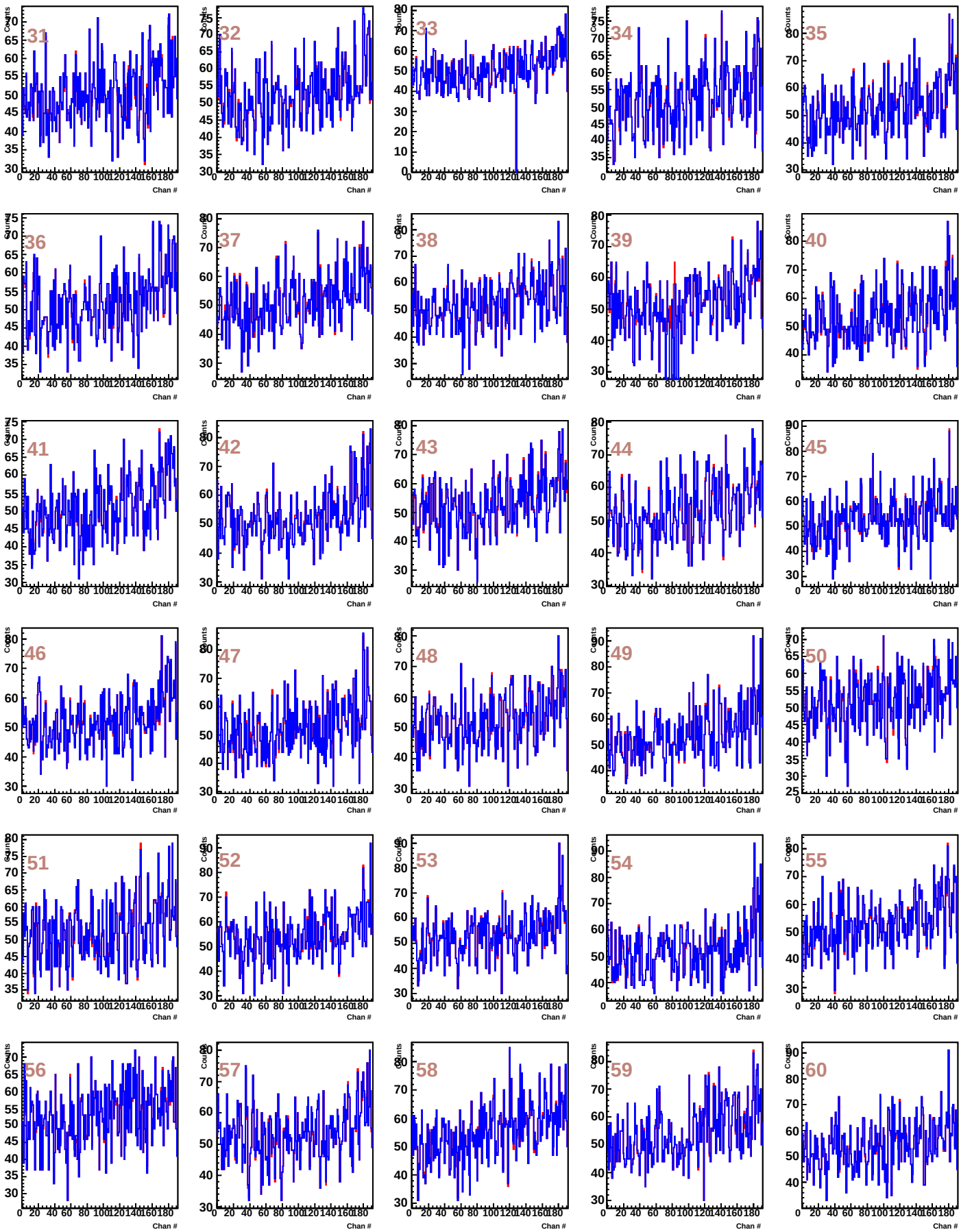

TOF electronics errors
No electronics error in 917 events
1
0.8
0.6
0.4
0.2
0
20
40
60
80
100
120


TOF incorrect bunchid errors
No incorrect bunchid values in 917 events!
1
0.8
0.6
0.4
0.2
0
20
40
60
80
100
120


TOF Hits by TrayHalf
10^4
10^3
10^2
10
1
20
40
60
80
100
120


TOF trays not read out
No read out errors in 917 events!
1
0.8
0.6
0.4
0.2
0
20
40
60
80
100
120


1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23
24
25
Not Active
26
27
28
29
30
Counts
Chan #


31
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37
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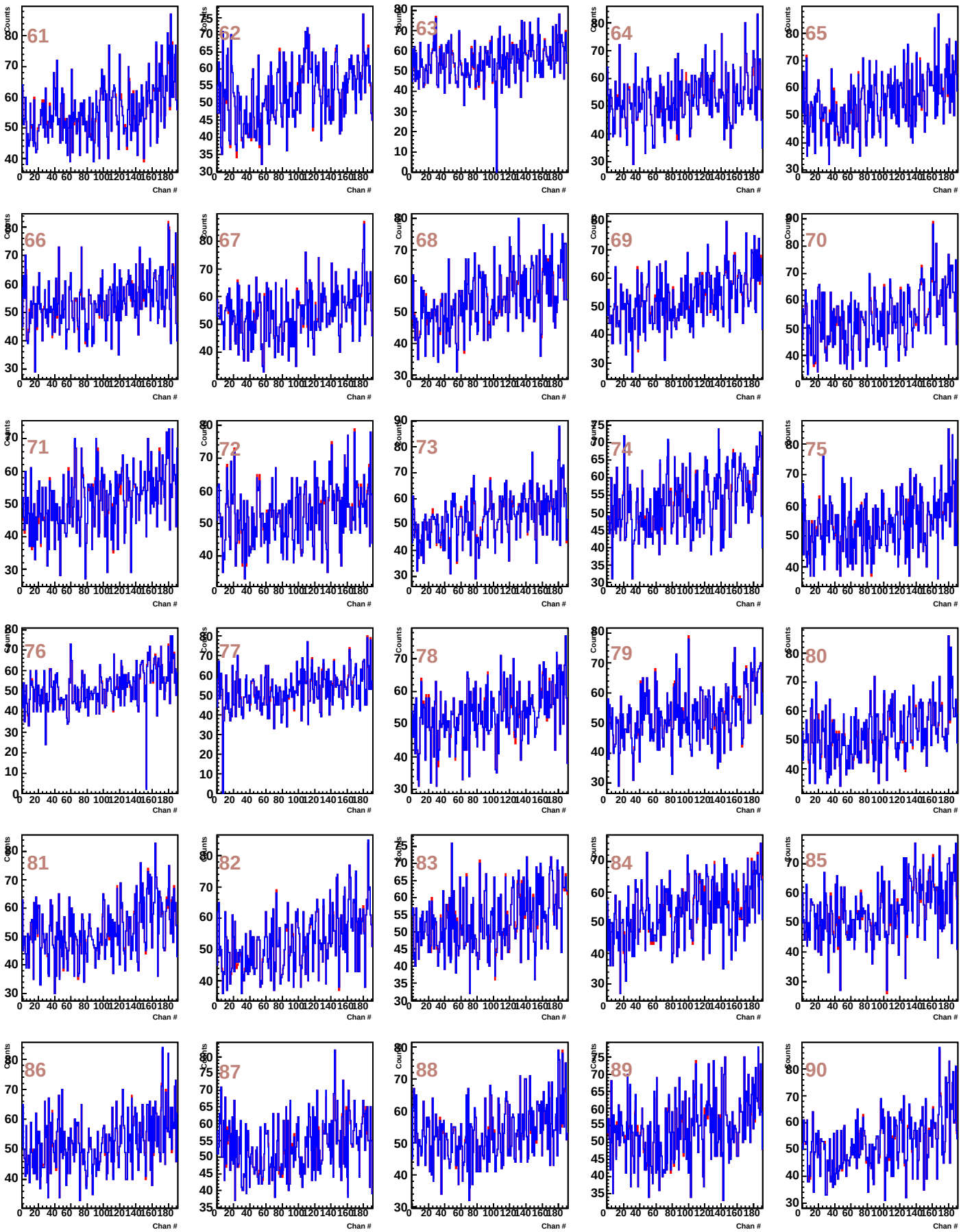
39
40
41
42
43
44
45
46
47
48
49
50
51
52
53
54
55
56
57
58
59
60
Counts
Chan #


61
62
63
64
65
66
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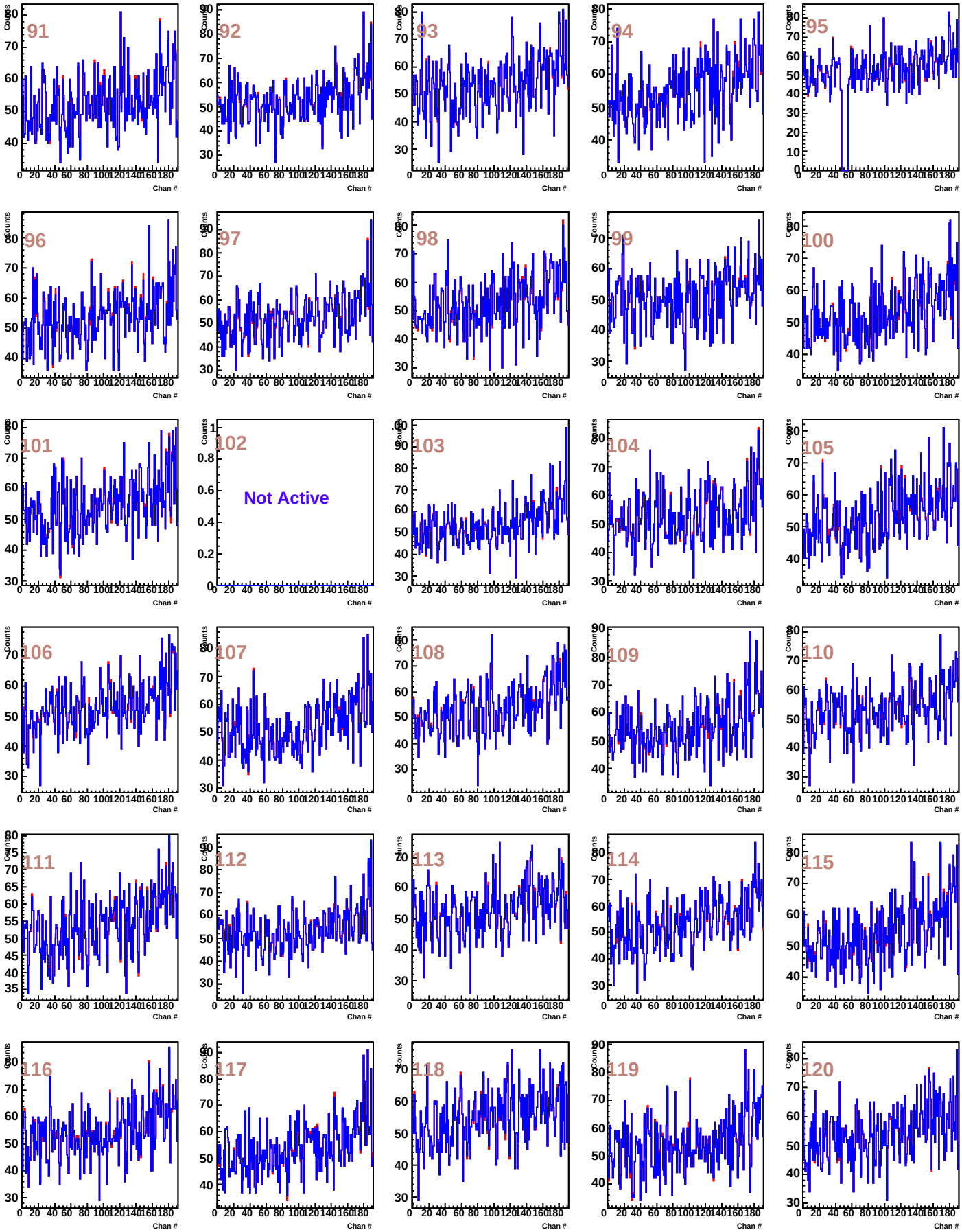
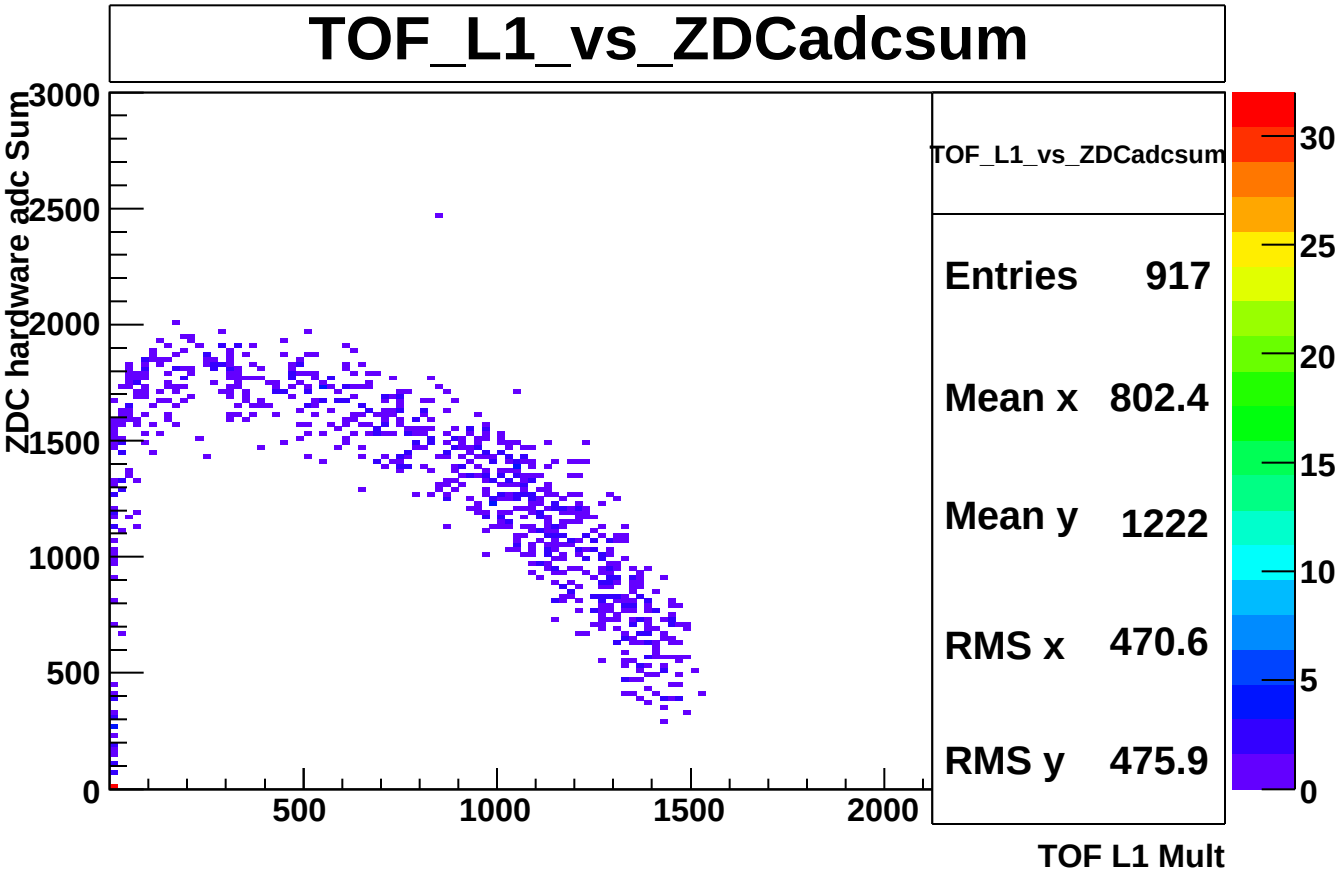
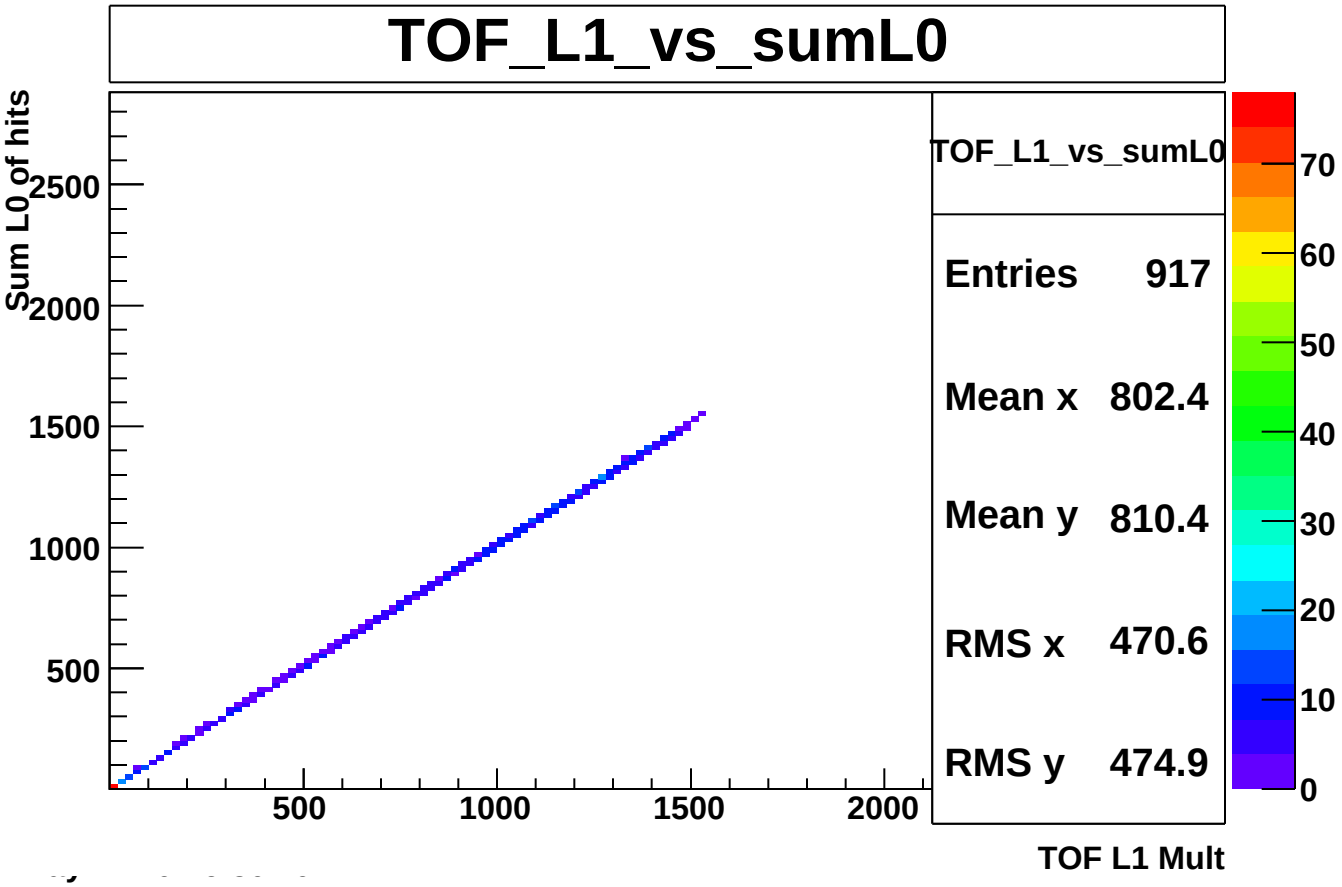
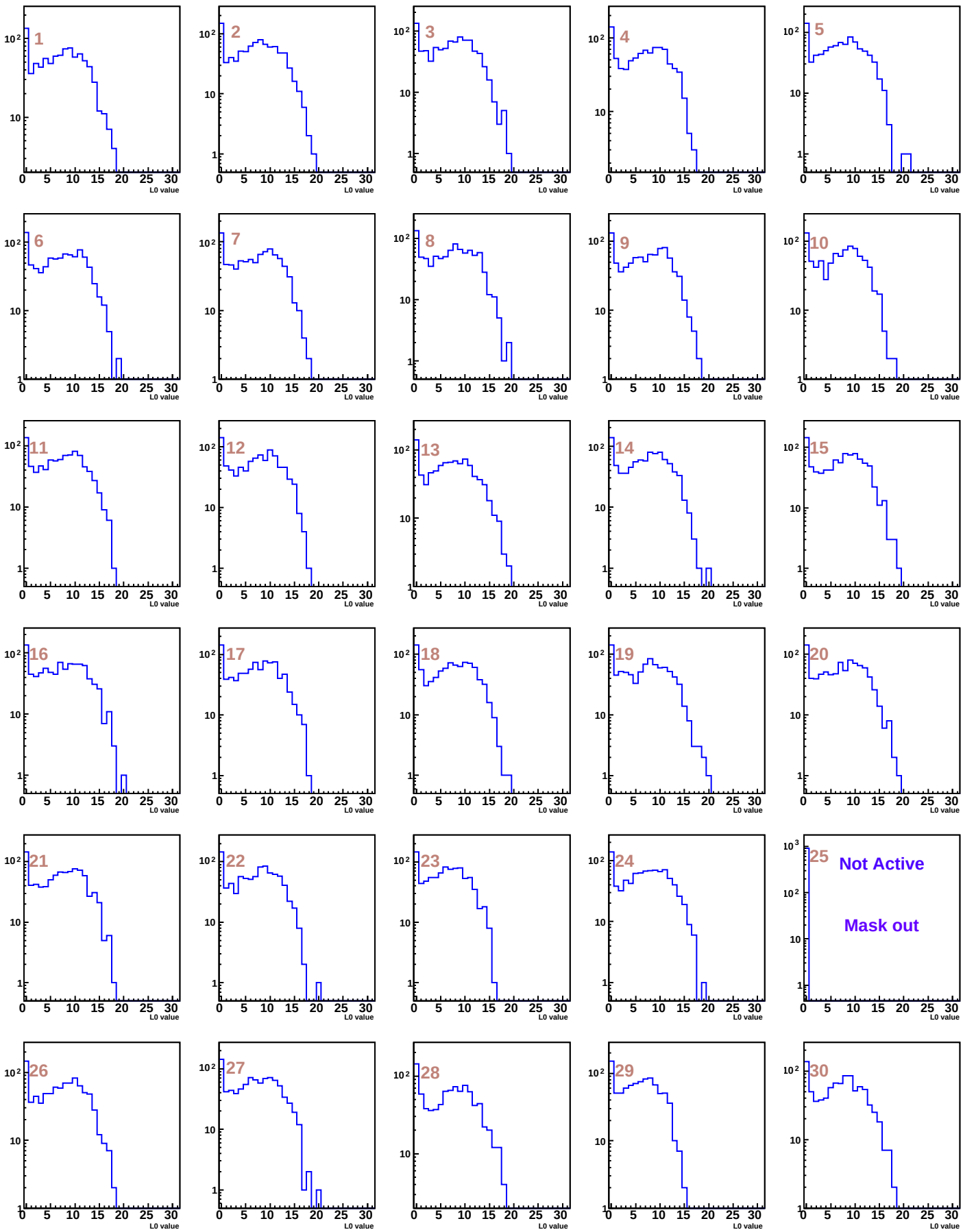
72
73
74
75
76
77
78
79
80
81
82
83
84
85
86
87
88
89
90
Counts
Chan #


91
92
93
94
95
96
97
98
99
100
101
102
Not Active
103
104
105
106
107
108
109
110
111
112
113
114
115
116
117
118
119
120
Counts
Chan #


TOF_L1_vs_ZDCadcsum
ZDC hardware adc Sum
TOF L1 Mult
TOF_L1_vs_ZDCadcsum
Entries 917
Mean x 802.4
Mean y 1222
RMS x 470.6
RMS y 475.9


TOF_L1_vs_sumL0
Sum L0 of hits
TOF L1 Mult
TOF_L1_vs_sumL0
Entries 917
Mean x 802.4
Mean y 810.4
RMS x 470.6
RMS y 474.9


1
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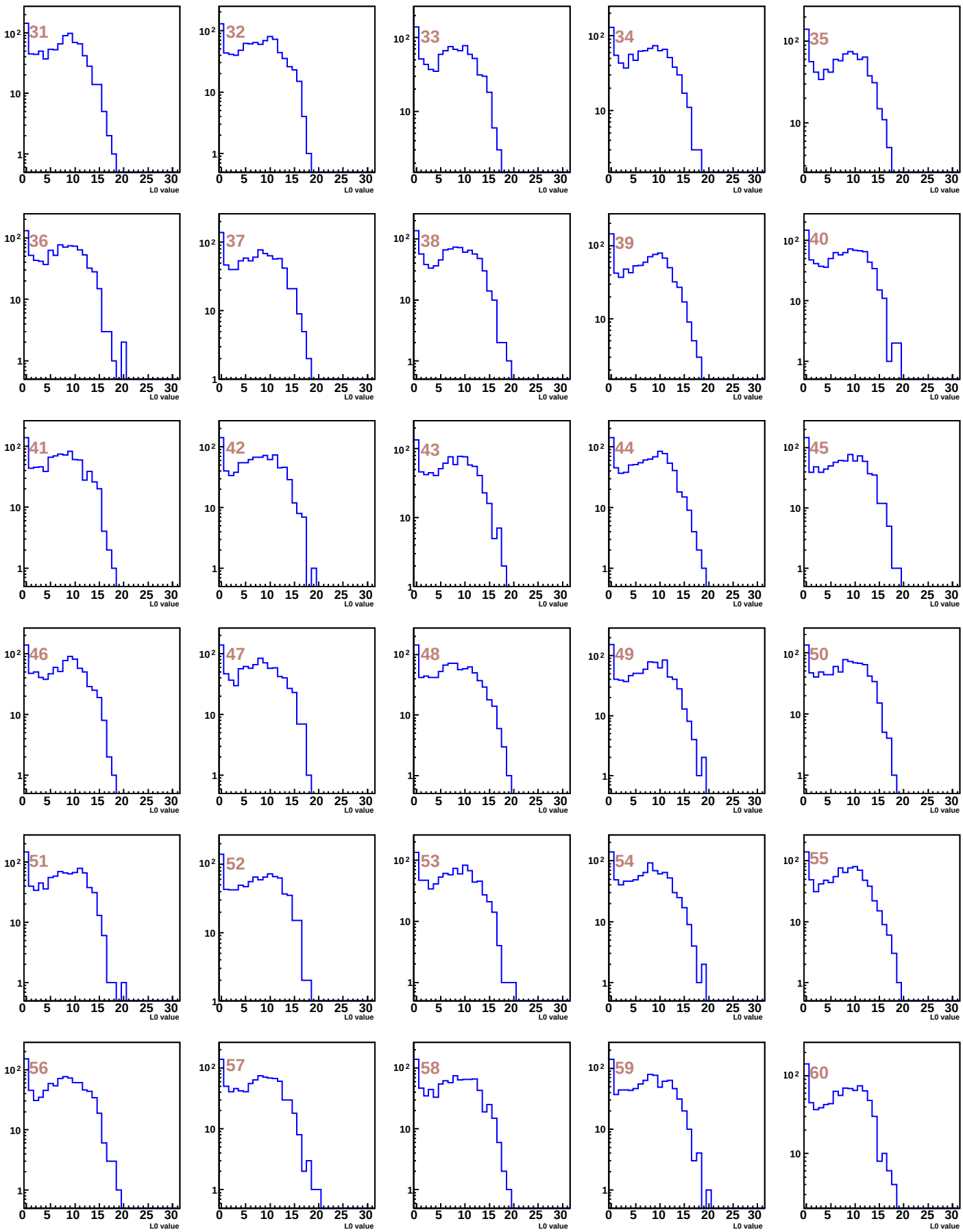
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23
24
25
Not Active
Mask out
26
27
28
29
30
L0 value


31
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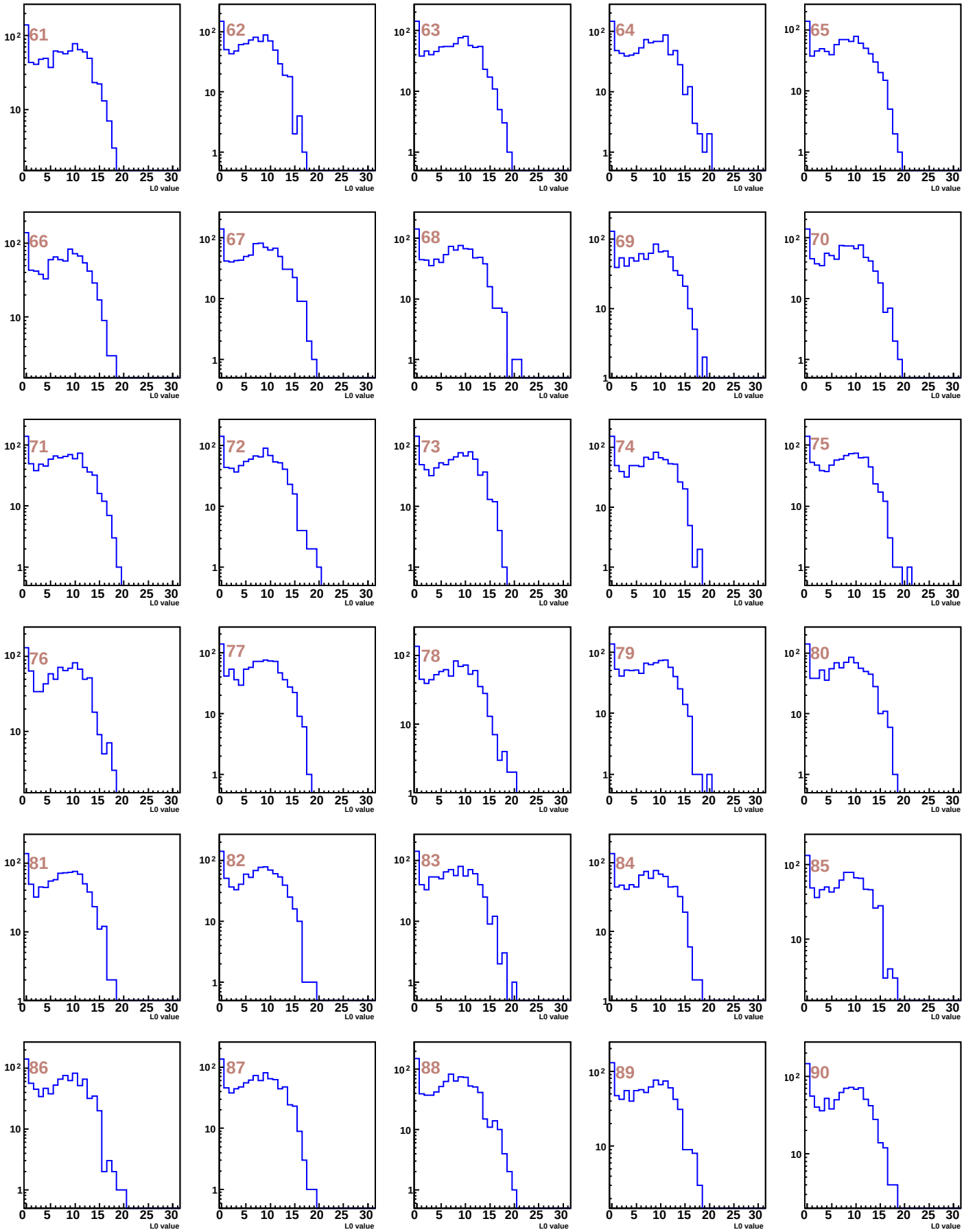
41
42
43
44
45
46
47
48
49
50
51
52
53
54
55
56
57
58
59
60
L0 value


61
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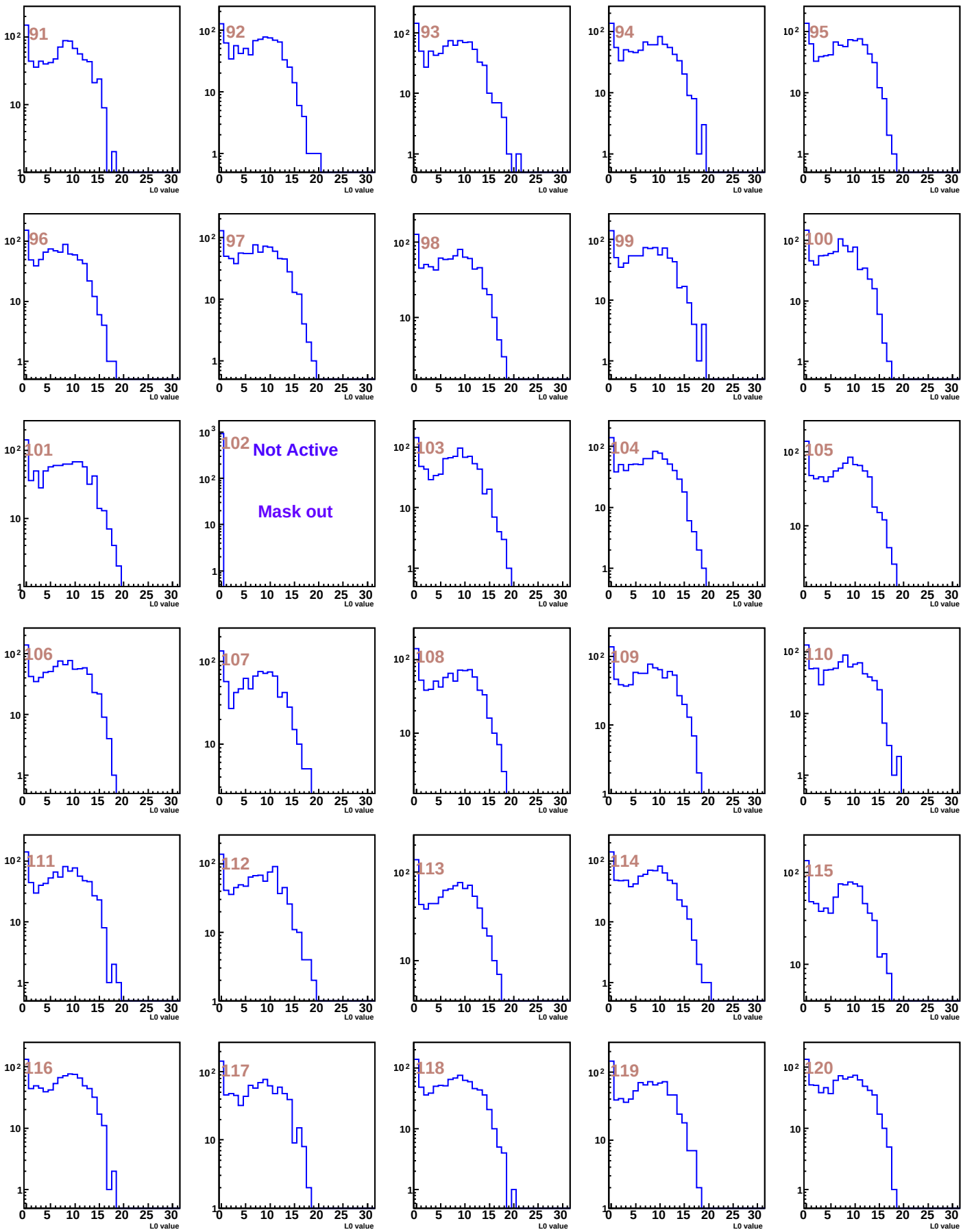
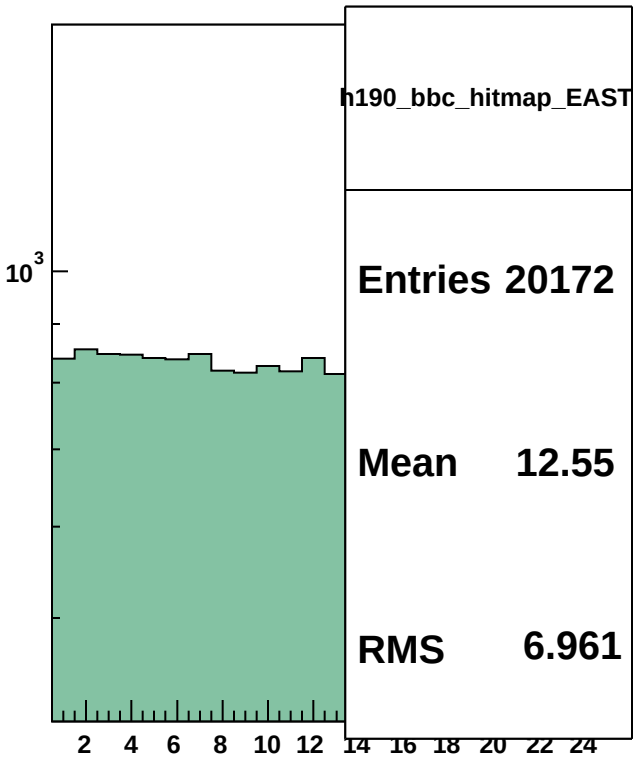
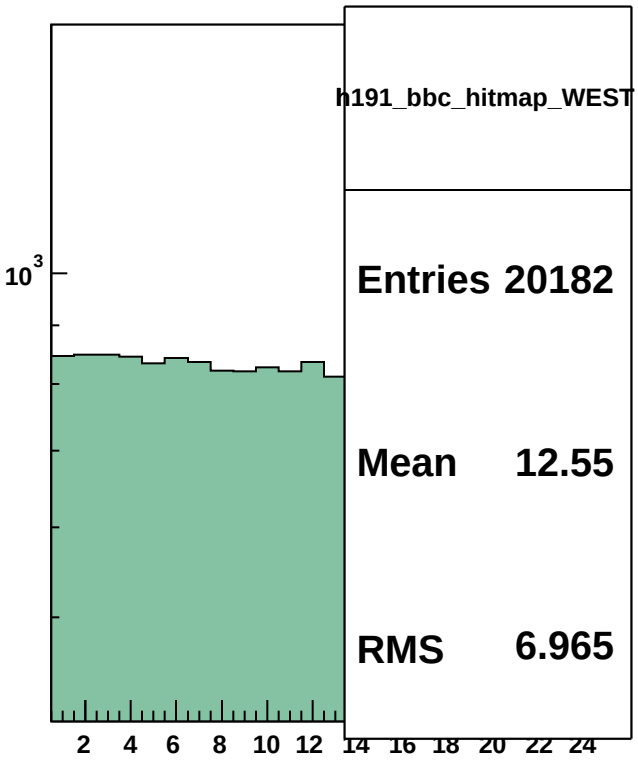
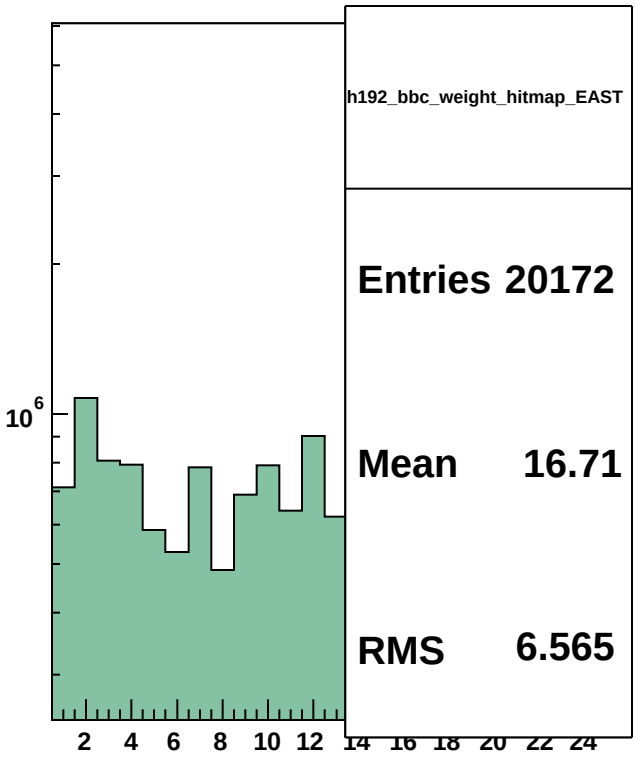
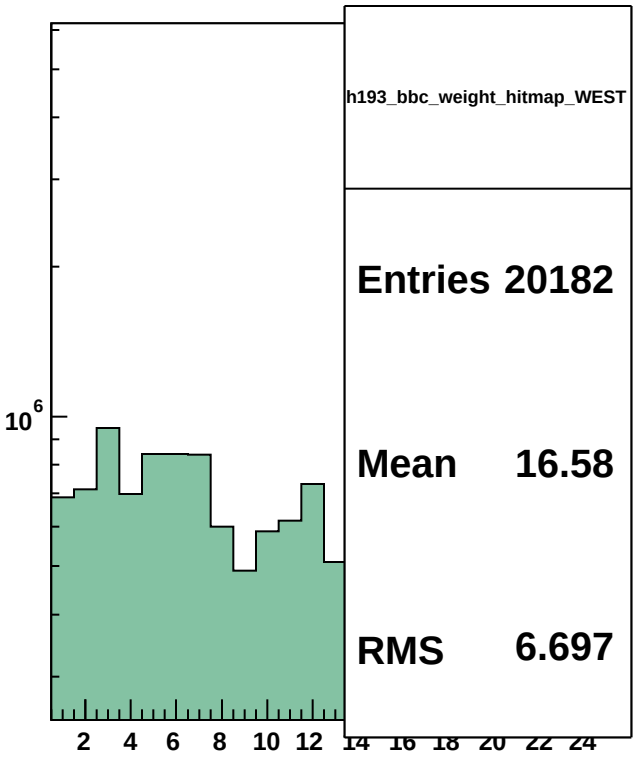
71
72
73
74
75
76
77
78
79
80
81
82
83
84
85
86
87
88
89
90
L0 value

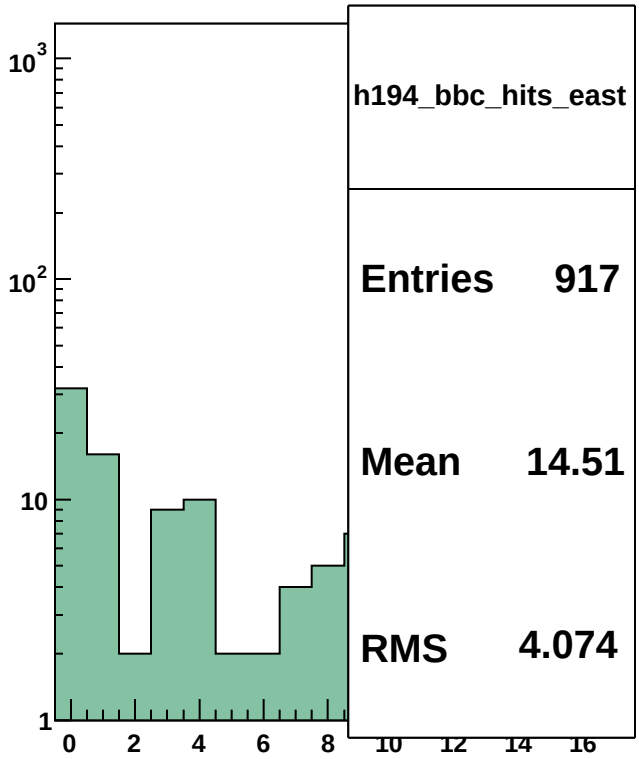

91
92
93
94
95
96
97
98
99
100
101
102
Not Active
Mask out
103
104
105
106
107
108
109
110
111
112
113
114
115
116
117
118
119
120
L0 value

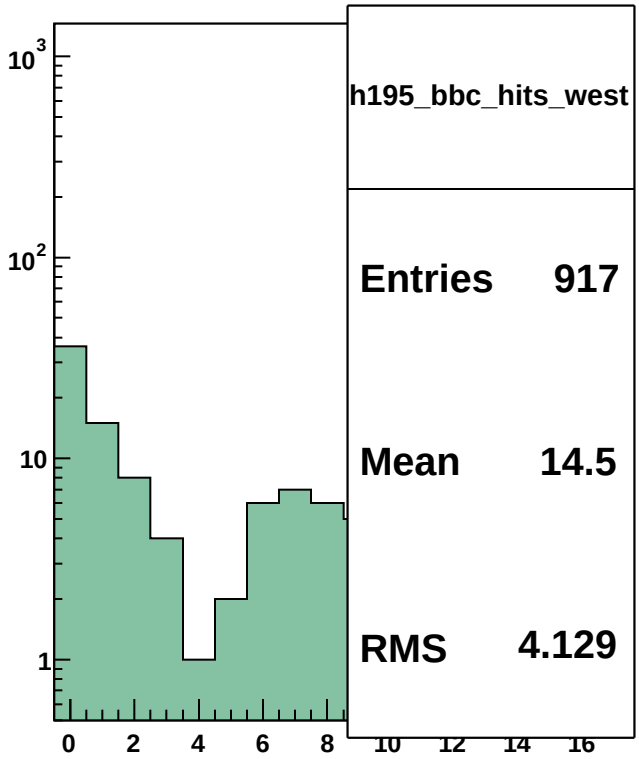

h190_bbc_hitmap_EAST
Entries 20172
Mean 12.55
RMS 6.961
10^3
2 4 6 8 10 12 14 16 18 20 22 24

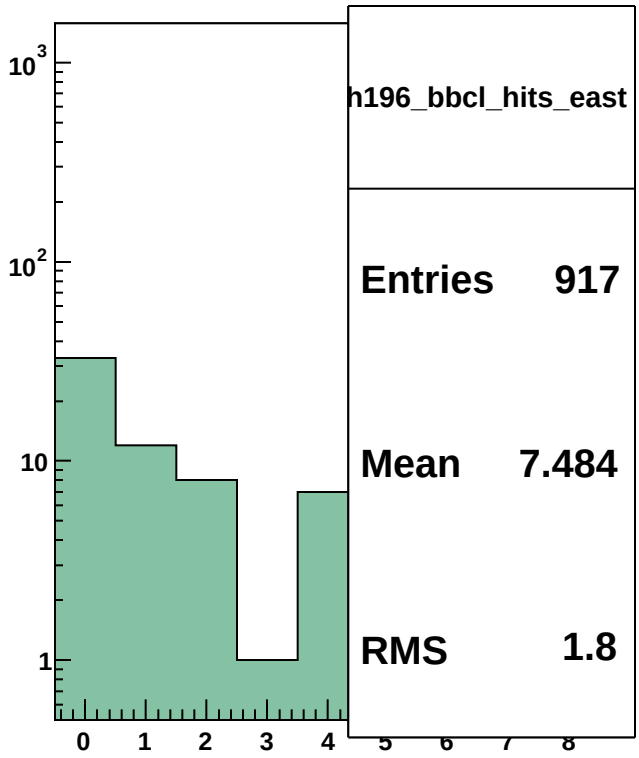

h191_bbc_hitmap_WEST
Entries 20182
Mean 12.55
RMS 6.965
10^3
2 4 6 8 10 12 14 16 18 20 22 24

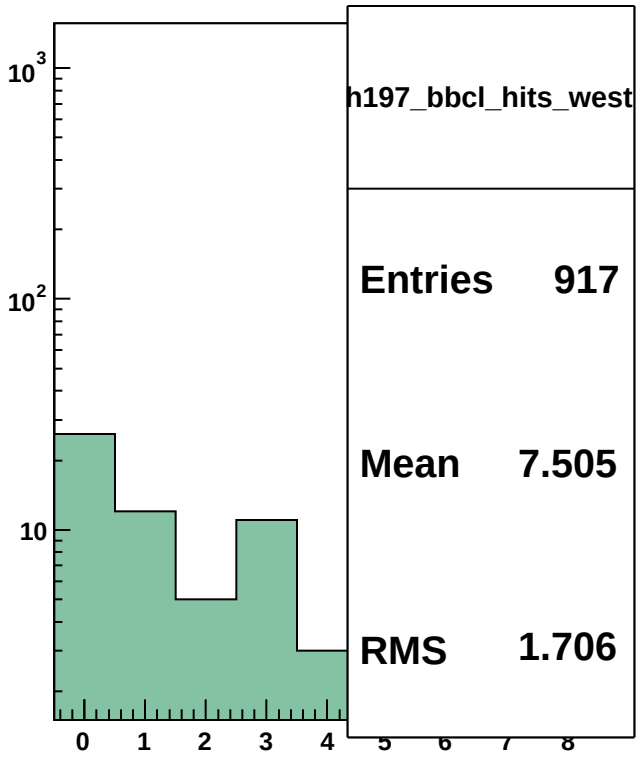

h192_bbc_weight_hitmap_EAST
Entries 20172
Mean 16.71
RMS 6.565
10^6
2 4 6 8 10 12 14 16 18 20 22 24

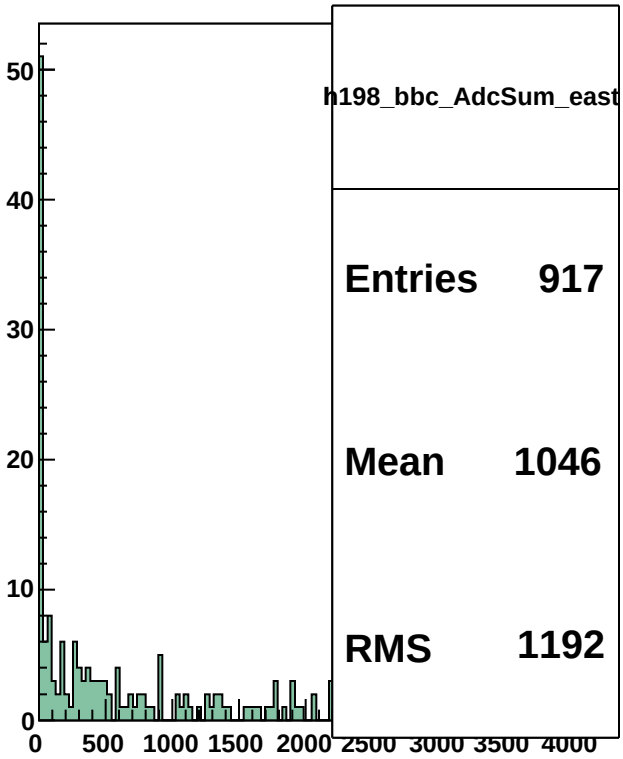

h193_bbc_weight_hitmap_WEST
Entries 20182
Mean 16.58
RMS 6.697
10^6
2 4 6 8 10 12 14 16 18 20 22 24

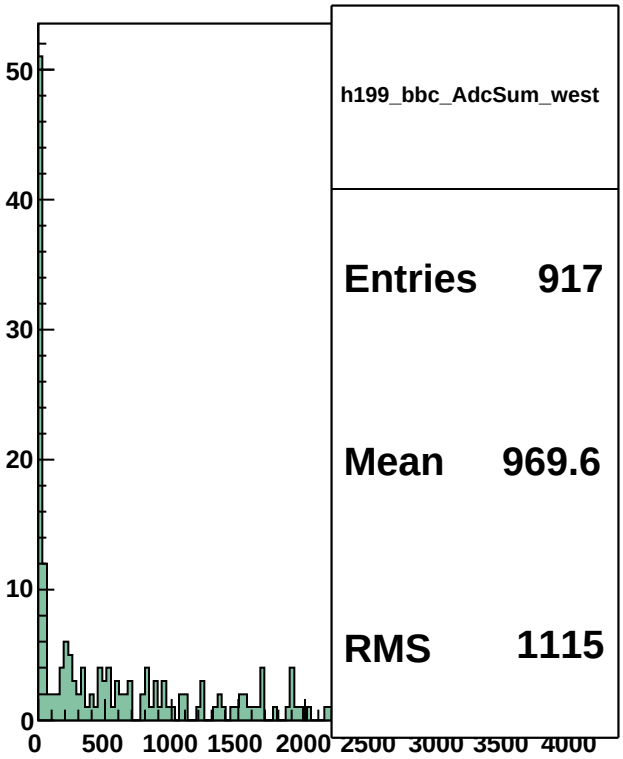

h194_bbc_hits_east
Entries 917
Mean 14.51
RMS 4.074

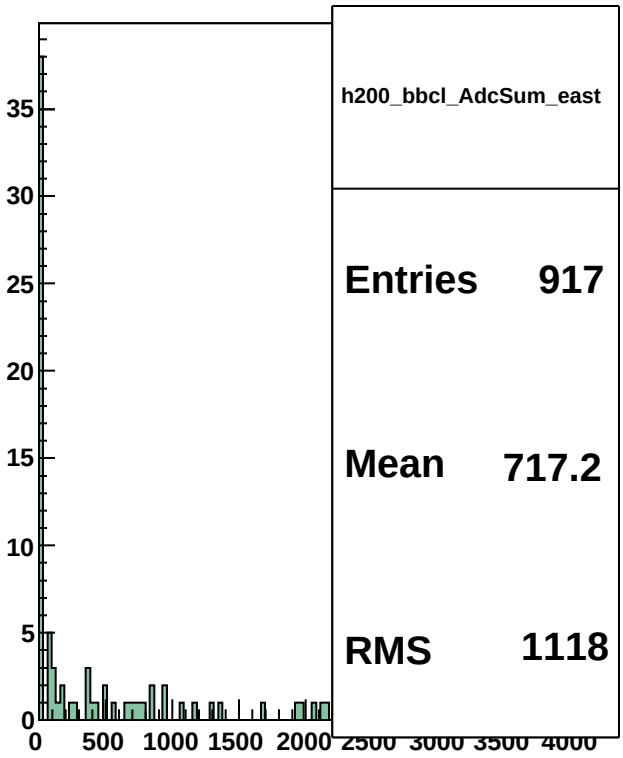

h195_bbc_hits_west
Entries 917
Mean 14.5
RMS 4.129

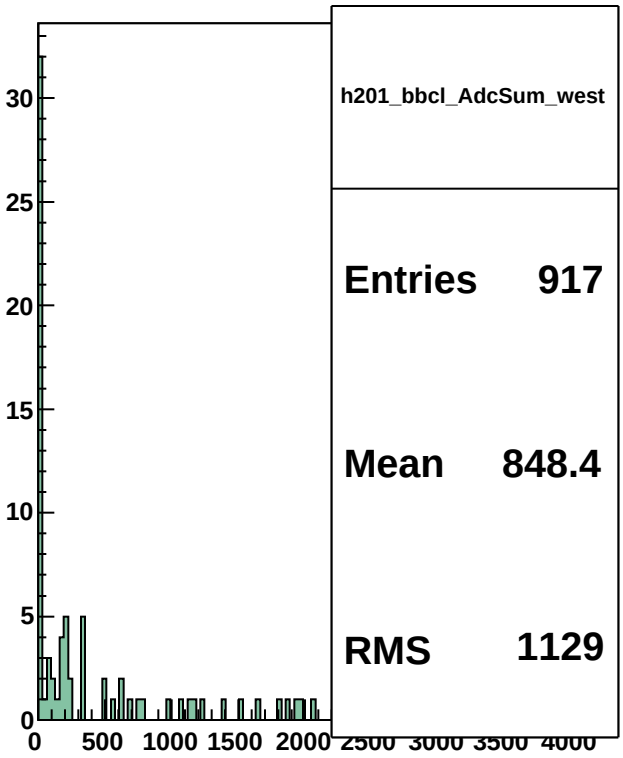

h196_bbcl_hits_east
Entries 917
Mean 7.484
RMS 1.8

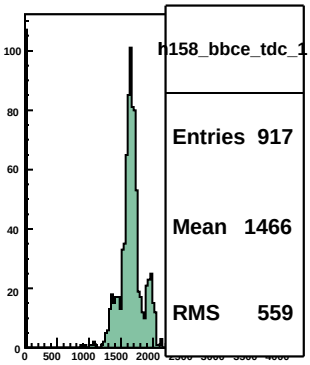

h197_bbcl_hits_west
Entries 917
Mean 7.505
RMS 1.706

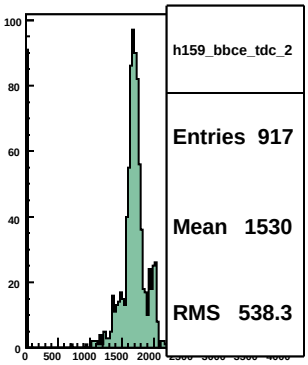

h198_bbc_AdcSum_east
Entries 917
Mean 1046
RMS 1192
0
10
20
30
40
50
0
500
1000
1500
2000
2500
3000
3500
4000

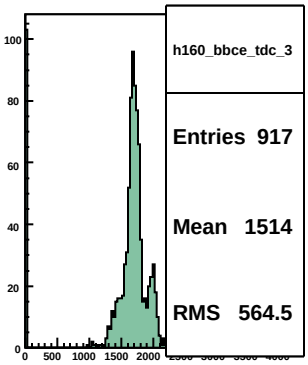

h199_bbc_AdcSum_west
Entries 917
Mean 969.6
RMS 1115
0
10
20
30
40
50
0
500
1000
1500
2000
2500
3000
3500
4000

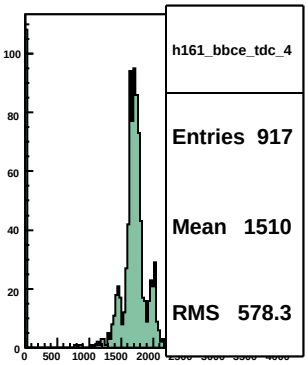

h200_bbcl_AdcSum_east
Entries 917
Mean 717.2
RMS 1118
0
5
10
15
20
25
30
35
0
500
1000
1500
2000
2500
3000
3500
4000

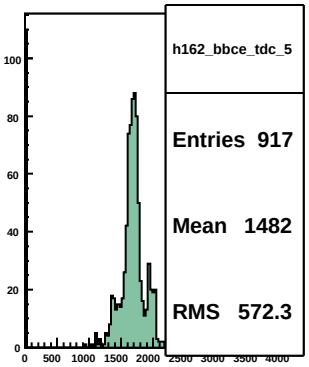

h201_bbcl_AdcSum_west
Entries 917
Mean 848.4
RMS 1129
0
5
10
15
20
25
30
0
500
1000
1500
2000
2500
3000
3500
4000

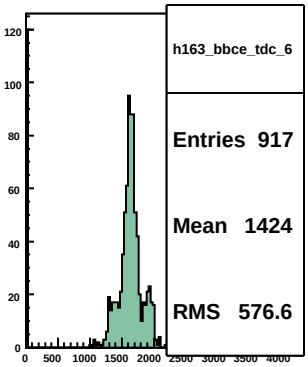

h158_bbce_tdc_1
Entries 917
Mean 1466
RMS 559

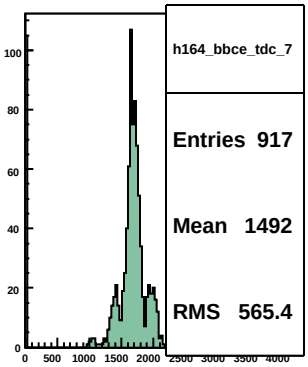

h159_bbce_tdc_2
Entries 917
Mean 1530
RMS 538.3

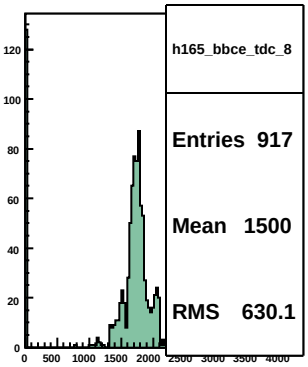

h160_bbce_tdc_3
Entries 917
Mean 1514
RMS 564.5

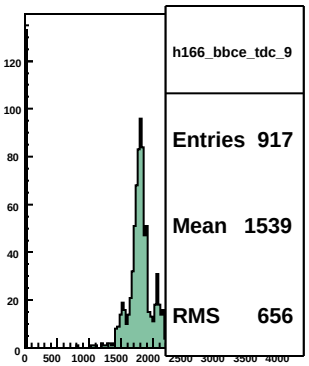

h161_bbce_tdc_4
Entries 917
Mean 1510
RMS 578.3

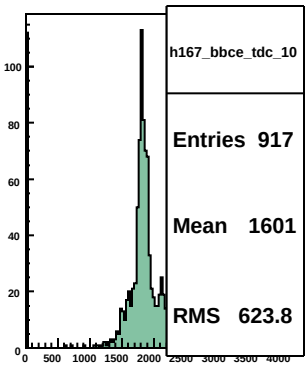

h162_bbce_tdc_5
Entries 917
Mean 1482
RMS 572.3

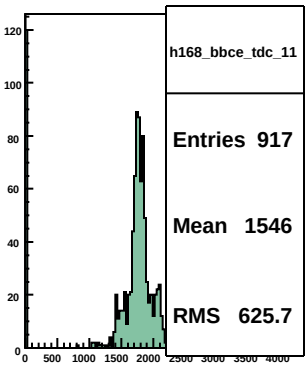

h163_bbce_tdc_6
Entries 917
Mean 1424
RMS 576.6

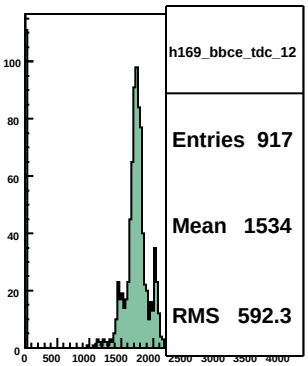

h164_bbce_tdc_7
Entries 917
Mean 1492
RMS 565.4

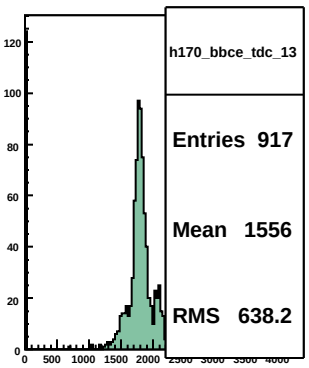

h165_bbce_tdc_8
Entries 917
Mean 1500
RMS 630.1

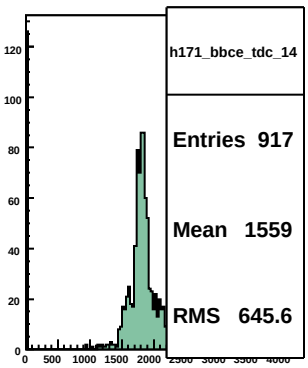

h166_bbce_tdc_9
Entries 917
Mean 1539
RMS 656

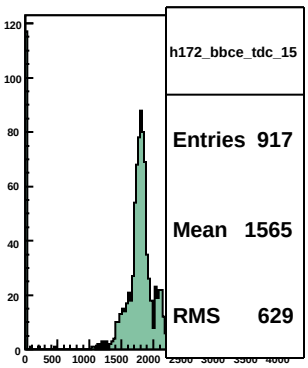

h167_bbce_tdc_10
Entries 917
Mean 1601
RMS 623.8

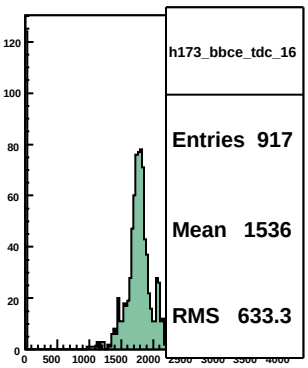

h168_bbce_tdc_11
Entries 917
Mean 1546
RMS 625.7

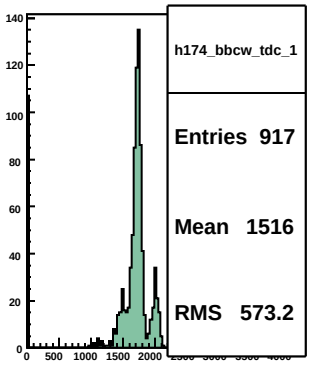

h169_bbce_tdc_12
Entries 917
Mean 1534
RMS 592.3

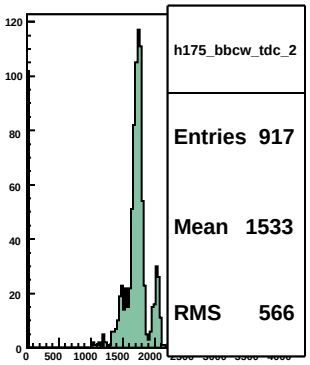

h170_bbce_tdc_13
Entries 917
Mean 1556
RMS 638.2

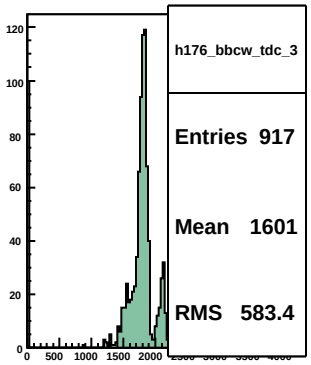

h171_bbce_tdc_14
Entries 917
Mean 1559
RMS 645.6

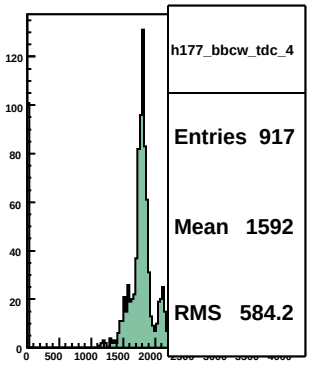

h172_bbce_tdc_15
Entries 917
Mean 1565
RMS 629

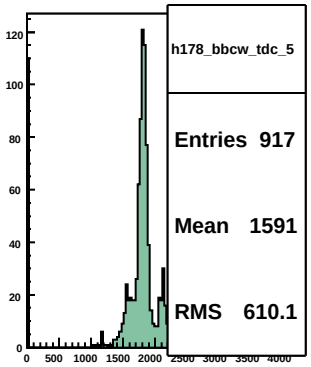

h173_bbce_tdc_16
Entries 917
Mean 1536
RMS 633.3

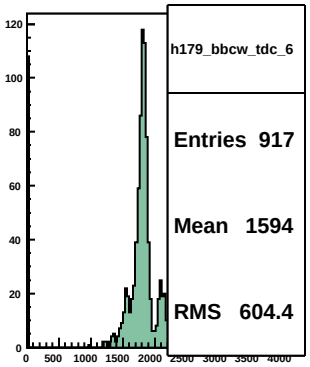

h174_bbcw_tdc_1
Entries 917
Mean 1516
RMS 573.2

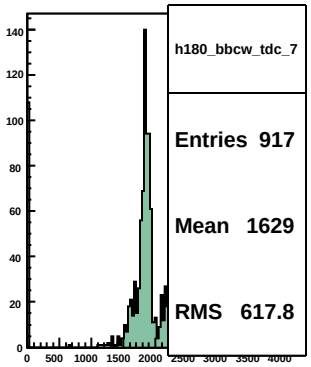

h175_bbcw_tdc_2
Entries 917
Mean 1533
RMS 566

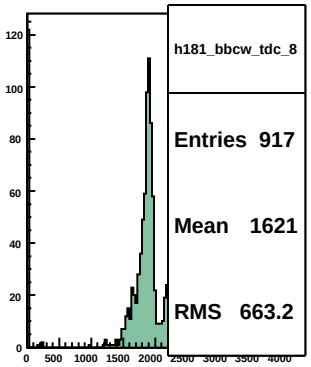

h176_bbcw_tdc_3
Entries 917
Mean 1601
RMS 583.4

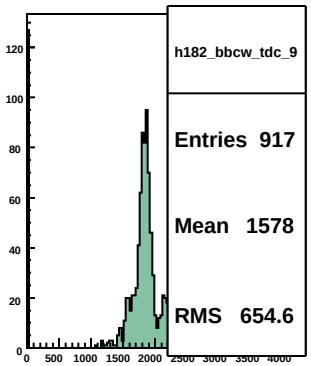

h177_bbcw_tdc_4
Entries 917
Mean 1592
RMS 584.2

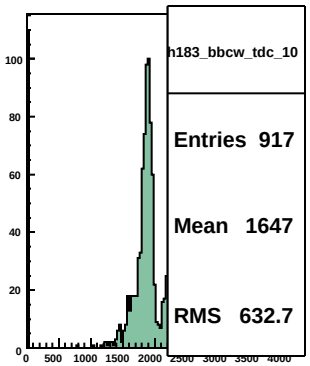

h178_bbcw_tdc_5
Entries 917
Mean 1591
RMS 610.1

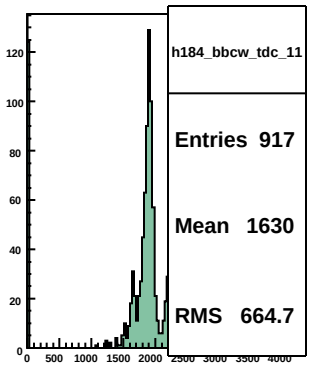

h179_bbcw_tdc_6
Entries 917
Mean 1594
RMS 604.4

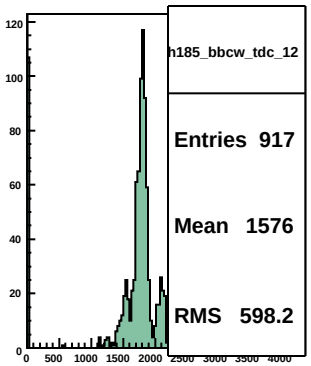

h180_bbcw_tdc_7
Entries 917
Mean 1629
RMS 617.8

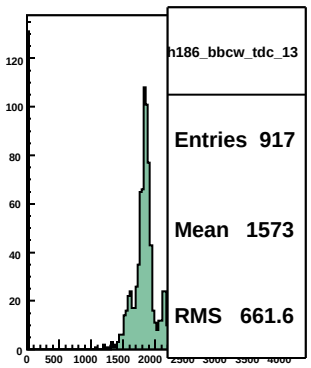

h181_bbcw_tdc_8
Entries 917
Mean 1621
RMS 663.2

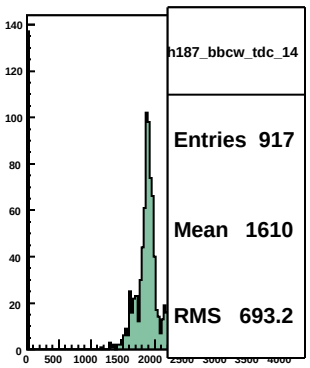

h182_bbcw_tdc_9
Entries 917
Mean 1578
RMS 654.6

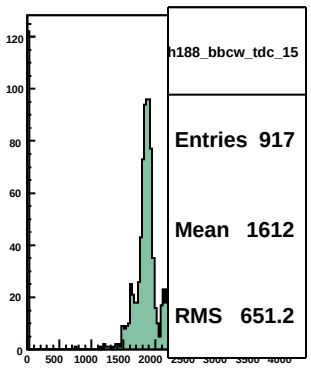

h183_bbcw_tdc_10
Entries 917
Mean 1647
RMS 632.7

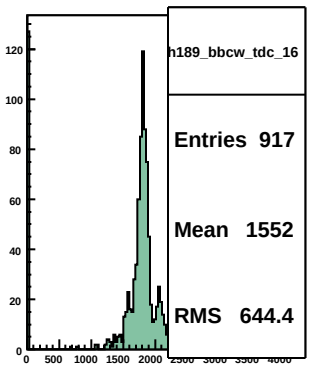

h184_bbcw_tdc_11
Entries 917
Mean 1630
RMS 664.7

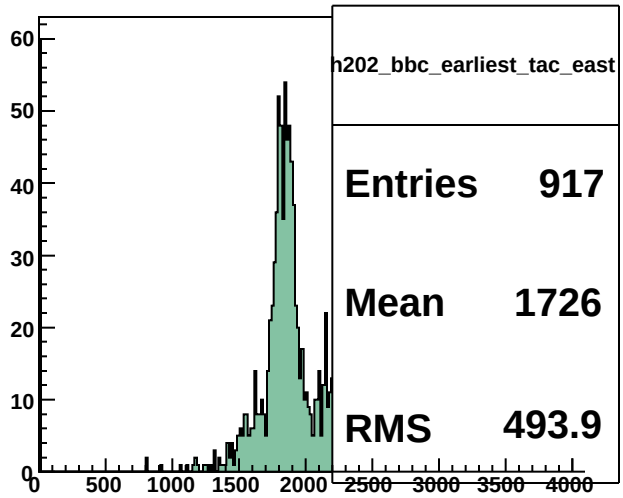

h185_bbcw_tdc_12
Entries 917
Mean 1576
RMS 598.2

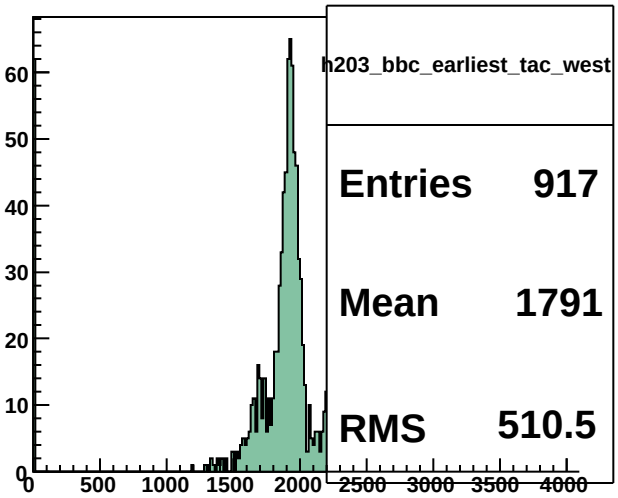

h186_bbcw_tdc_13
Entries 917
Mean 1573
RMS 661.6

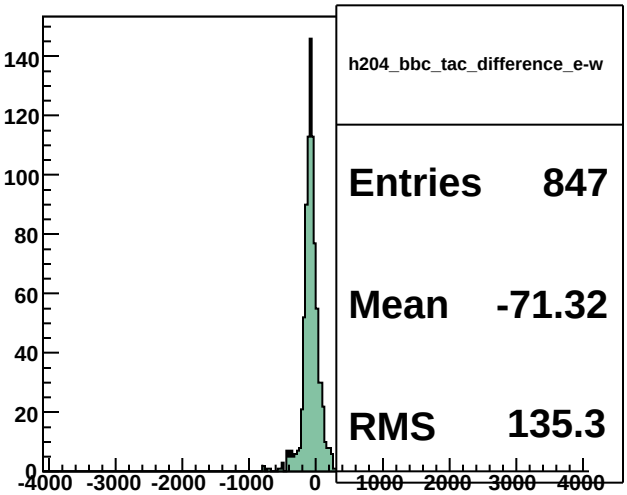

h187_bbcw_tdc_14
Entries 917
Mean 1610
RMS 693.2

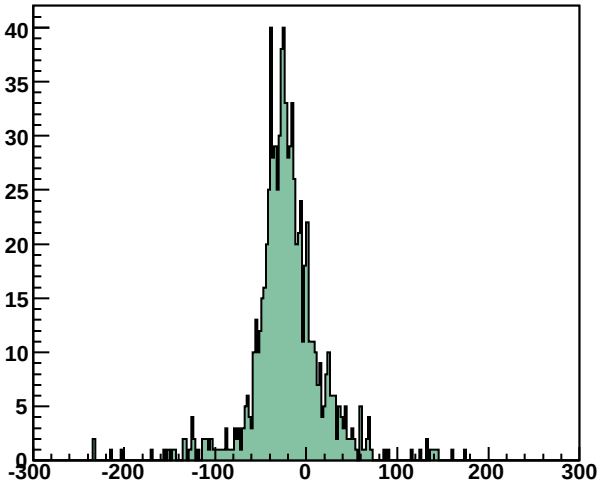

h188_bbcw_tdc_15
Entries 917
Mean 1612
RMS 651.2

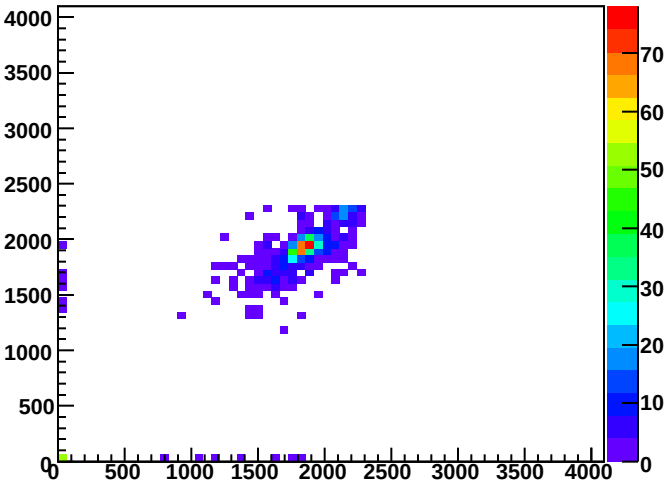

h189_bbcw_tdc_16
Entries 917
Mean 1552
RMS 644.4

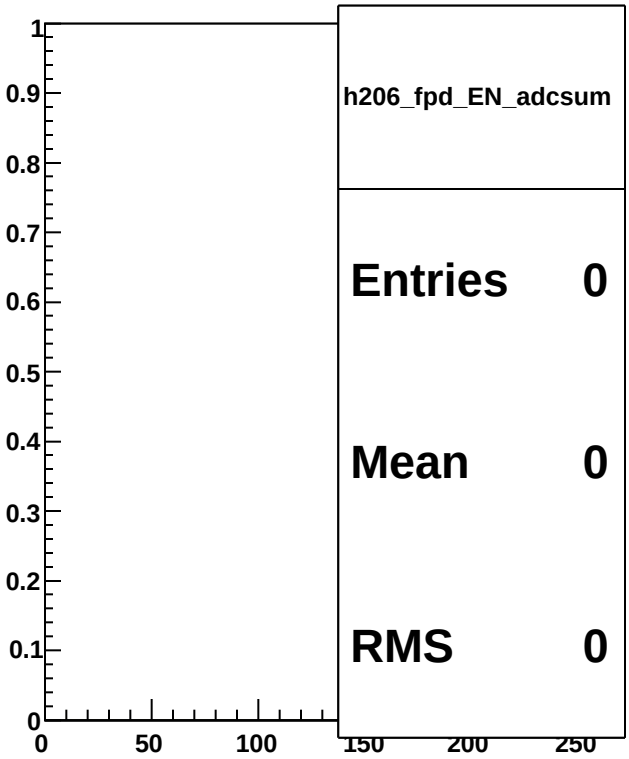

h202_bbc_earliest_tac_east
Entries 917
Mean 1726
RMS 493.9

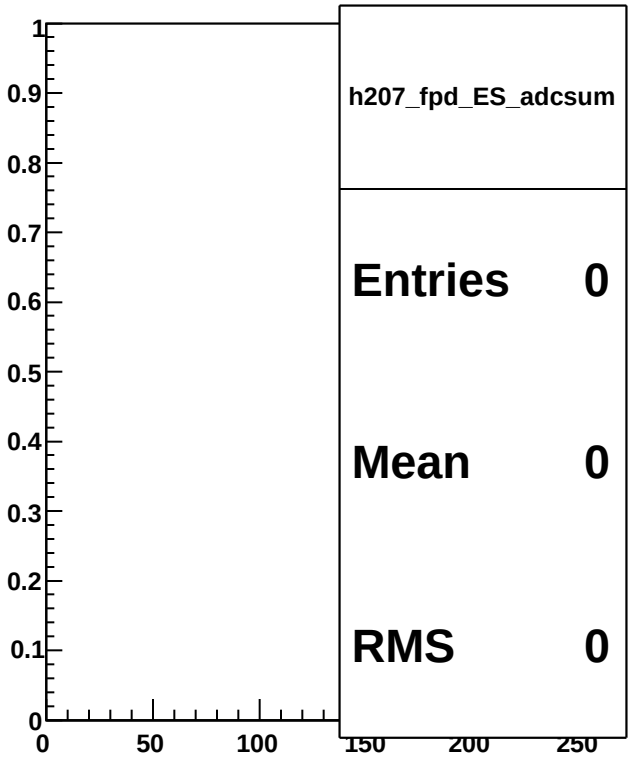

h203_bbc_earliest_tac_west
Entries 917
Mean 1791
RMS 510.5

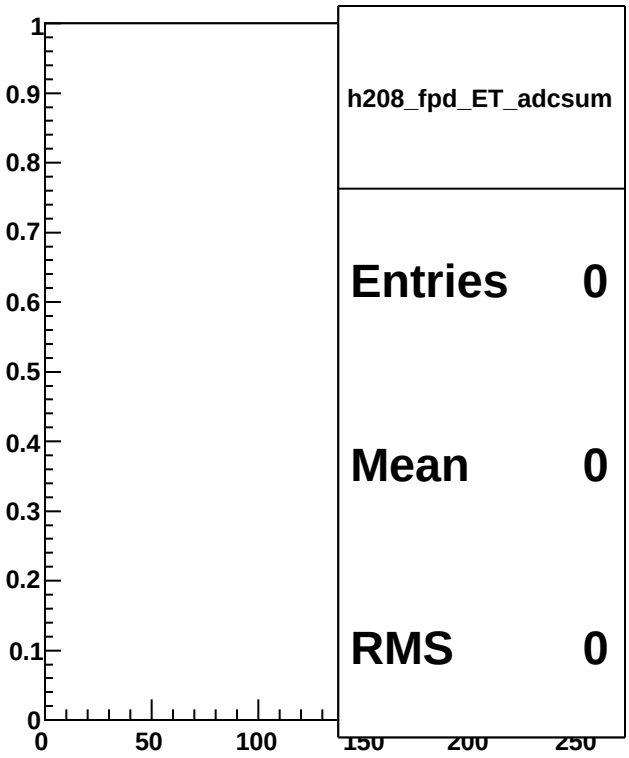

h204_bbc_tac_difference_e-w
Entries 847
Mean -71.32
RMS 135.3

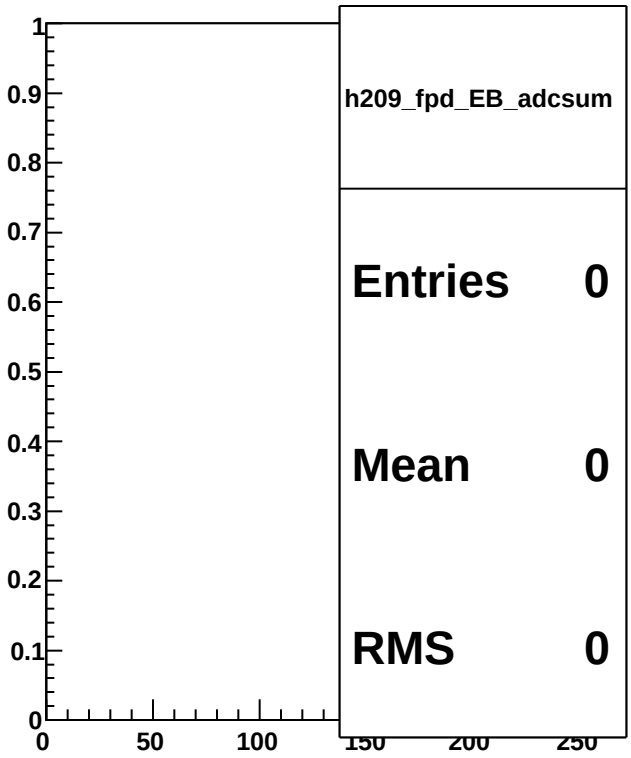


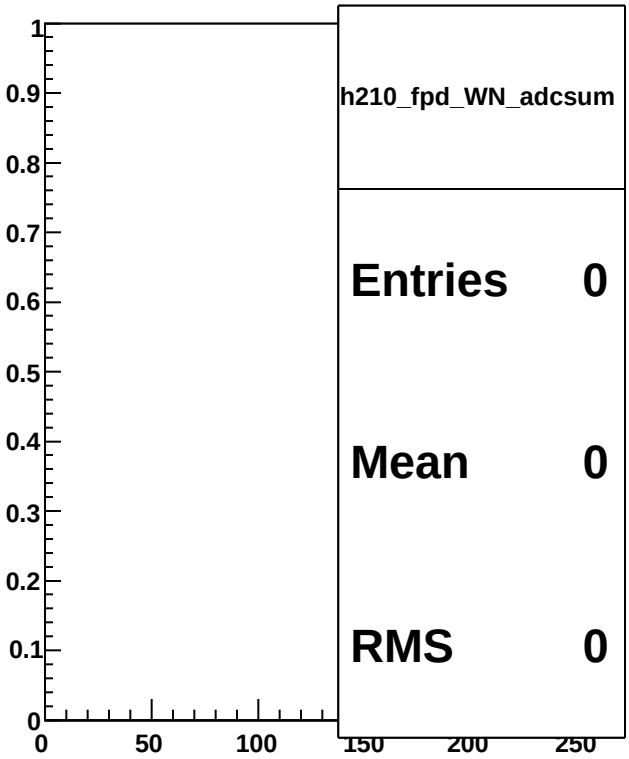


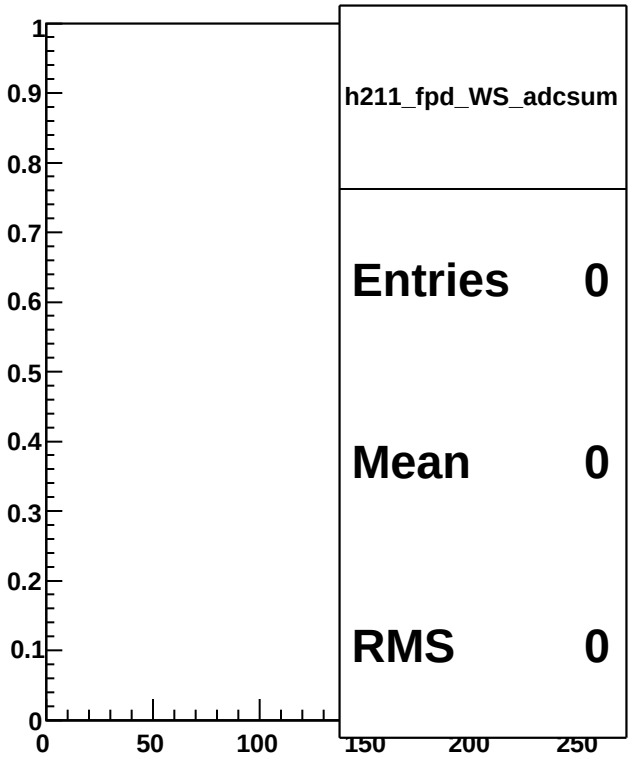

h206_fpd_EN_adcsum
Entries 0
Mean 0
RMS 0

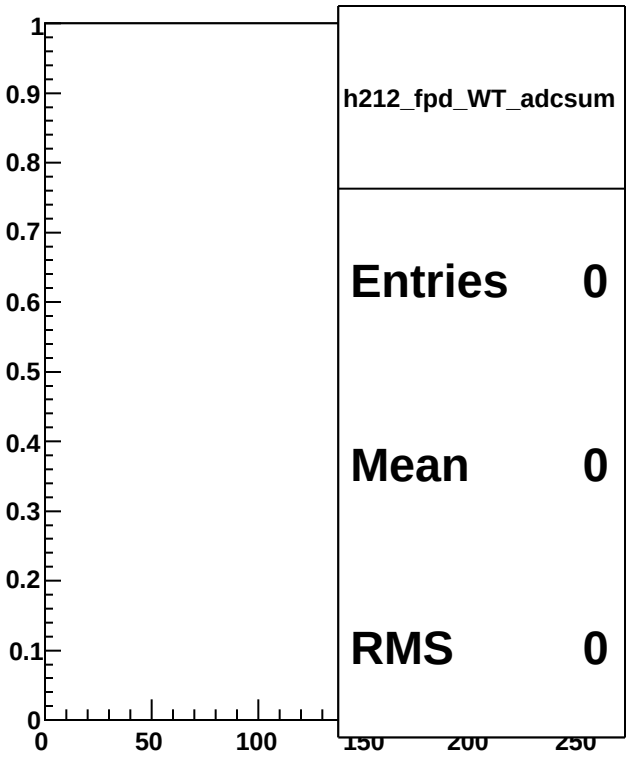

h207_fpd_ES_adcsum
Entries 0
Mean 0
RMS 0

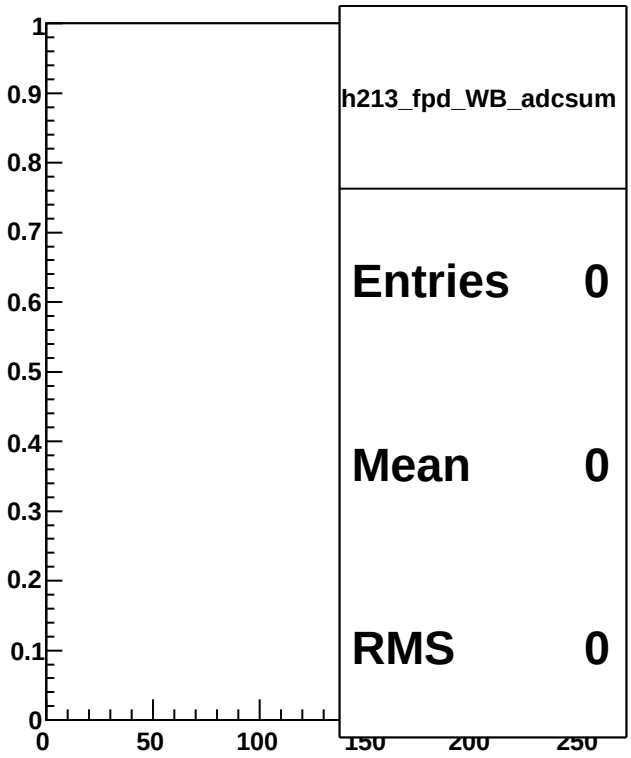

h208_fpd_ET_adcsum
Entries 0
Mean 0
RMS 0

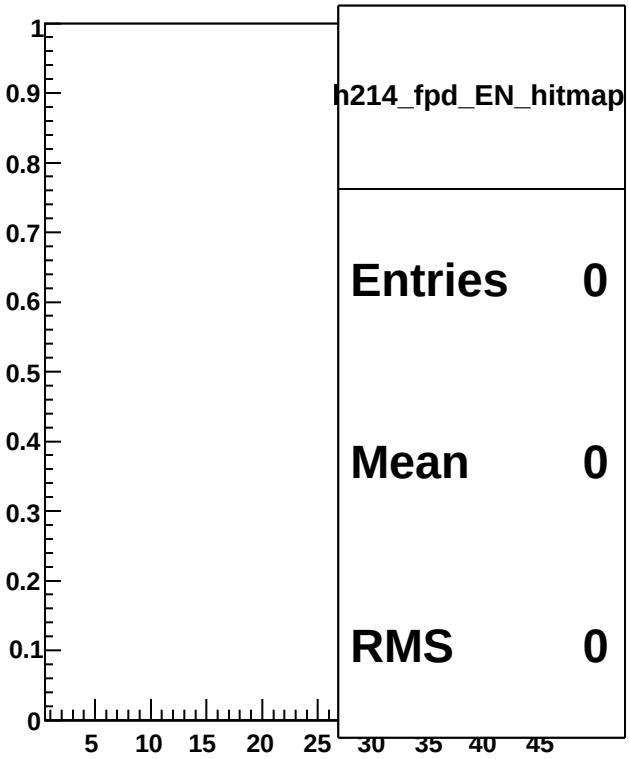

h209_fpd_EB_adcsum
Entries 0
Mean 0
RMS 0

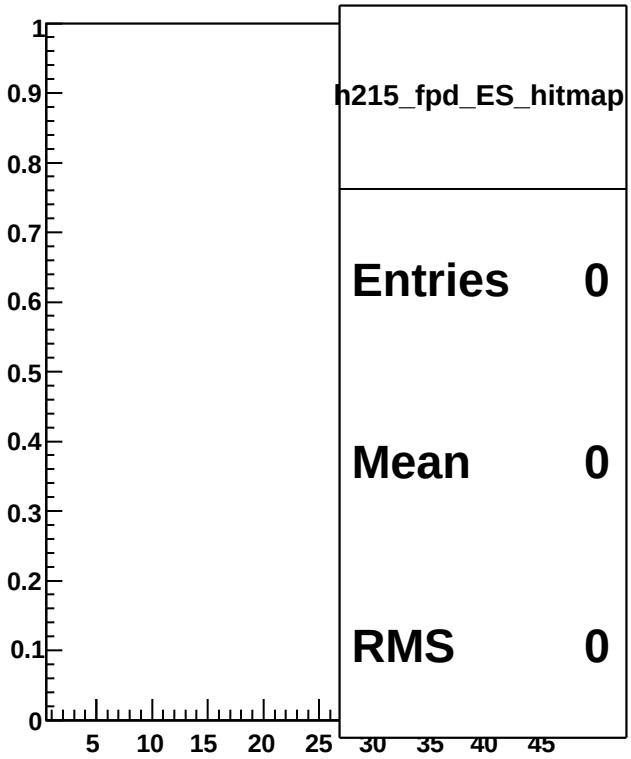

h210_fpd_WN_adcsum
Entries 0
Mean 0
RMS 0

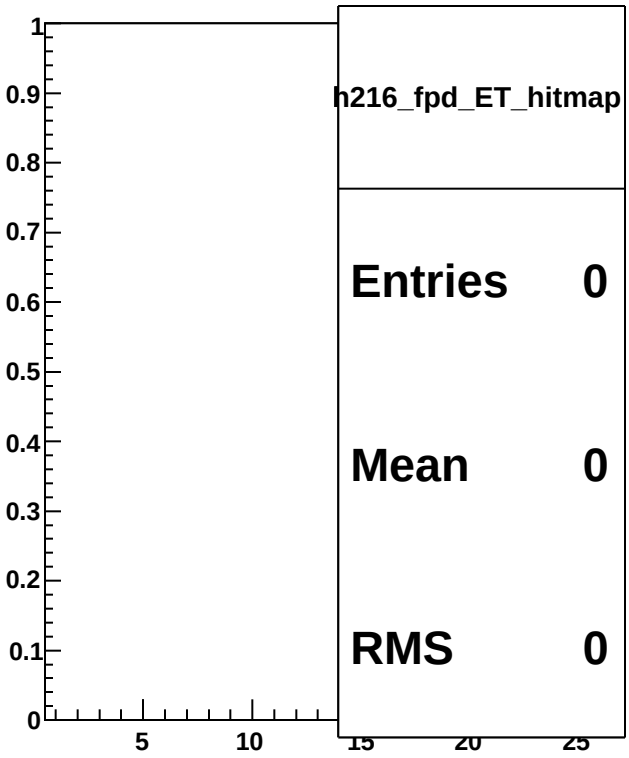

h211_fpd_WS_adcsum
Entries 0
Mean 0
RMS 0

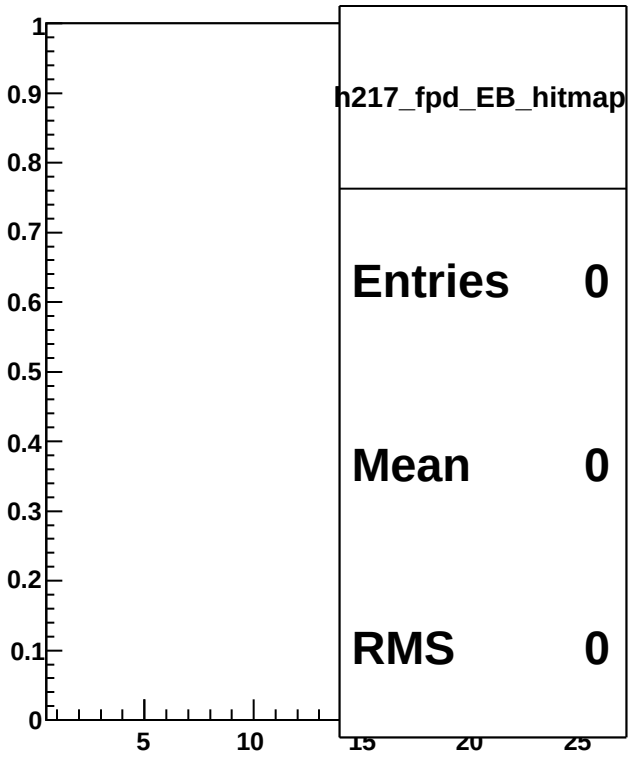

h212_fpd_WT_adcsum
Entries 0
Mean 0
RMS 0

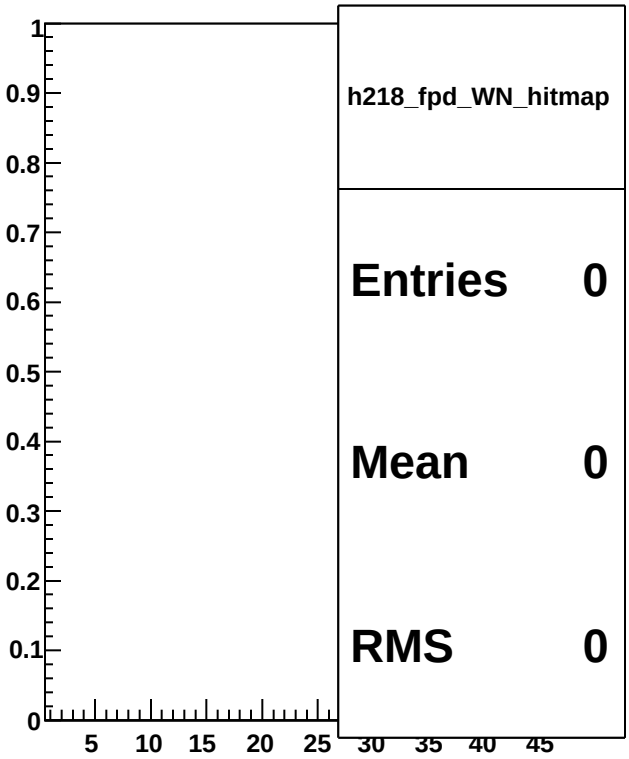

h213_fpd_WB_adcsum
Entries 0
Mean 0
RMS 0

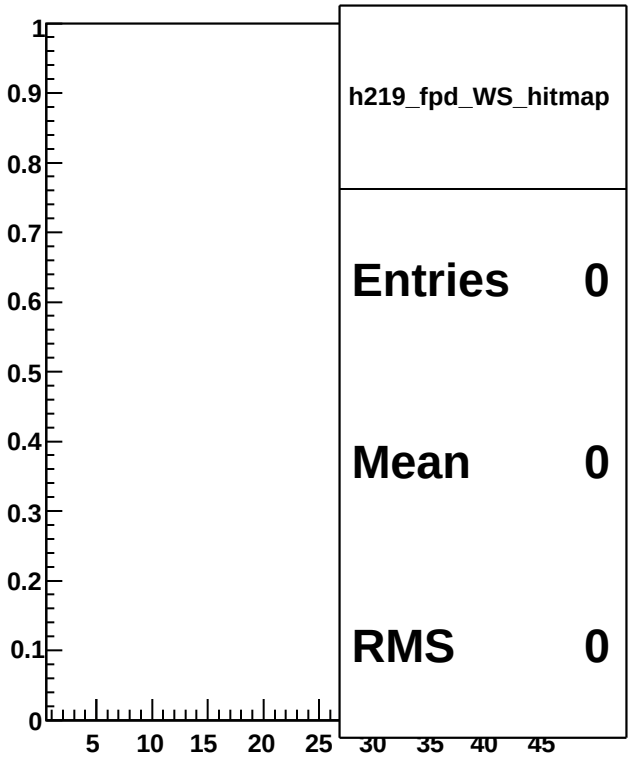

h214_fpd_EN_hitmap
Entries 0
Mean 0
RMS 0

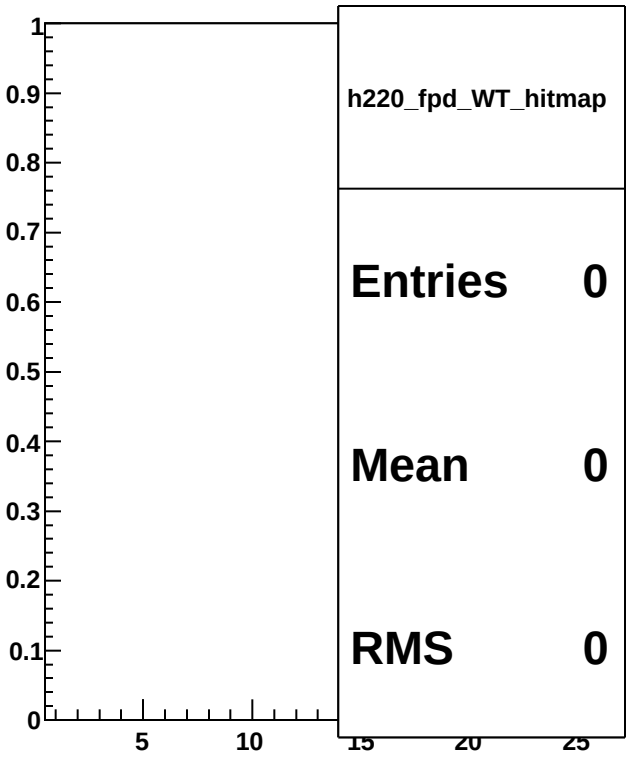

h215_fpd_ES_hitmap
Entries 0
Mean 0
RMS 0

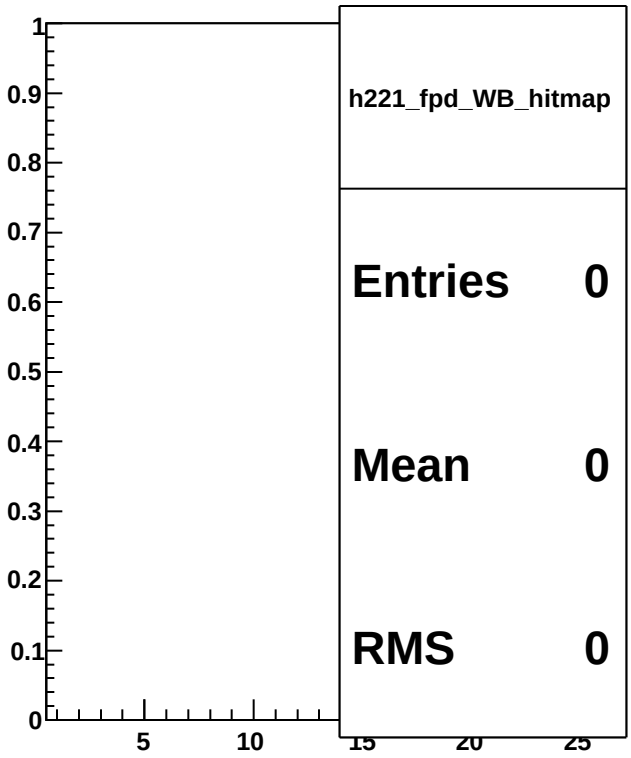

h216_fpd_ET_hitmap
Entries 0
Mean 0
RMS 0

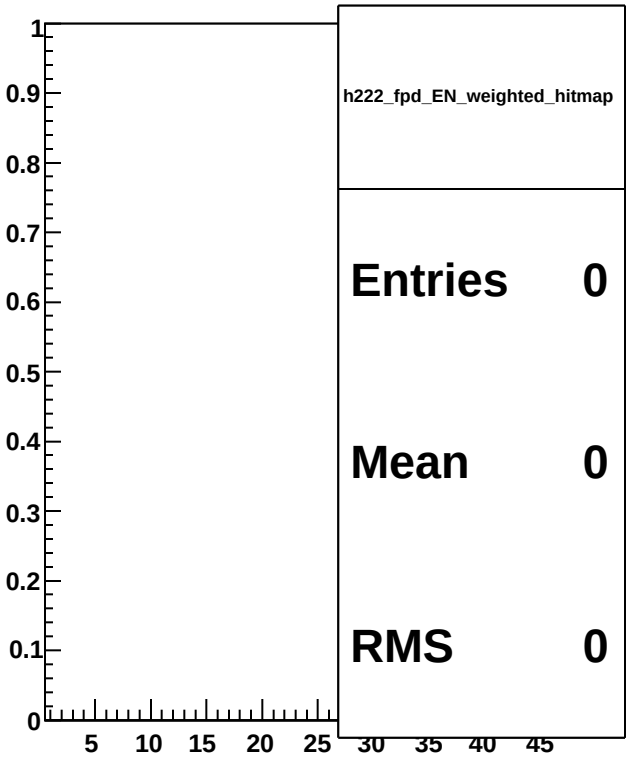

h217_fpd_EB_hitmap
Entries 0
Mean 0
RMS 0

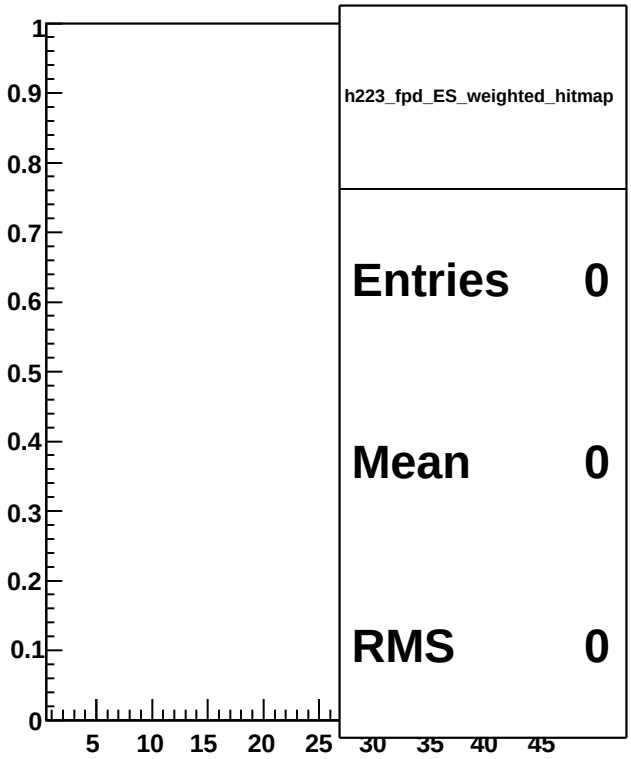

h218_fpd_WN_hitmap
Entries 0
Mean 0
RMS 0

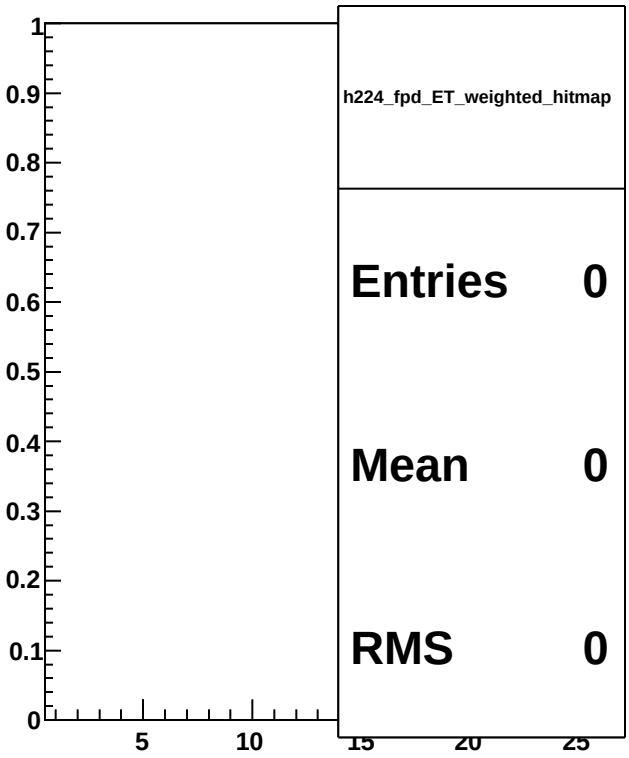

h219_fpd_WS_hitmap
Entries 0
Mean 0
RMS 0

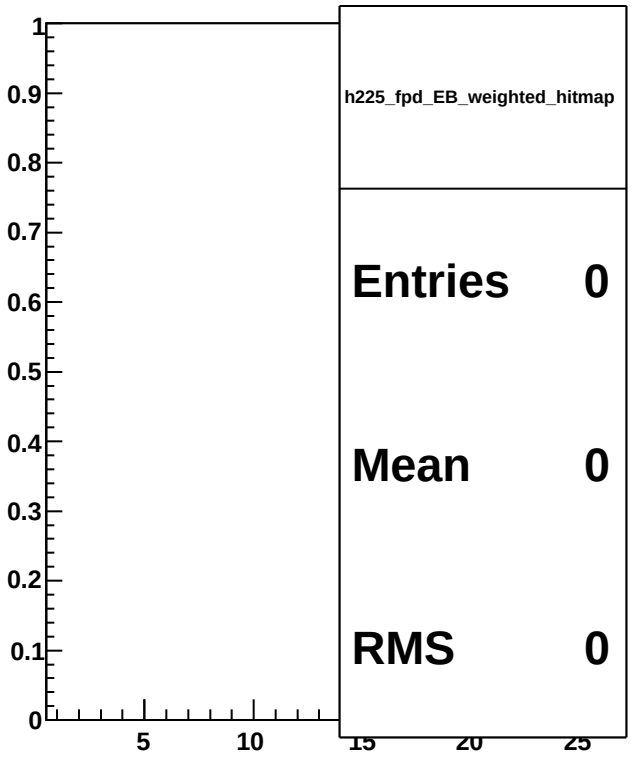

h220_fpd_WT_hitmap
Entries 0
Mean 0
RMS 0

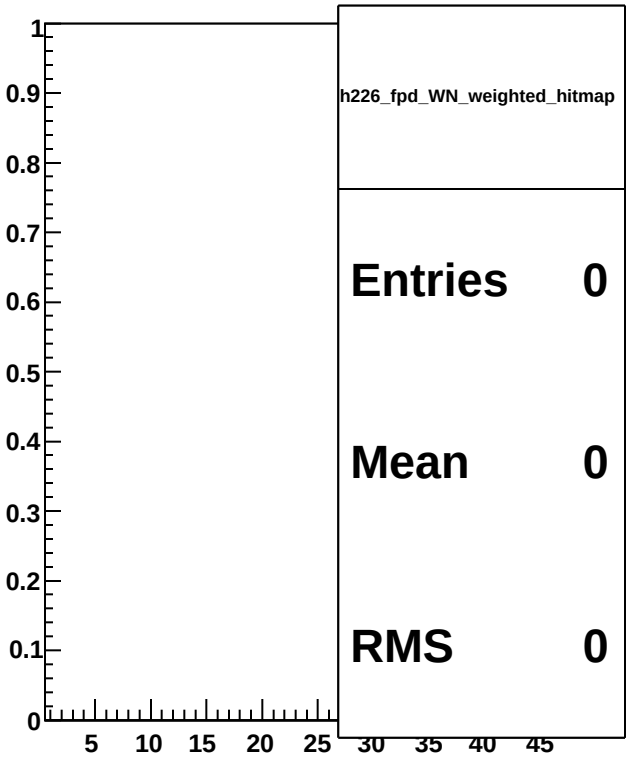

h221_fpd_WB_hitmap
Entries 0
Mean 0
RMS 0

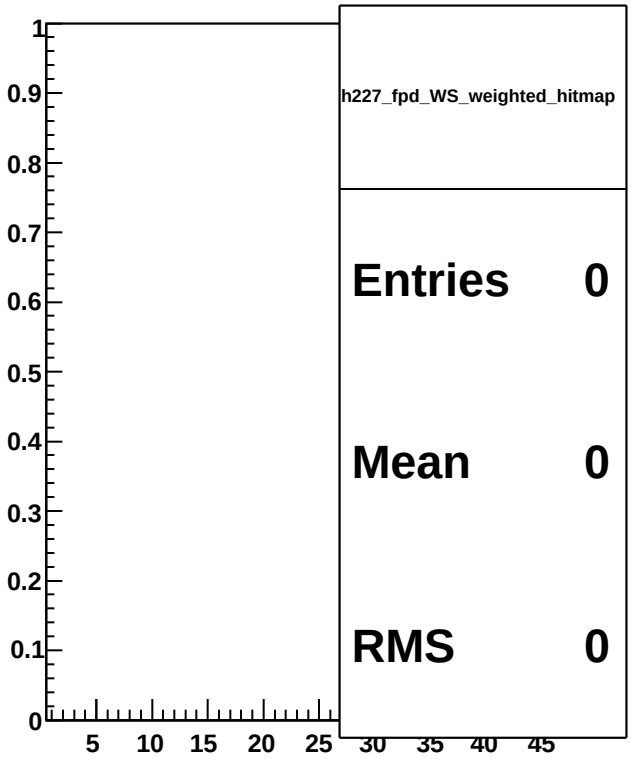

h222_fpd_EN_weighted_hitmap
Entries 0
Mean 0
RMS 0

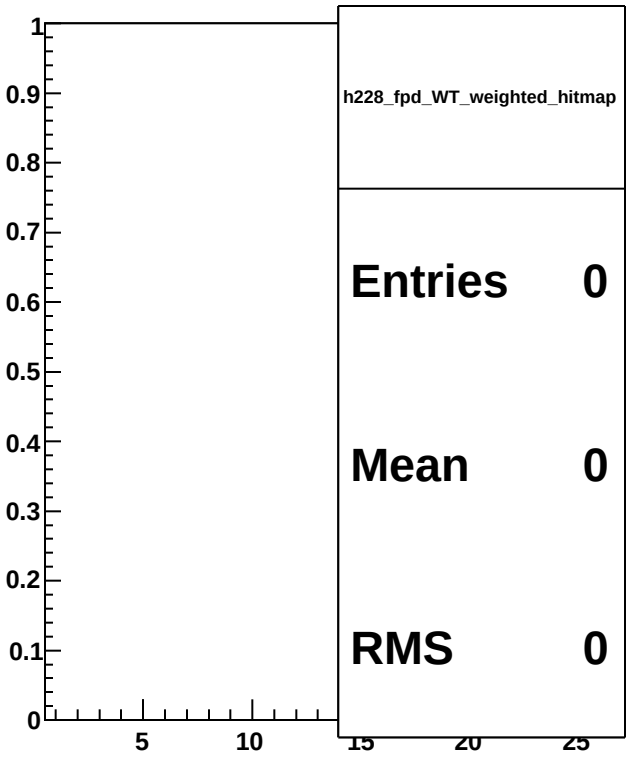

h223_fpd_ES_weighted_hitmap
Entries 0
Mean 0
RMS 0

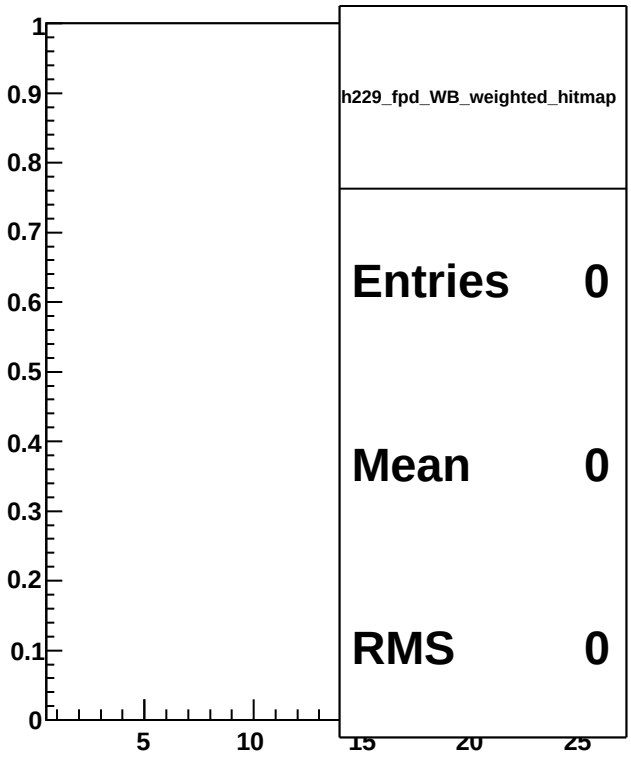

h224_fpd_ET_weighted_hitmap
Entries 0
Mean 0
RMS 0

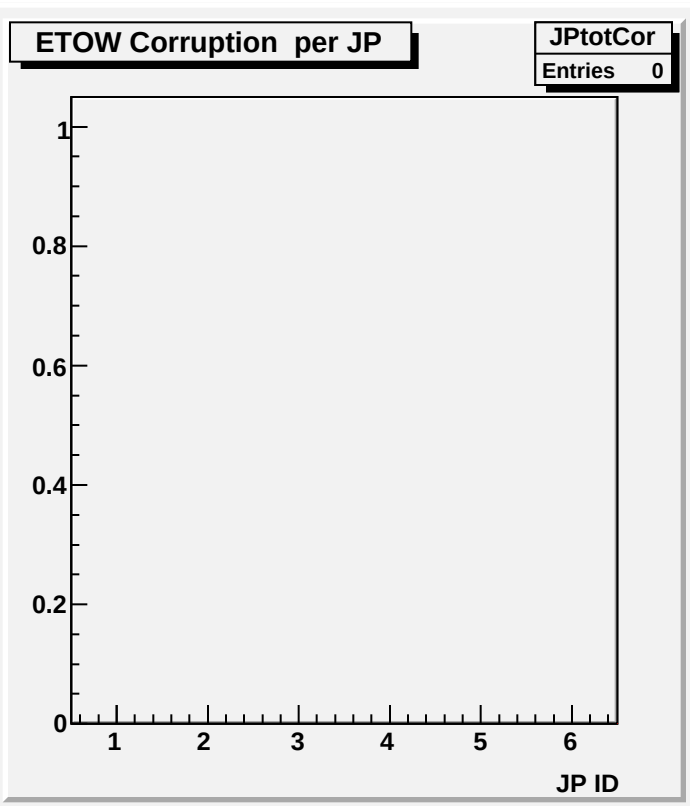

h225_fpd_EB_weighted_hitmap
Entries 0
Mean 0
RMS 0

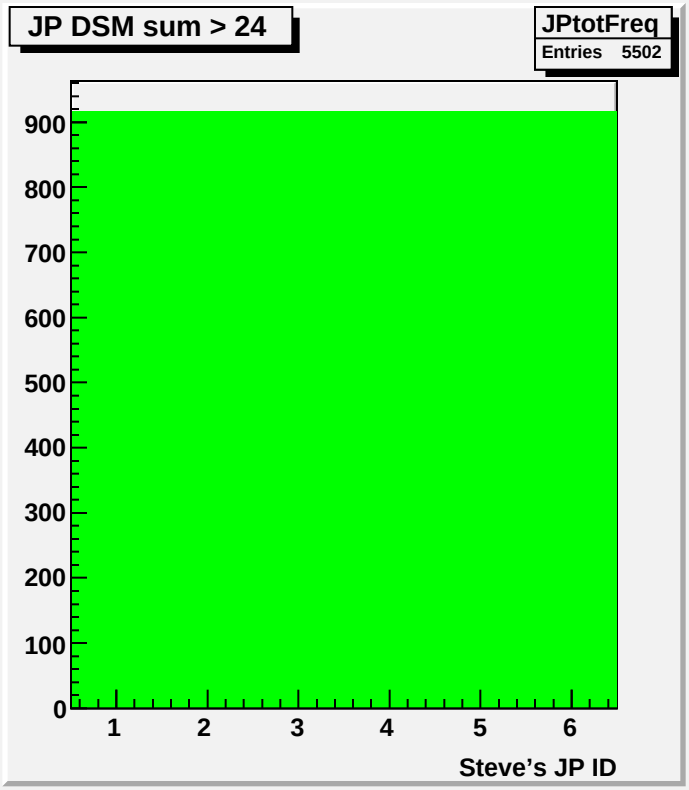

h226_fpd_WN_weighted_hitmap
Entries 0
Mean 0
RMS 0

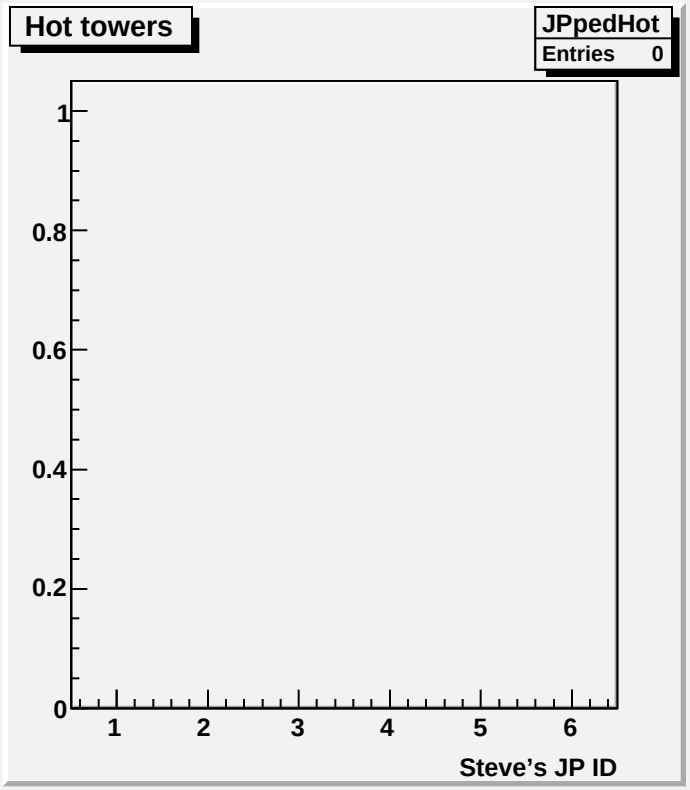

h227_fpd_WS_weighted_hitmap
Entries 0
Mean 0
RMS 0


h228_fpd_WT_weighted_hitmap
Entries 0
Mean 0
RMS 0


h229_fpd_WB_weighted_hitmap
Entries 0
Mean 0
RMS 0







ETOW masked (cr-ch-name): 1-97-12TC05, 4-117-07TC05, 4-18-06TA03, 2-98-02TC06, 6-67-11TB08,

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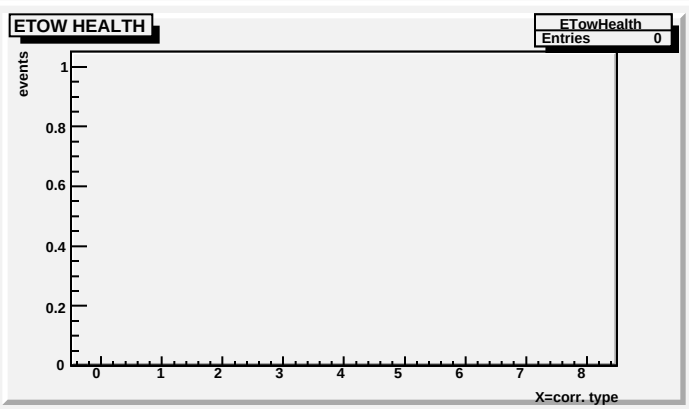
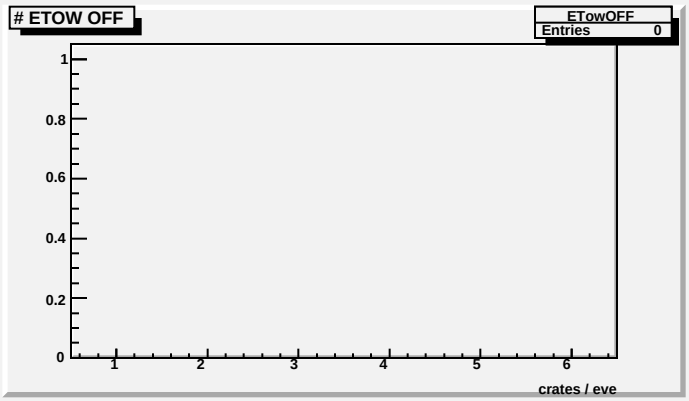
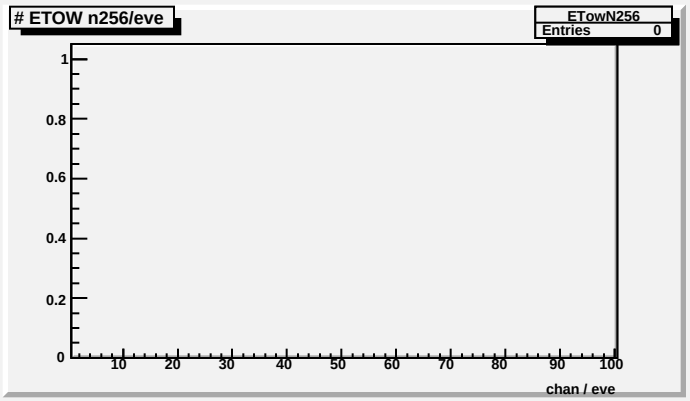
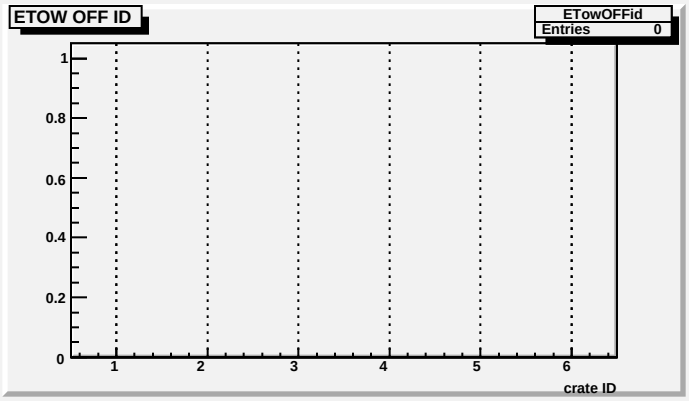
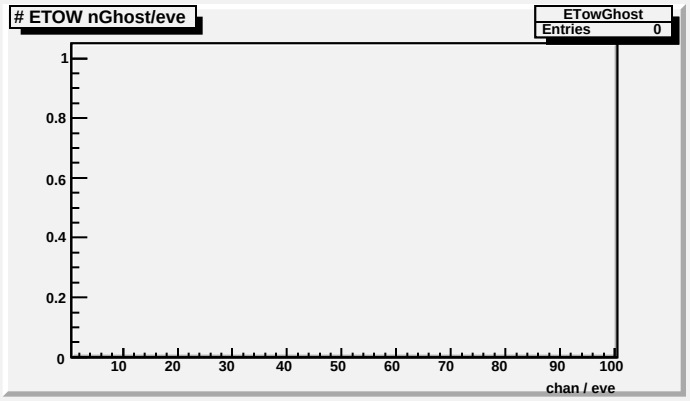

ETOW HEALTH
ETowHealth
Entries 0
events
X=corr. type

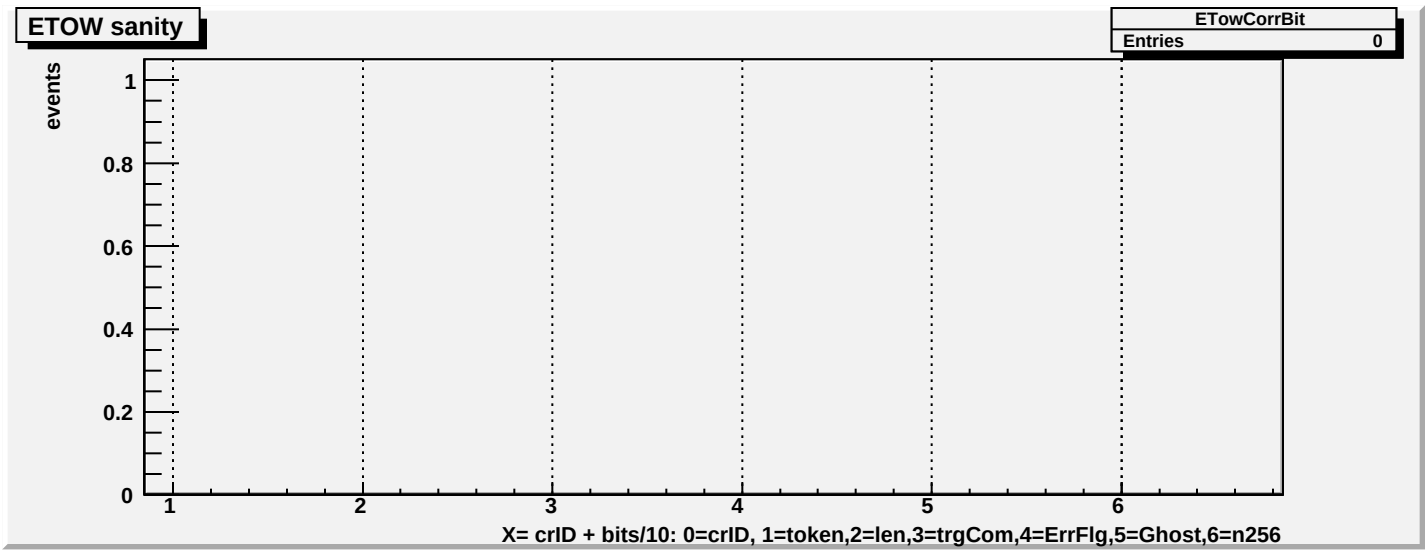
ETOW head corr
ETowHeadCorr
Entries 0
crates / eve

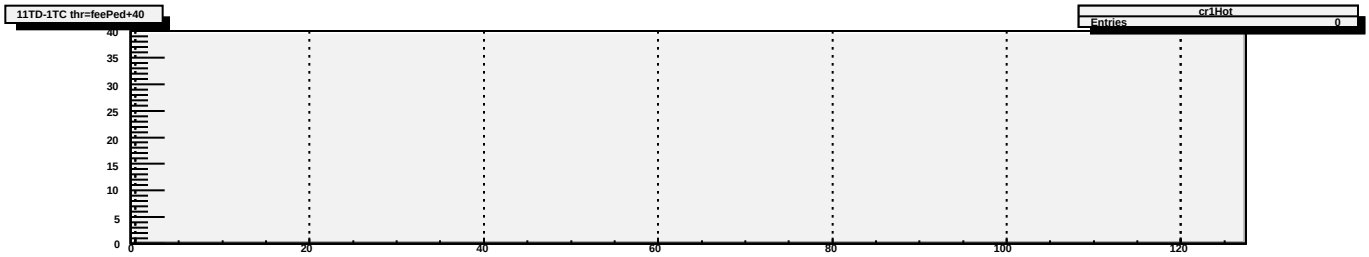

ETOW OFF
ETowOFF
Entries 0
crates / eve

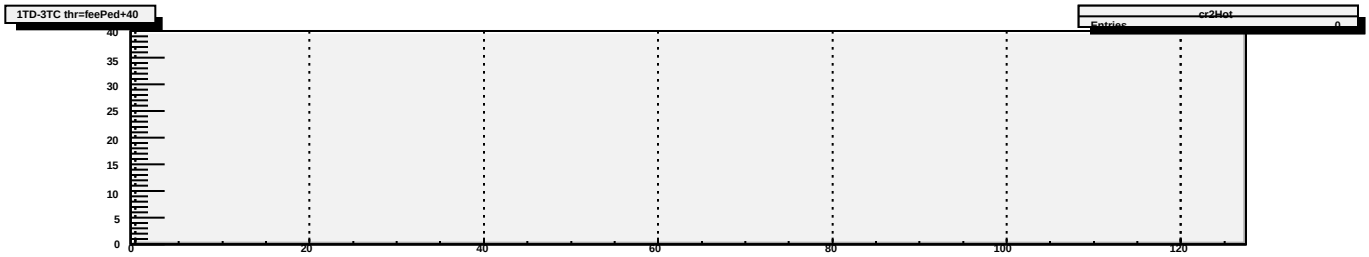

ETOW n256/eve
ETowN256
Entries 0
chan / eve

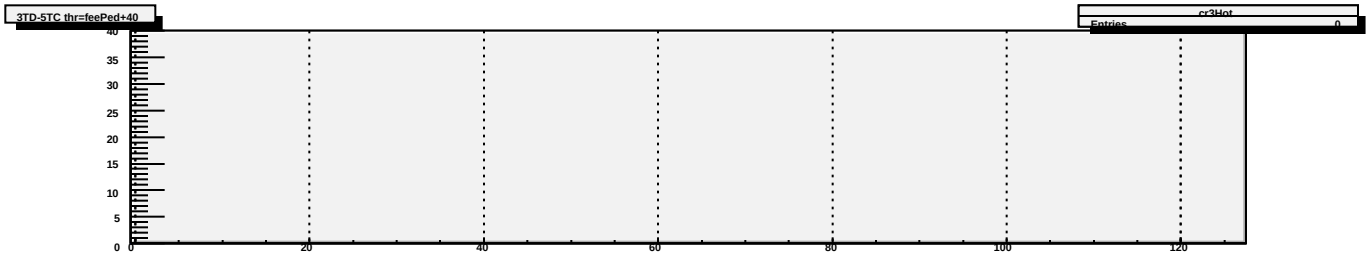

ETOW OFF ID
ETowOFFid
Entries 0
crate ID

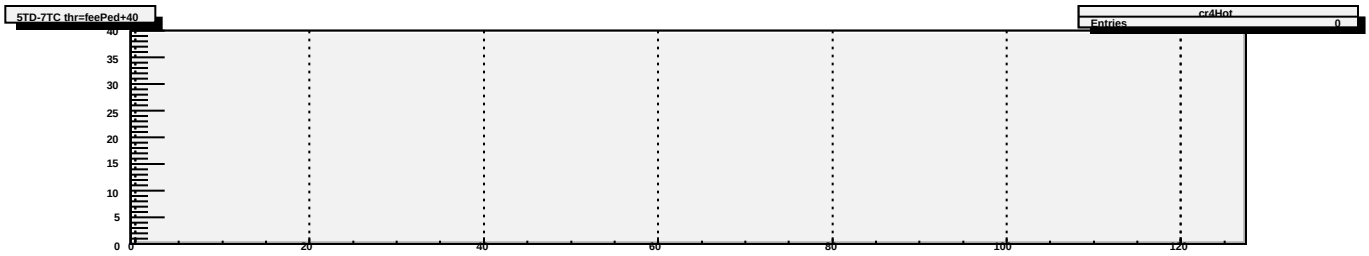

ETOW nGhost/eve
ETowGhost
Entries 0
chan / eve

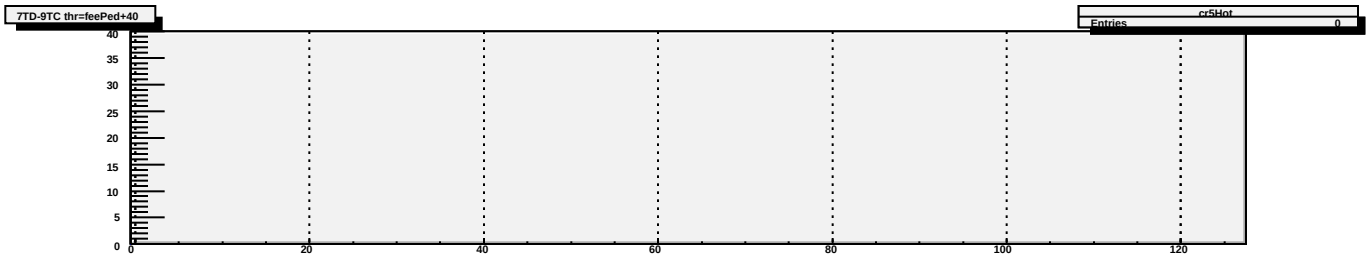

ETOW sanity
ETowCorrBit
Entries 0
events
X= crID + bits/10: 0=crID, 1=token,2=len,3=trgCom,4=ErrFlg,5=Ghost,6=n256

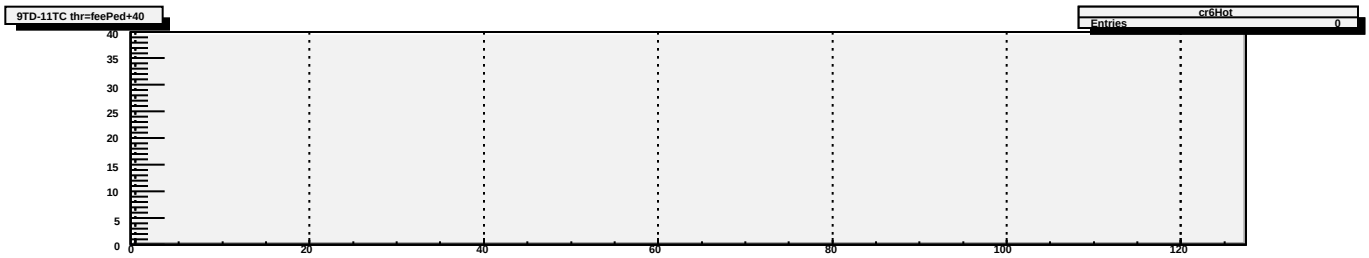

11TD-1TC thr=feePed+40
cr1Hot
Entries 0

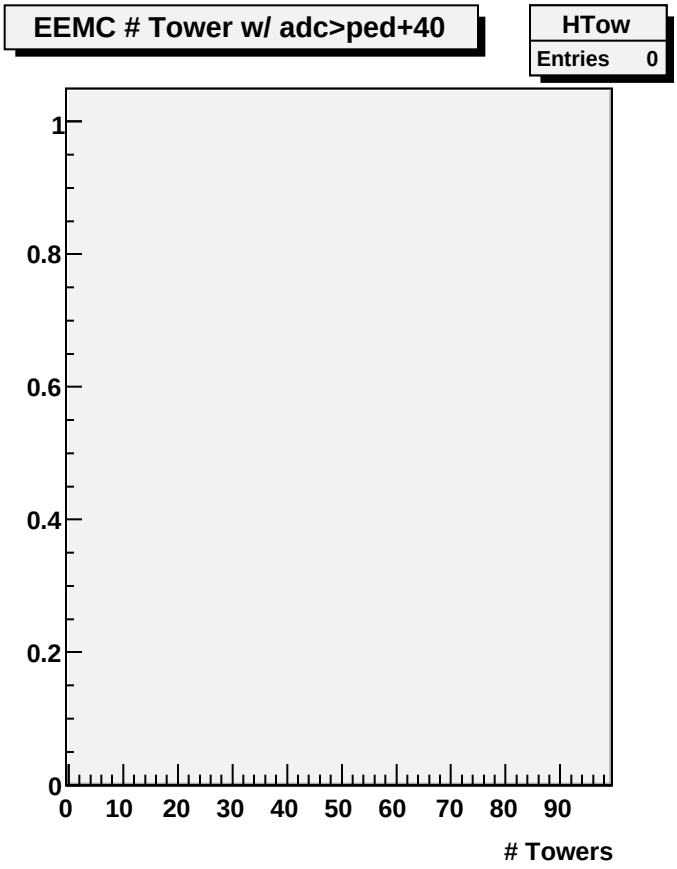

1TD-3TC thr=feePed+40
cr2Hot
Entries 0

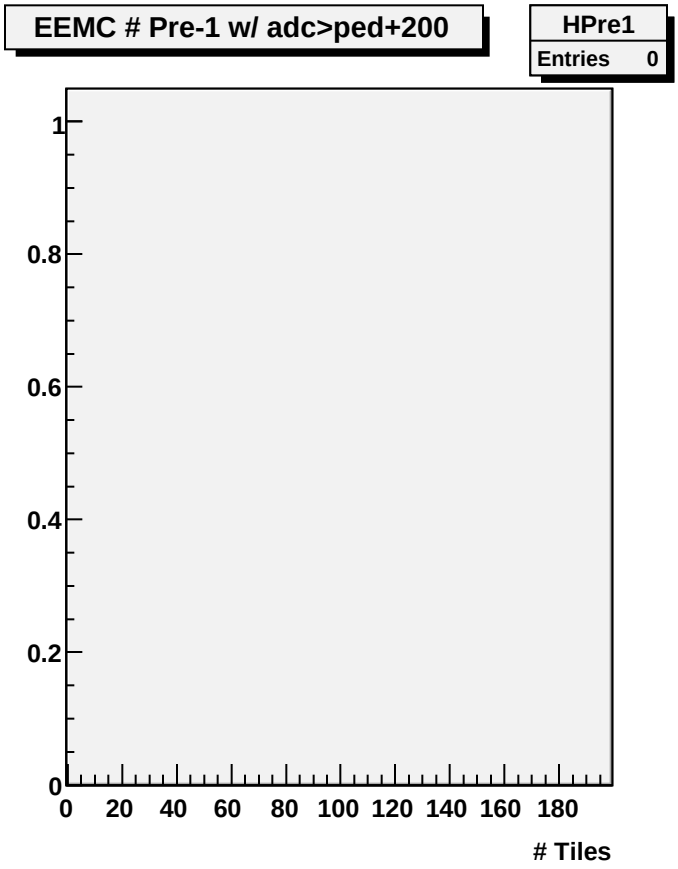

3TD-5TC thr=feePed+40
cr3Hot
Entries 0

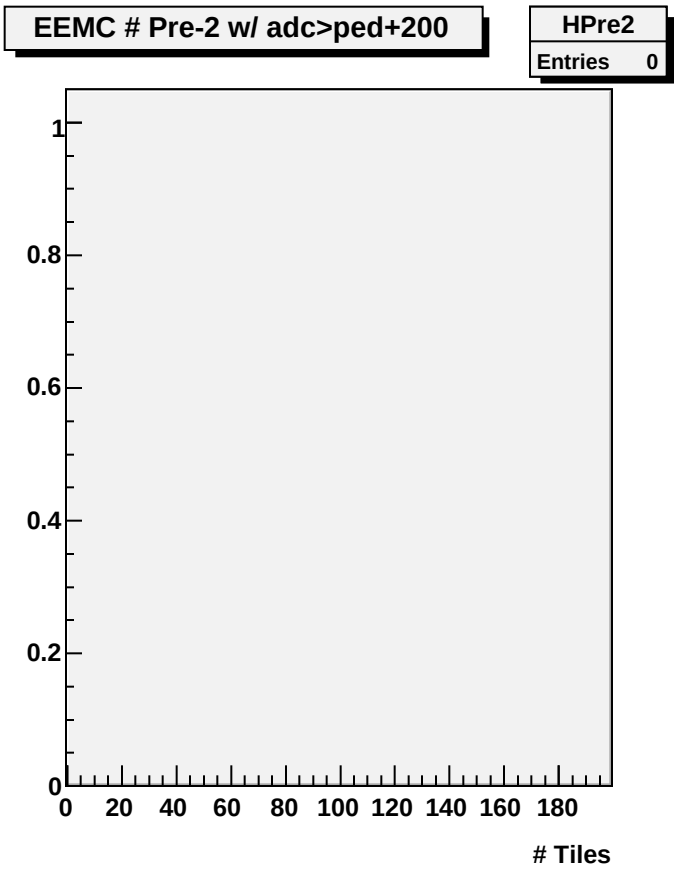

5TD-7TC thr=feePed+40
cr4Hot
Entries 0

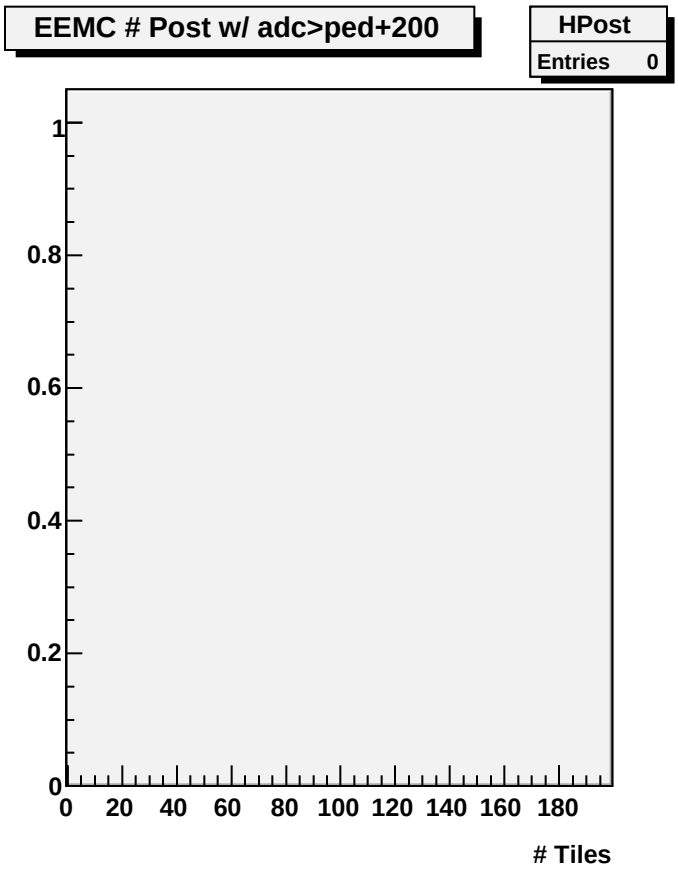

7TD-9TC thr=feePed+40
cr5Hot
Entries 0

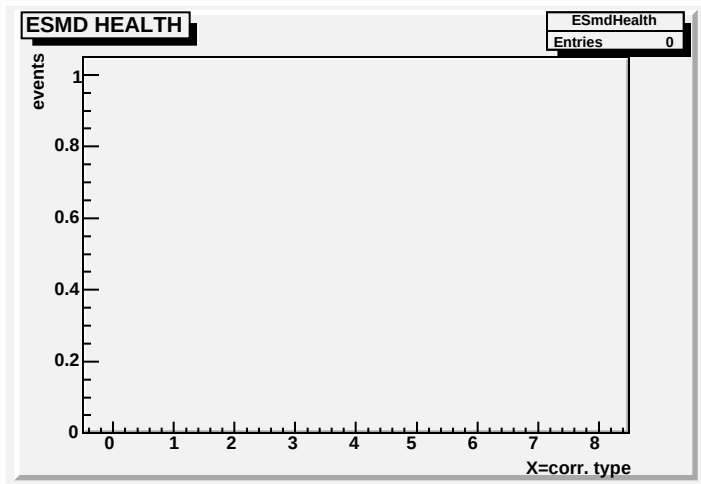

9TD-11TC thr=feePed+40
cr6Hot
Entries 0

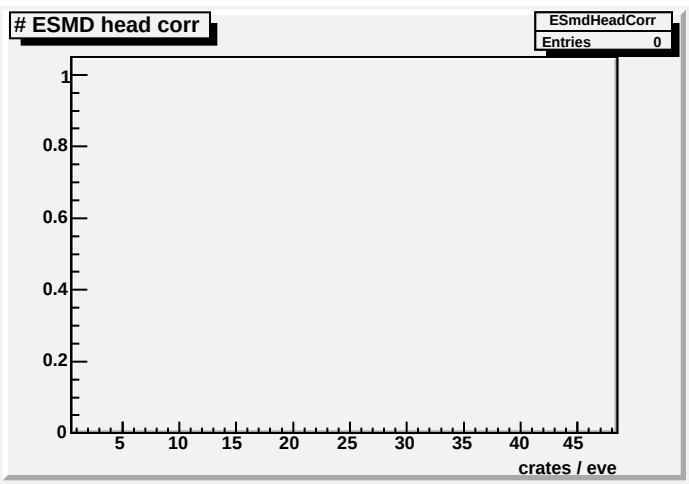

EEMC # Tower w/ adc>ped+40
HTow
Entries 0
1
0.8
0.6
0.4
0.2
0
0 10 20 30 40 50 60 70 80 90
Towers

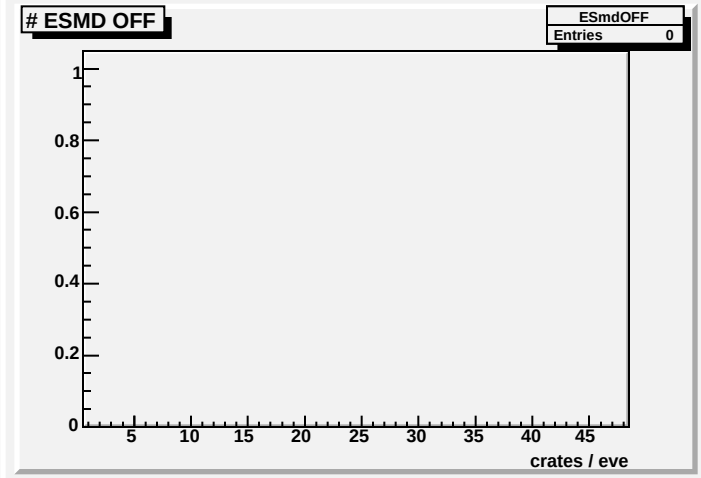

EEMC # Pre-1 w/ adc>ped+200
HPre1
Entries 0
1
0.8
0.6
0.4
0.2
0
0 20 40 60 80 100 120 140 160 180
Tiles

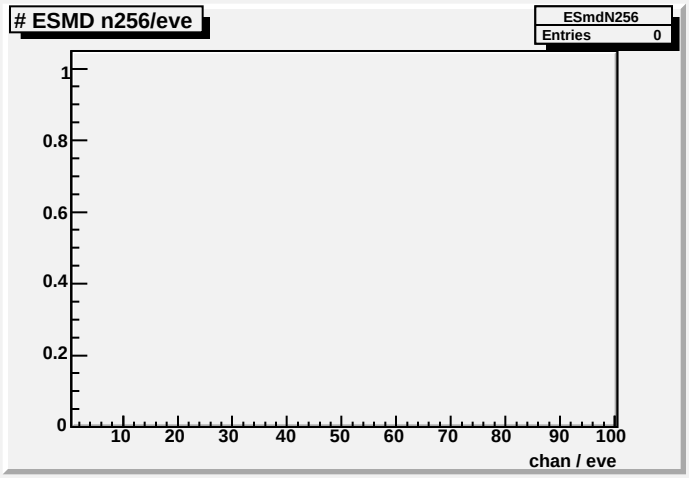

EEMC # Pre-2 w/ adc>ped+200
HPre2
Entries 0
1
0.8
0.6
0.4
0.2
0
0 20 40 60 80 100 120 140 160 180
Tiles

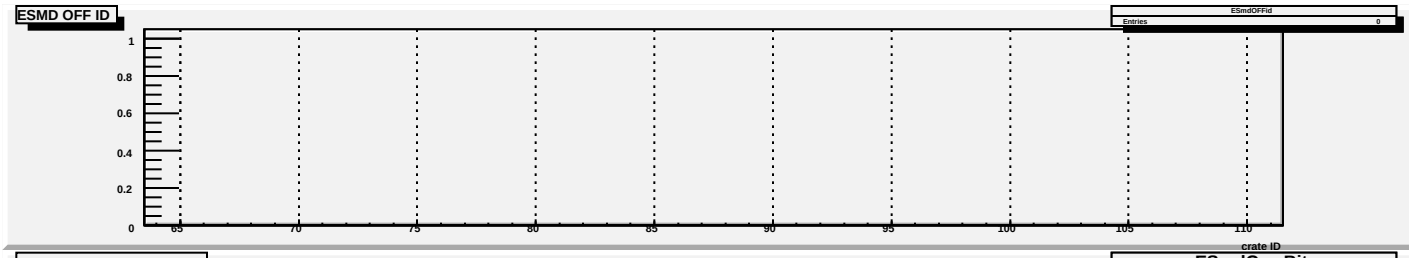

EEMC # Post w/ adc>ped+200
HPost
Entries 0
1
0.8
0.6
0.4
0.2
0
0 20 40 60 80 100 120 140 160 180
Tiles

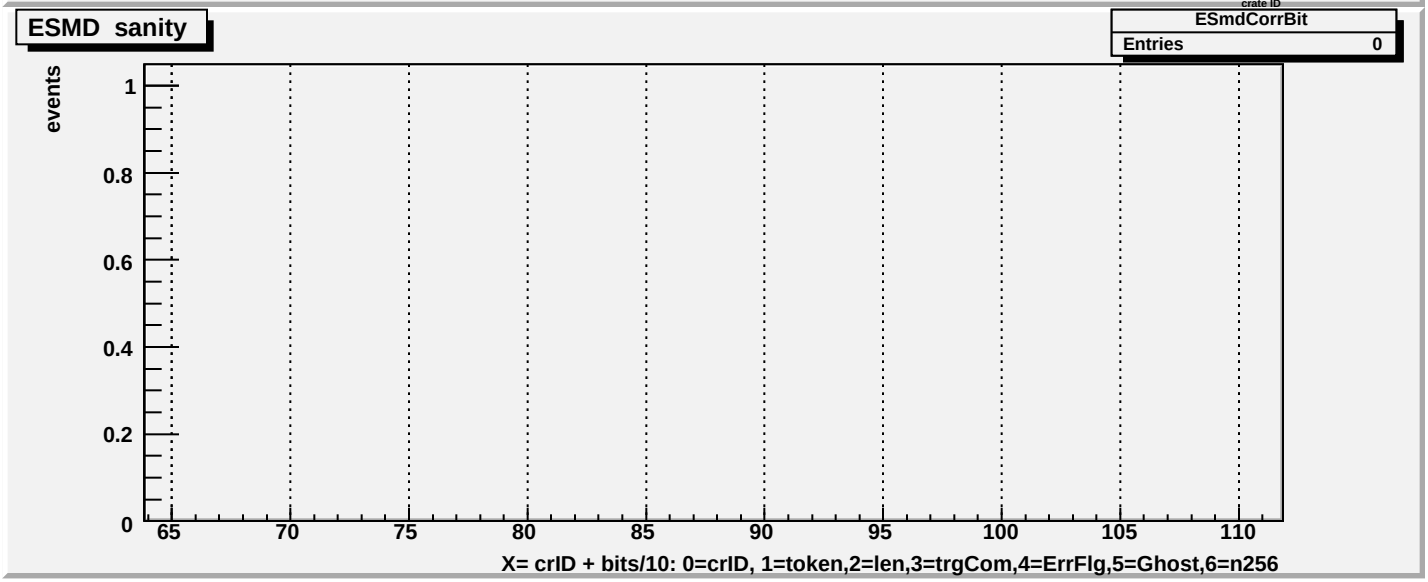

ESMD HEALTH
ESmdHealth
Entries 0
events
X=corr. type

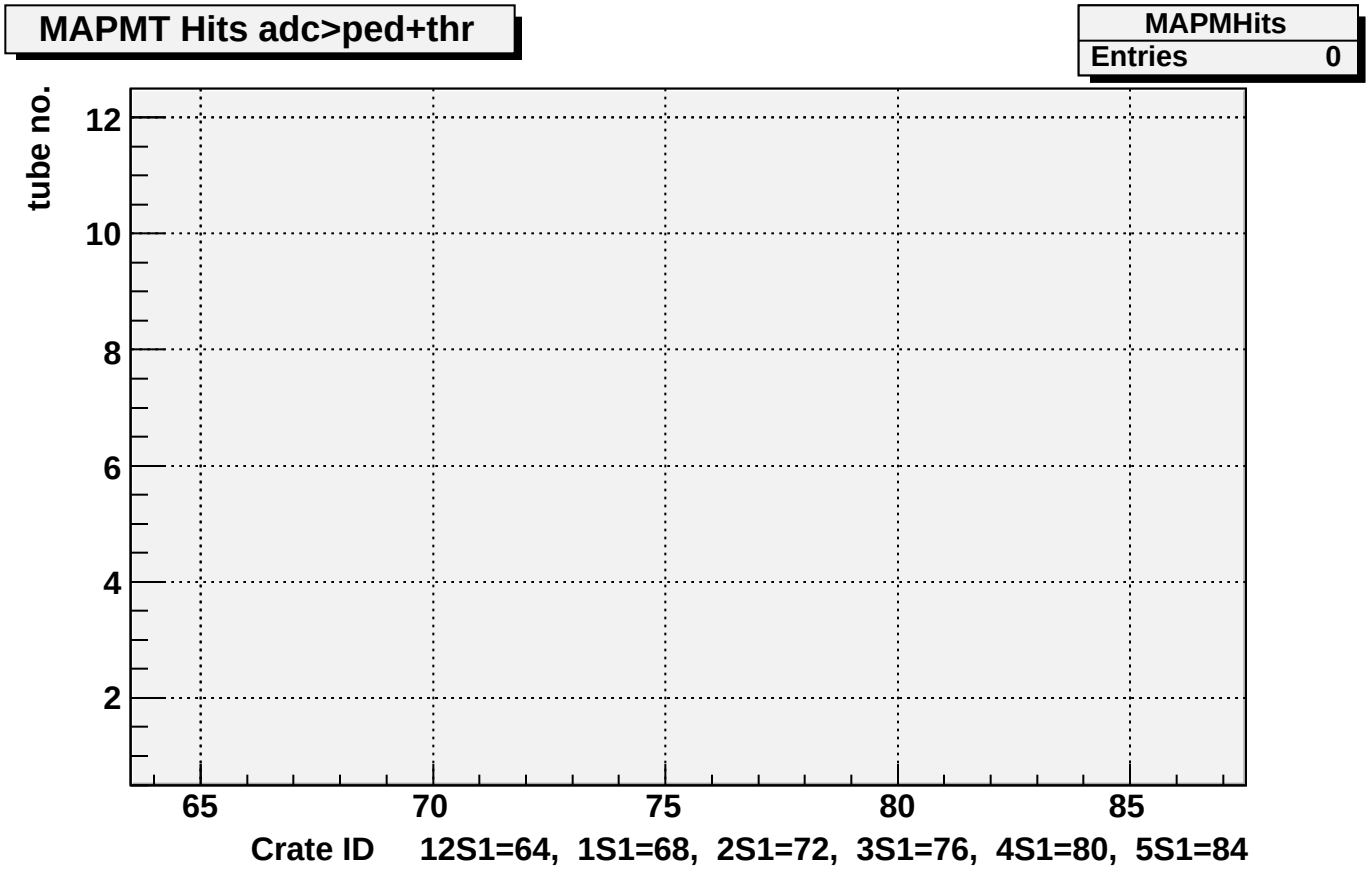

ESMD head corr
ESmdHeadCorr
Entries 0
crates / eve

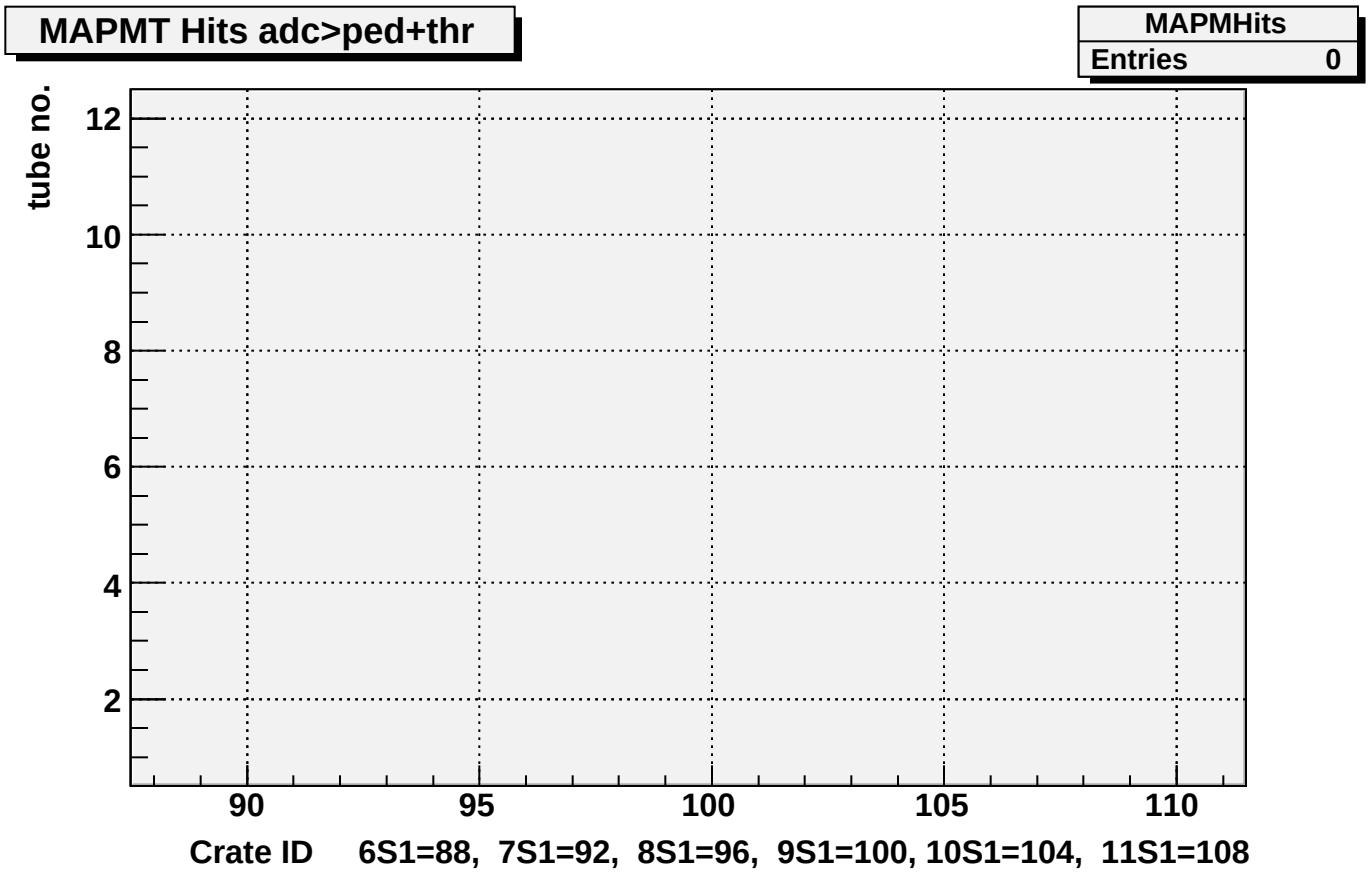

ESMD OFF
ESmdOFF
Entries 0
crates / eve

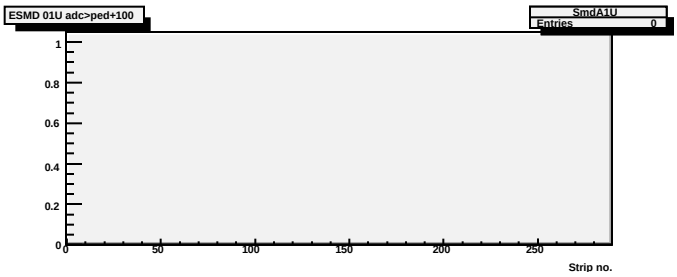

ESMD n256/eve
ESmdN256
Entries 0
chan / eve

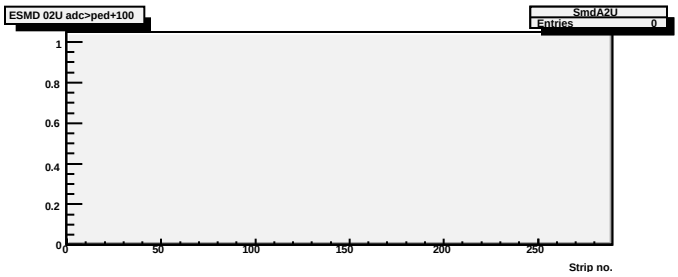

ESMD OFF ID
ESmdOFFid
Entries 0
crate ID

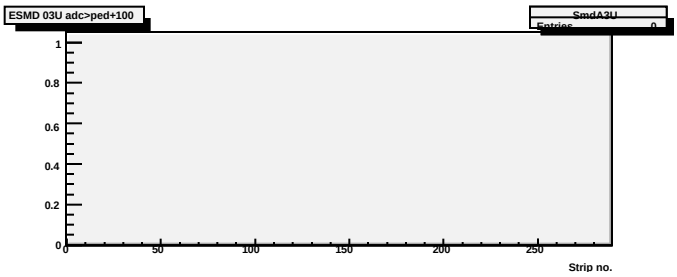

ESMD sanity
ESmdCorrBit
Entries 0
events
X= crID + bits/10: 0=crID, 1=token,2=len,3=trgCom,4=ErrFlg,5=Ghost,6=n256

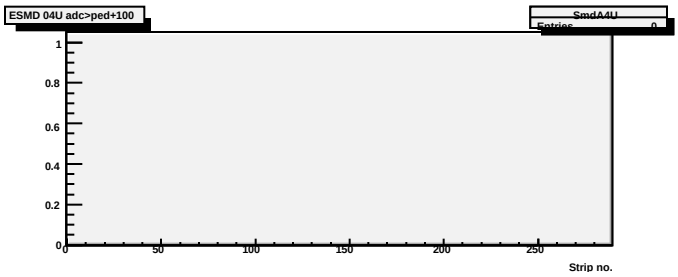

MAPMT Hits adc>ped+thr
MAPMHits
Entries 0
tube no.
12
10
8
6
4
2
65
70
75
80
85
Crate ID 12S1=64, 1S1=68, 2S1=72, 3S1=76, 4S1=80, 5S1=84

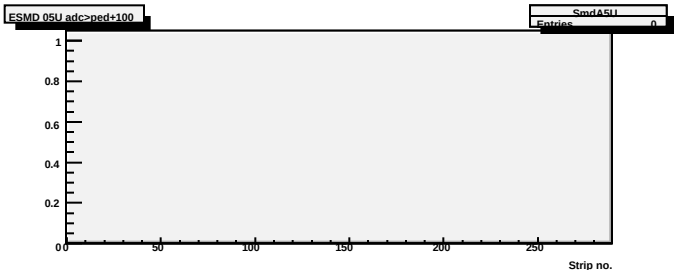

MAPMT Hits adc>ped+thr
MAPMHits
Entries 0
tube no.
12
10
8
6
4
2
90
95
100
105
110
Crate ID 6S1=88, 7S1=92, 8S1=96, 9S1=100, 10S1=104, 11S1=108

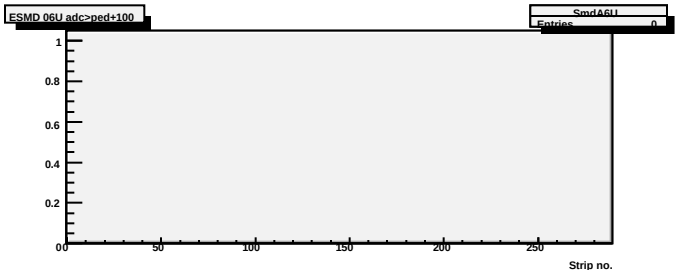

ESMD 01U adc>ped+100
SmdA1U
Entries 0
Strip no.

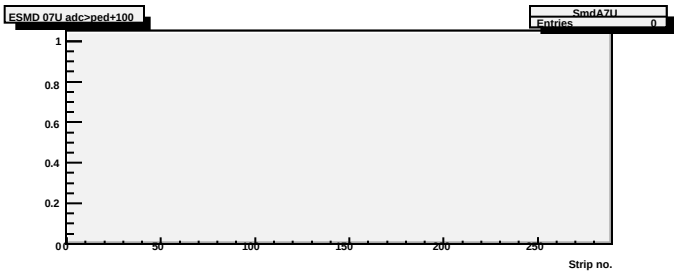

ESMD 02U adc>ped+100
SmdA2U
Entries 0
Strip no.

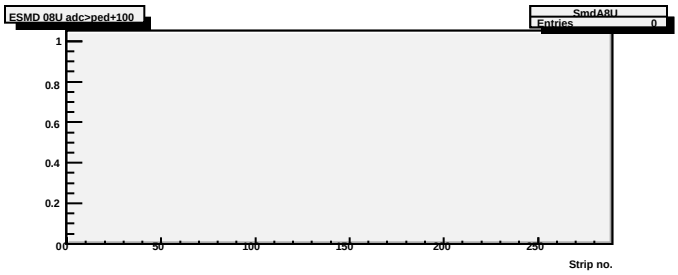

ESMD 03U adc>ped+100
SmdA3U
Entries 0
Strip no.

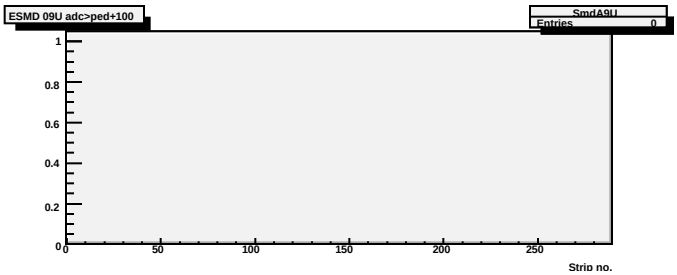

ESMD 04U adc>ped+100
SmdA4U
Entries 0
Strip no.

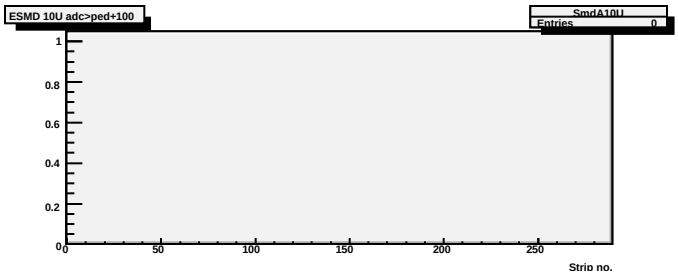

ESMD 05U adc>ped+100
SmdA5U
Entries 0
Strip no.

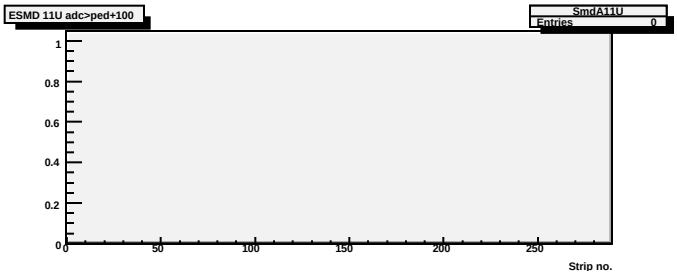

ESMD 06U adc>ped+100
SmdA6U
Entries 0
Strip no.

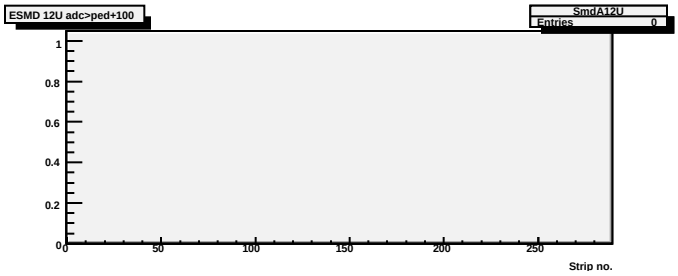

ESMD 07U adc>ped+100
SmdA7U
Entries 0
Strip no.

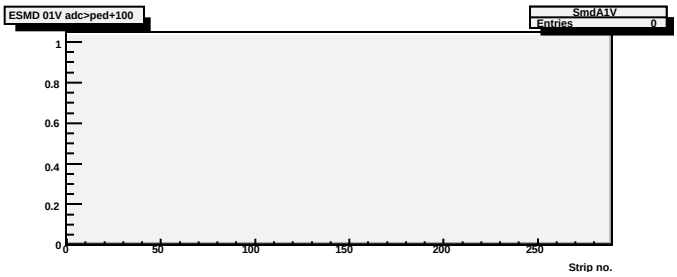

ESMD 08U adc>ped+100
SmdA8U
Entries 0
Strip no.

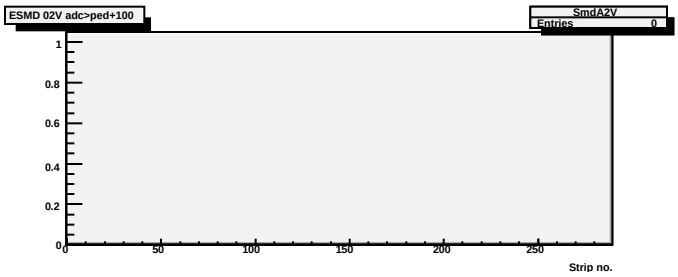

ESMD 09U adc>ped+100
SmdA9U
Entries 0
Strip no.

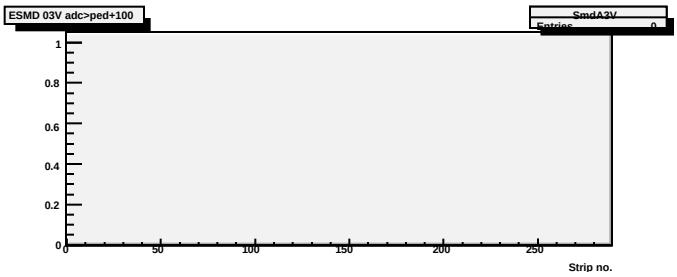

ESMD 10U adc>ped+100
SmdA10U
Entries 0
Strip no.

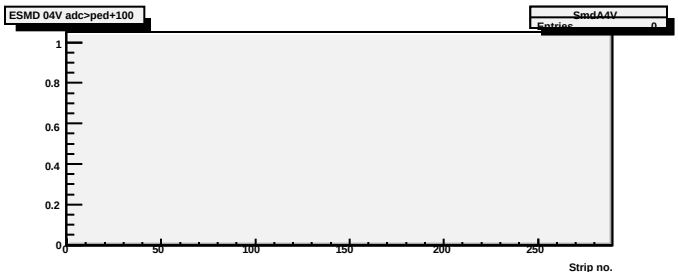

ESMD 11U adc>ped+100
SmdA11U
Entries 0
Strip no.

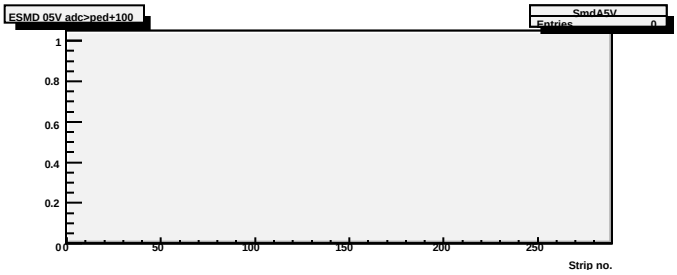

ESMD 12U adc>ped+100
SmdA12U
Entries 0
Strip no.

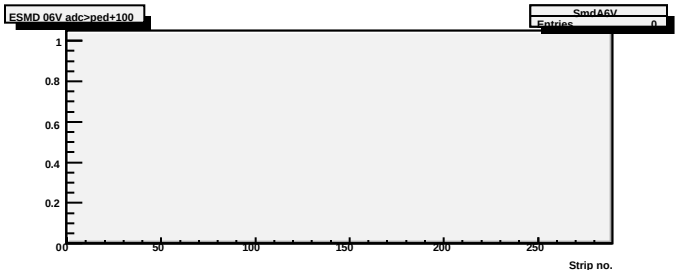

ESMD 01V adc>ped+100
SmdA1V
Entries 0
Strip no.

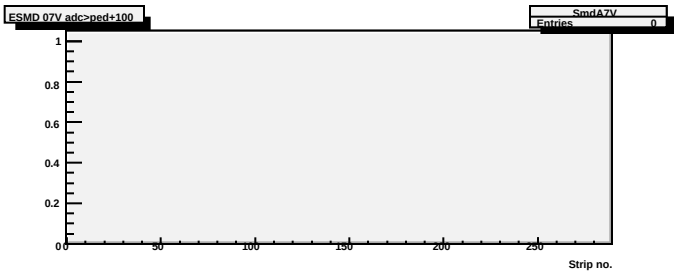

ESMD 02V adc>ped+100
SmdA2V
Entries 0
Strip no.

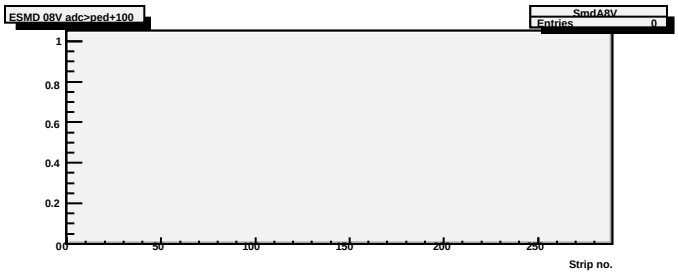

ESMD 03V adc>ped+100
SmdA3V
Entries 0
Strip no.

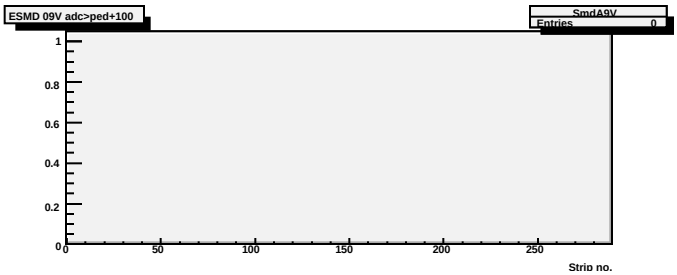

ESMD 04V adc>ped+100
SmdA4V
Entries 0
Strip no.

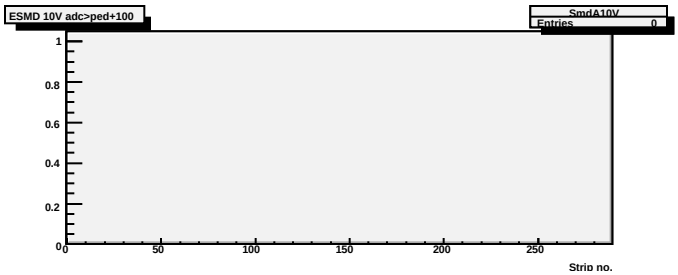

ESMD 05V adc>ped+100
SmdA5V
Entries 0
Strip no.

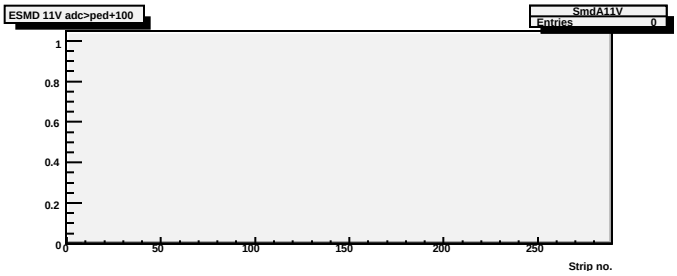

ESMD 06V adc>ped+100
SmdA6V
Entries 0
Strip no.

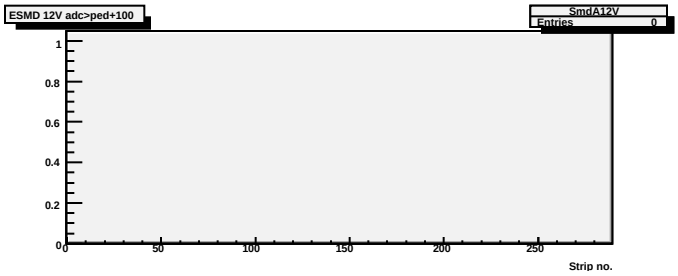

ESMD 07V adc>ped+100
SmdA7V
Entries 0
Strip no.

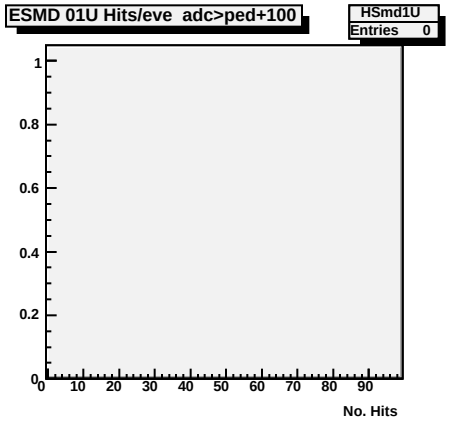

ESMD 08V adc>ped+100
SmdA8V
Entries 0
Strip no.

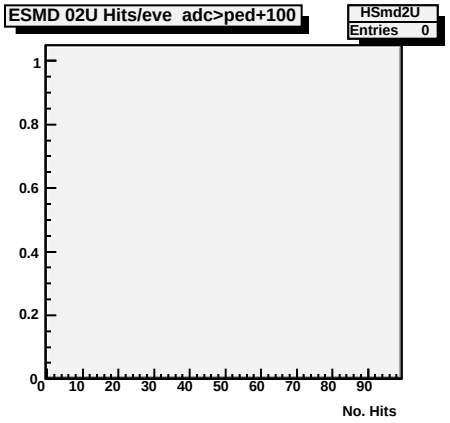

ESMD 09V adc>ped+100
SmdA9V
Entries 0
Strip no.

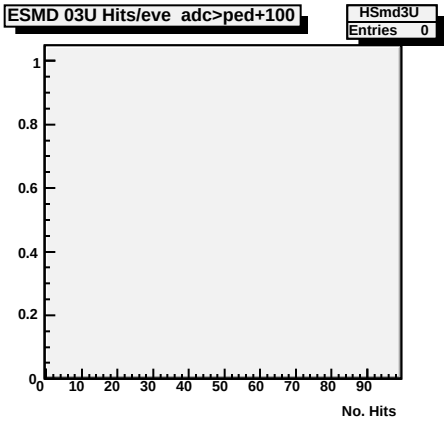

ESMD 10V adc>ped+100
SmdA10V
Entries 0
Strip no.

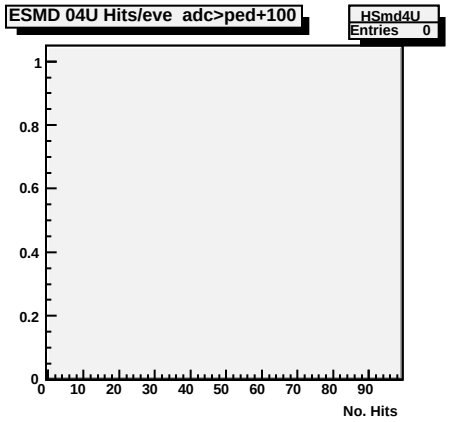

ESMD 11V adc>ped+100
SmdA11V
Entries 0
Strip no.

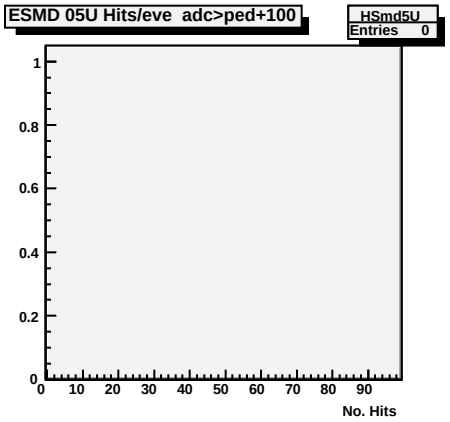

ESMD 12V adc>ped+100
SmdA12V
Entries 0
Strip no.

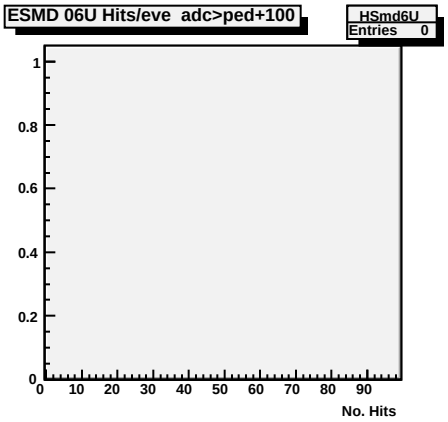

ESMD 01U Hits/eve adc>ped+100
HSmd1U
Entries 0
No. Hits

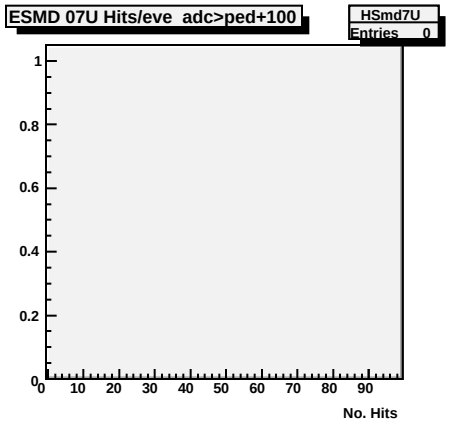

ESMD 02U Hits/eve adc>ped+100
HSmd2U
Entries 0
No. Hits

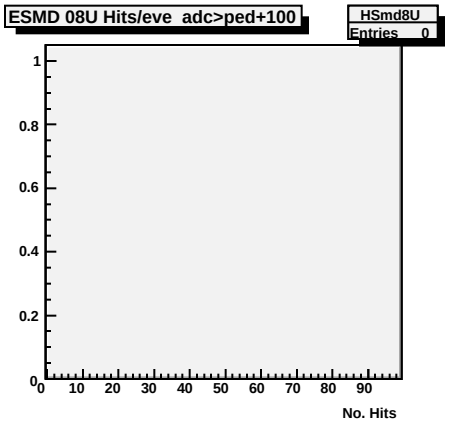

ESMD 03U Hits/eve adc>ped+100
HSmd3U
Entries 0
No. Hits

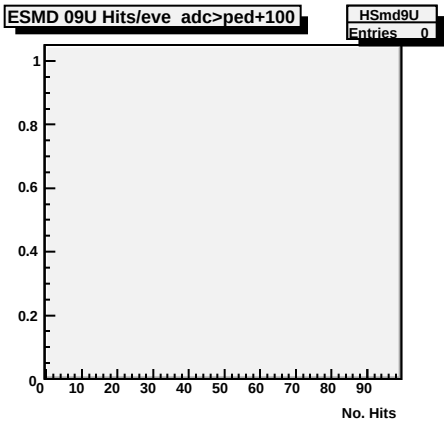

ESMD 04U Hits/eve adc>ped+100
HSmd4U
Entries 0
No. Hits

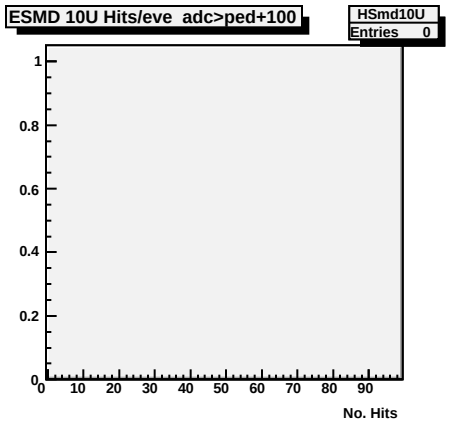

ESMD 05U Hits/eve adc>ped+100
HSmd5U
Entries 0
No. Hits

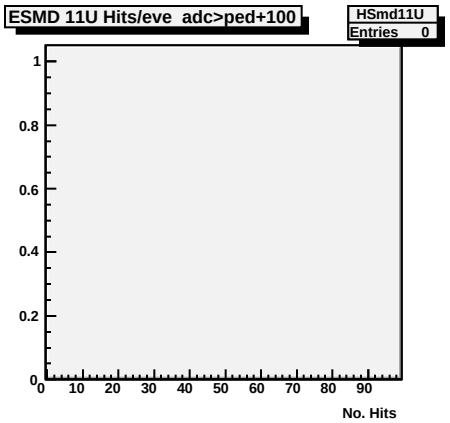

ESMD 06U Hits/eve adc>ped+100
HSmd6U
Entries 0
No. Hits

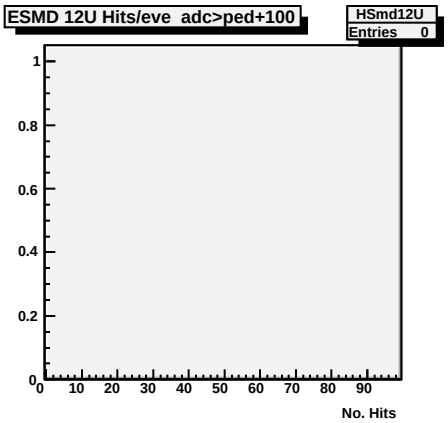

ESMD 07U Hits/eve adc>ped+100
HSmd7U
Entries 0
No. Hits

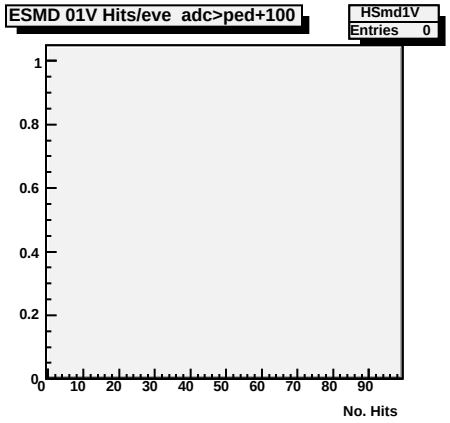

ESMD 08U Hits/eve adc>ped+100
HSmd8U
Entries 0
No. Hits

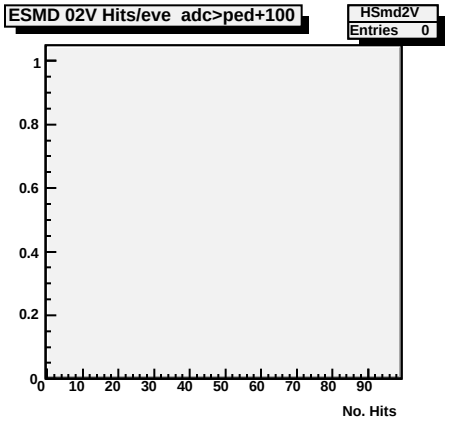

ESMD 09U Hits/eve adc>ped+100
HSmd9U
Entries 0
No. Hits

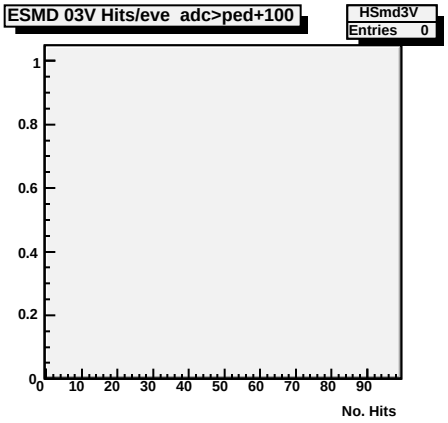

ESMD 10U Hits/eve adc>ped+100
HSmd10U
Entries 0
No. Hits

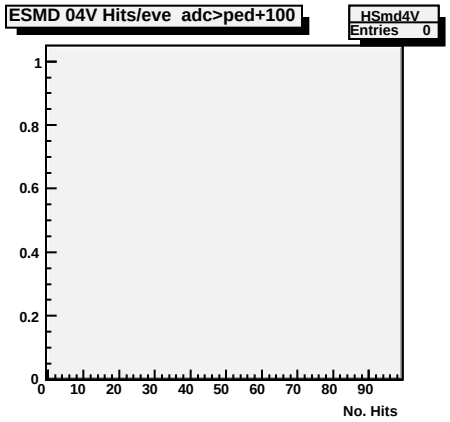

ESMD 11U Hits/eve adc>ped+100
HSmd11U
Entries 0
No. Hits

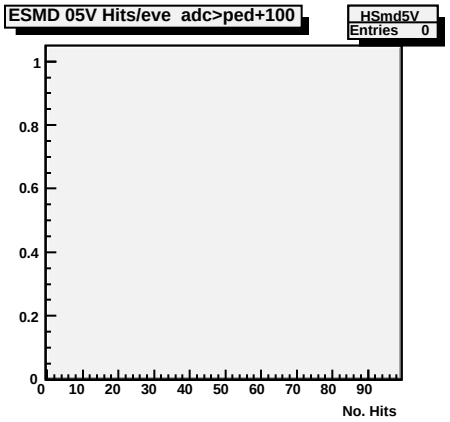

ESMD 12U Hits/eve adc>ped+100
HSmd12U
Entries 0
No. Hits

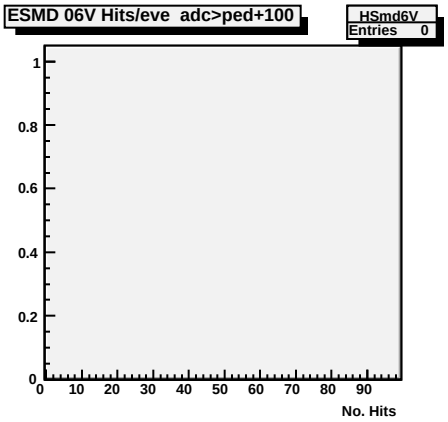

ESMD 01V Hits/eve adc>ped+100
HSmd1V
Entries 0
No. Hits

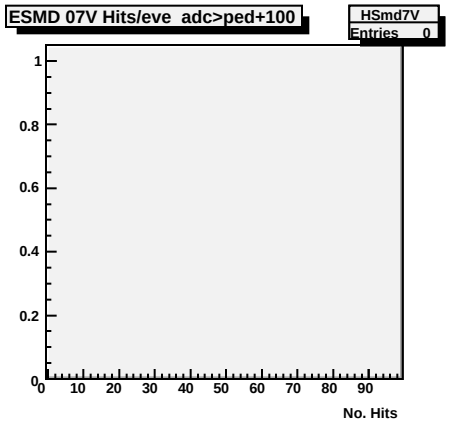

ESMD 02V Hits/eve adc>ped+100
HSmd2V
Entries 0
No. Hits

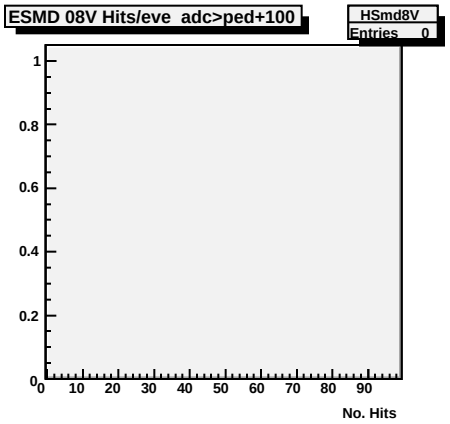

ESMD 03V Hits/eve adc>ped+100
HSmd3V
Entries 0
No. Hits

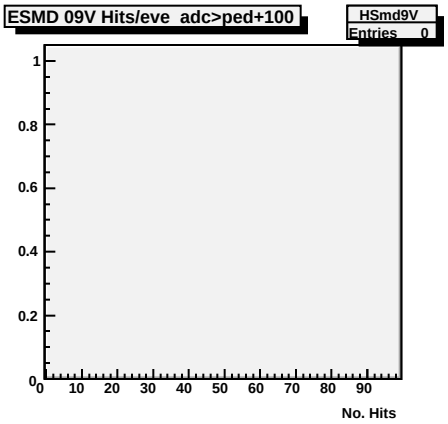

ESMD 04V Hits/eve adc>ped+100
HSmd4V
Entries 0
No. Hits

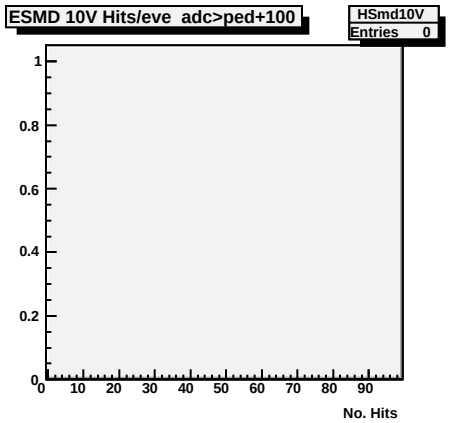

ESMD 05V Hits/eve adc>ped+100
HSmd5V
Entries 0
No. Hits

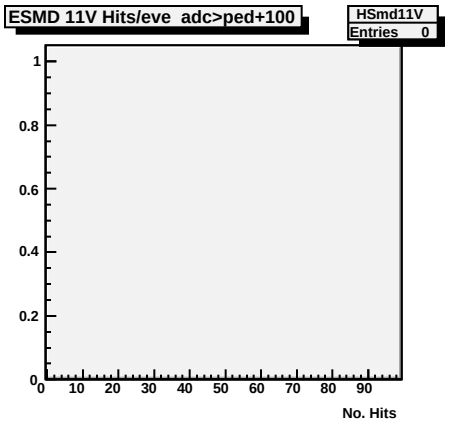

ESMD 06V Hits/eve adc>ped+100
HSmd6V
Entries 0
No. Hits

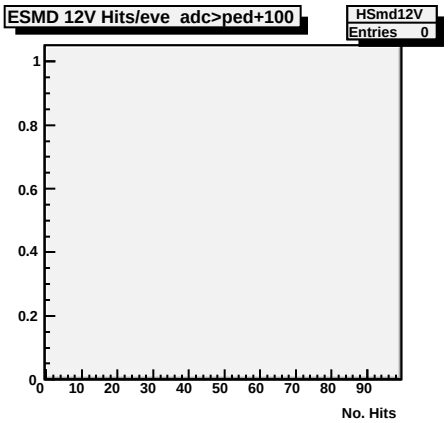

ESMD 07V Hits/eve adc>ped+100
HSmd7V
Entries 0
No. Hits

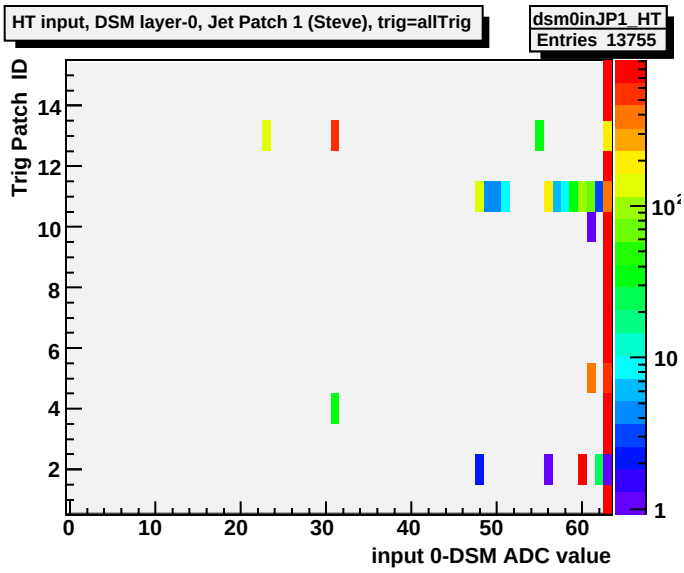

ESMD 08V Hits/eve adc>ped+100
HSmd8V
Entries 0
No. Hits

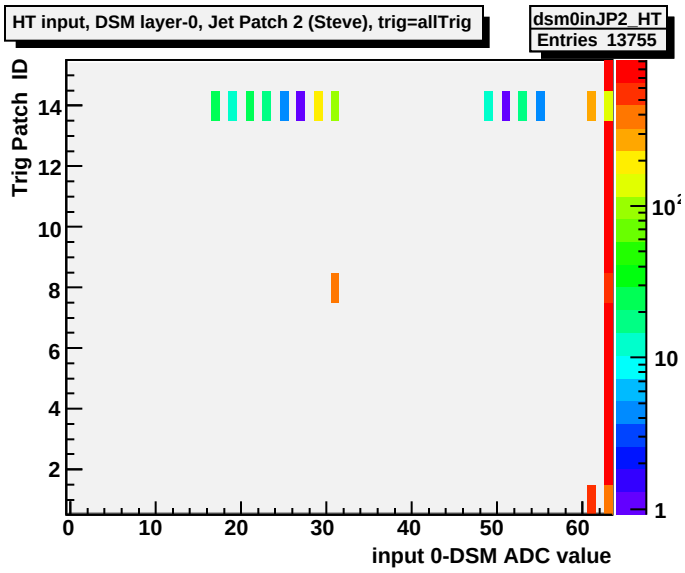

ESMD 09V Hits/eve adc>ped+100
HSmd9V
Entries 0
No. Hits

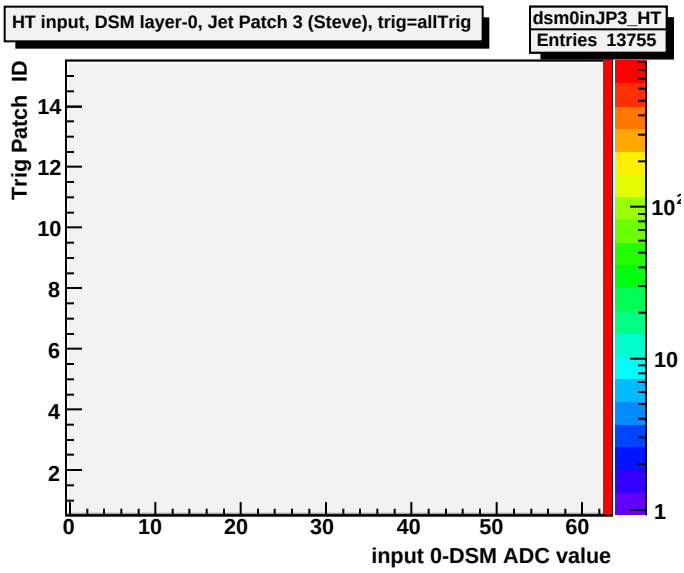

ESMD 10V Hits/eve adc>ped+100
HSmd10V
Entries 0
No. Hits

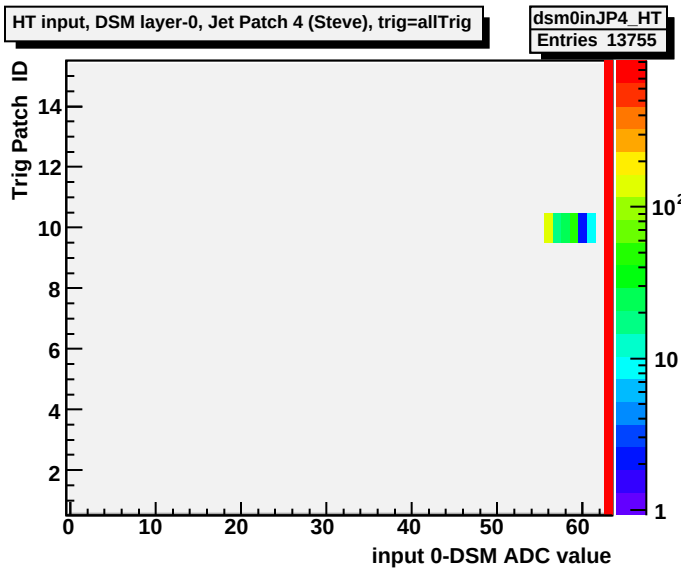

ESMD 11V Hits/eve adc>ped+100
HSmd11V
Entries 0
No. Hits

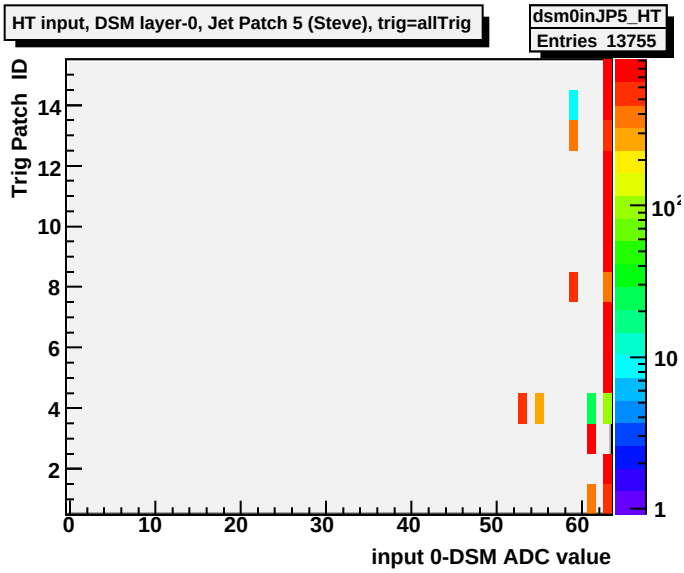

ESMD 12V Hits/eve adc>ped+100
HSmd12V
Entries 0
No. Hits

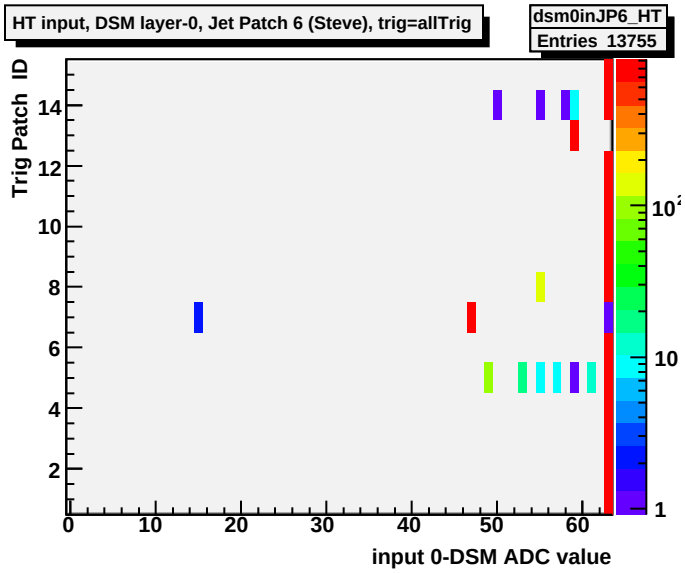

HT input, DSM layer-0, Jet Patch 1 (Steve), trig=allTrig
dsm0inJP1_HT
Entries 13755
Trig Patch ID
input 0-DSM ADC value

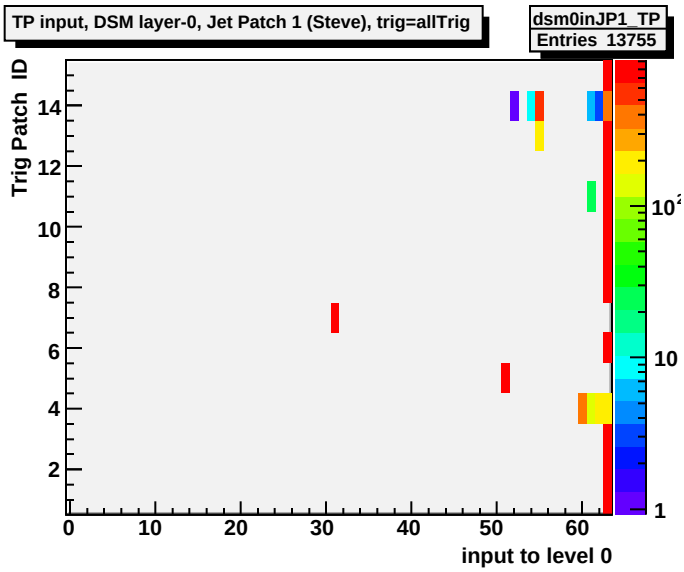

HT input, DSM layer-0, Jet Patch 2 (Steve), trig=allTrig
dsm0inJP2_HT
Entries 13755
Trig Patch ID
input 0-DSM ADC value

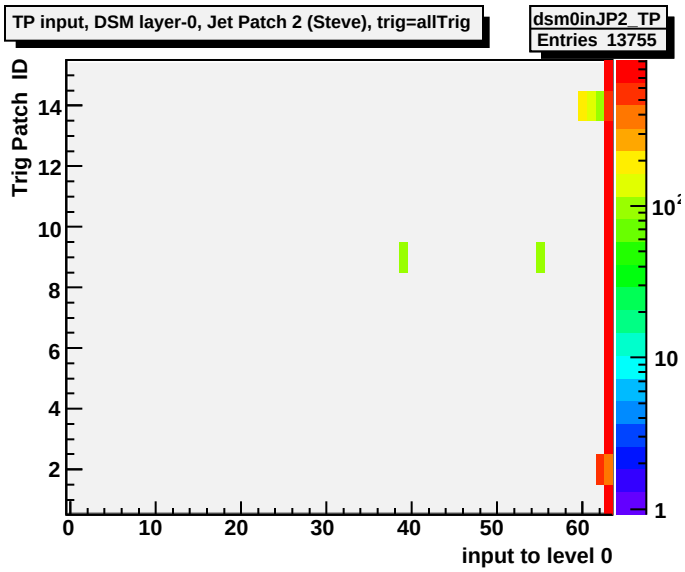

HT input, DSM layer-0, Jet Patch 3 (Steve), trig=allTrig
dsm0inJP3_HT
Entries 13755
Trig Patch ID
input 0-DSM ADC value

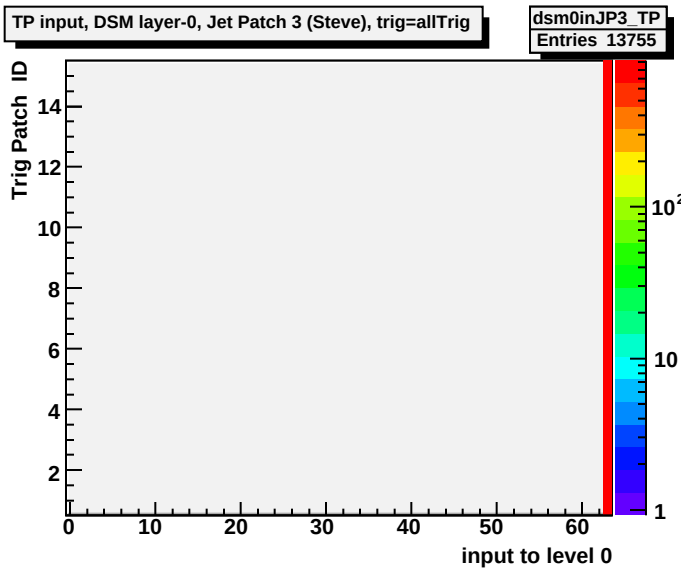

HT input, DSM layer-0, Jet Patch 4 (Steve), trig=allTrig
dsm0inJP4_HT
Entries 13755
Trig Patch ID
input 0-DSM ADC value

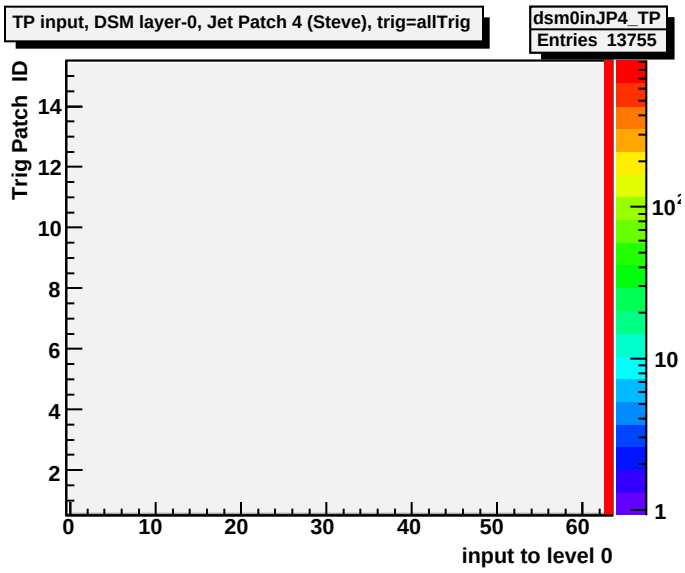

HT input, DSM layer-0, Jet Patch 5 (Steve), trig=allTrig
dsm0inJP5_HT
Entries 13755
Trig Patch ID
input 0-DSM ADC value

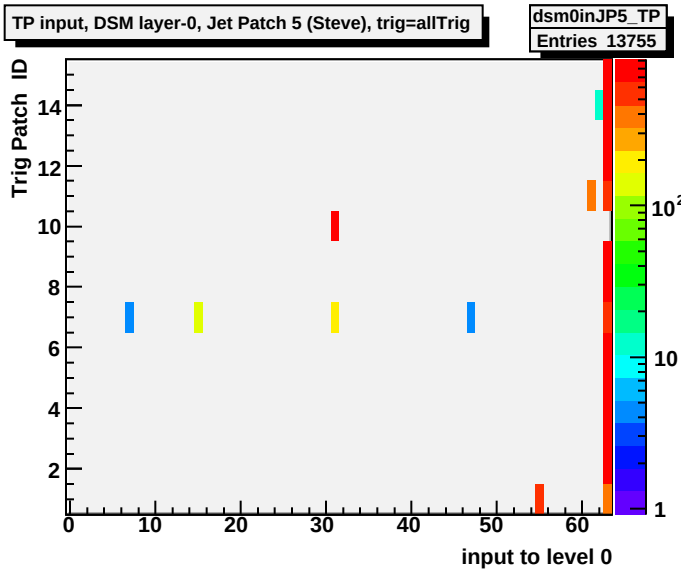

HT input, DSM layer-0, Jet Patch 6 (Steve), trig=allTrig
dsm0inJP6_HT
Entries 13755
Trig Patch ID
input 0-DSM ADC value

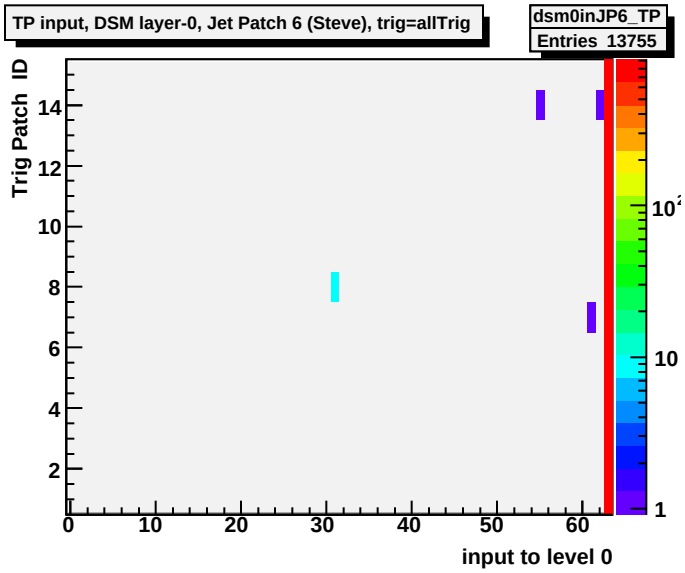

TP input, DSM layer-0, Jet Patch 1 (Steve), trig=allTrig
dsm0inJP1_TP
Entries 13755
Trig Patch ID
input to level 0

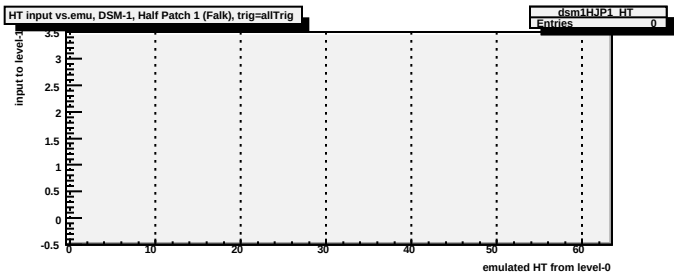

TP input, DSM layer-0, Jet Patch 2 (Steve), trig=allTrig
dsm0inJP2_TP
Entries 13755
Trig Patch ID
input to level 0

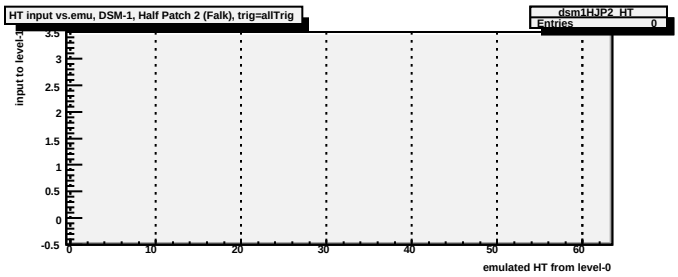

TP input, DSM layer-0, Jet Patch 3 (Steve), trig=allTrig
dsm0inJP3_TP
Entries 13755
Trig Patch ID
input to level 0

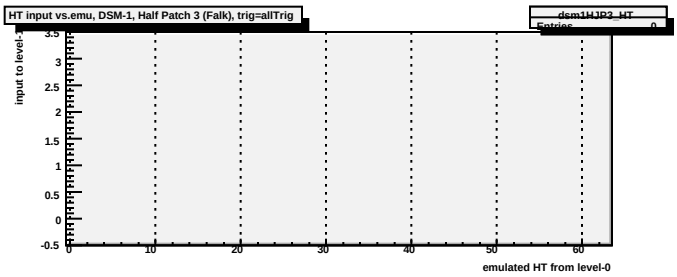

TP input, DSM layer-0, Jet Patch 4 (Steve), trig=allTrig
dsm0inJP4_TP
Entries 13755
Trig Patch ID
input to level 0

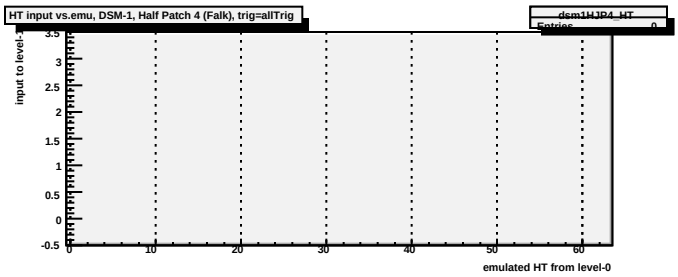

TP input, DSM layer-0, Jet Patch 5 (Steve), trig=allTrig
dsm0inJP5_TP
Entries 13755
Trig Patch ID
input to level 0

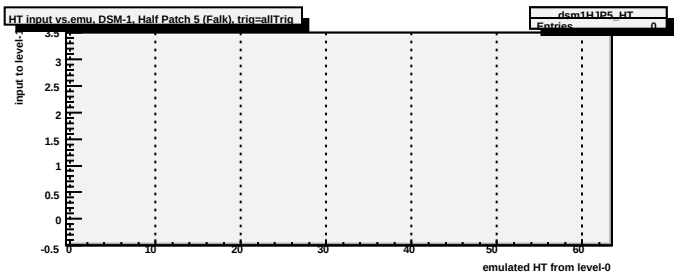

TP input, DSM layer-0, Jet Patch 6 (Steve), trig=allTrig
dsm0inJP6_TP
Entries 13755
Trig Patch ID
input to level 0

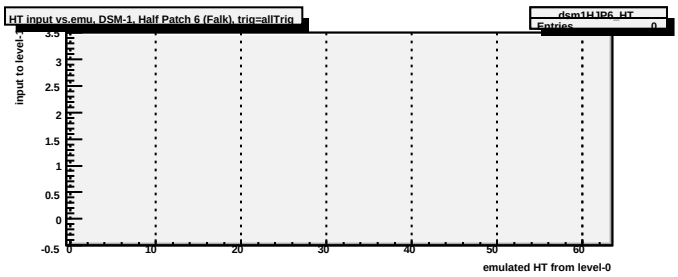

HT input vs.emu, DSM-1, Half Patch 1 (Falk), trig=allTrig
dsm1HJP1_HT
Entries 0
input to level-1
emulated HT from level-0

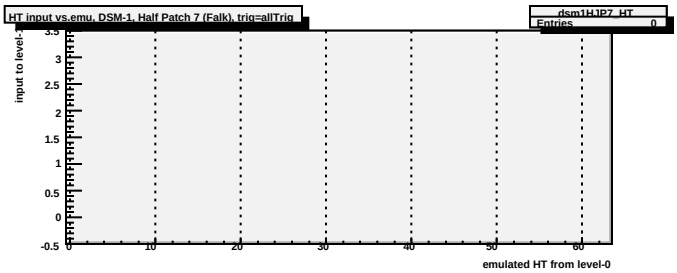

HT input vs.emu, DSM-1, Half Patch 2 (Falk), trig=allTrig
dsm1HJP2_HT
Entries 0
input to level-1
emulated HT from level-0

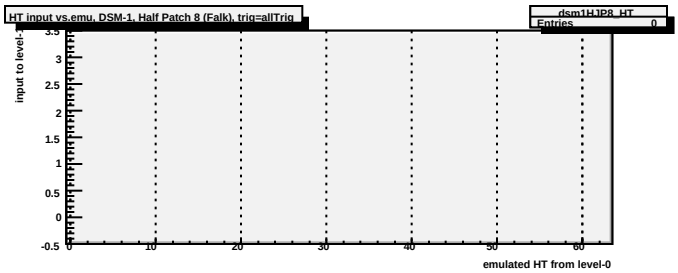

HT input vs.emu, DSM-1, Half Patch 3 (Falk), trig=allTrig
dsm1HJP3_HT
Entries 0
input to level-1
emulated HT from level-0

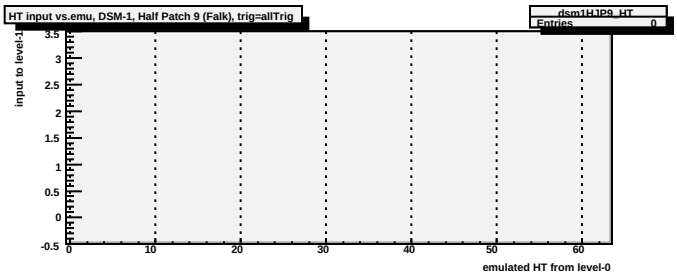

HT input vs.emu, DSM-1, Half Patch 4 (Falk), trig=allTrig
dsm1HJP4_HT
Entries 0
input to level-1
emulated HT from level-0

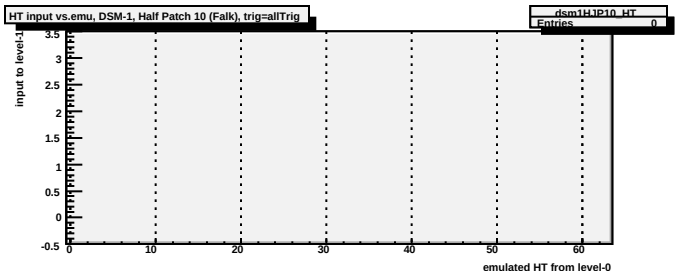

HT input vs.emu, DSM-1, Half Patch 5 (Falk), trig=allTrig
dsm1HJP5_HT
Entries 0
input to level-1
emulated HT from level-0

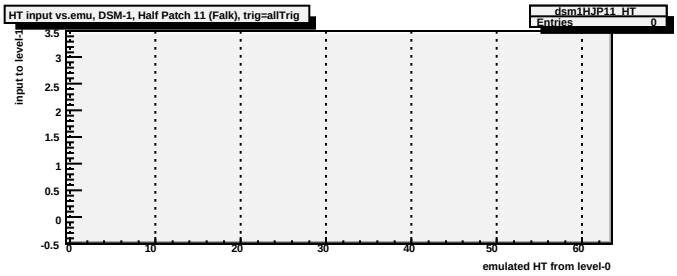

HT input vs.emu, DSM-1, Half Patch 6 (Falk), trig=allTrig
dsm1HJP6_HT
Entries 0
input to level-1
emulated HT from level-0

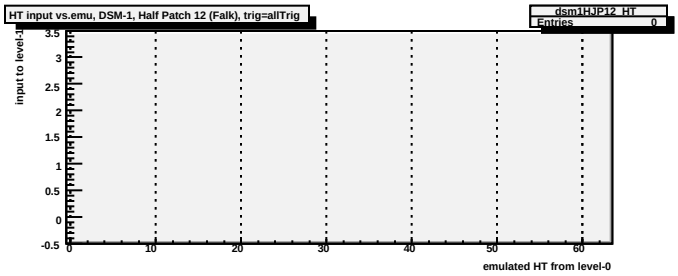

HT input vs.emu, DSM-1, Half Patch 7 (Falk), trig=allTrig
dsm1HJP7_HT
Entries 0
input to level-1
emulated HT from level-0

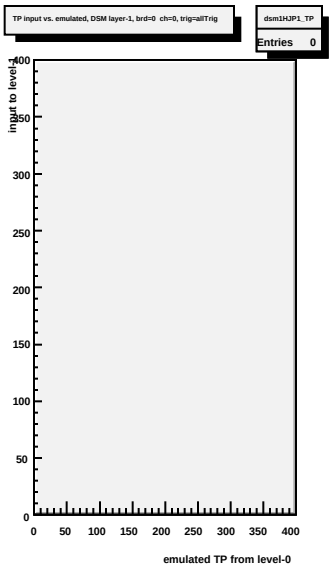

HT input vs.emu, DSM-1, Half Patch 8 (Falk), trig=allTrig
dsm1HJP8_HT
Entries 0
input to level-1
emulated HT from level-0

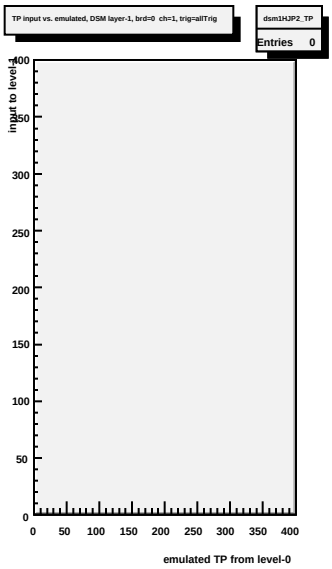

HT input vs.emu, DSM-1, Half Patch 9 (Falk), trig=allTrig
dsm1HJP9_HT
Entries 0
input to level-1
emulated HT from level-0

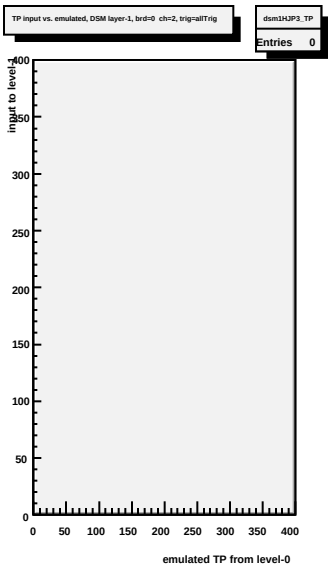

HT input vs.emu, DSM-1, Half Patch 10 (Falk), trig=allTrig
dsm1HJP10_HT
Entries 0
input to level-1
emulated HT from level-0

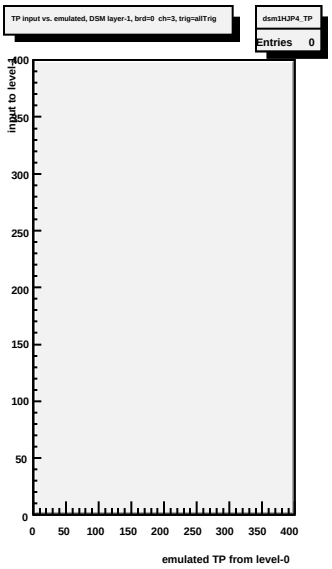

HT input vs.emu, DSM-1, Half Patch 11 (Falk), trig=allTrig
dsm1HJP11_HT
Entries 0
input to level-1
emulated HT from level-0

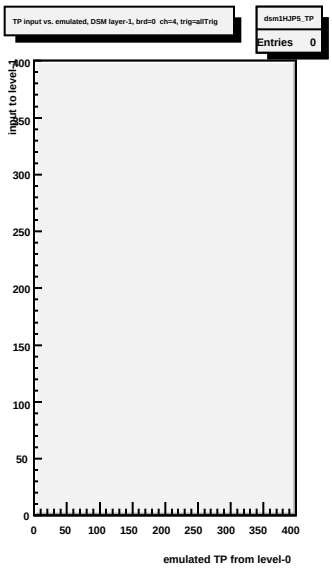

HT input vs.emu, DSM-1, Half Patch 12 (Falk), trig=allTrig
dsm1HJP12_HT
Entries 0
input to level-1
emulated HT from level-0

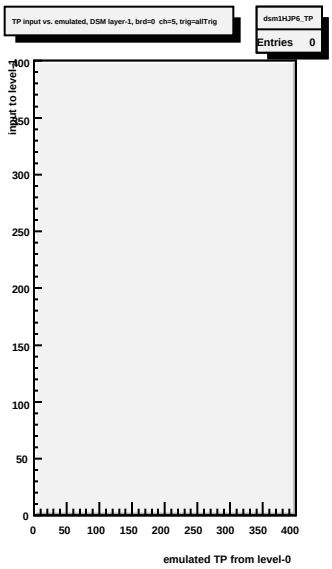

TP input vs. emulated, DSM layer-1, brd=0 ch=0, trig=allTrig
dsm1HJP1_TP
Entries 0
input to level-1
emulated TP from level-0

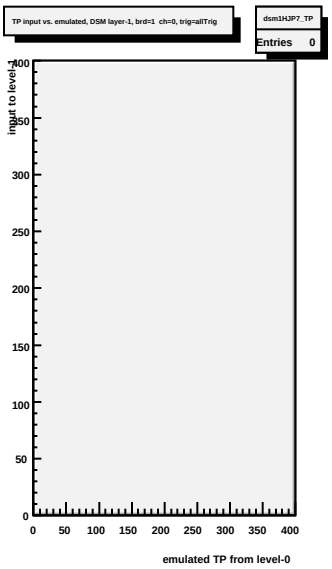

TP input vs. emulated, DSM layer-1, brd=0 ch=1, trig=allTrig
dsm1HJP2_TP
Entries 0
input to level-1
emulated TP from level-0

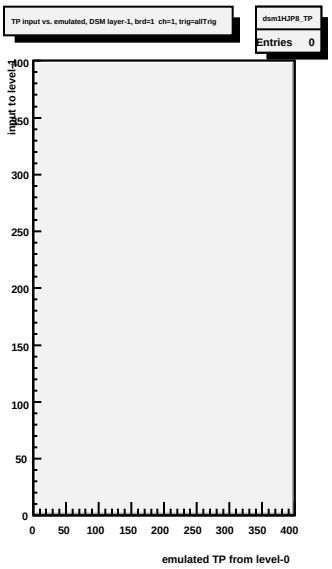

TP input vs. emulated, DSM layer-1, brd=0 ch=2, trig=allTrig
dsm1HJP3_TP
Entries 0
input to level-1
emulated TP from level-0

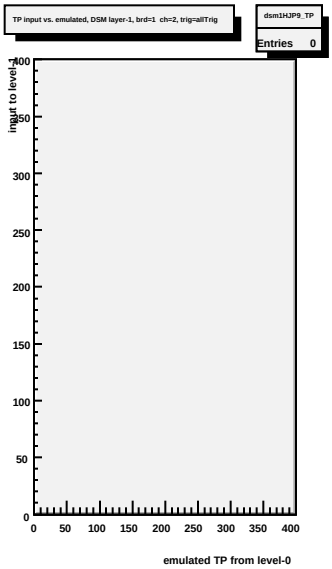

TP input vs. emulated, DSM layer-1, brd=0 ch=3, trig=allTrig
dsm1HJP4_TP
Entries 0
input to level-1
emulated TP from level-0

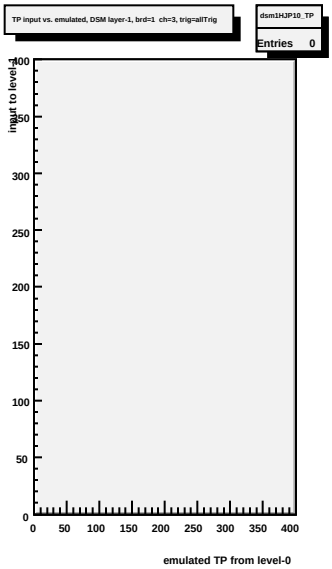

TP input vs. emulated, DSM layer-1, brd=0 ch=4, trig=allTrig
dsm1HJP5_TP
Entries 0
input to level-1
emulated TP from level-0

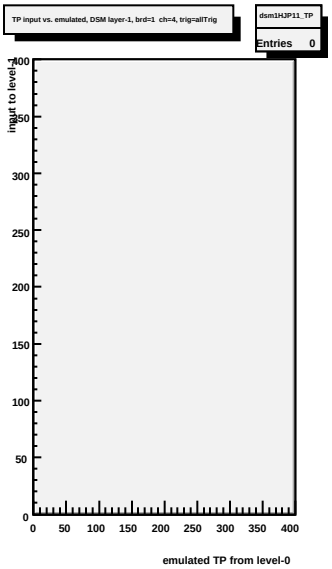

TP input vs. emulated, DSM layer-1, brd=0 ch=5, trig=allTrig
dsm1HJP6_TP
Entries 0
input to level-1
emulated TP from level-0

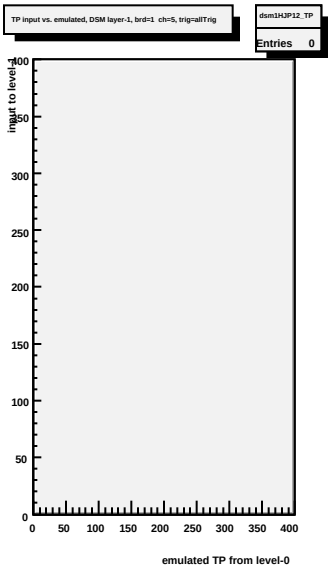

TP input vs. emulated, DSM layer-1, brd=1 ch=0, trig=allTrig
dsm1HJP7_TP
Entries 0
input to level-1
emulated TP from level-0

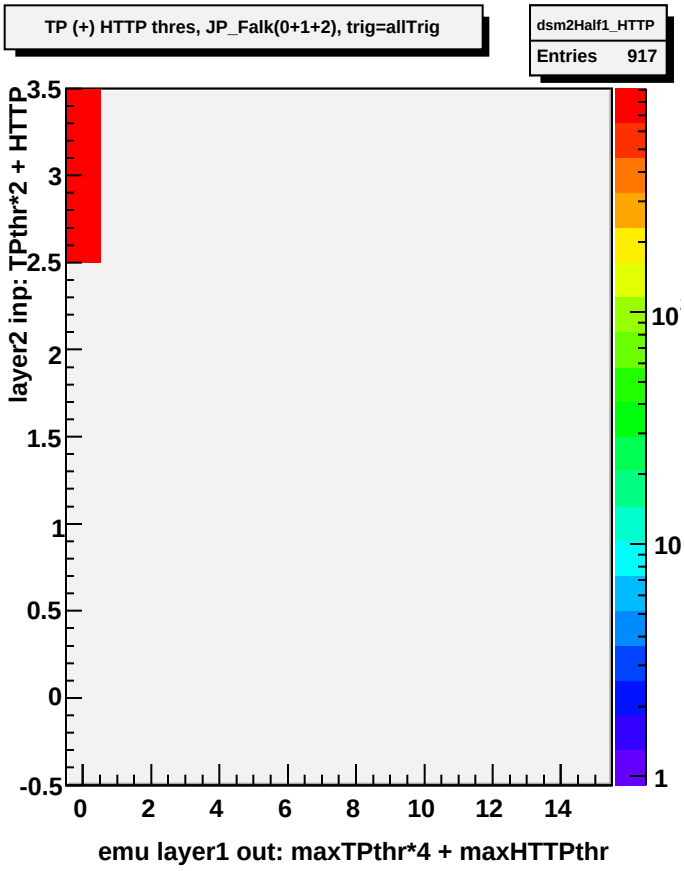

TP input vs. emulated, DSM layer-1, brd=1 ch=1, trig=allTrig
dsm1HJP8_TP
Entries 0
input to level-1
emulated TP from level-0

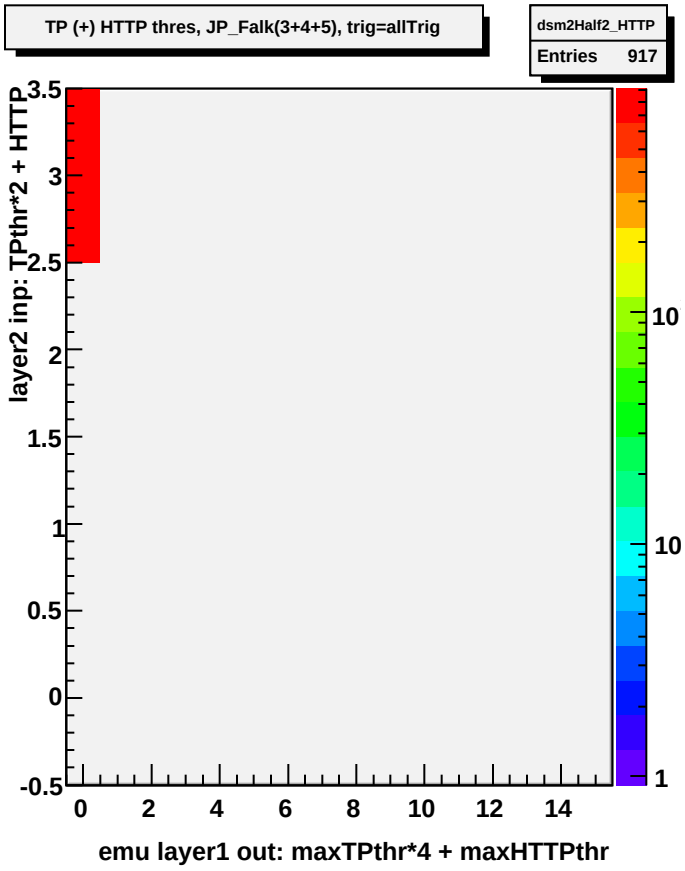

TP input vs. emulated, DSM layer-1, brd=1 ch=2, trig=allTrig
dsm1HJP9_TP
Entries 0
input to level-1
emulated TP from level-0

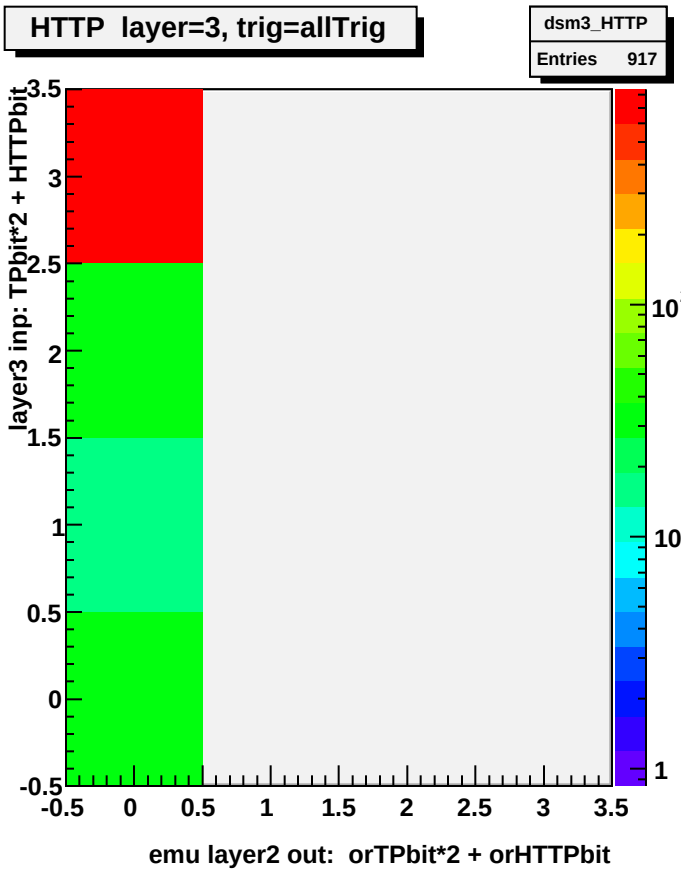

TP input vs. emulated, DSM layer-1, brd=1 ch=3, trig=allTrig
dsm1HJP10_TP
Entries 0
input to level-1
emulated TP from level-0

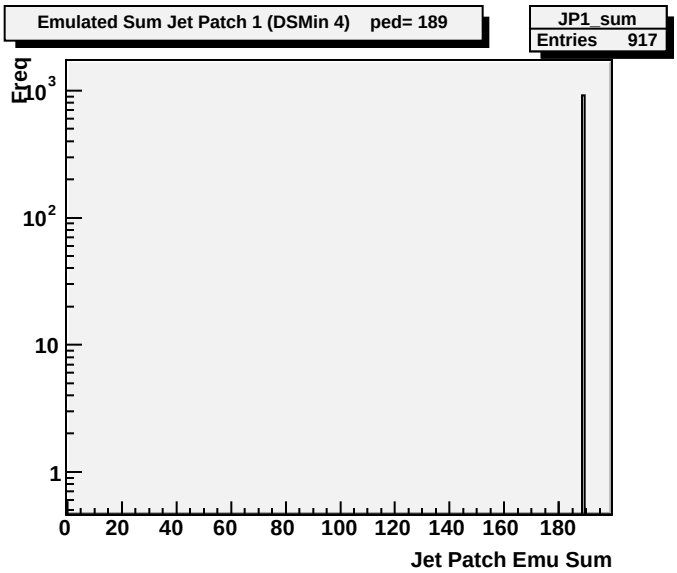

TP input vs. emulated, DSM layer-1, brd=1 ch=4, trig=allTrig
dsm1HJP11_TP
Entries 0
input to level-1
emulated TP from level-0

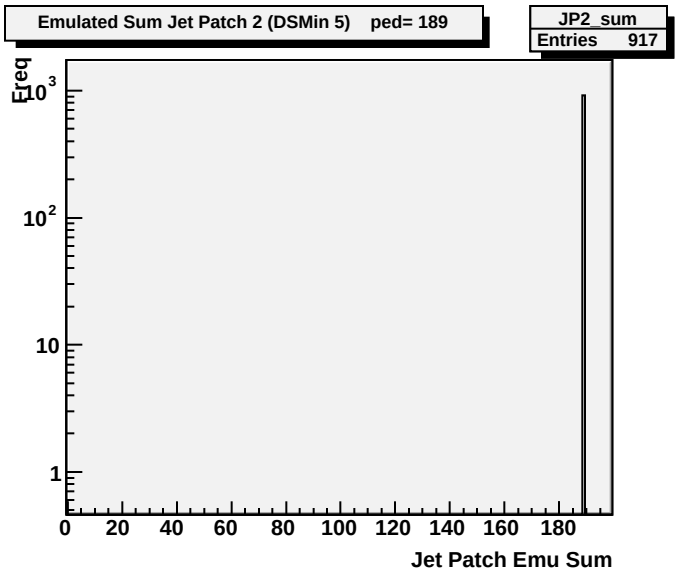

TP input vs. emulated, DSM layer-1, brd=1 ch=5, trig=allTrig
dsm1HJP12_TP
Entries 0
input to level-1
emulated TP from level-0

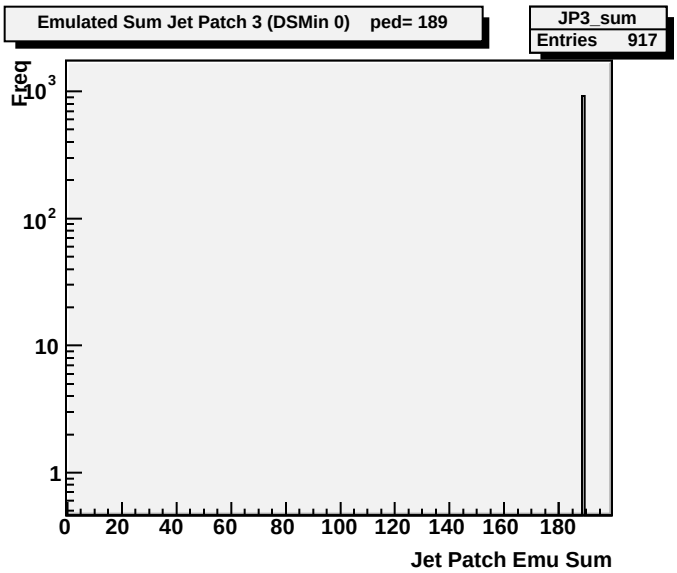

TP (+) HTTP thres, JP_Falk(0+1+2), trig=allTrig
dsm2Half1_HTTP
Entries 917
layer2 inp: TPthr*2 + HTTP
emu layer1 out: maxTPthr*4 + maxHTTPthr

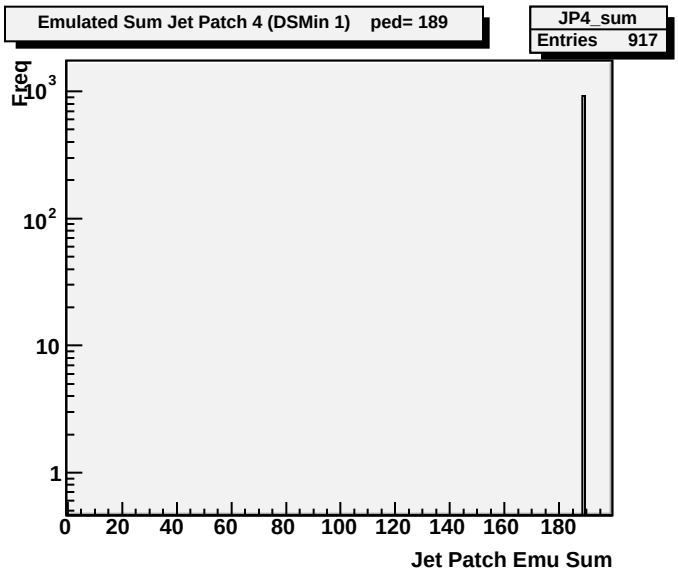

TP (+) HTTP thres, JP_Falk(3+4+5), trig=allTrig
dsm2Half2_HTTP
Entries 917
layer2 inp: TPthr*2 + HTTP
emu layer1 out: maxTPthr*4 + maxHTTPthr

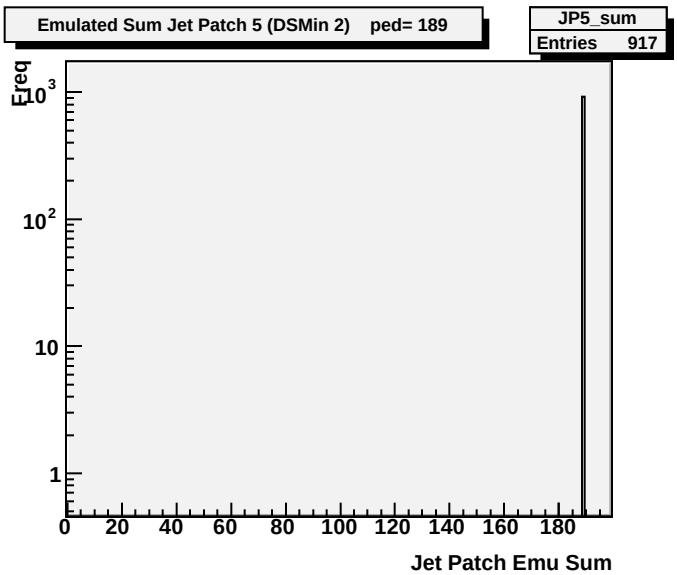

HTTP layer=3, trig=allTrig
dsm3_HTTP
Entries 917
layer3 inp: TPbit*2 + HTTPbit
emu layer2 out: orTPbit*2 + orHTTPbit

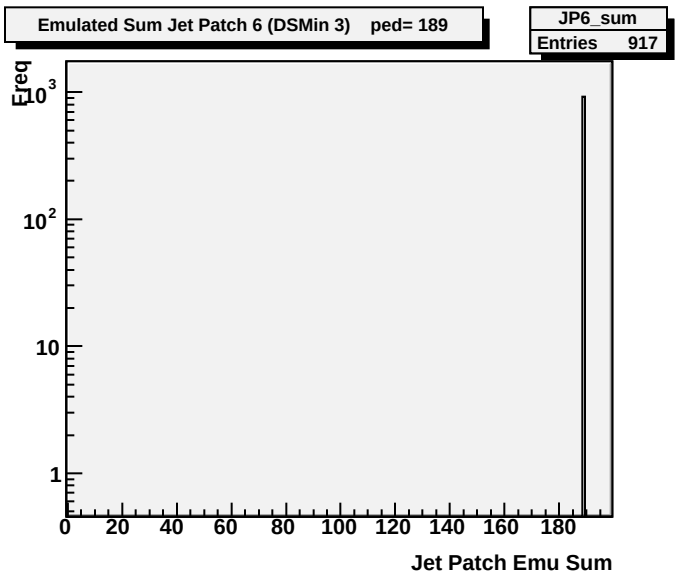

Emulated Sum Jet Patch 1 (DSMin 4) ped= 189
JP1_sum
Entries 917
Freq
10^3
10^2
10
1
0 20 40 60 80 100 120 140 160 180
Jet Patch Emu Sum

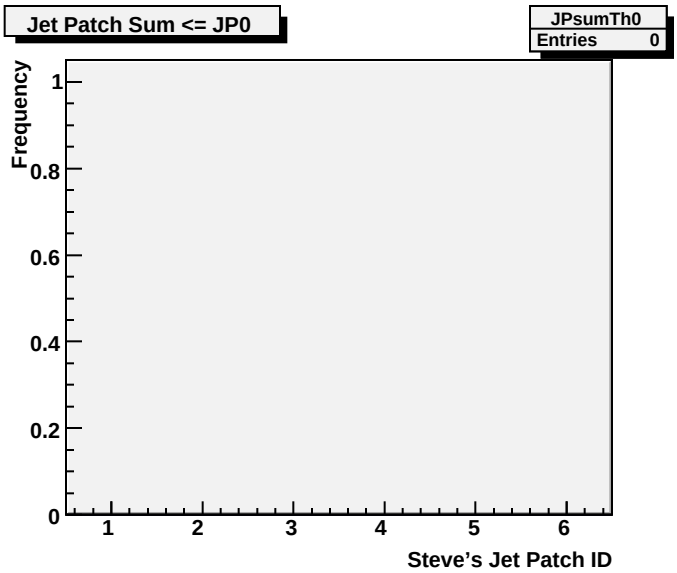

Emulated Sum Jet Patch 2 (DSMin 5) ped= 189
JP2_sum
Entries 917
Freq
10^3
10^2
10
1
0 20 40 60 80 100 120 140 160 180
Jet Patch Emu Sum

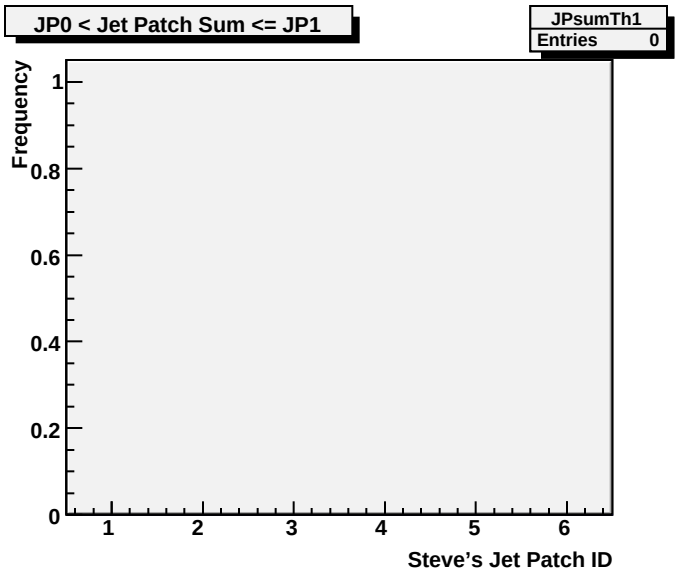

Emulated Sum Jet Patch 3 (DSMin 0) ped= 189
JP3_sum
Entries 917
Freq
10^3
10^2
10
1
0 20 40 60 80 100 120 140 160 180
Jet Patch Emu Sum

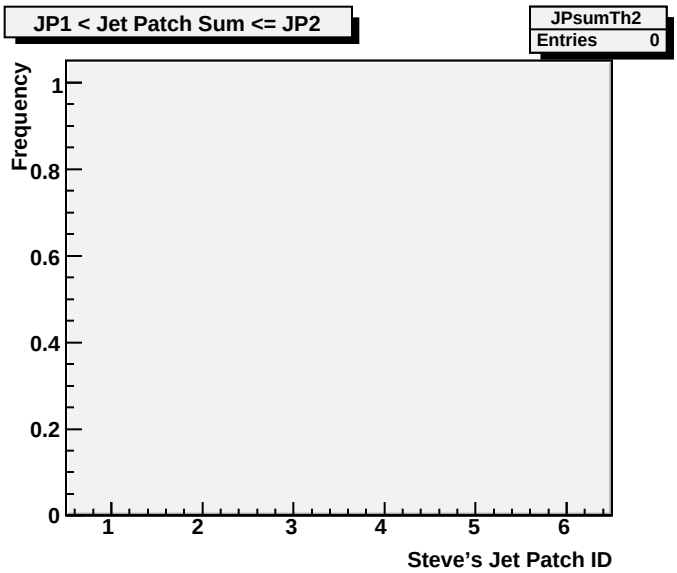

Emulated Sum Jet Patch 4 (DSMin 1) ped= 189
JP4_sum
Entries 917
Freq
10^3
10^2
10
1
0 20 40 60 80 100 120 140 160 180
Jet Patch Emu Sum

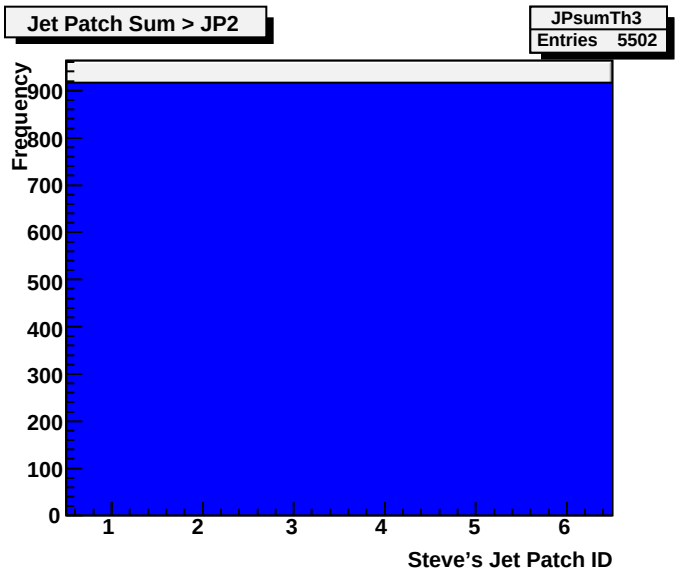

Emulated Sum Jet Patch 5 (DSMin 2) ped= 189
JP5_sum
Entries 917
Freq
10^3
10^2
10
1
0 20 40 60 80 100 120 140 160 180
Jet Patch Emu Sum

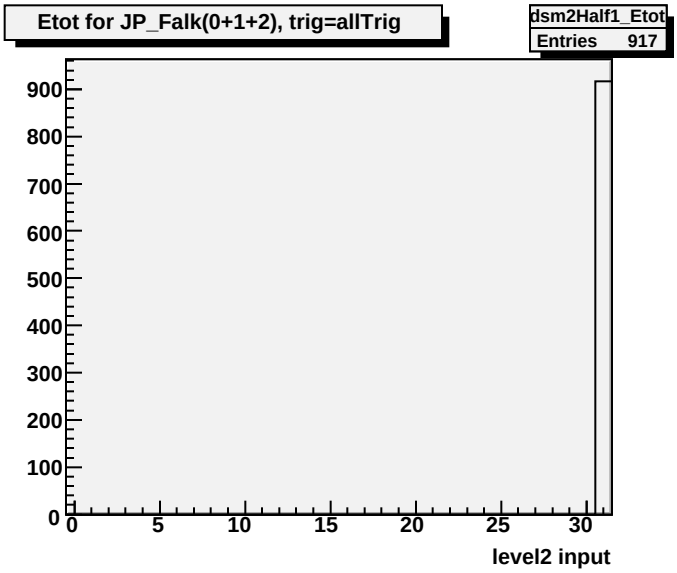

Emulated Sum Jet Patch 6 (DSMin 3) ped= 189
JP6_sum
Entries 917
Freq
10^3
10^2
10
1
0 20 40 60 80 100 120 140 160 180
Jet Patch Emu Sum

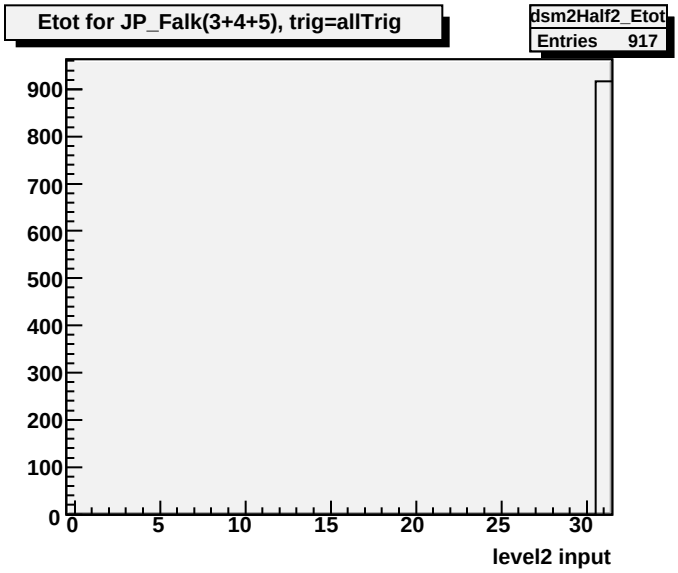

Jet Patch Sum <= JP0
JPsumTh0
Entries 0
Frequency
Steve's Jet Patch ID

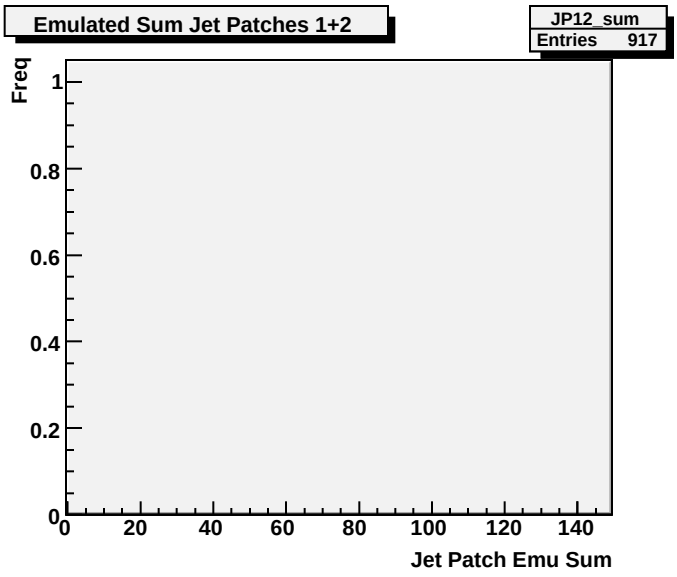

JP0 < Jet Patch Sum <= JP1
JPsumTh1
Entries 0
Frequency
Steve's Jet Patch ID

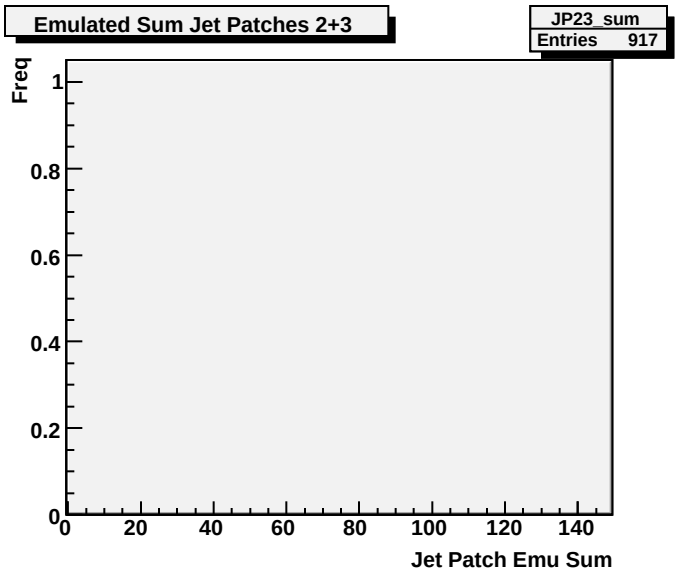

JP1 < Jet Patch Sum <= JP2
JPsumTh2
Entries 0
Frequency
Steve's Jet Patch ID

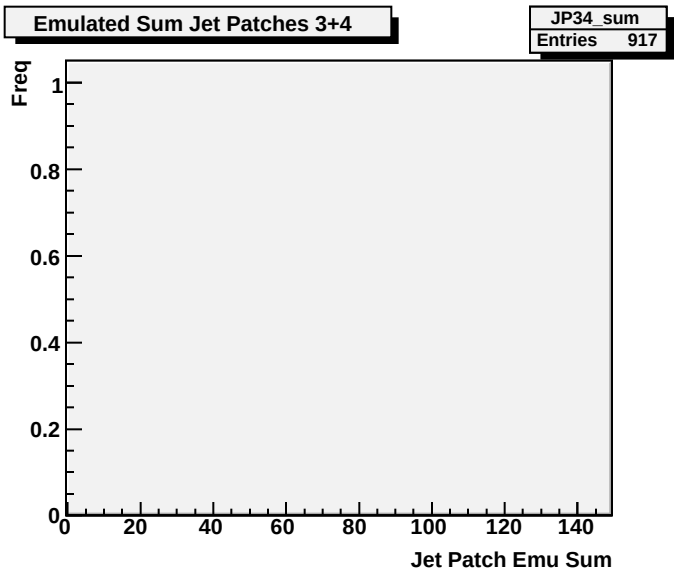

Jet Patch Sum > JP2
JPsumTh3
Entries 5502
Frequency
Steve's Jet Patch ID

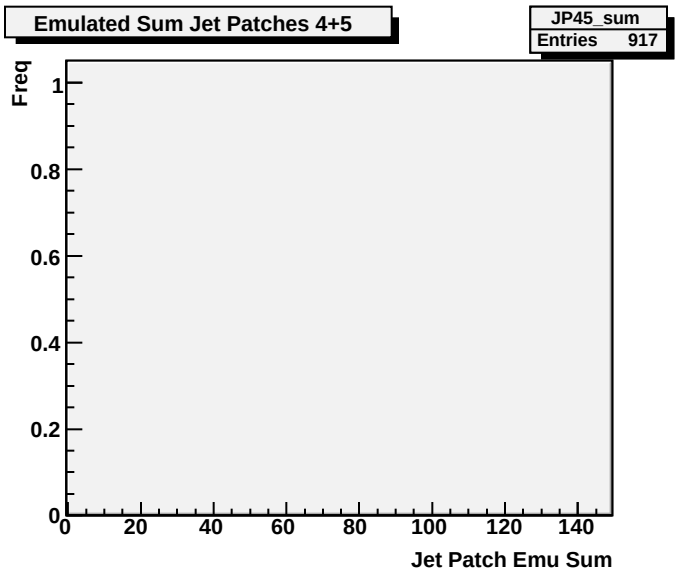

Etot for JP_Falk(0+1+2), trig=allTrig
dsm2Half1_Etot
Entries 917
level2 input

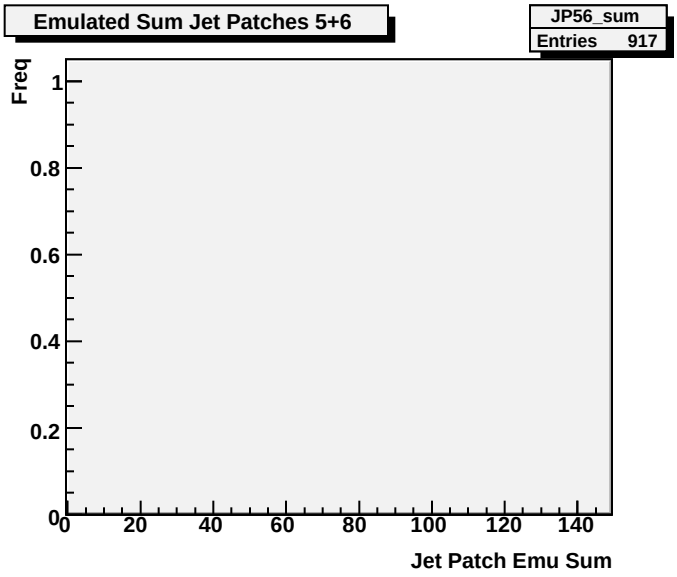

Etot for JP_Falk(3+4+5), trig=allTrig
dsm2Half2_Etot
Entries 917
level2 input

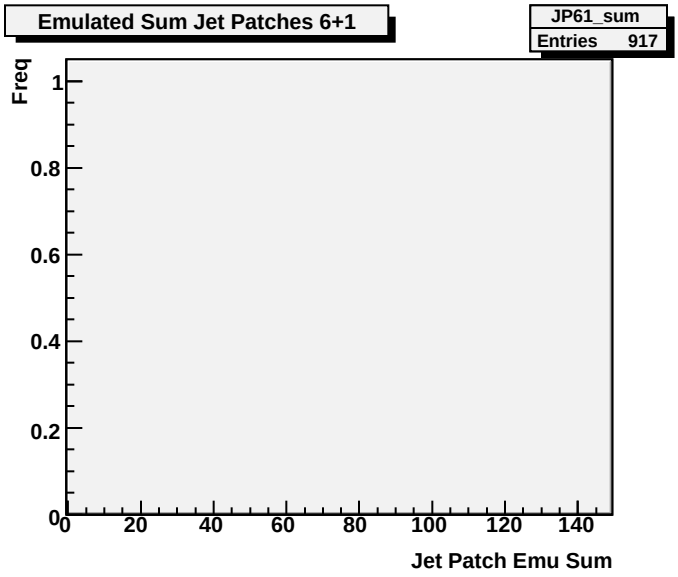

Emulated Sum Jet Patches 1+2
JP12_sum
Entries 917
Freq
Jet Patch Emu Sum

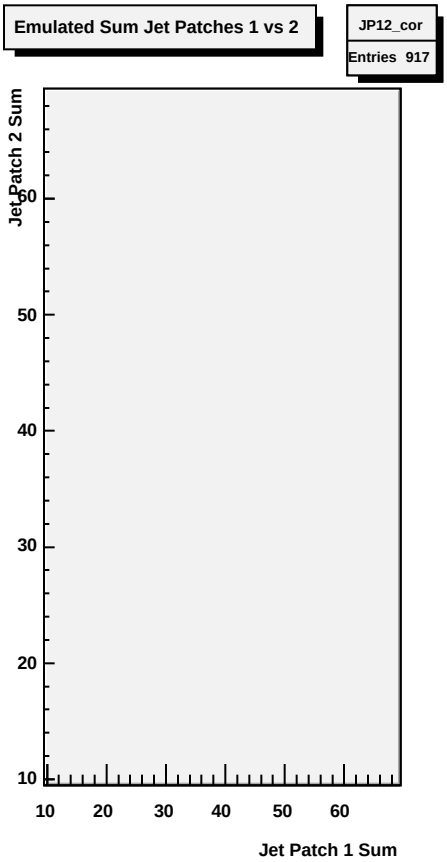

Emulated Sum Jet Patches 2+3
JP23_sum
Entries 917
Freq
Jet Patch Emu Sum

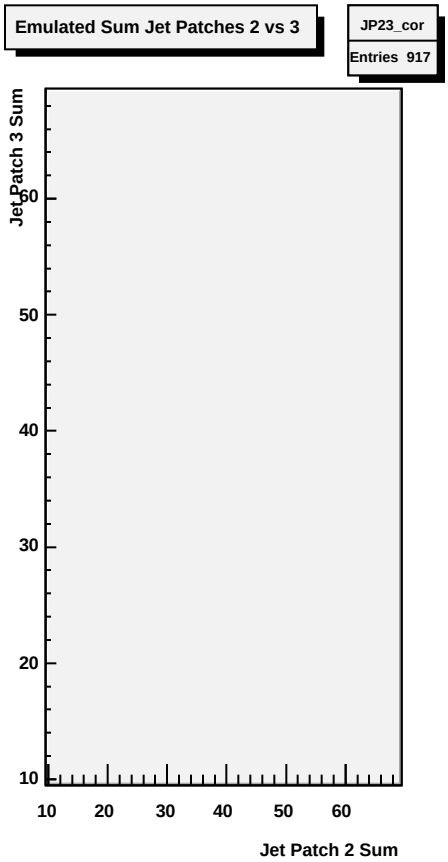

Emulated Sum Jet Patches 3+4
JP34_sum
Entries 917
Freq
Jet Patch Emu Sum

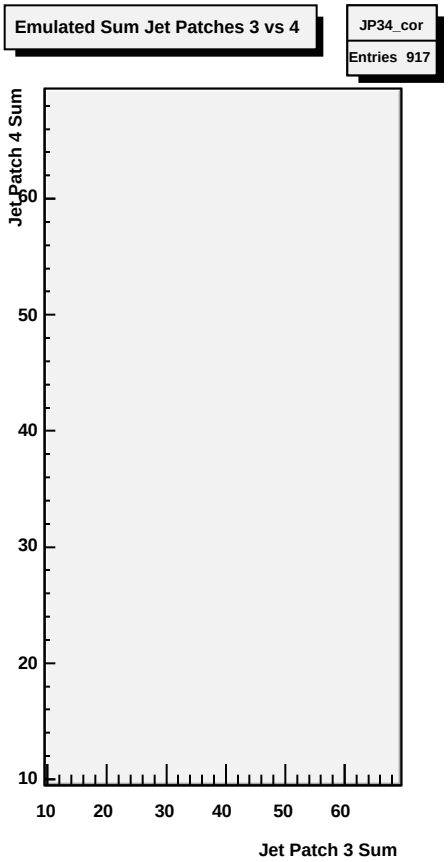

Emulated Sum Jet Patches 4+5
JP45_sum
Entries 917
Freq
Jet Patch Emu Sum

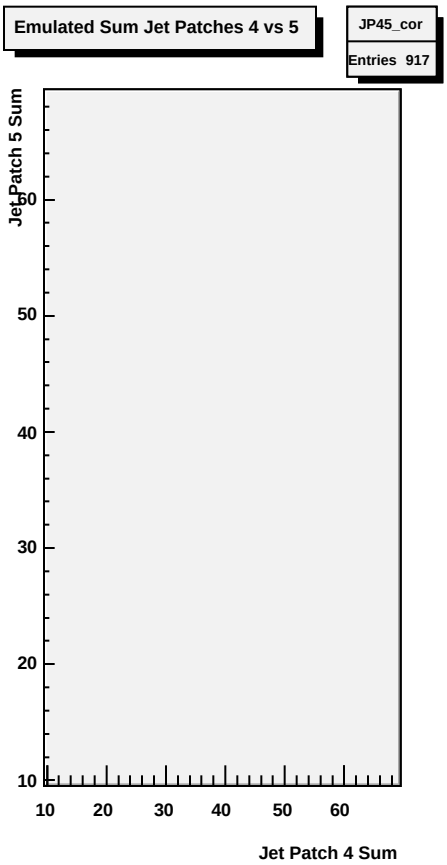

Emulated Sum Jet Patches 5+6
JP56_sum
Entries 917
Freq
Jet Patch Emu Sum

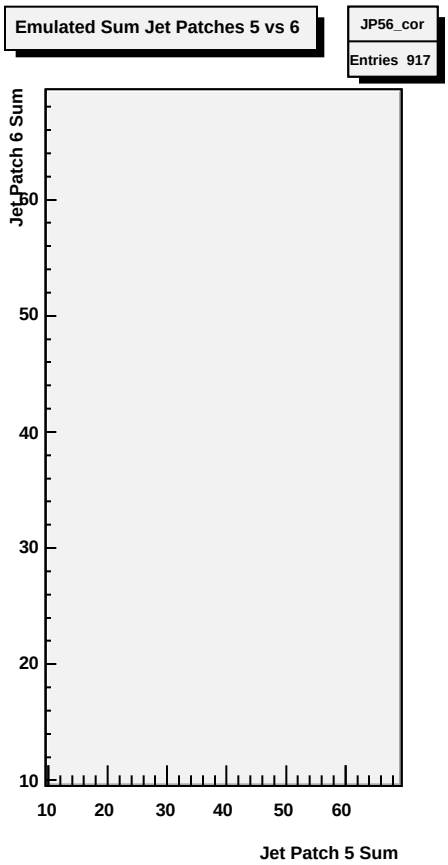

Emulated Sum Jet Patches 6+1
JP61_sum
Entries 917
Freq
Jet Patch Emu Sum

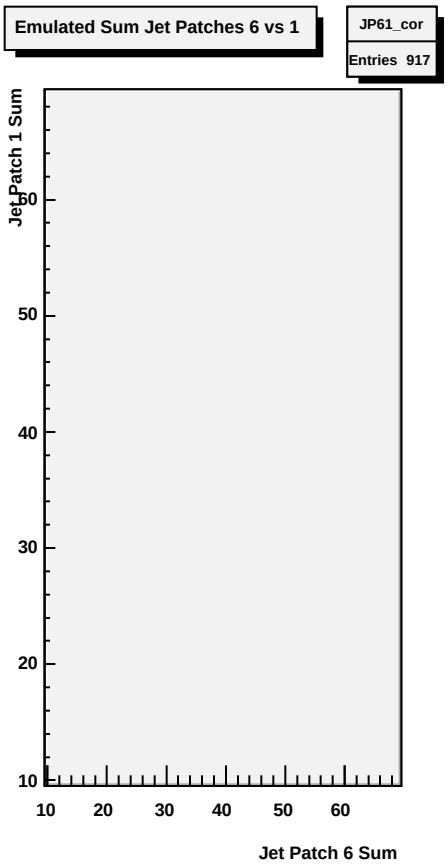

Emulated Sum Jet Patches 1 vs 2
JP12_cor
Entries 917
Jet Patch 2 Sum
Jet Patch 1 Sum

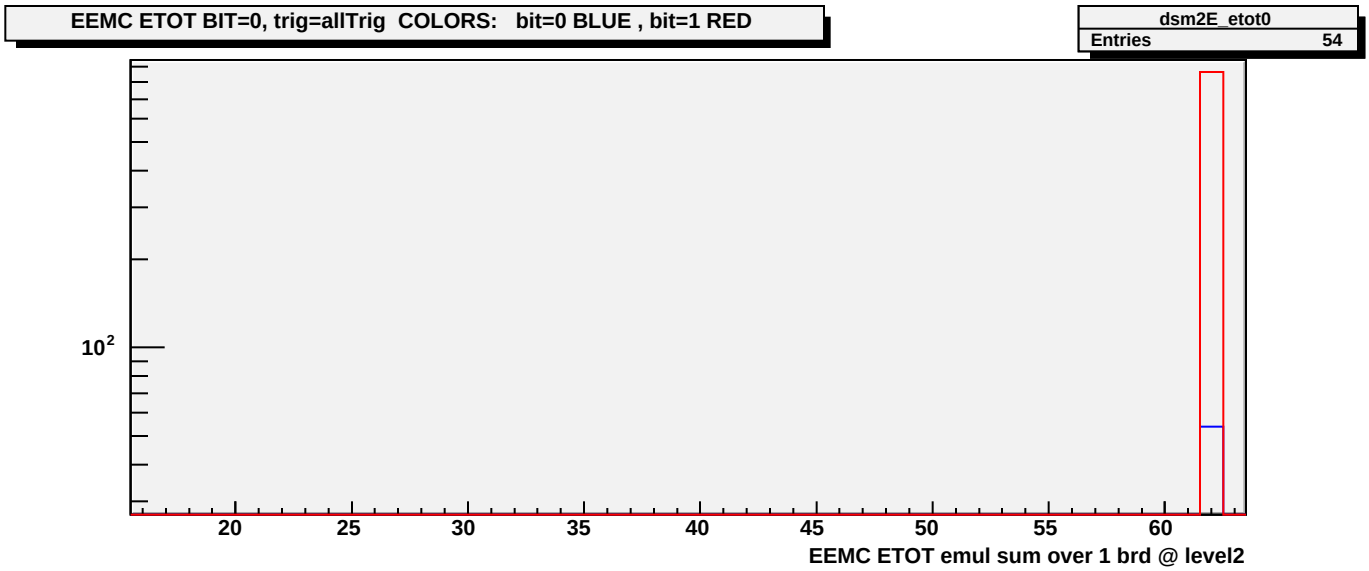

Emulated Sum Jet Patches 2 vs 3
JP23_cor
Entries 917
Jet Patch 3 Sum
Jet Patch 2 Sum

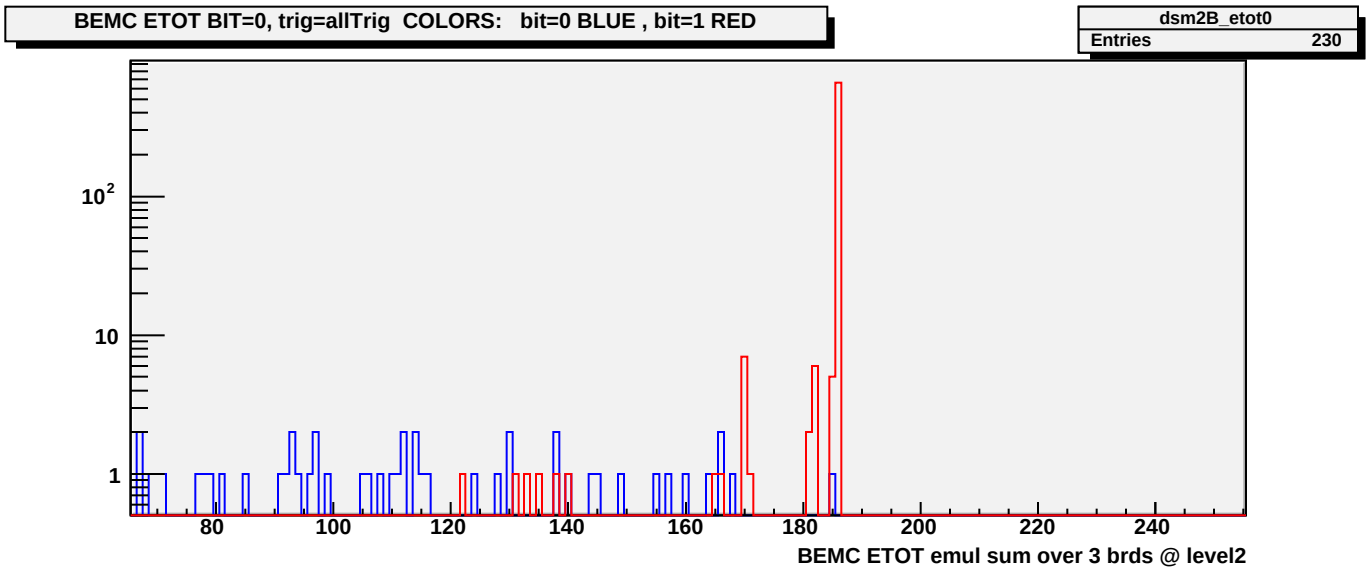

Emulated Sum Jet Patches 3 vs 4
JP34_cor
Entries 917
Jet Patch 4 Sum
Jet Patch 3 Sum

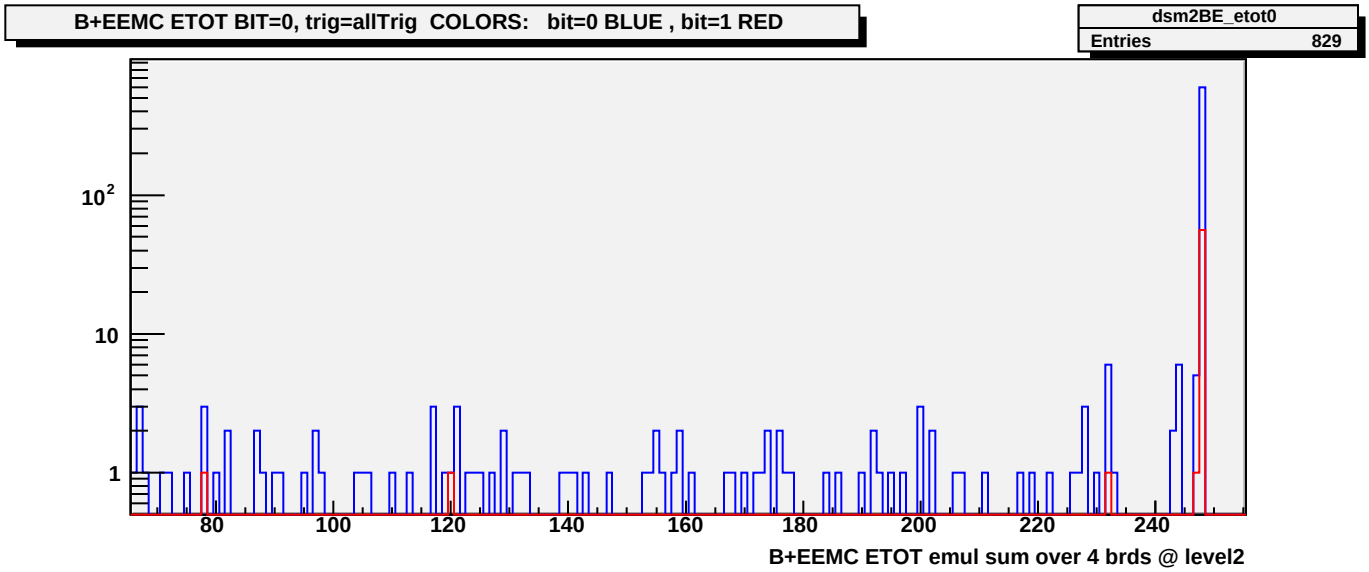

Emulated Sum Jet Patches 4 vs 5
JP45_cor
Entries 917
Jet Patch 5 Sum
Jet Patch 4 Sum

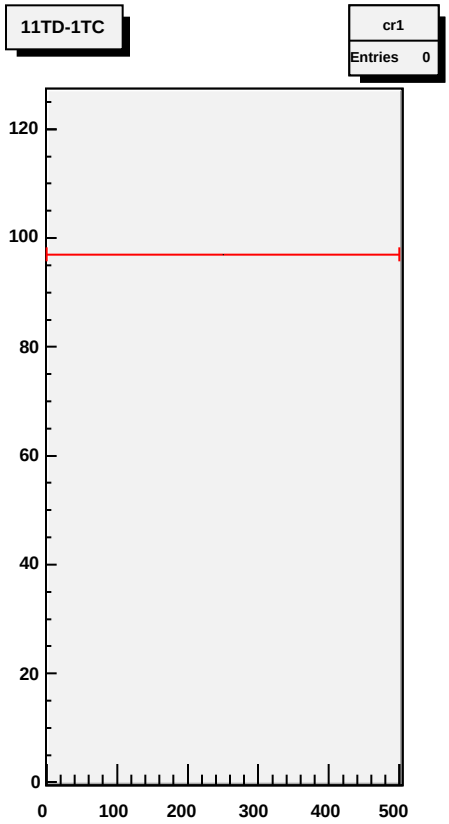

Emulated Sum Jet Patches 5 vs 6
JP56_cor
Entries 917
Jet Patch 6 Sum
Jet Patch 5 Sum

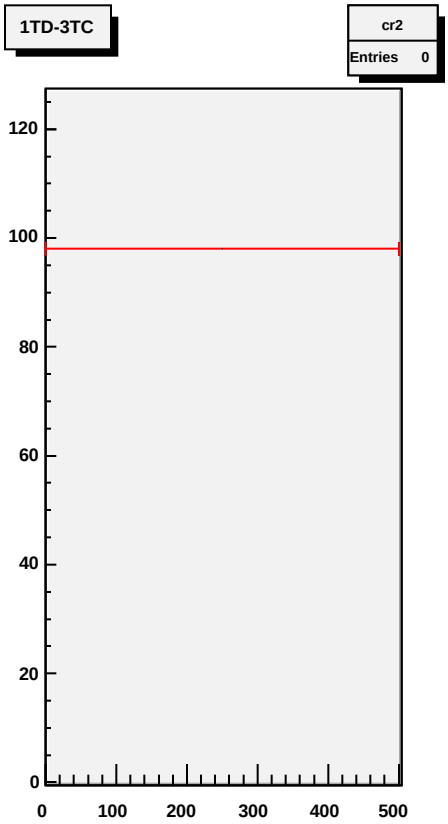

Emulated Sum Jet Patches 6 vs 1
JP61_cor
Entries 917
Jet Patch 1 Sum
Jet Patch 6 Sum

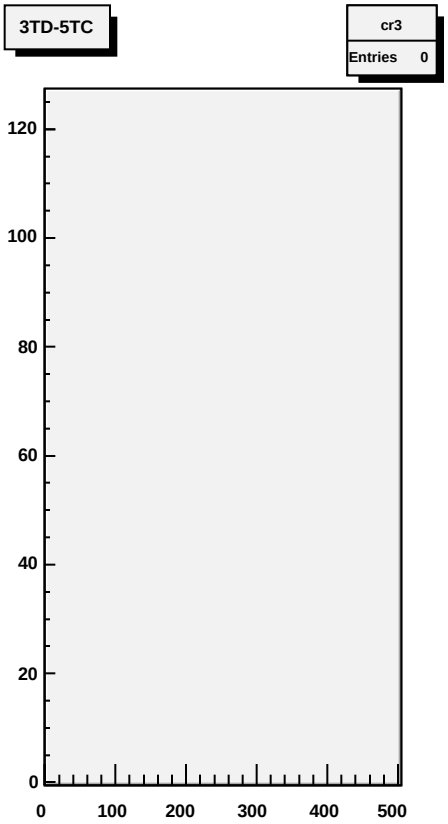

EEMC ETOT BIT=0, trig=allTrig COLORS: bit=0 BLUE , bit=1 RED
dsm2E_etot0
Entries 54
10^2
20
25
30
35
40
45
50
55
60
EEMC ETOT emul sum over 1 brd @ level2

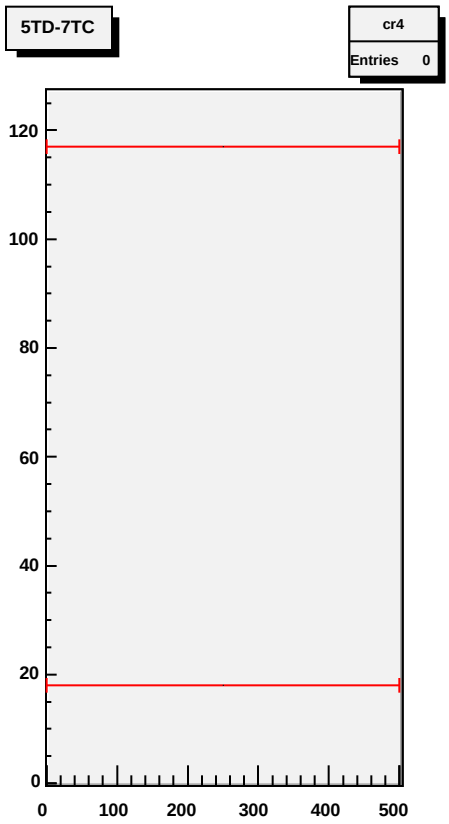

BEMC ETOT BIT=0, trig=allTrig COLORS: bit=0 BLUE , bit=1 RED
dsm2B_etot0
Entries 230
10^2
10
1
80
100
120
140
160
180
200
220
240
BEMC ETOT emul sum over 3 brds @ level2

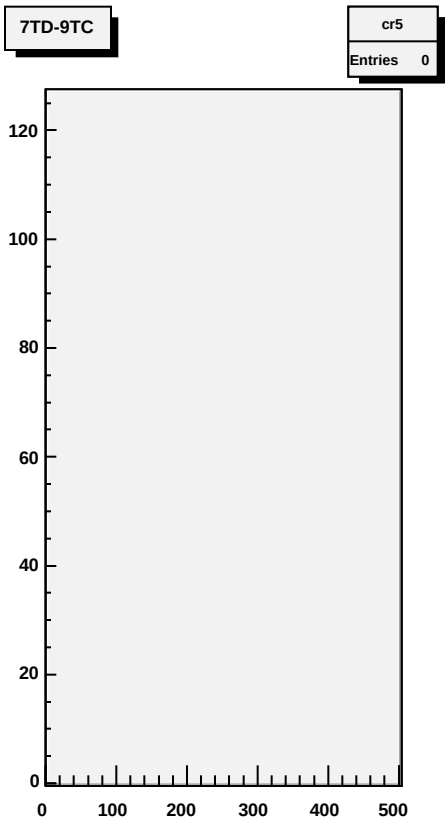

B+EEMC ETOT BIT=0, trig=allTrig COLORS: bit=0 BLUE , bit=1 RED
dsm2BE_etot0
Entries 829
10^2
10
1
80
100
120
140
160
180
200
220
240
B+EEMC ETOT emul sum over 4 brds @ level2

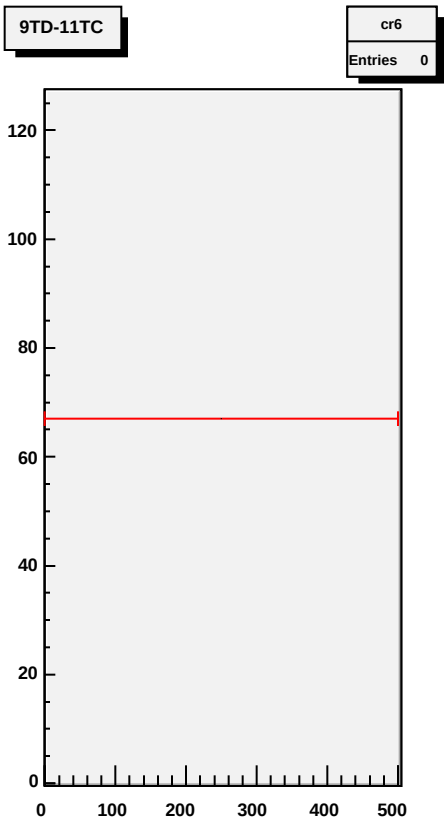

11TD-1TC
cr1
Entries 0
120
100
80
60
40
20
0
0
100
200
300
400
500


1TD-3TC
cr2
Entries 0
120
100
80
60
40
20
0
0
100
200
300
400
500

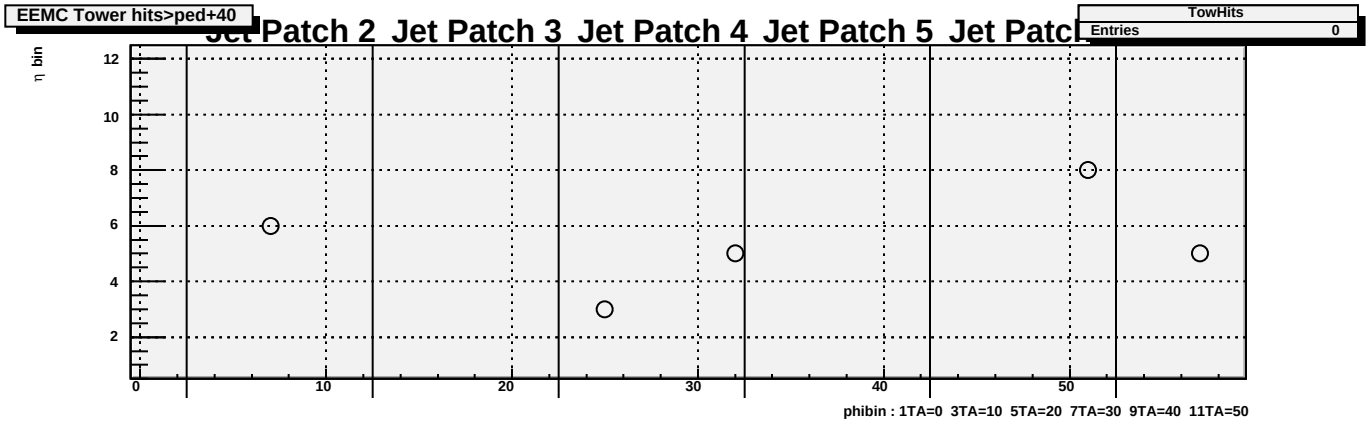

3TD-5TC
cr3
Entries 0
120
100
80
60
40
20
0
0
100
200
300
400
500


5TD-7TC
cr4
Entries 0
120
100
80
60
40
20
0
0
100
200
300
400
500

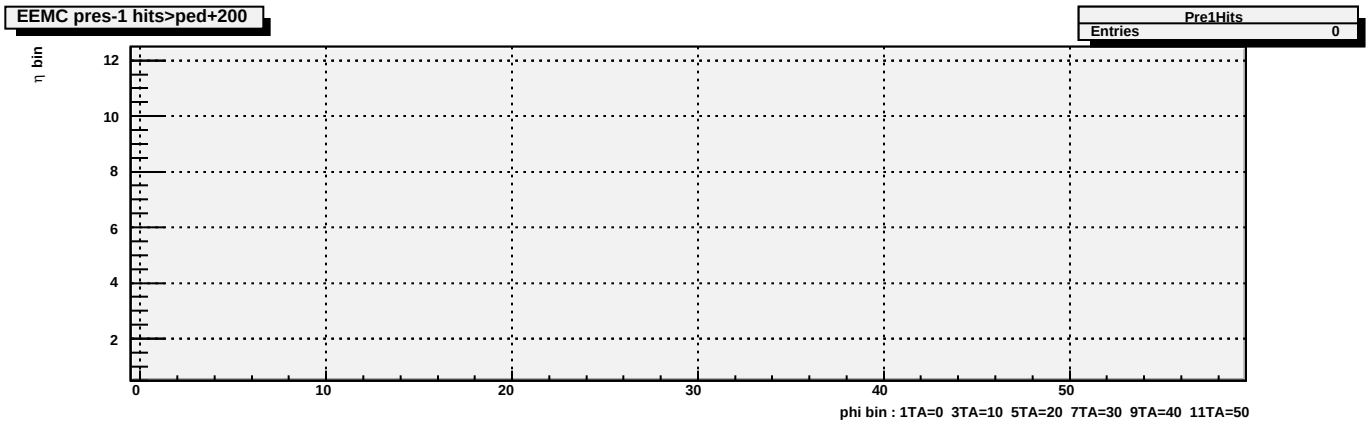

7TD-9TC
cr5
Entries 0
120
100
80
60
40
20
0
0
100
200
300
400
500


9TD-11TC
cr6
Entries 0
120
100
80
60
40
20
0
0
100
200
300
400
500

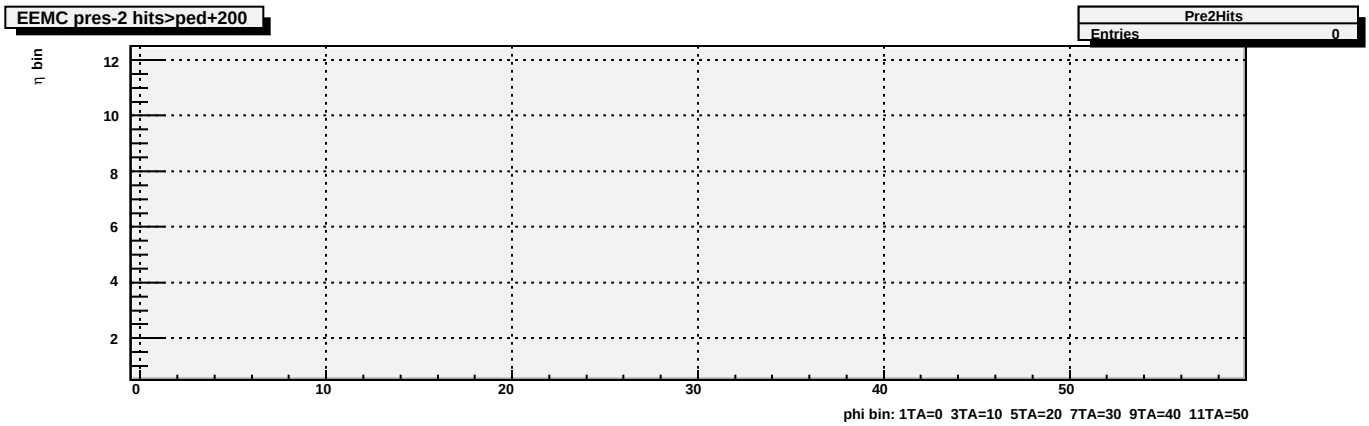
EEMC Tower hits>ped+40



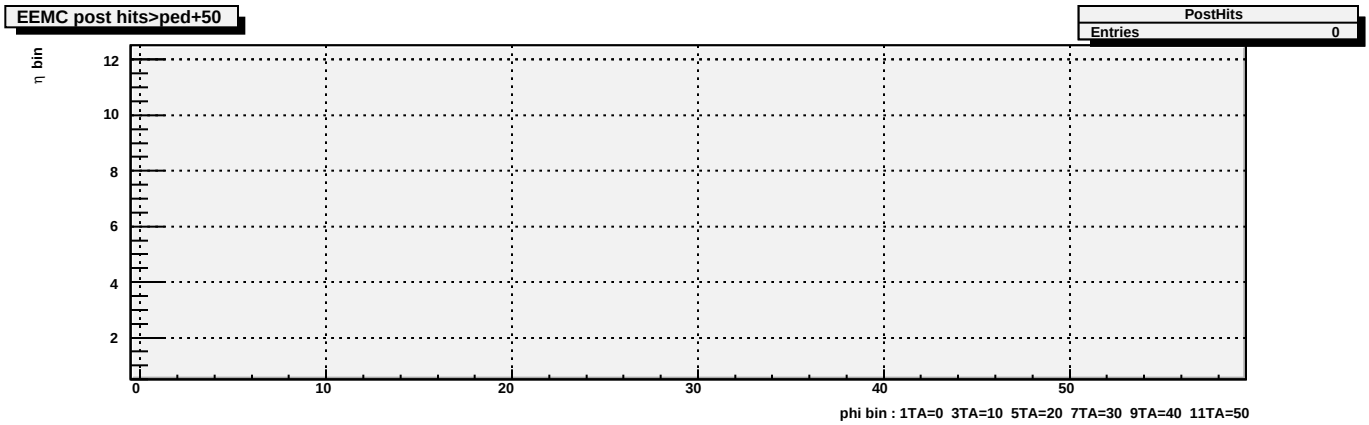
EEMC pres-1 hits>ped+200

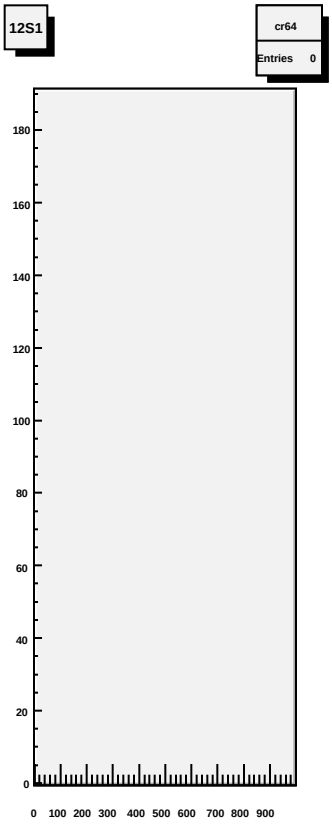


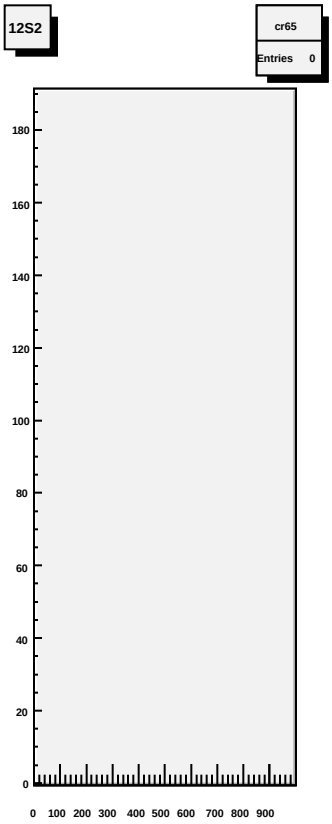
EEMC pres-2 hits>ped+200

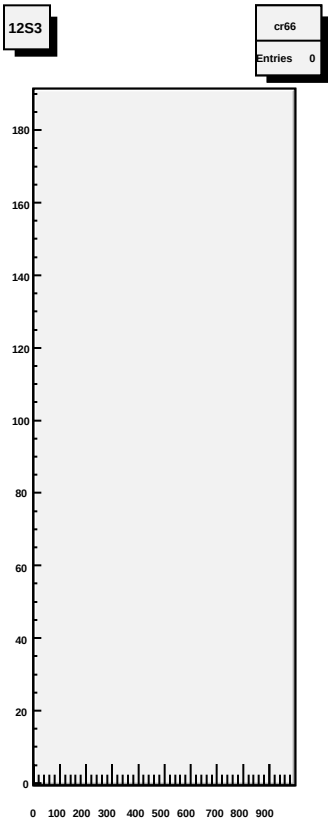


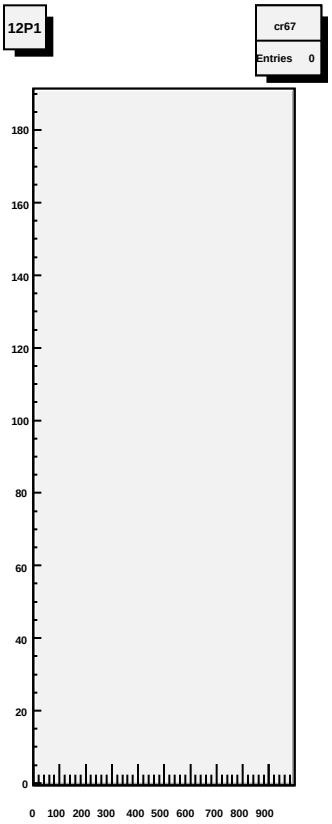
EEMC post hits>ped+50

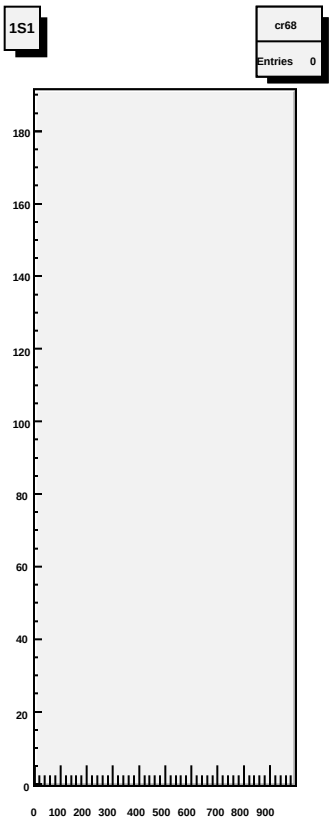


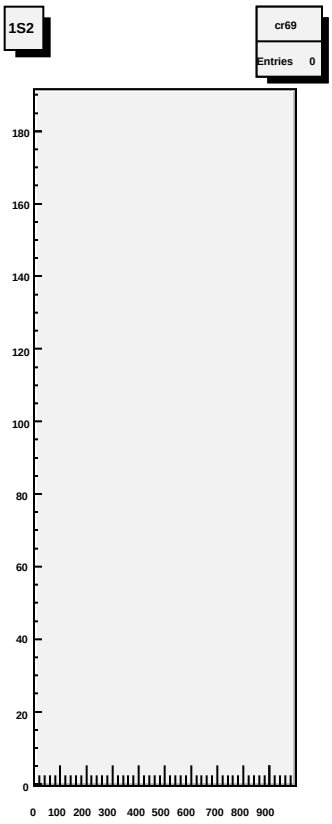

12S1
cr64
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

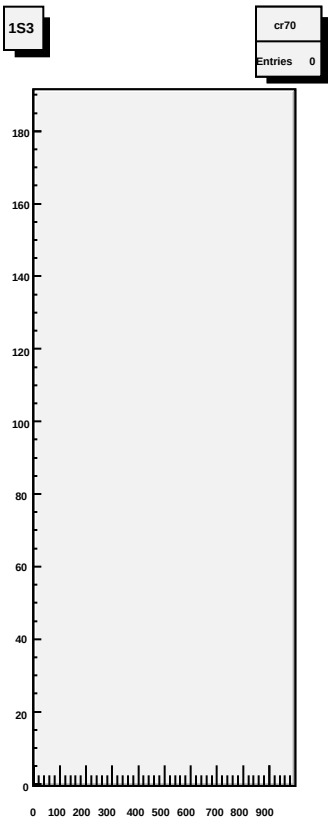

12S2
cr65
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

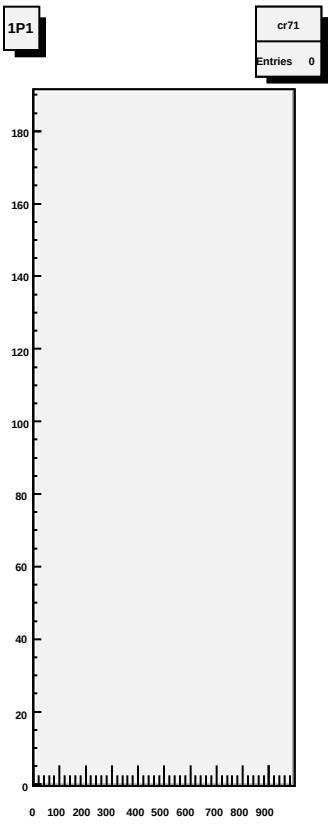

12S3
cr66
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

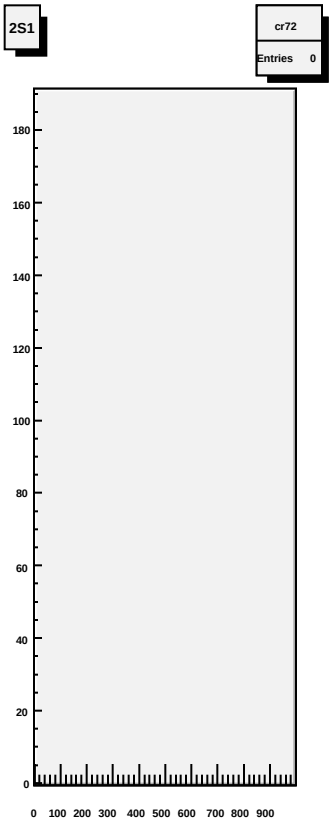

12P1
cr67
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

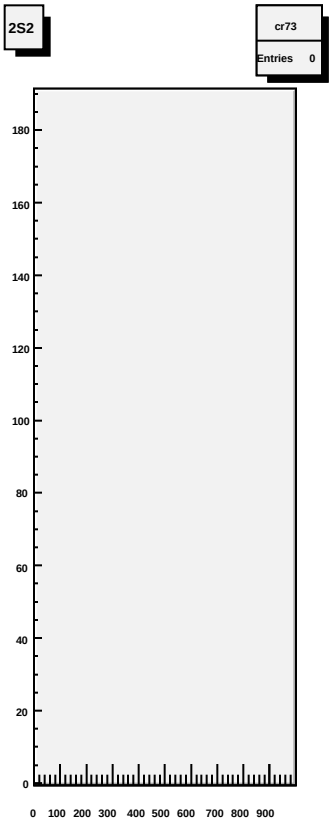

1S1
cr68
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

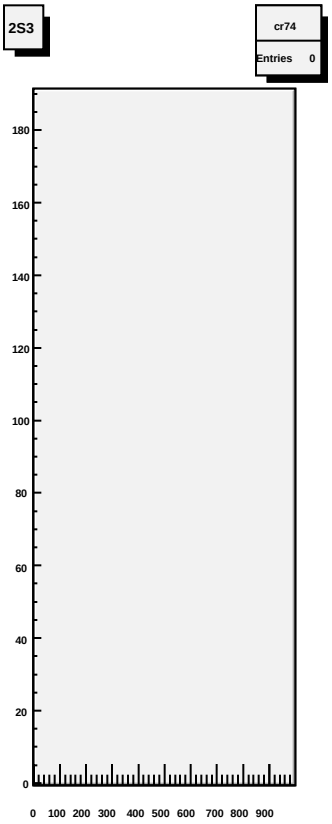

1S2
cr69
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

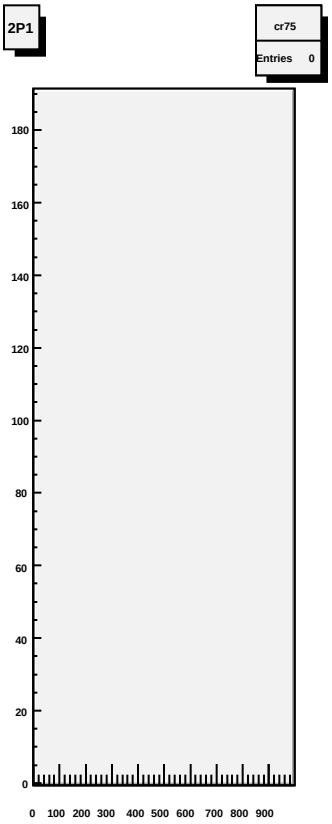

1S3
cr70
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

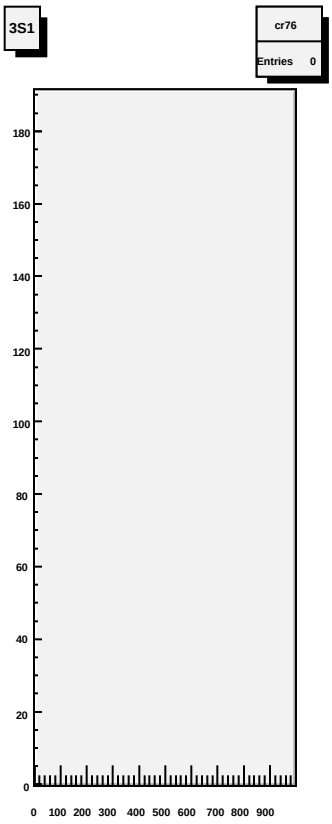

1P1
cr71
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

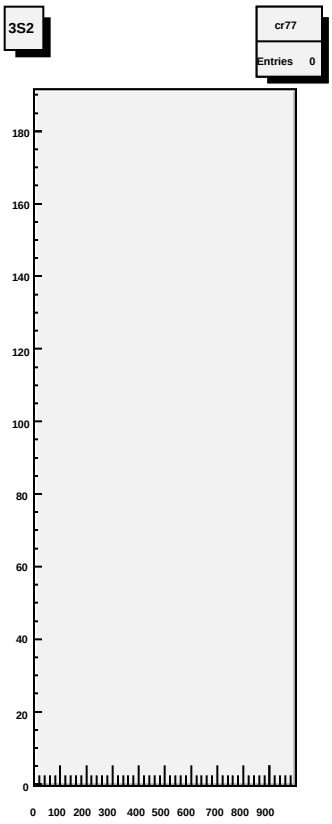

2S1
cr72
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

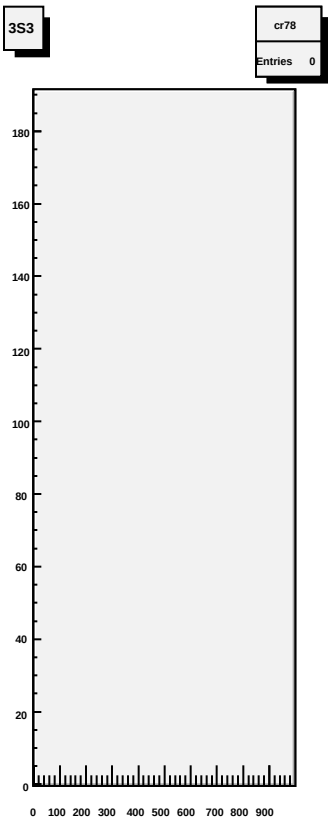

2S2
cr73
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

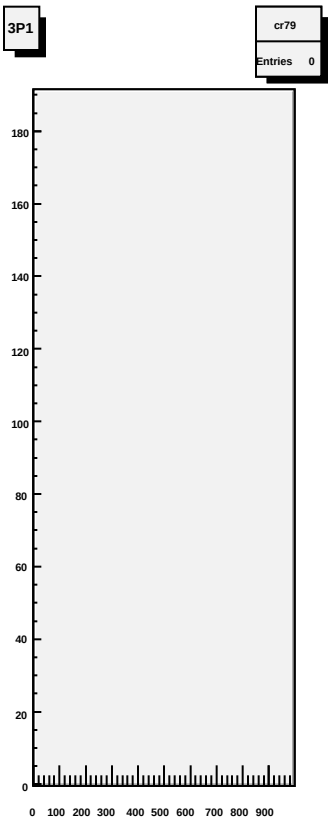

2S3
cr74
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

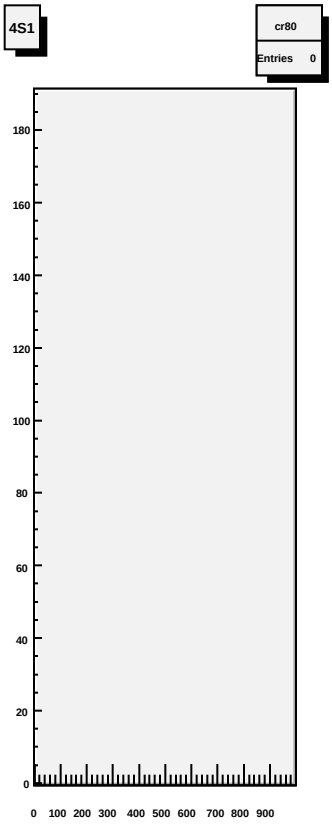

2P1
cr75
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

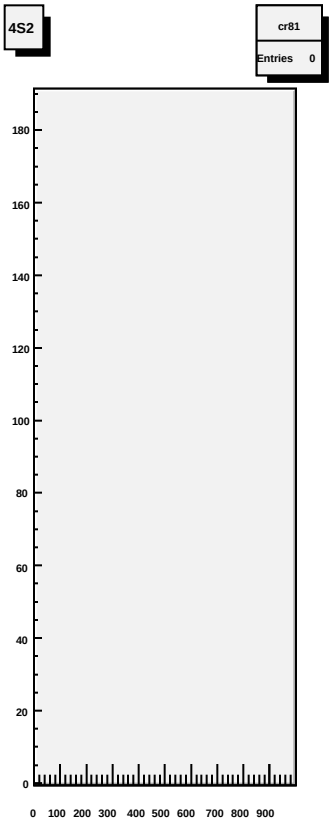

3S1
cr76
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

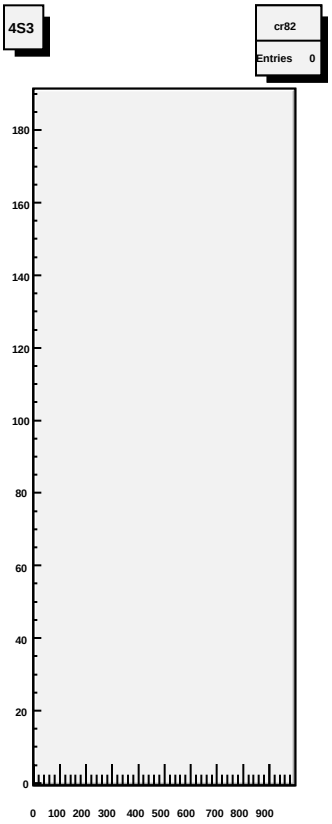

3S2
cr77
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

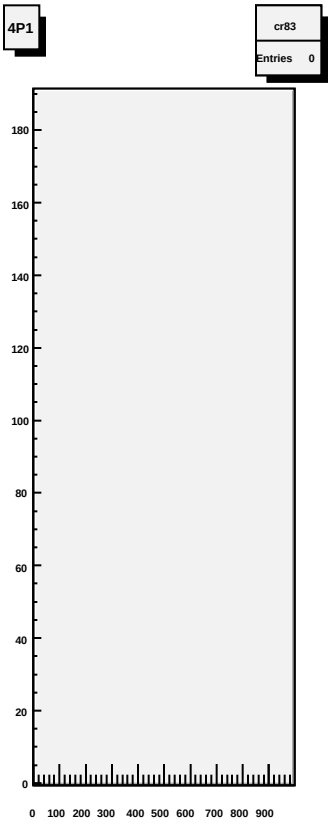

3S3
cr78
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

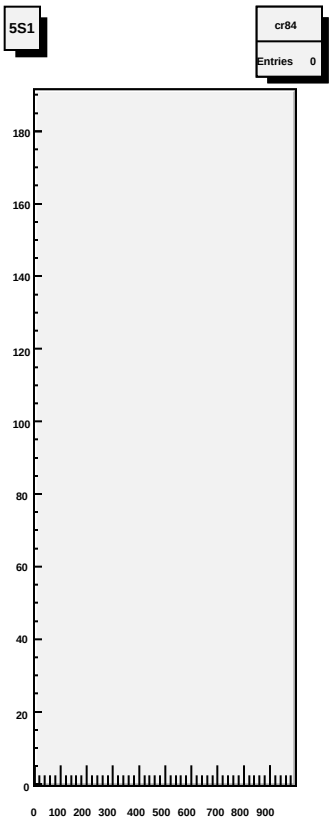

3P1
cr79
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

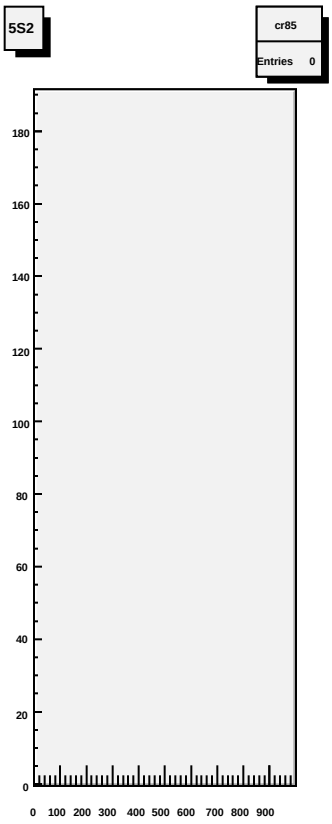

4S1
cr80
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

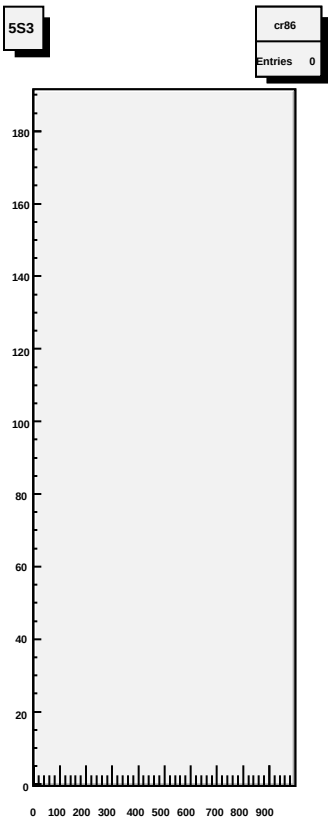

4S2
cr81
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

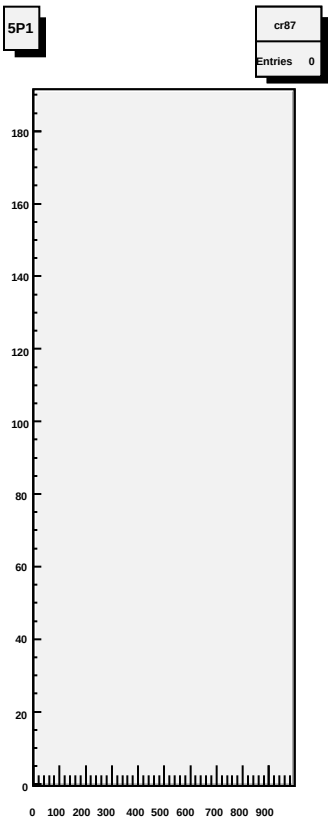

4S3
cr82
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

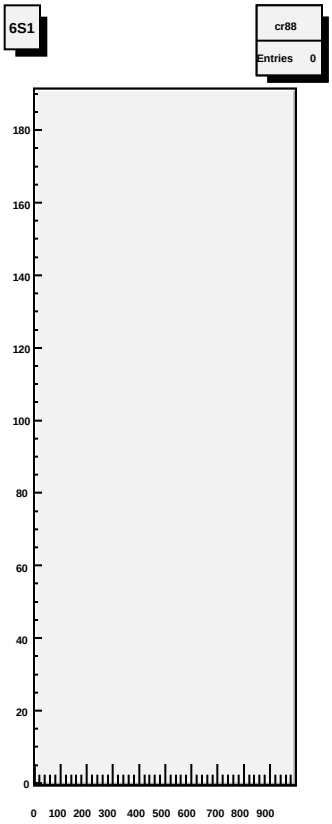

4P1
cr83
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

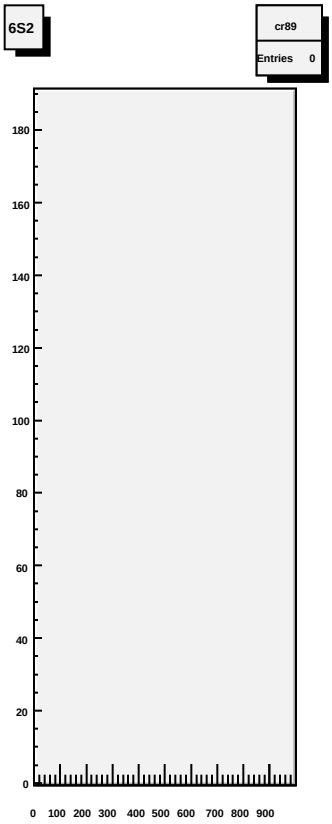

5S1
cr84
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

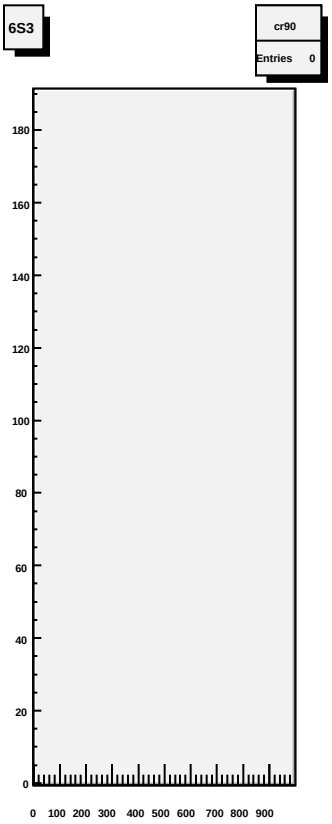

5S2
cr85
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

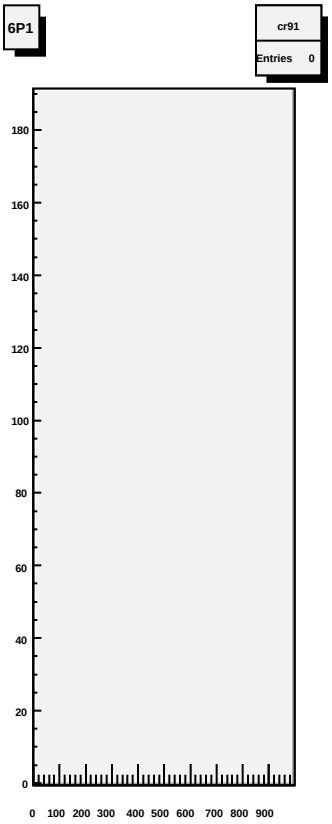

5S3
cr86
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

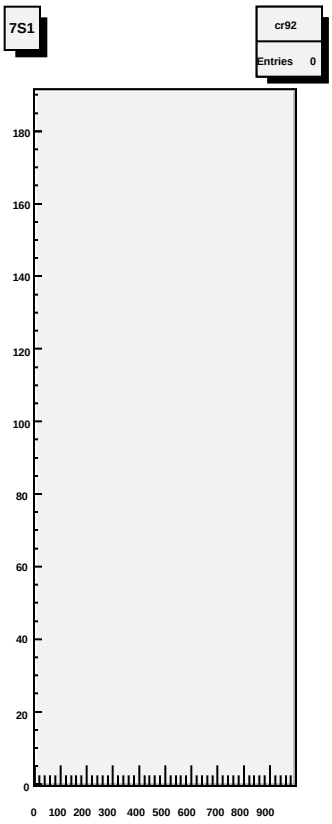

5P1
cr87
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

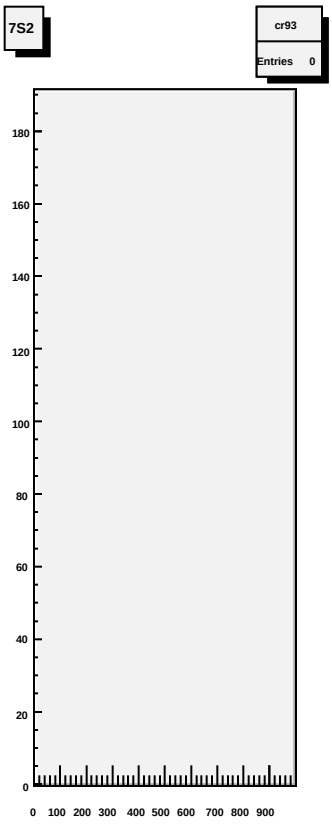

6S1
cr88
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

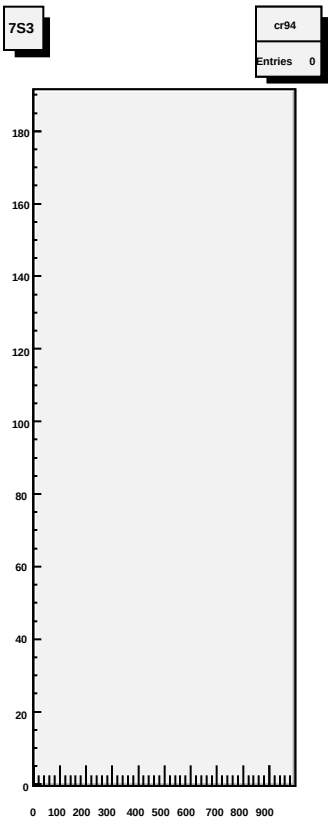

6S2
cr89
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

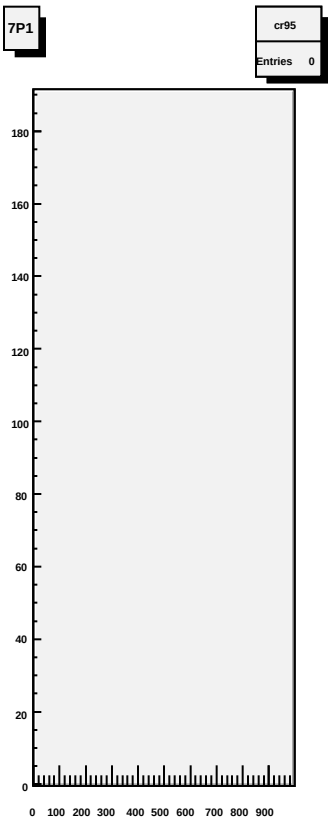

6S3
cr90
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

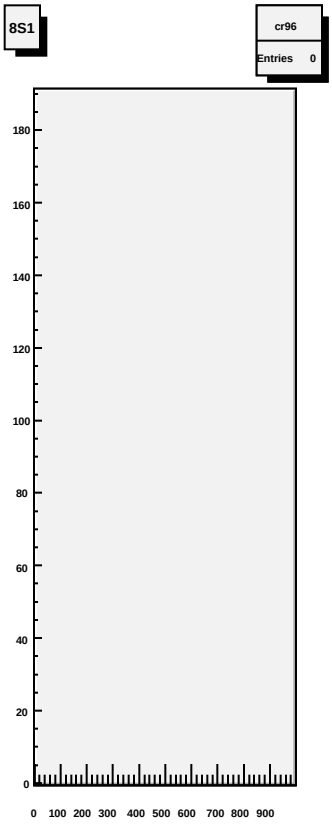

6P1
cr91
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

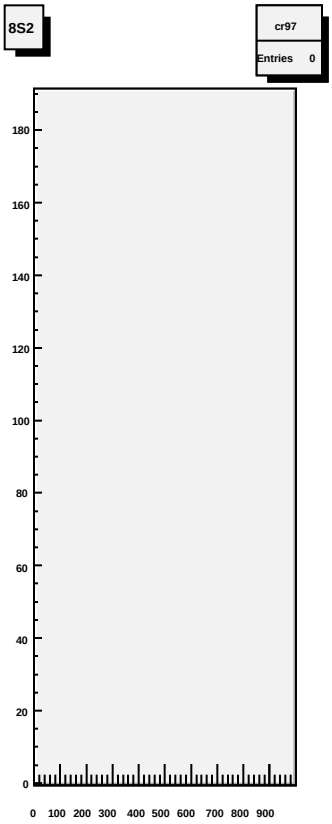

7S1
cr92
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

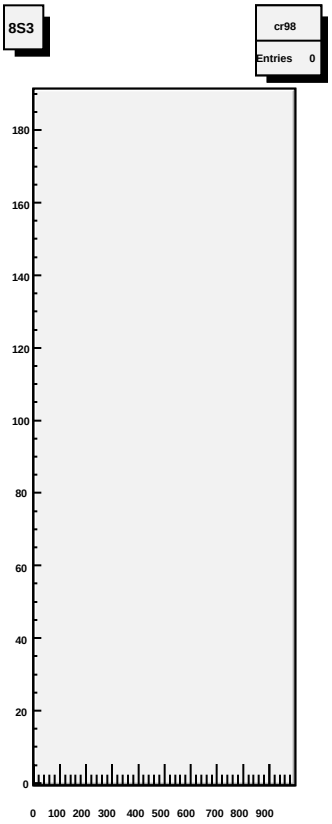

7S2
cr93
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

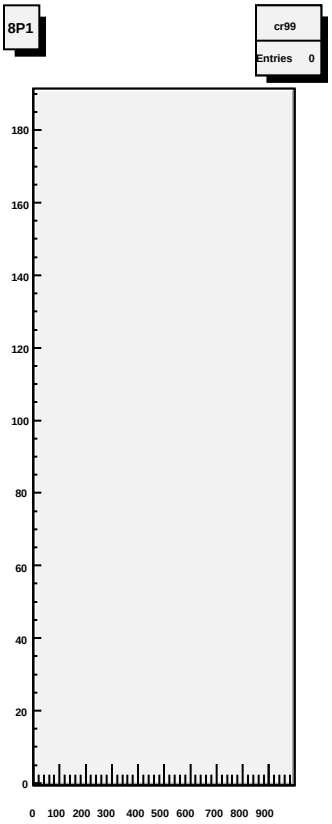

7S3
cr94
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

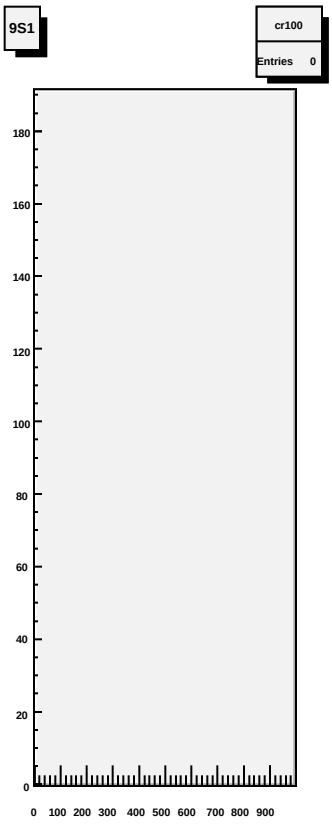

7P1
cr95
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

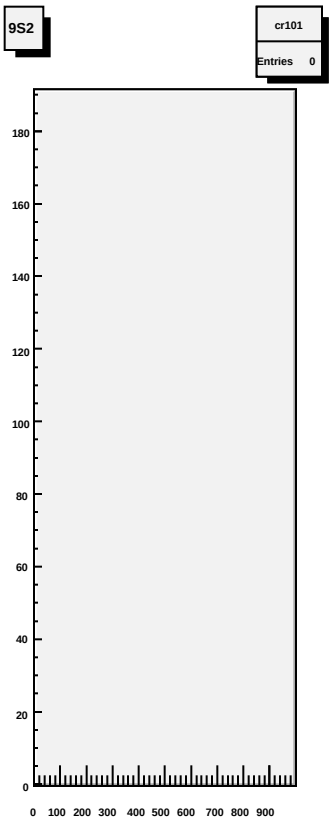

8S1
cr96
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

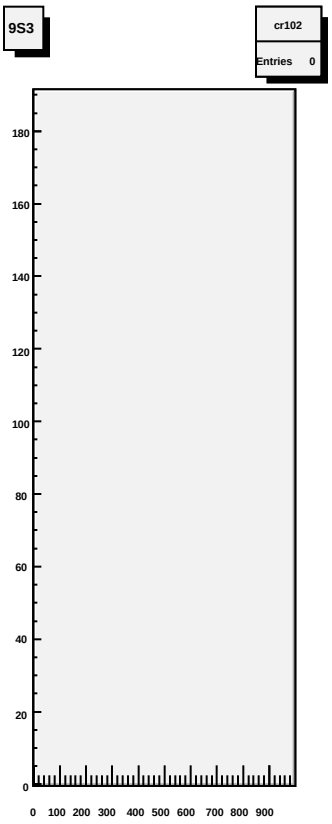

8S2
cr97
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

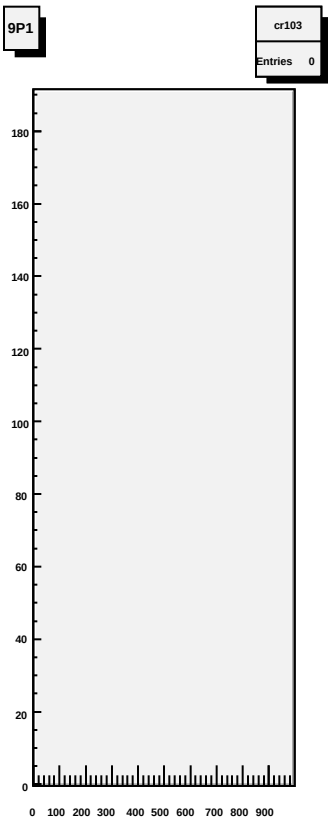

8S3
cr98
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

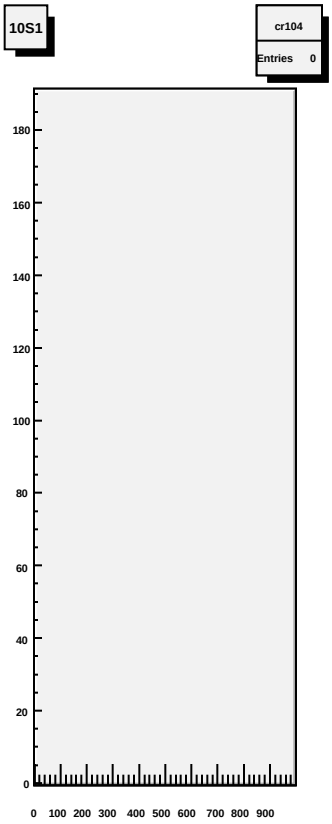

8P1
cr99
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

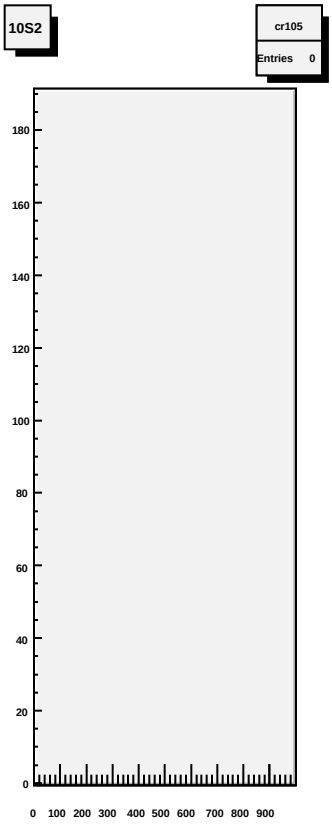

9S1
cr100
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

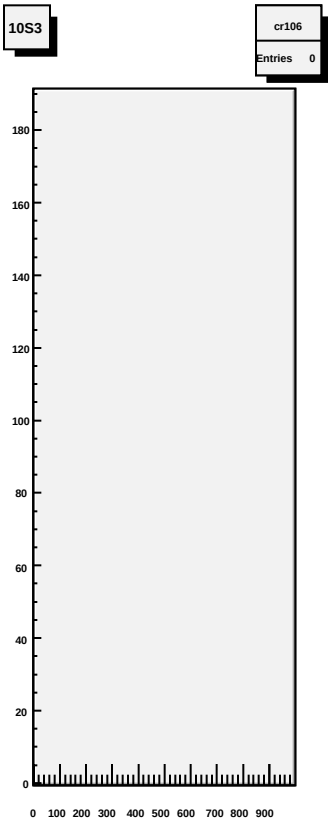

9S2
cr101
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

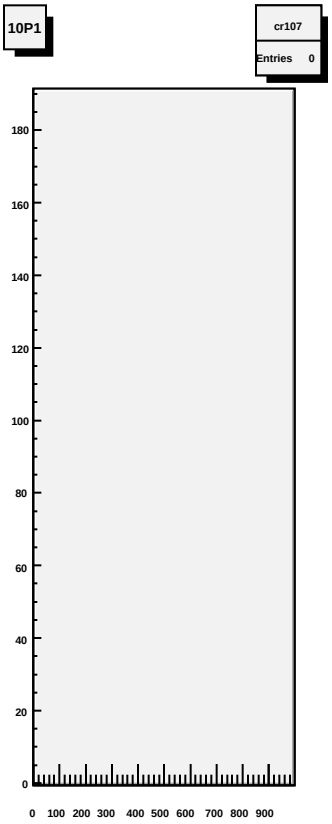

9S3
cr102
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

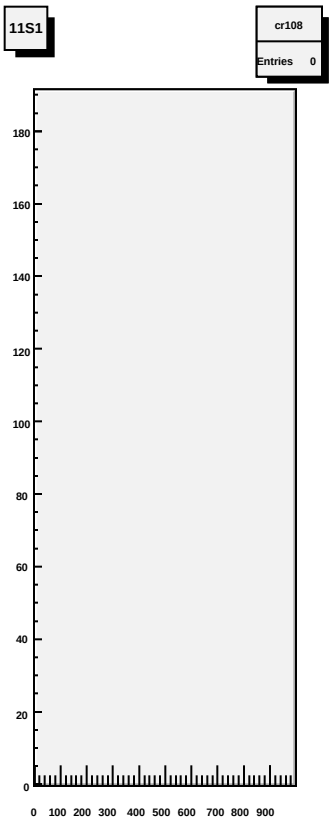

9P1
cr103
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

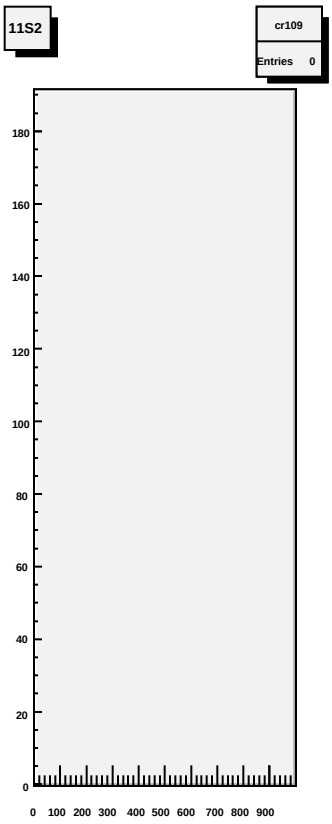

10S1
cr104
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

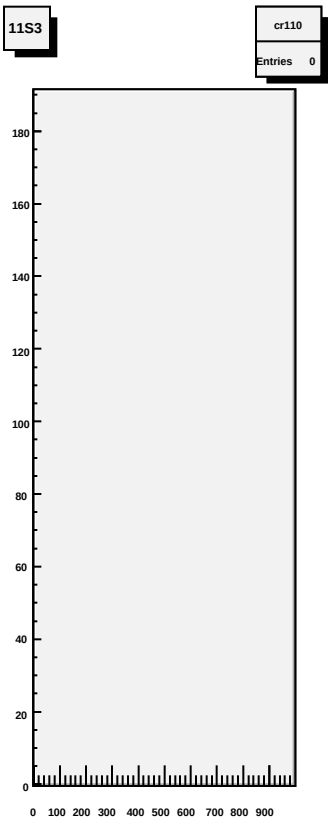

10S2
cr105
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

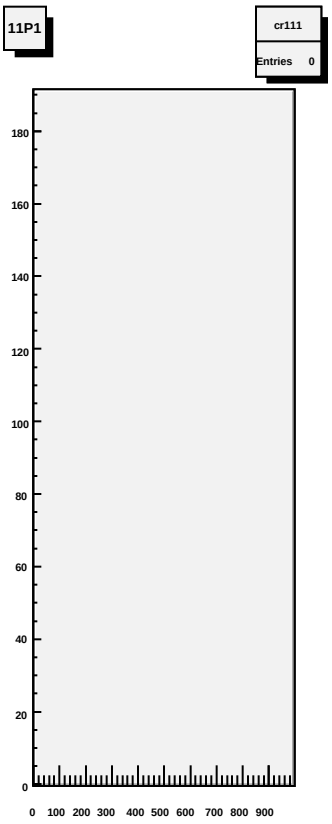

10S3
cr106
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

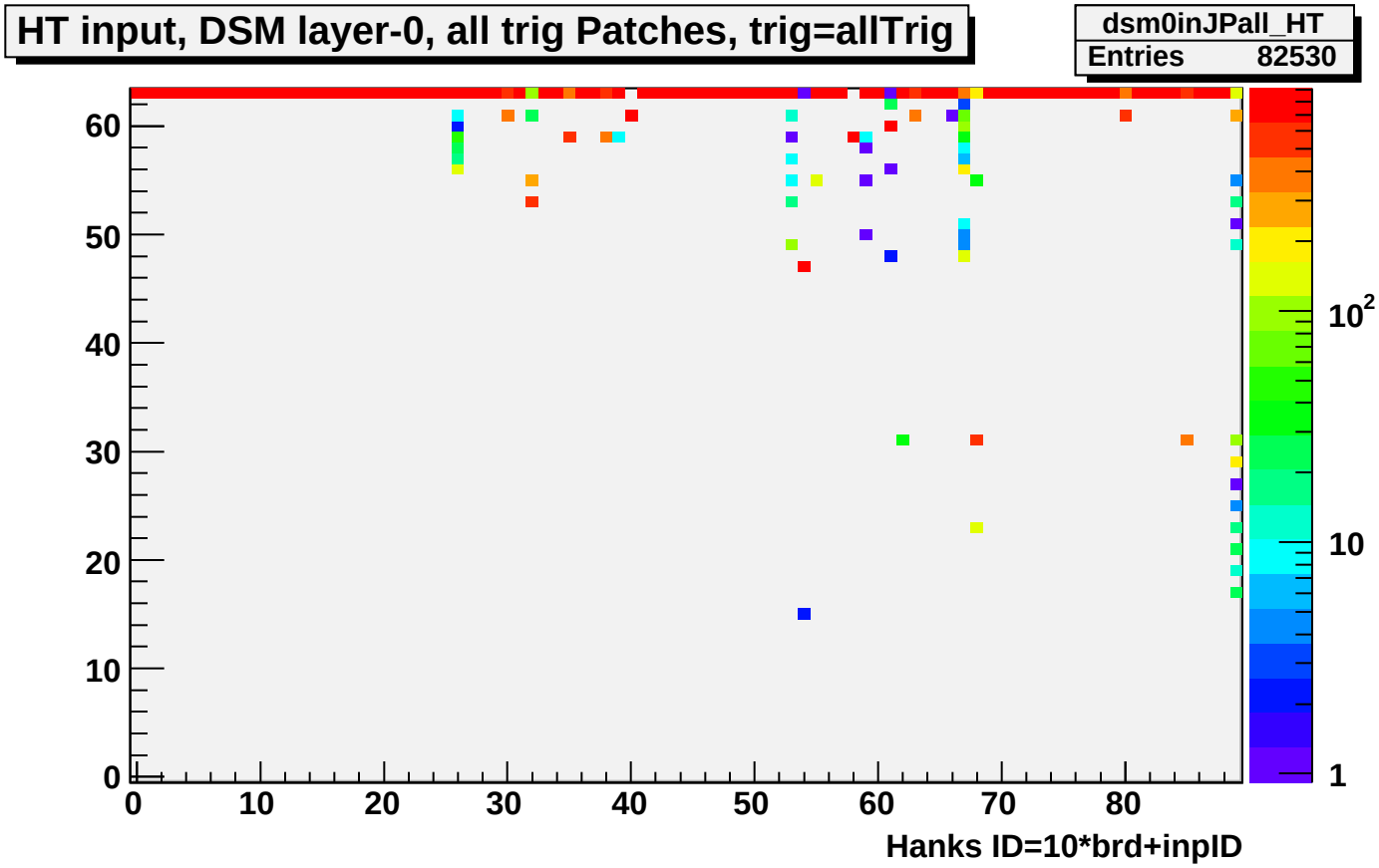

10P1
cr107
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

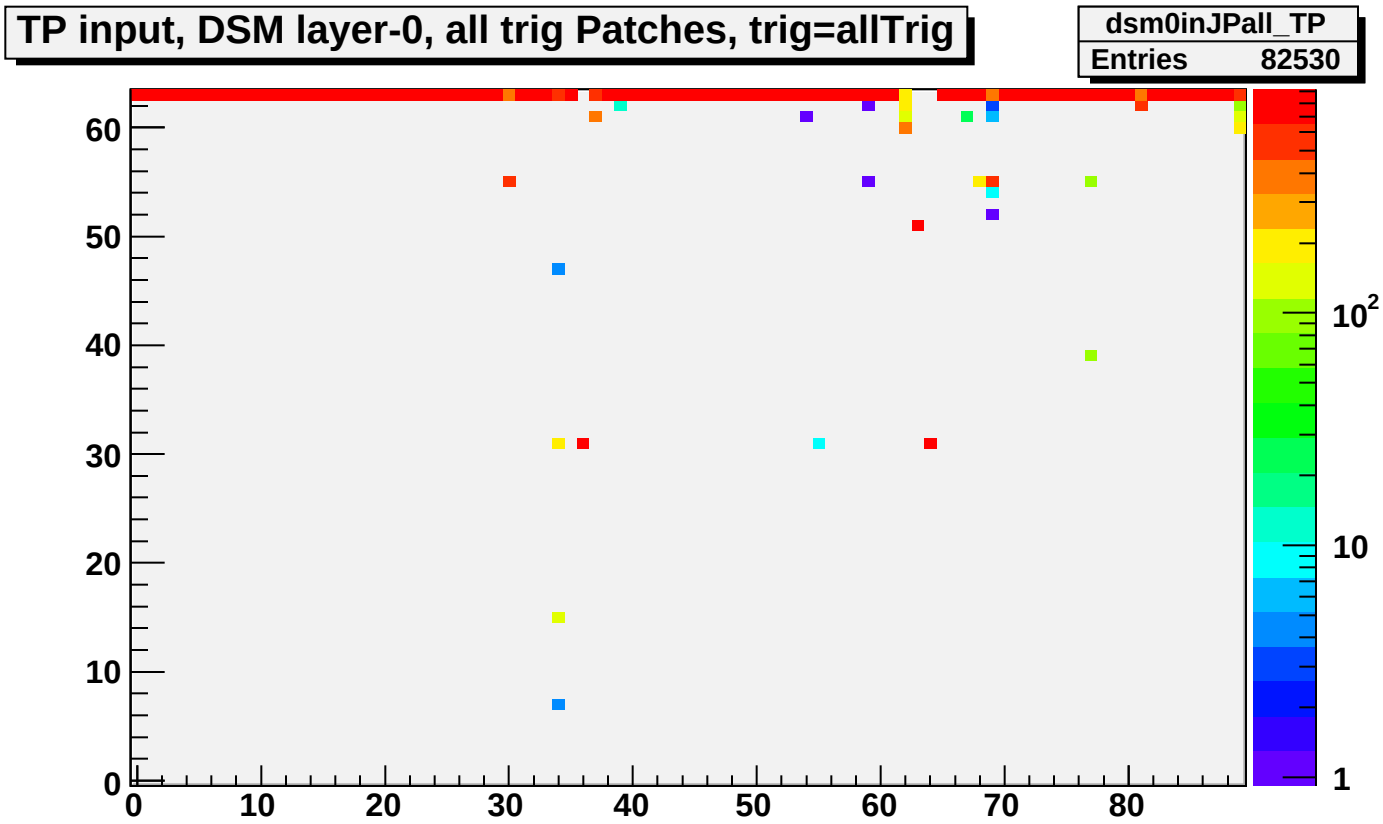

11S1
cr108
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

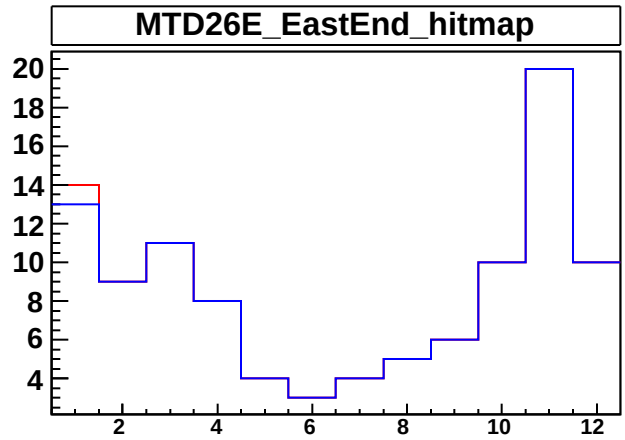

11S2
cr109
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

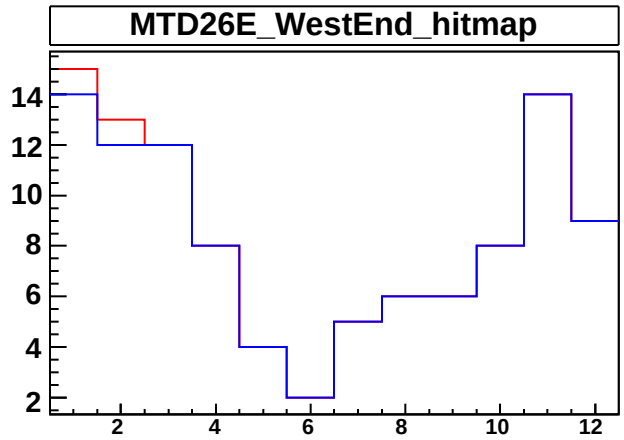

11S3
cr110
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

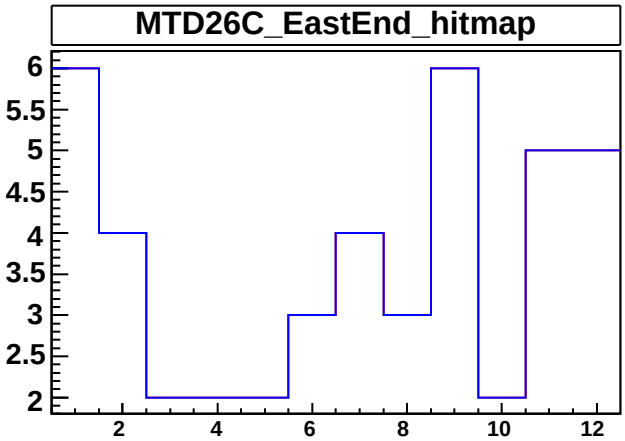

11P1
cr111
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

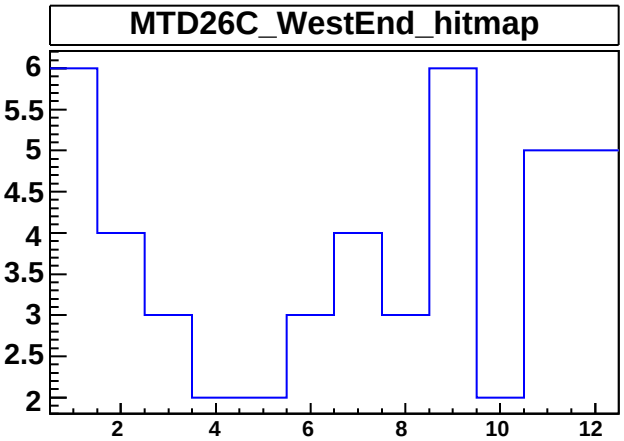

HT input, DSM layer-0, all trig Patches, trig=allTrig
dsm0inJPall_HT
Entries 82530
Hanks ID=10*brd+inpID

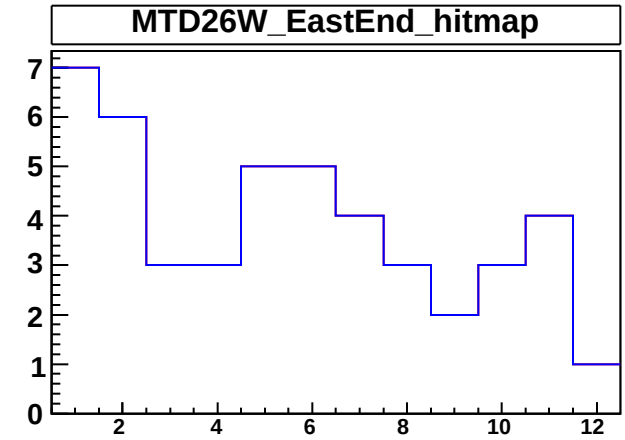

TP input, DSM layer-0, all trig Patches, trig=allTrig
dsm0inJPall_TP
Entries 82530

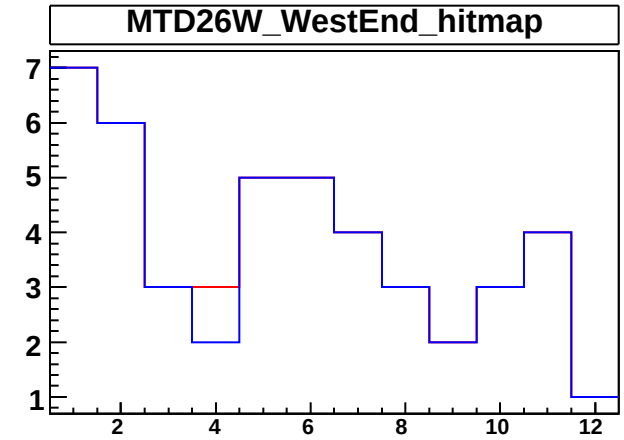

MTD26E_EastEnd_hitmap

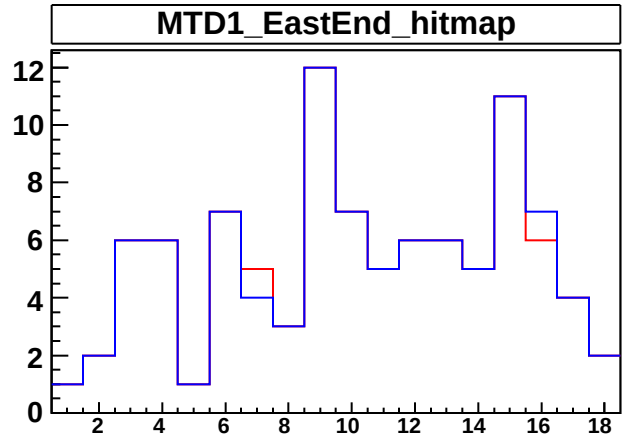

MTD26E_WestEnd_hitmap

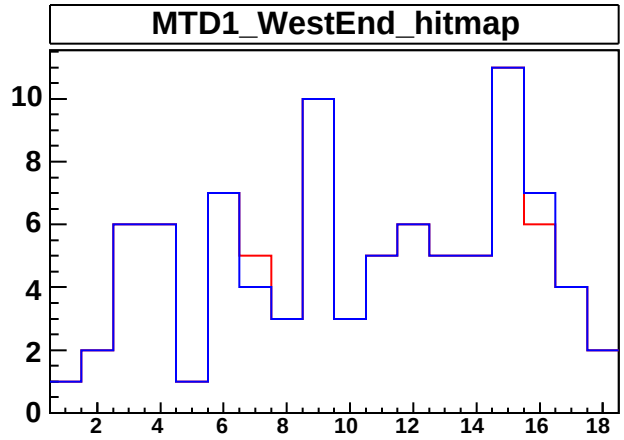

MTD26C_EastEnd_hitmap

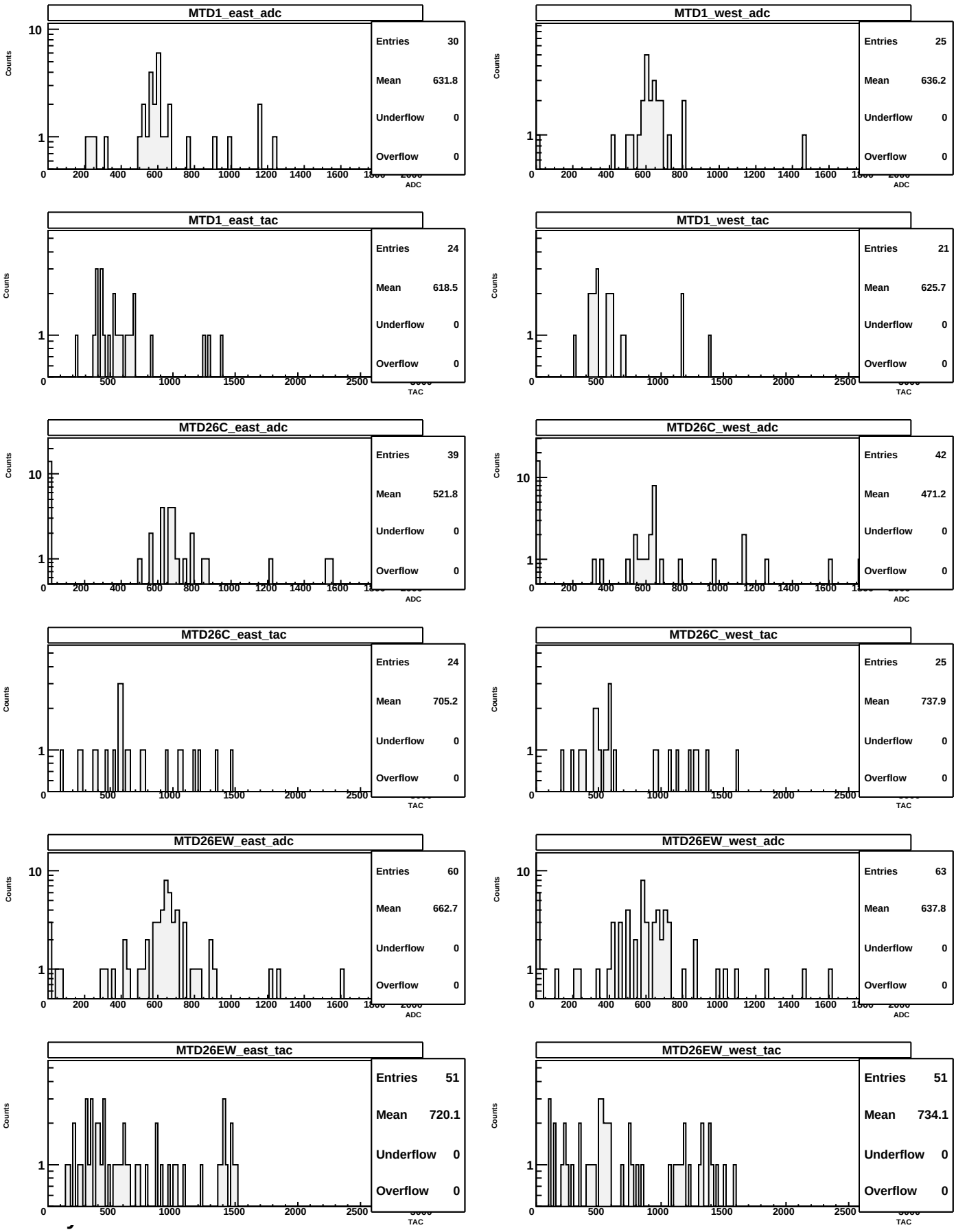

MTD26C_WestEnd_hitmap

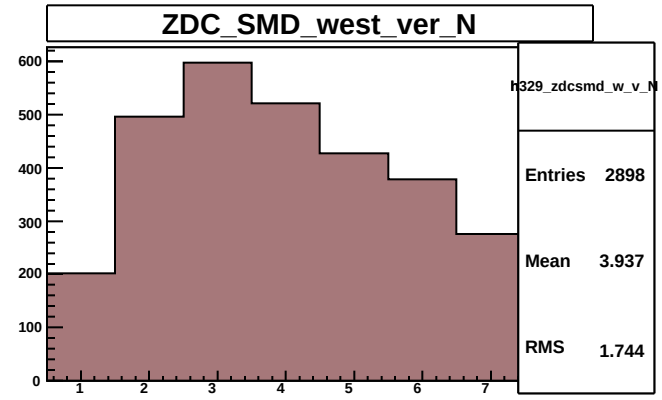

MTD26W_EastEnd_hitmap

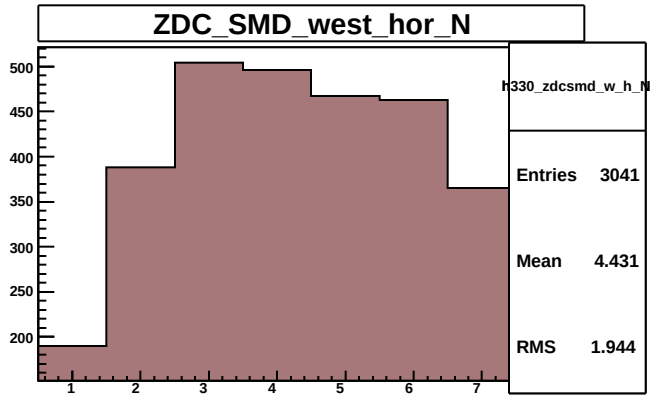

MTD26W_WestEnd_hitmap

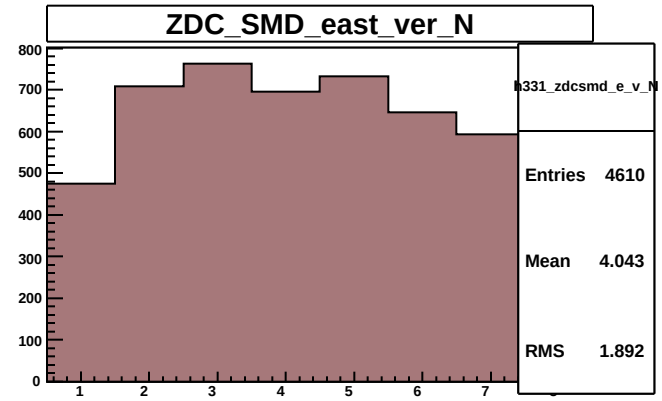

MTD1_EastEnd_hitmap

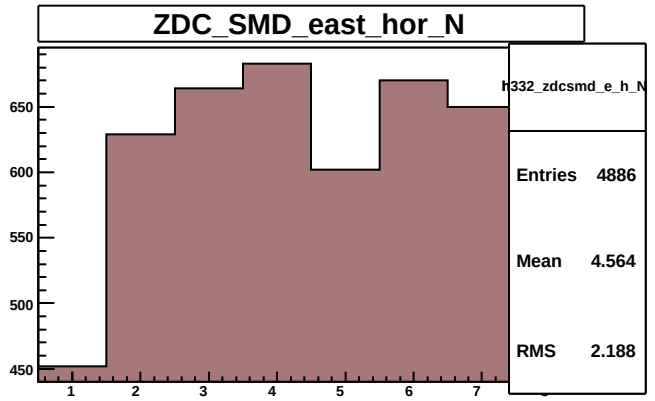

MTD1_WestEnd_hitmap

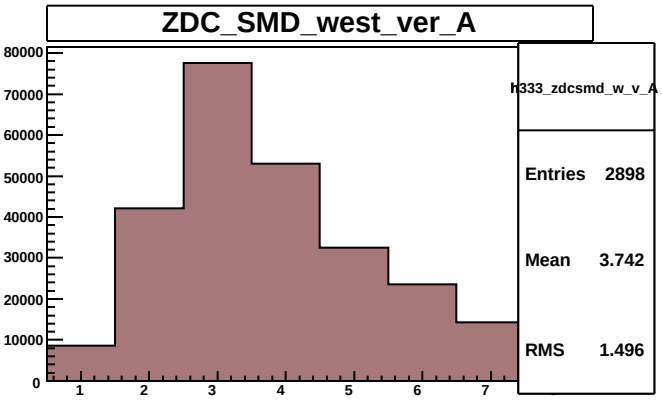

MTD1_east_adc
Entries 30
Mean 631.8
Underflow 0
Overflow 0
MTD1_west_adc
Entries 25
Mean 636.2
Underflow 0
Overflow 0
MTD1_east_tac
Entries 24
Mean 618.5
Underflow 0
Overflow 0
MTD1_west_tac
Entries 21
Mean 625.7
Underflow 0
Overflow 0
MTD26C_east_adc
Entries 39
Mean 521.8
Underflow 0
Overflow 0
MTD26C_west_adc
Entries 42
Mean 471.2
Underflow 0
Overflow 0
MTD26C_east_tac
Entries 24
Mean 705.2
Underflow 0
Overflow 0
MTD26C_west_tac
Entries 25
Mean 737.9
Underflow 0
Overflow 0
MTD26EW_east_adc
Entries 60
Mean 662.7
Underflow 0
Overflow 0
MTD26EW_west_adc
Entries 63
Mean 637.8
Underflow 0
Overflow 0
MTD26EW_east_tac
Entries 51
Mean 720.1
Underflow 0
Overflow 0
MTD26EW_west_tac
Entries 51
Mean 734.1
Underflow 0
Overflow 0
Counts
ADC
TAC

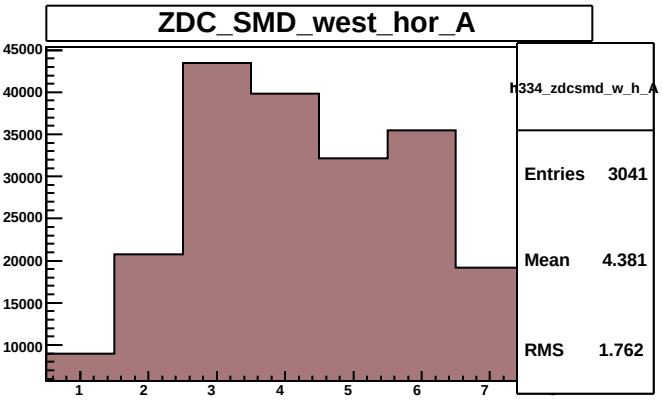

ZDC_SMD_west_ver_N
h329_zdcsmd_w_v_N
Entries 2898
Mean 3.937
RMS 1.744

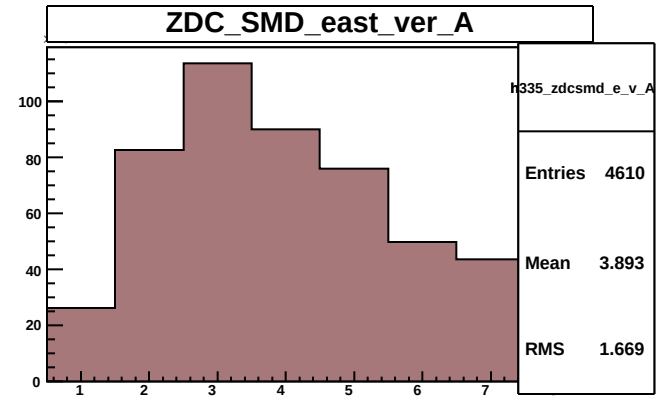

ZDC_SMD_west_hor_N
h330_zdcsmd_w_h_N
Entries 3041
Mean 4.431
RMS 1.944

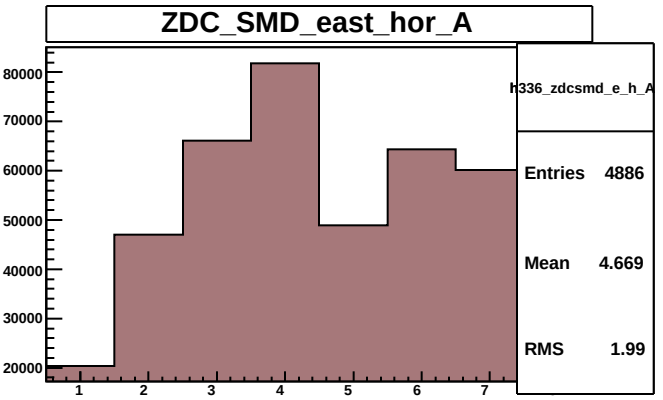

ZDC_SMD_east_ver_N
h331_zdcsmd_e_v_N
Entries 4610
Mean 4.043
RMS 1.892

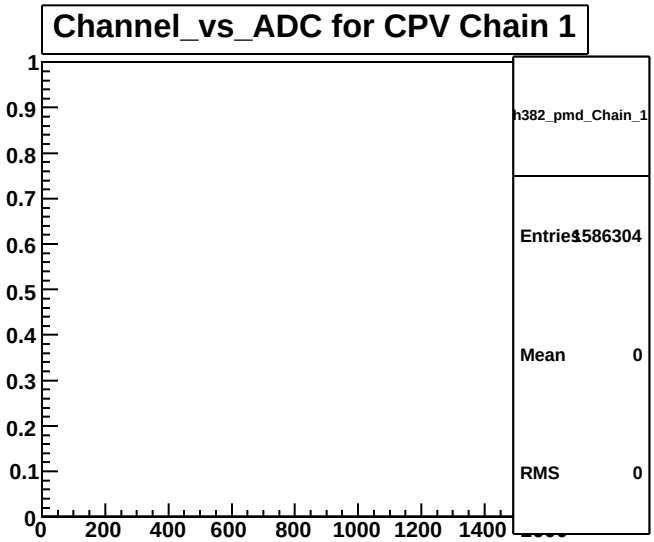

ZDC_SMD_east_hor_N
h332_zdcsmd_e_h_N
Entries 4886
Mean 4.564
RMS 2.188

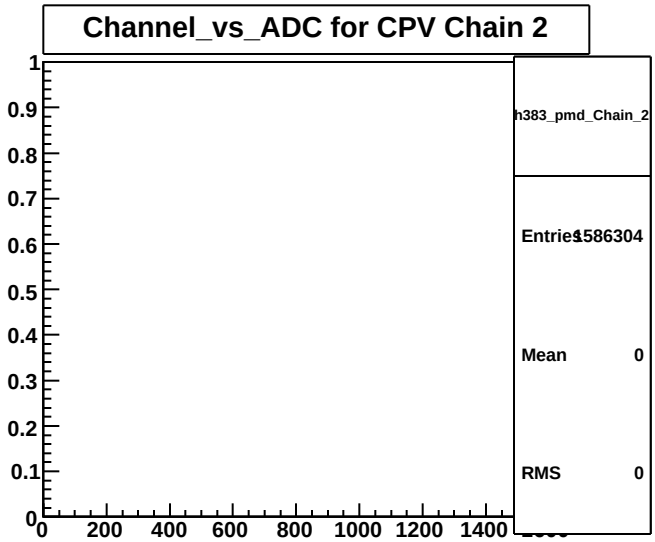

ZDC_SMD_west_ver_A
h333_zdcsmd_w_v_A
Entries 2898
Mean 3.742
RMS 1.496

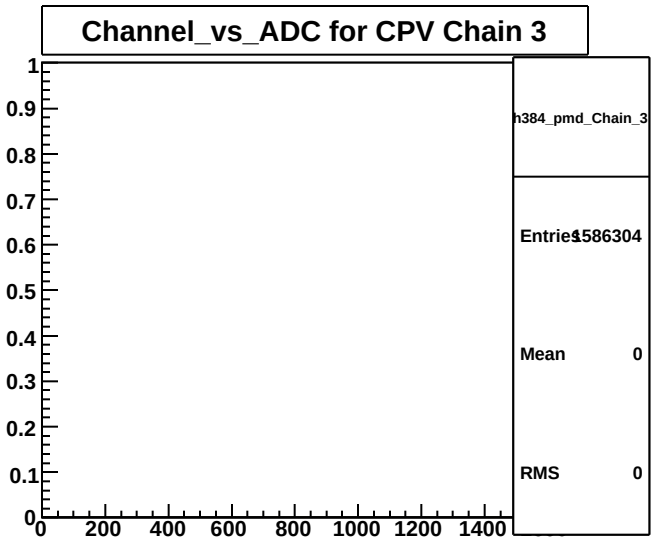

ZDC_SMD_west_hor_A
h334_zdcsmd_w_h_A
Entries 3041
Mean 4.381
RMS 1.762

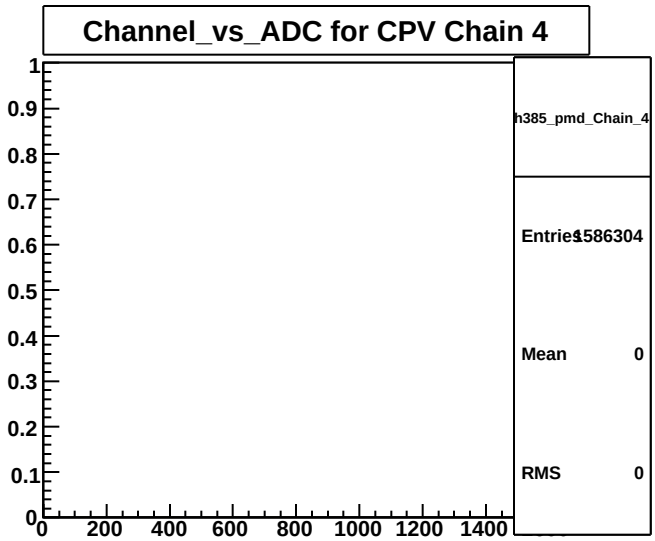

ZDC_SMD_east_ver_A
h335_zdcsmd_e_v_A
Entries 4610
Mean 3.893
RMS 1.669

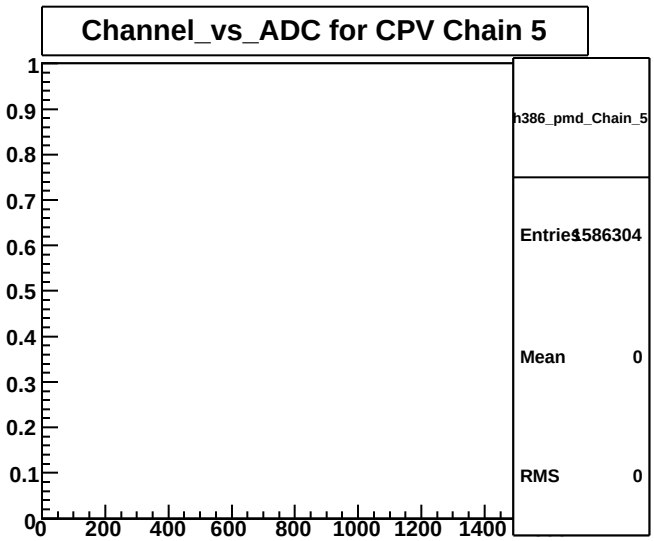

ZDC_SMD_east_hor_A
h336_zdcsmd_e_h_A
Entries 4886
Mean 4.669
RMS 1.99

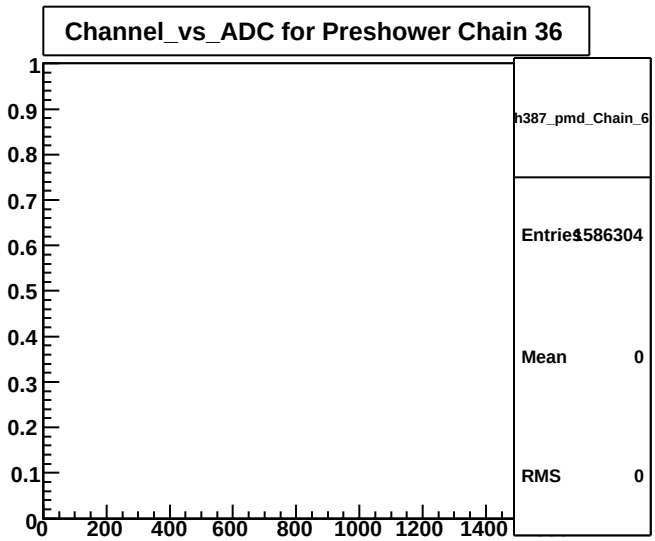

Channel_vs_ADC for CPV Chain 1
h382_pmd_Chain_1
Entries 1586304
Mean 0
RMS 0

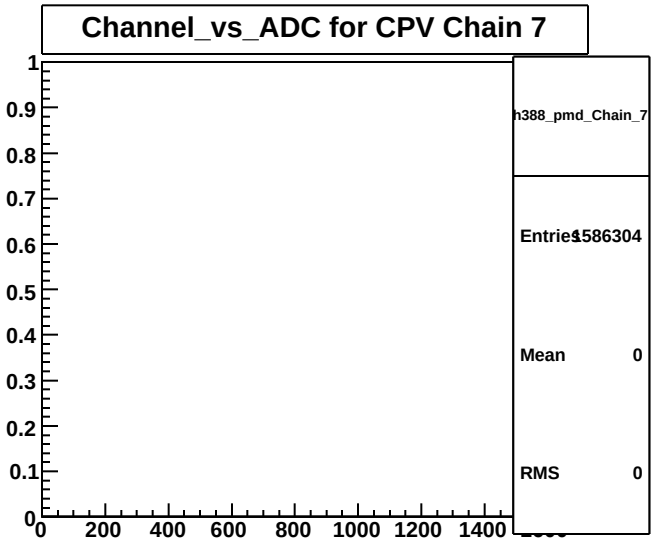

Channel_vs_ADC for CPV Chain 2
h383_pmd_Chain_2
Entries 1586304
Mean 0
RMS 0

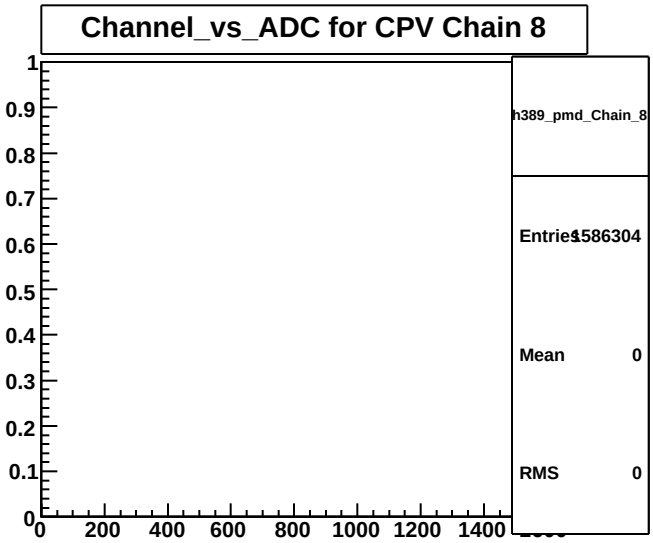

Channel_vs_ADC for CPV Chain 3
h384_pmd_Chain_3
Entries 1586304
Mean 0
RMS 0

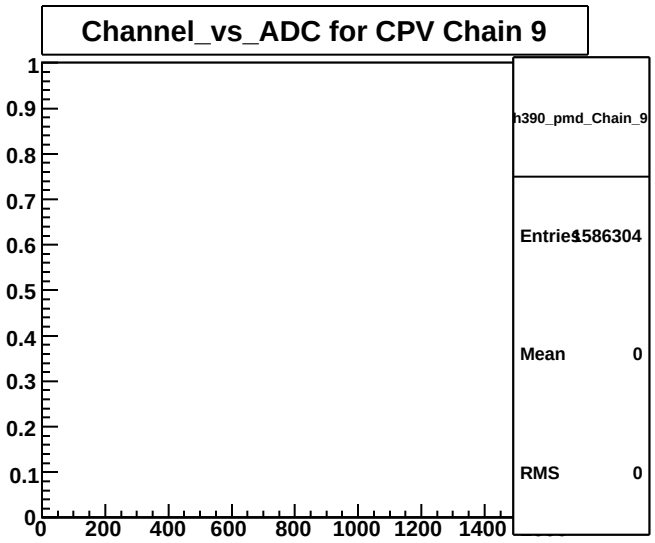

Channel_vs_ADC for CPV Chain 4
h385_pmd_Chain_4
Entries 1586304
Mean 0
RMS 0

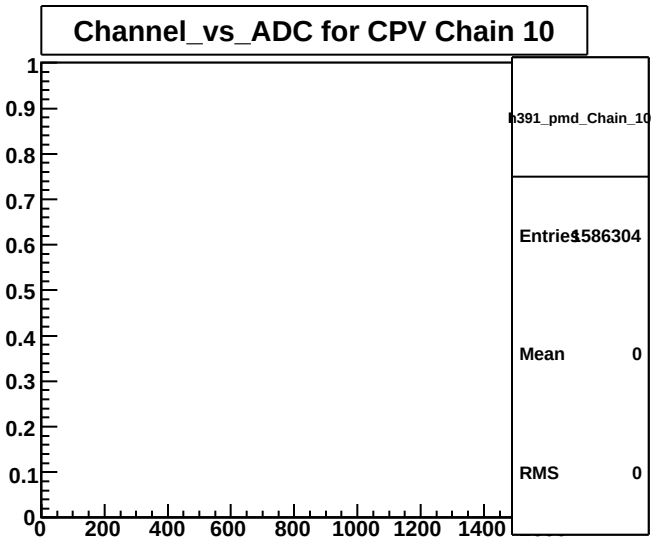

Channel_vs_ADC for CPV Chain 5
h386_pmd_Chain_5
Entries 1586304
Mean 0
RMS 0

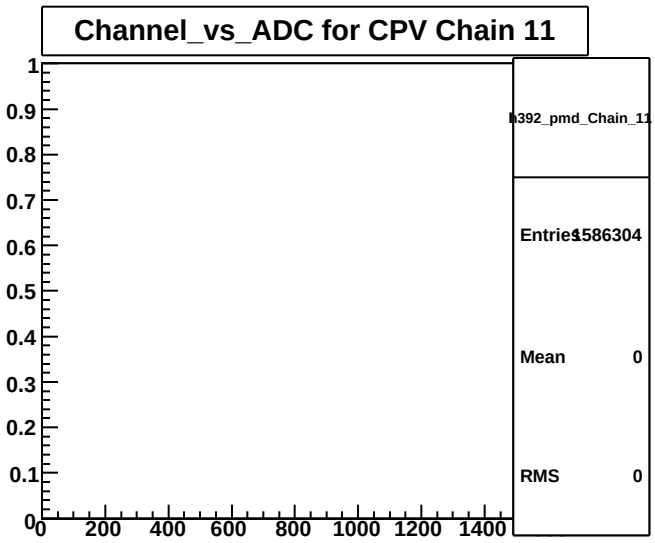

Channel_vs_ADC for Preshower Chain 36
h387_pmd_Chain_6
Entries 1586304
Mean 0
RMS 0

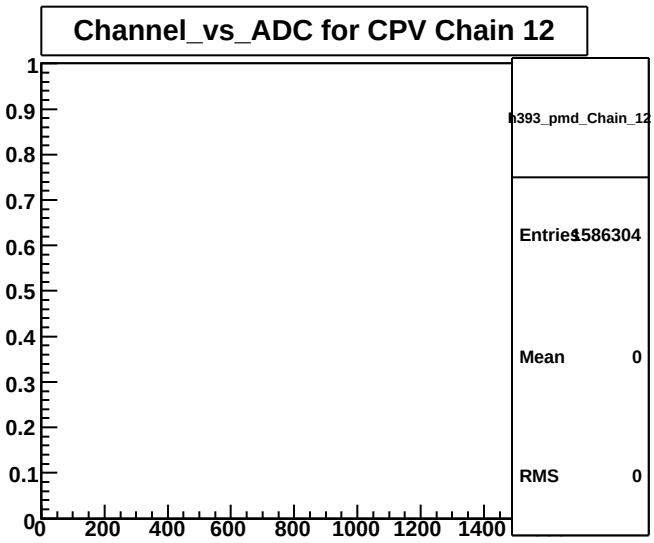

Channel_vs_ADC for CPV Chain 7
h388_pmd_Chain_7
Entries 1586304
Mean 0
RMS 0

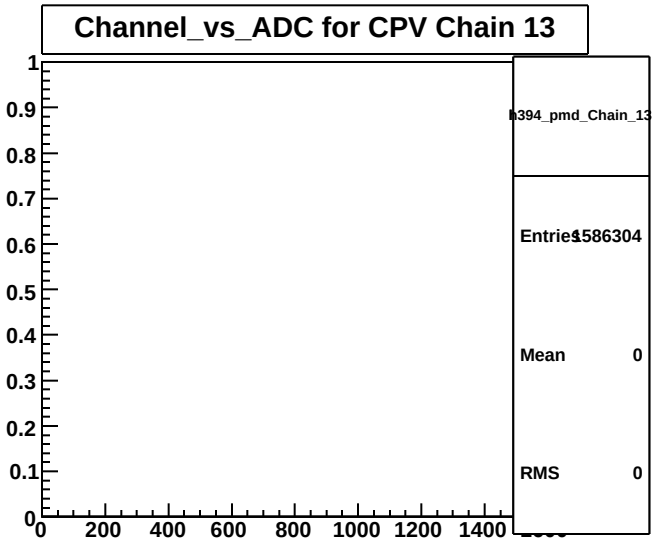

Channel_vs_ADC for CPV Chain 8
h389_pmd_Chain_8
Entries 1586304
Mean 0
RMS 0

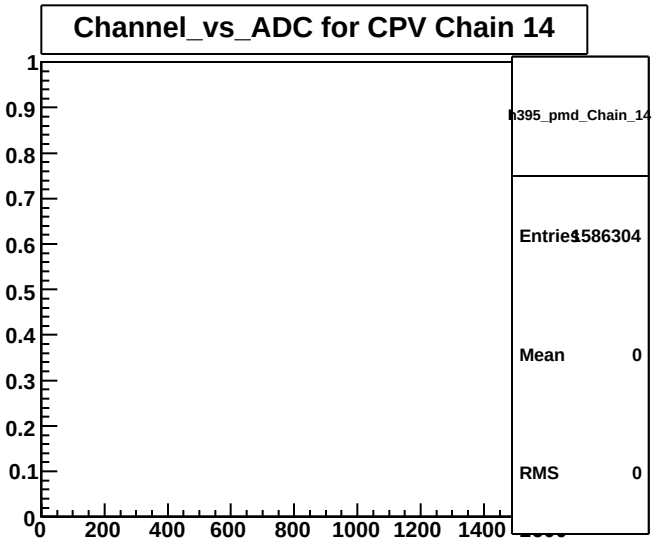

Channel_vs_ADC for CPV Chain 9
h390_pmd_Chain_9
Entries 1586304
Mean 0
RMS 0

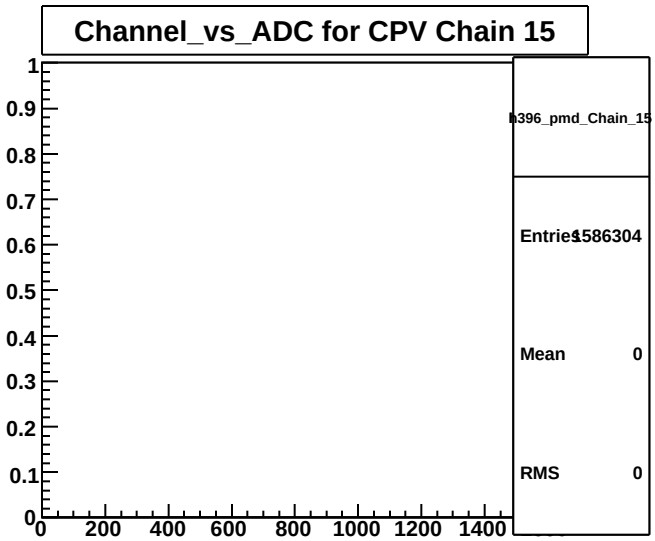

Channel_vs_ADC for CPV Chain 10
h391_pmd_Chain_10
Entries 1586304
Mean 0
RMS 0

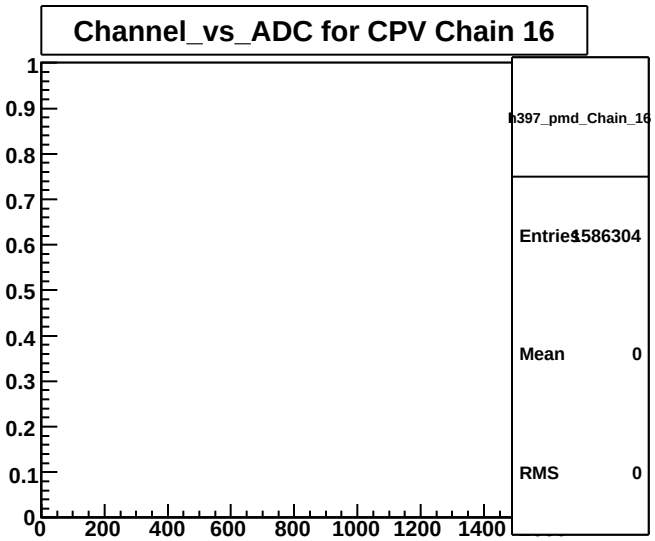

Channel_vs_ADC for CPV Chain 11
h392_pmd_Chain_11
Entries 1586304
Mean 0
RMS 0

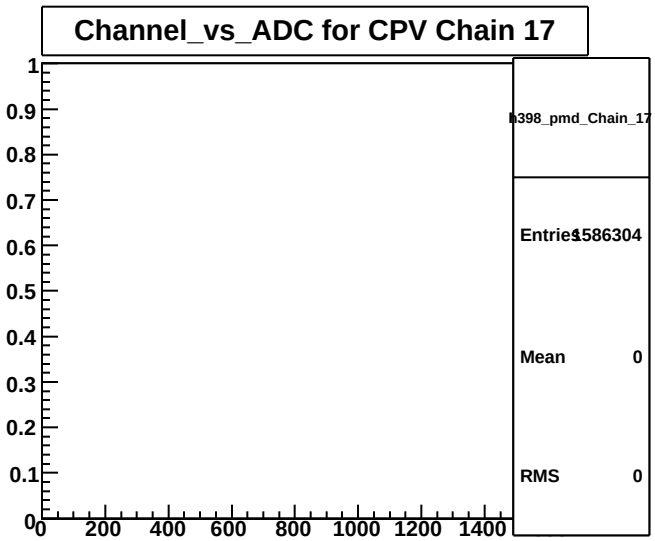

Channel_vs_ADC for CPV Chain 12
h393_pmd_Chain_12
Entries 1586304
Mean 0
RMS 0

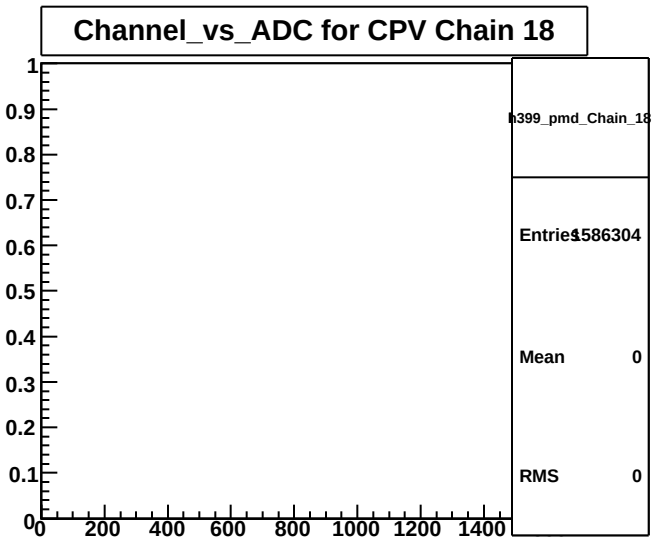

Channel_vs_ADC for CPV Chain 13
h394_pmd_Chain_13
Entries 1586304
Mean 0
RMS 0

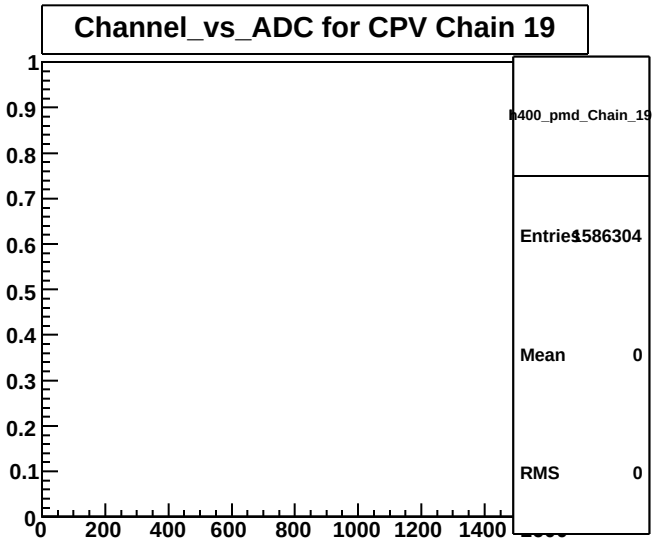

Channel_vs_ADC for CPV Chain 14
h395_pmd_Chain_14
Entries 1586304
Mean 0
RMS 0

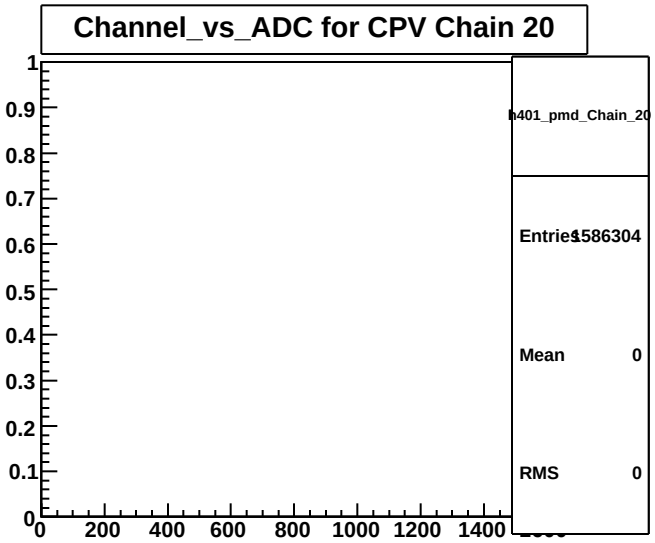

Channel_vs_ADC for CPV Chain 15
h396_pmd_Chain_15
Entries 1586304
Mean 0
RMS 0

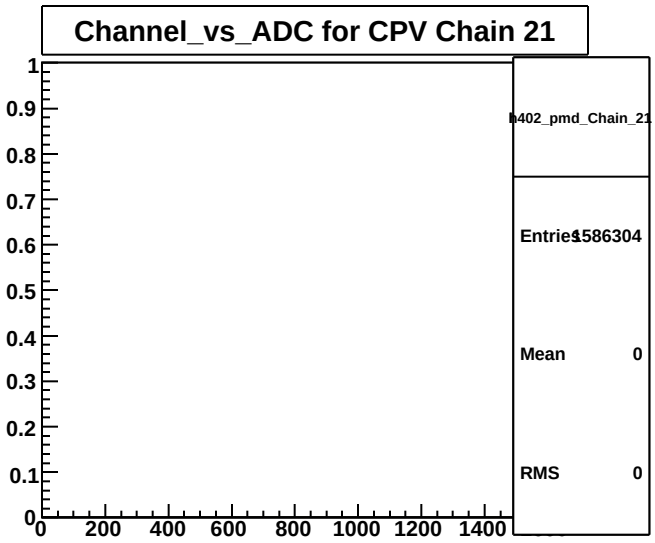

Channel_vs_ADC for CPV Chain 16
h397_pmd_Chain_16
Entries 1586304
Mean 0
RMS 0

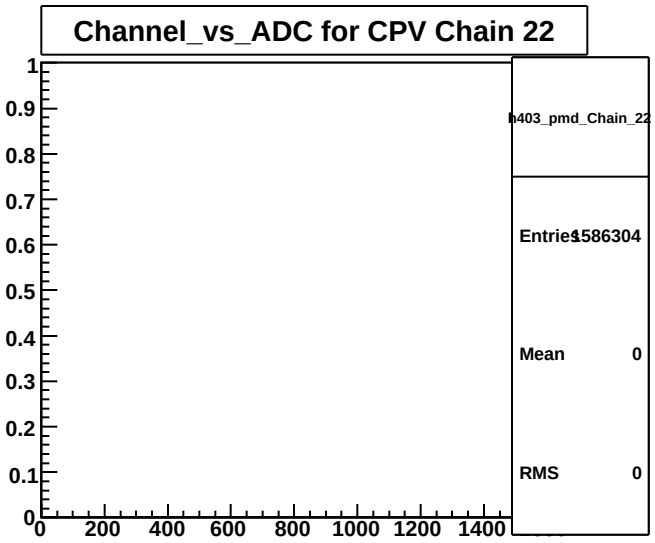

Channel_vs_ADC for CPV Chain 17
h398_pmd_Chain_17
Entries 1586304
Mean 0
RMS 0

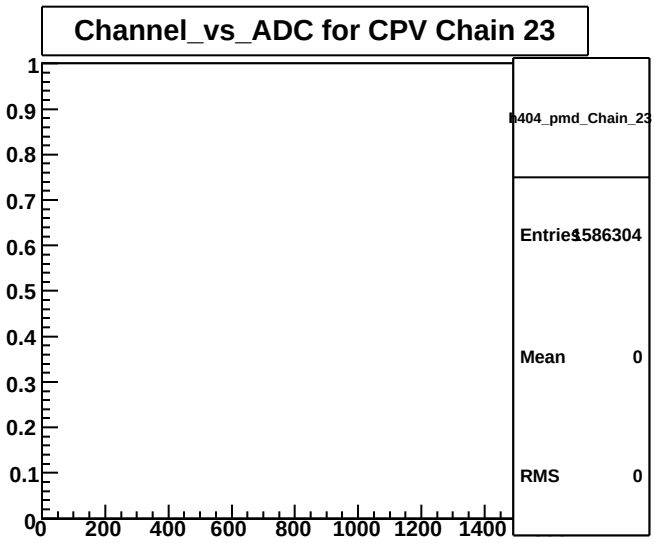

Channel_vs_ADC for CPV Chain 18
h399_pmd_Chain_18
Entries 1586304
Mean 0
RMS 0

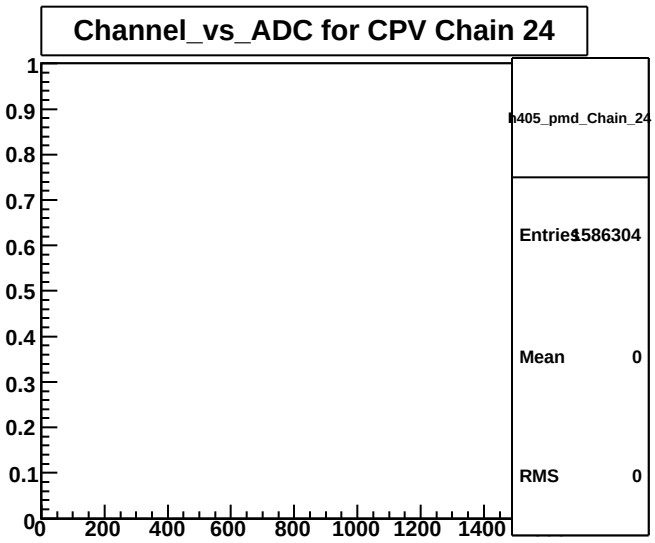

Channel_vs_ADC for CPV Chain 19
h400_pmd_Chain_19
Entries 1586304
Mean 0
RMS 0

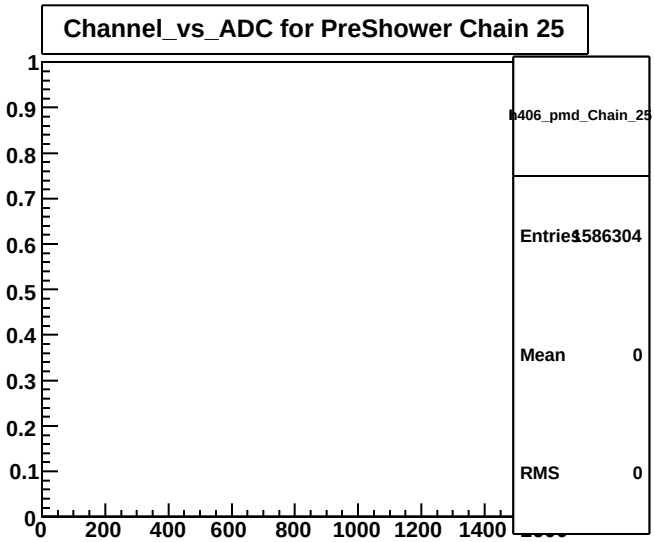

Channel_vs_ADC for CPV Chain 20
h401_pmd_Chain_20
Entries 1586304
Mean 0
RMS 0

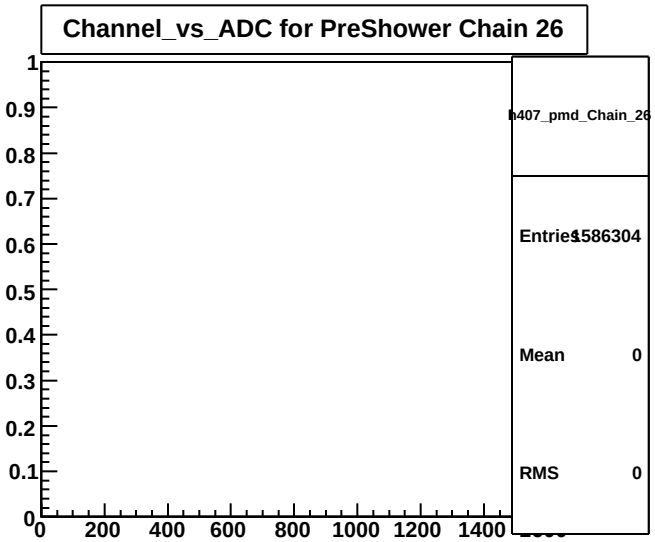

Channel_vs_ADC for CPV Chain 21
h402_pmd_Chain_21
Entries 1586304
Mean 0
RMS 0

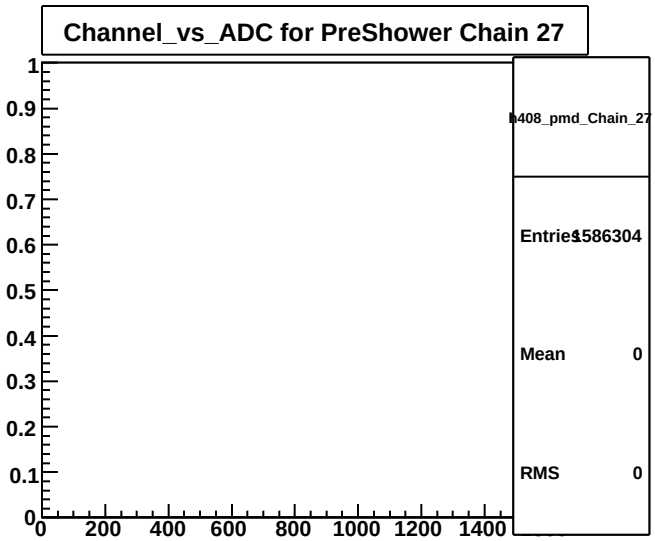

Channel_vs_ADC for CPV Chain 22
h403_pmd_Chain_22
Entries 1586304
Mean 0
RMS 0

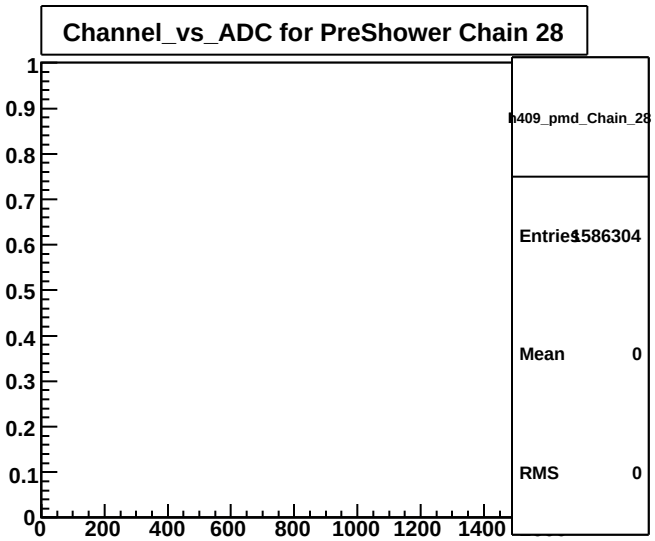

Channel_vs_ADC for CPV Chain 23
h404_pmd_Chain_23
Entries 1586304
Mean 0
RMS 0

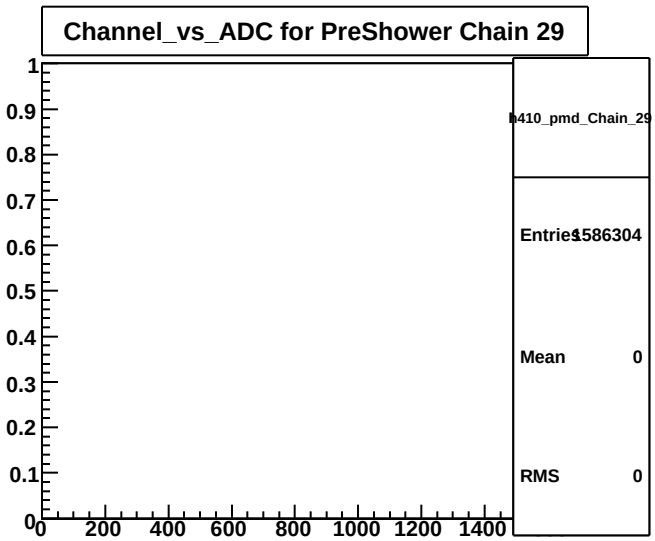

Channel_vs_ADC for CPV Chain 24
h405_pmd_Chain_24
Entries 1586304
Mean 0
RMS 0

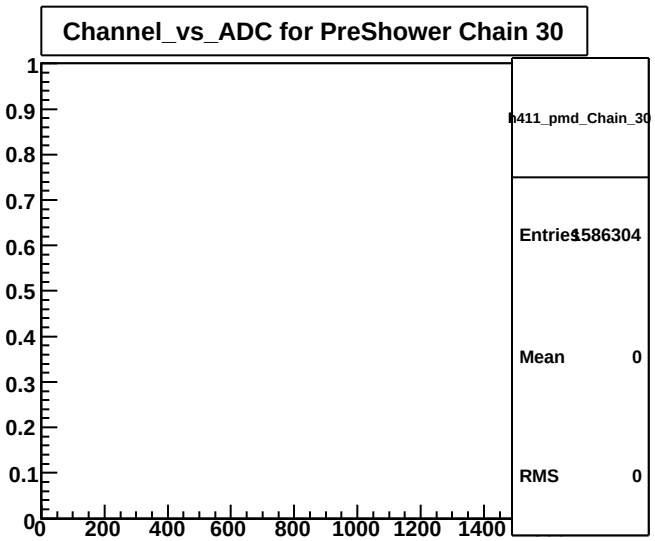

Channel_vs_ADC for PreShower Chain 25
h406_pmd_Chain_25
Entries 1586304
Mean 0
RMS 0

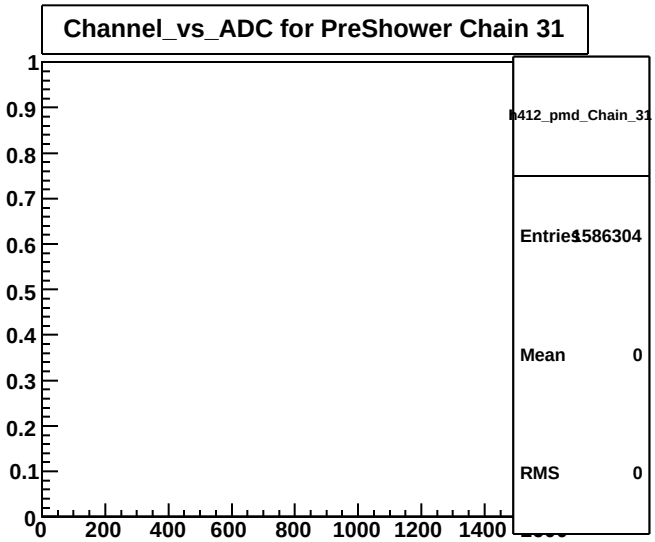

Channel_vs_ADC for PreShower Chain 26
h407_pmd_Chain_26
Entries 1586304
Mean 0
RMS 0

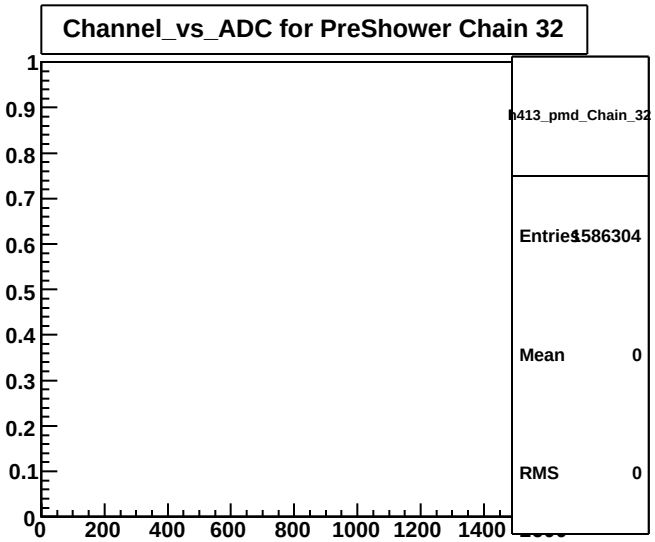

Channel_vs_ADC for PreShower Chain 27
h408_pmd_Chain_27
Entries 1586304
Mean 0
RMS 0

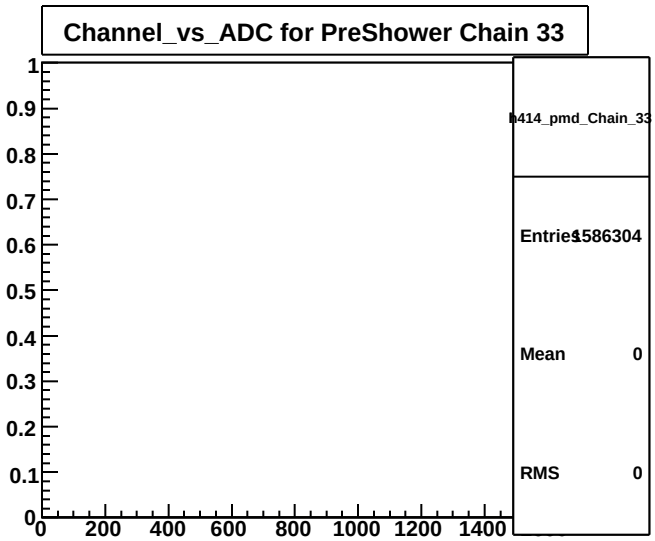

Channel_vs_ADC for PreShower Chain 28
h409_pmd_Chain_28
Entries 1586304
Mean 0
RMS 0

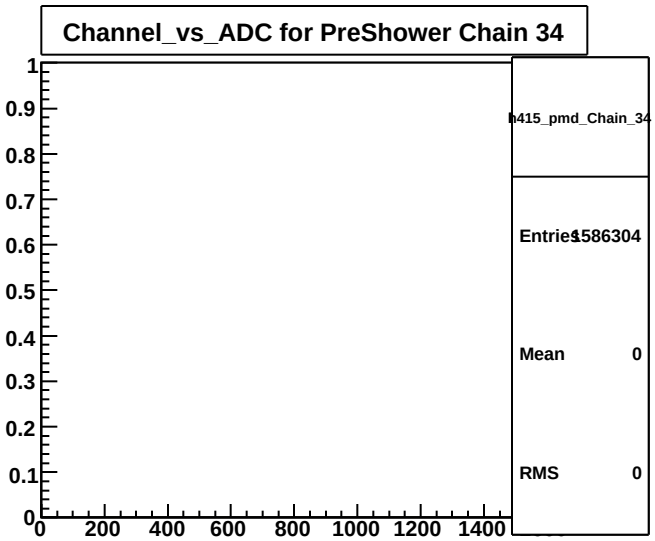

Channel_vs_ADC for PreShower Chain 29
h410_pmd_Chain_29
Entries 1586304
Mean 0
RMS 0

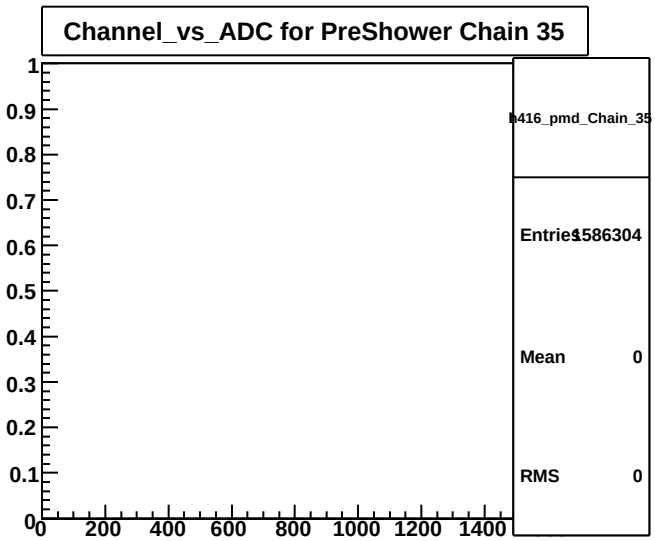

Channel_vs_ADC for PreShower Chain 30
h411_pmd_Chain_30
Entries 1586304
Mean 0
RMS 0

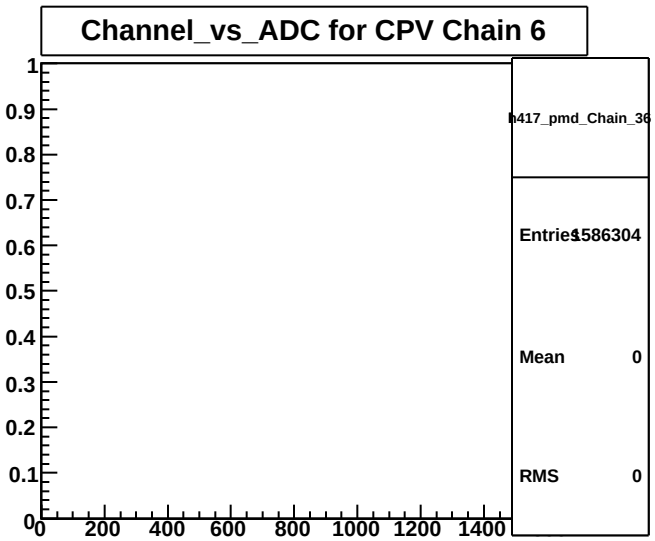

Channel_vs_ADC for PreShower Chain 31
h412_pmd_Chain_31
Entries 1586304
Mean 0
RMS 0

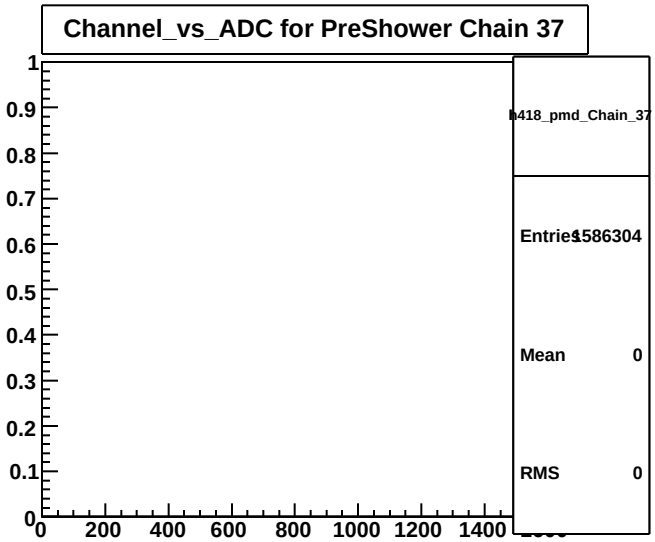

Channel_vs_ADC for PreShower Chain 32
h413_pmd_Chain_32
Entries 1586304
Mean 0
RMS 0

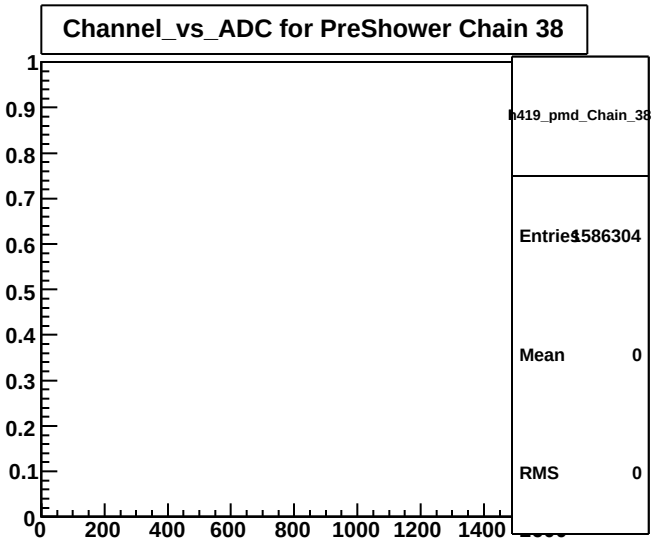

Channel_vs_ADC for PreShower Chain 33
h414_pmd_Chain_33
Entries 1586304
Mean 0
RMS 0

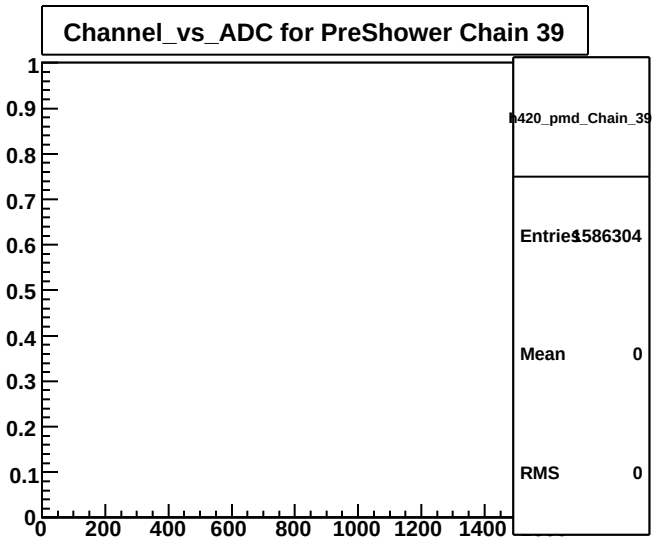

Channel_vs_ADC for PreShower Chain 34
h415_pmd_Chain_34
Entries 1586304
Mean 0
RMS 0

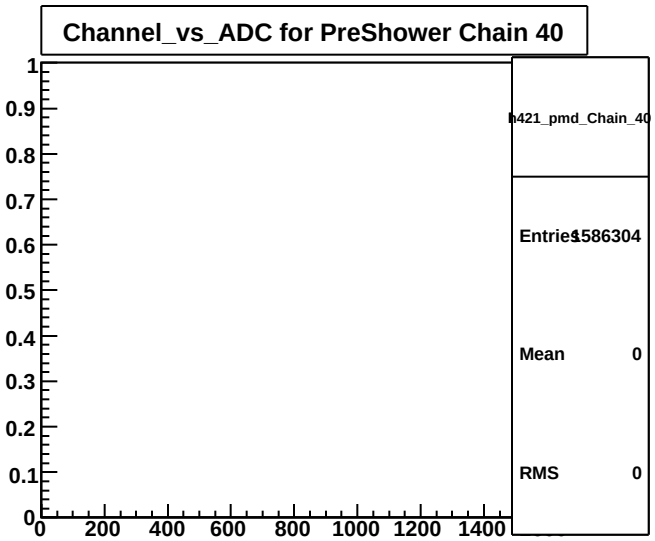

Channel_vs_ADC for PreShower Chain 35
h416_pmd_Chain_35
Entries 1586304
Mean 0
RMS 0

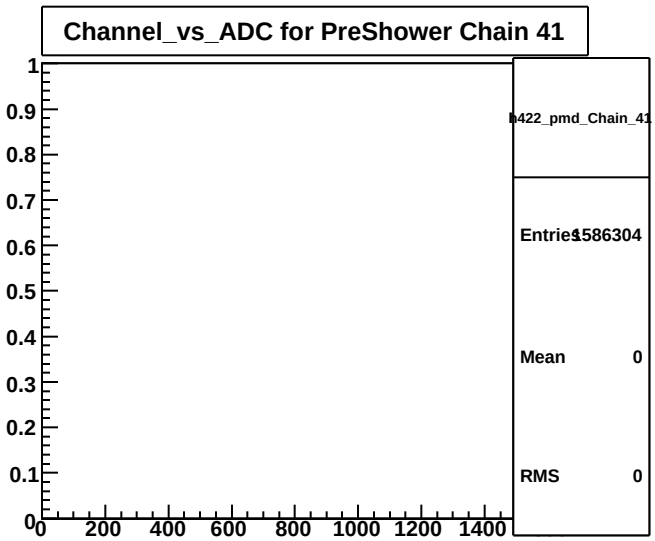

Channel_vs_ADC for CPV Chain 6
h417_pmd_Chain_36
Entries 1586304
Mean 0
RMS 0

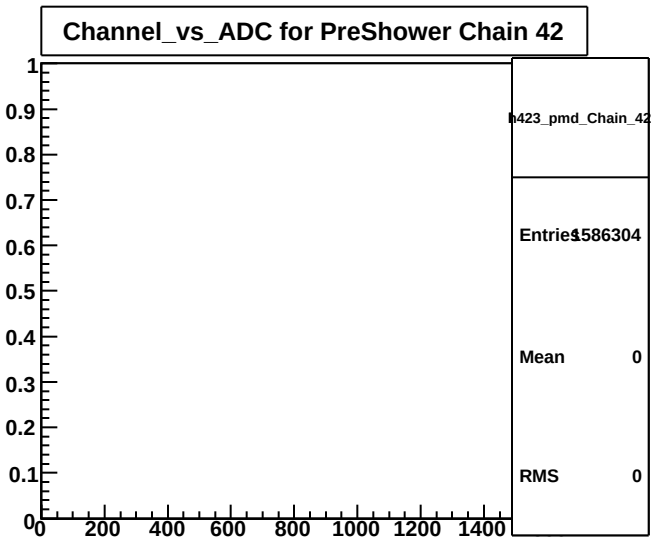

Channel_vs_ADC for PreShower Chain 37
h418_pmd_Chain_37
Entries 1586304
Mean 0
RMS 0

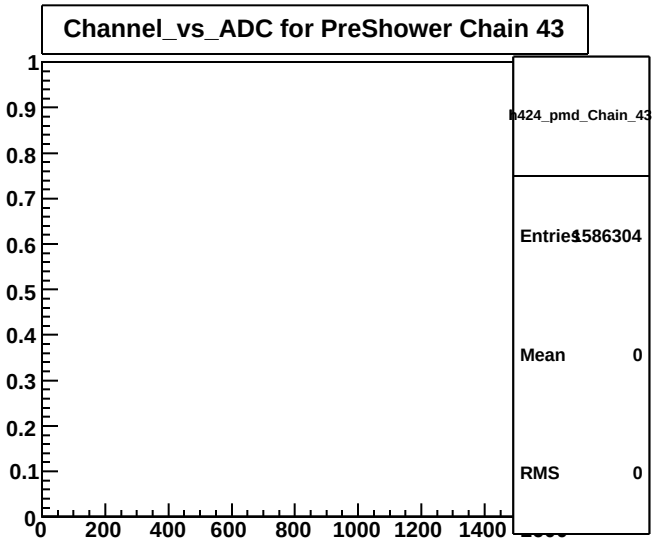

Channel_vs_ADC for PreShower Chain 38
h419_pmd_Chain_38
Entries 1586304
Mean 0
RMS 0

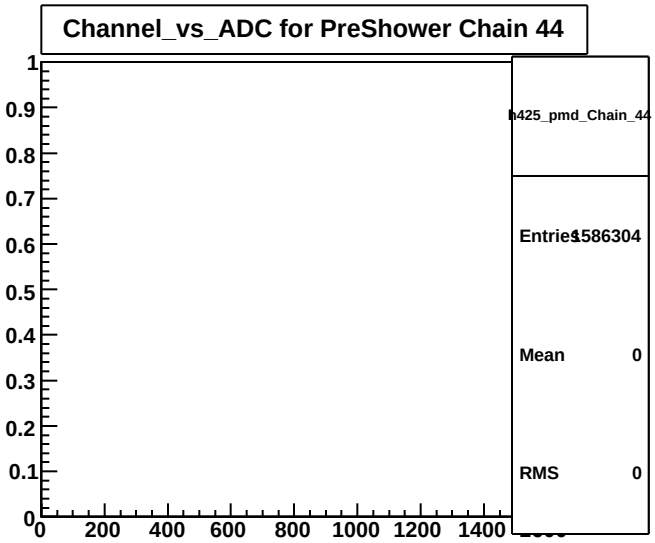

Channel_vs_ADC for PreShower Chain 39
h420_pmd_Chain_39
Entries 1586304
Mean 0
RMS 0

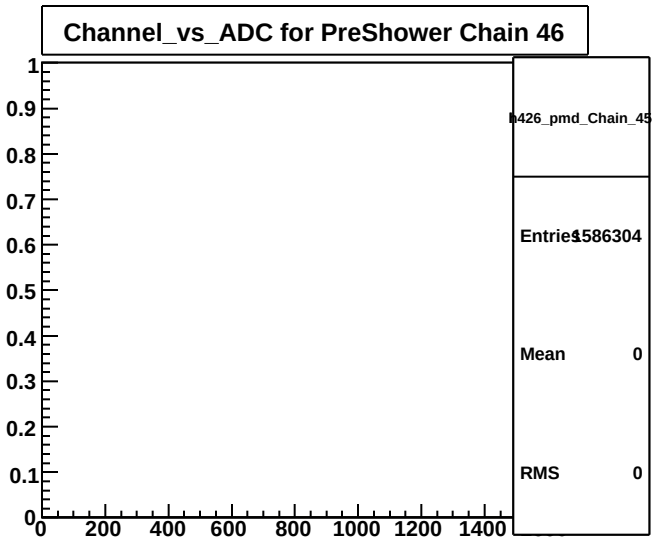

Channel_vs_ADC for PreShower Chain 40
h421_pmd_Chain_40
Entries 1586304
Mean 0
RMS 0

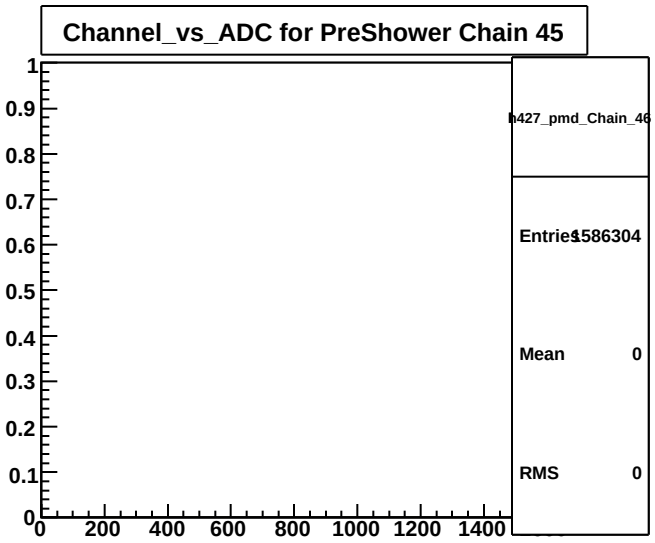

Channel_vs_ADC for PreShower Chain 41
h422_pmd_Chain_41
Entries 1586304
Mean 0
RMS 0

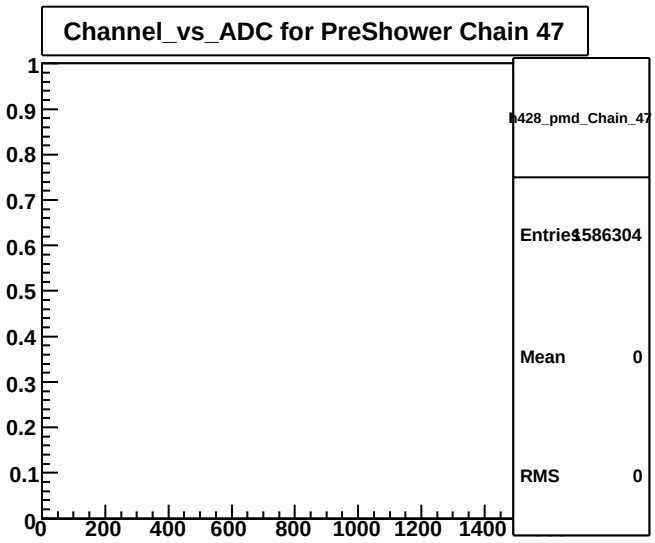

Channel_vs_ADC for PreShower Chain 42
h423_pmd_Chain_42
Entries 1586304
Mean 0
RMS 0

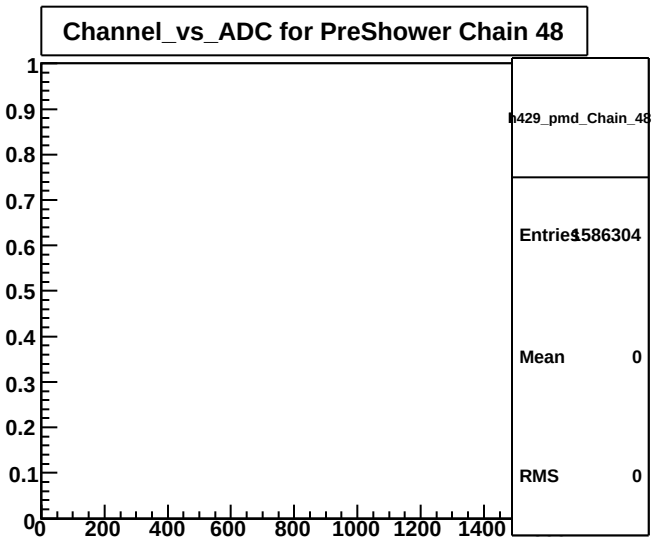

Channel_vs_ADC for PreShower Chain 43
h424_pmd_Chain_43
Entries 1586304
Mean 0
RMS 0

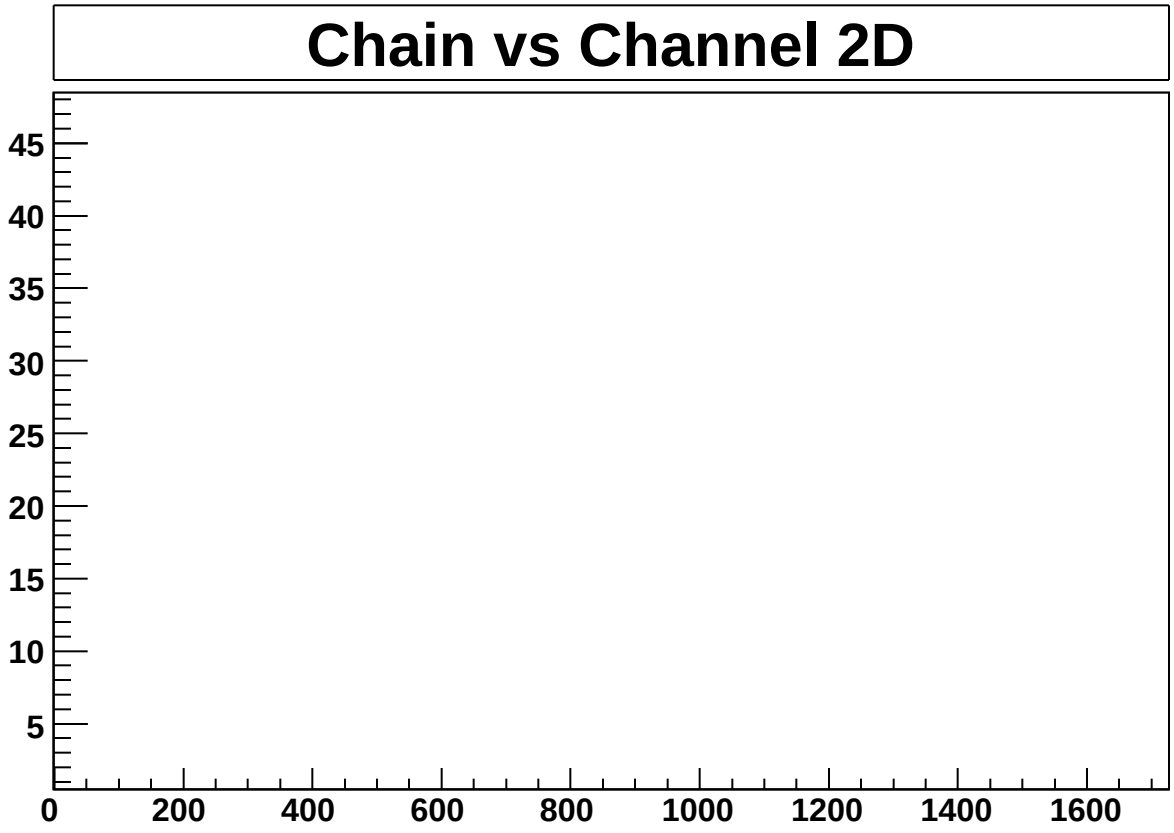

Channel_vs_ADC for PreShower Chain 44
h425_pmd_Chain_44
Entries 1586304
Mean 0
RMS 0

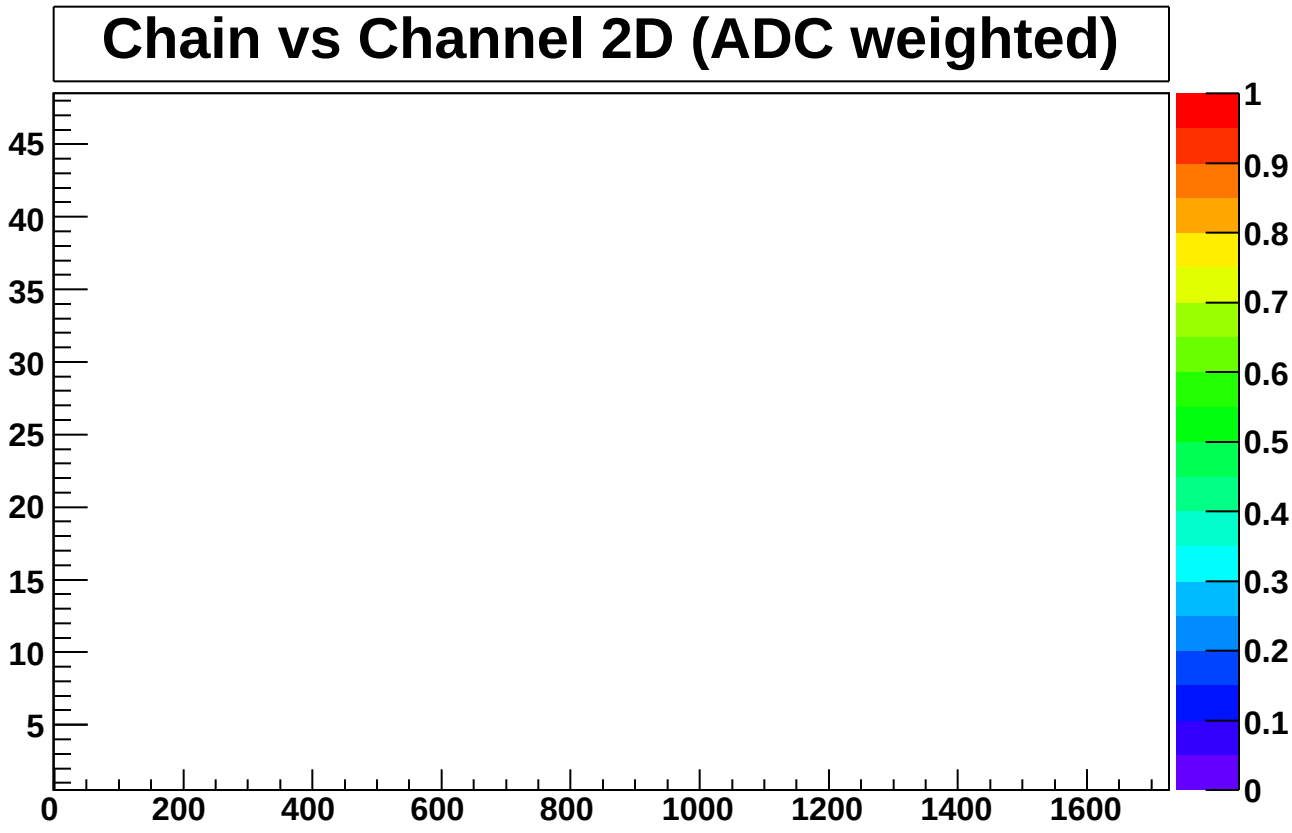

Channel_vs_ADC for PreShower Chain 46
h426_pmd_Chain_45
Entries 1586304
Mean 0
RMS 0


Channel_vs_ADC for PreShower Chain 45
h427_pmd_Chain_46
Entries 1586304
Mean 0
RMS 0

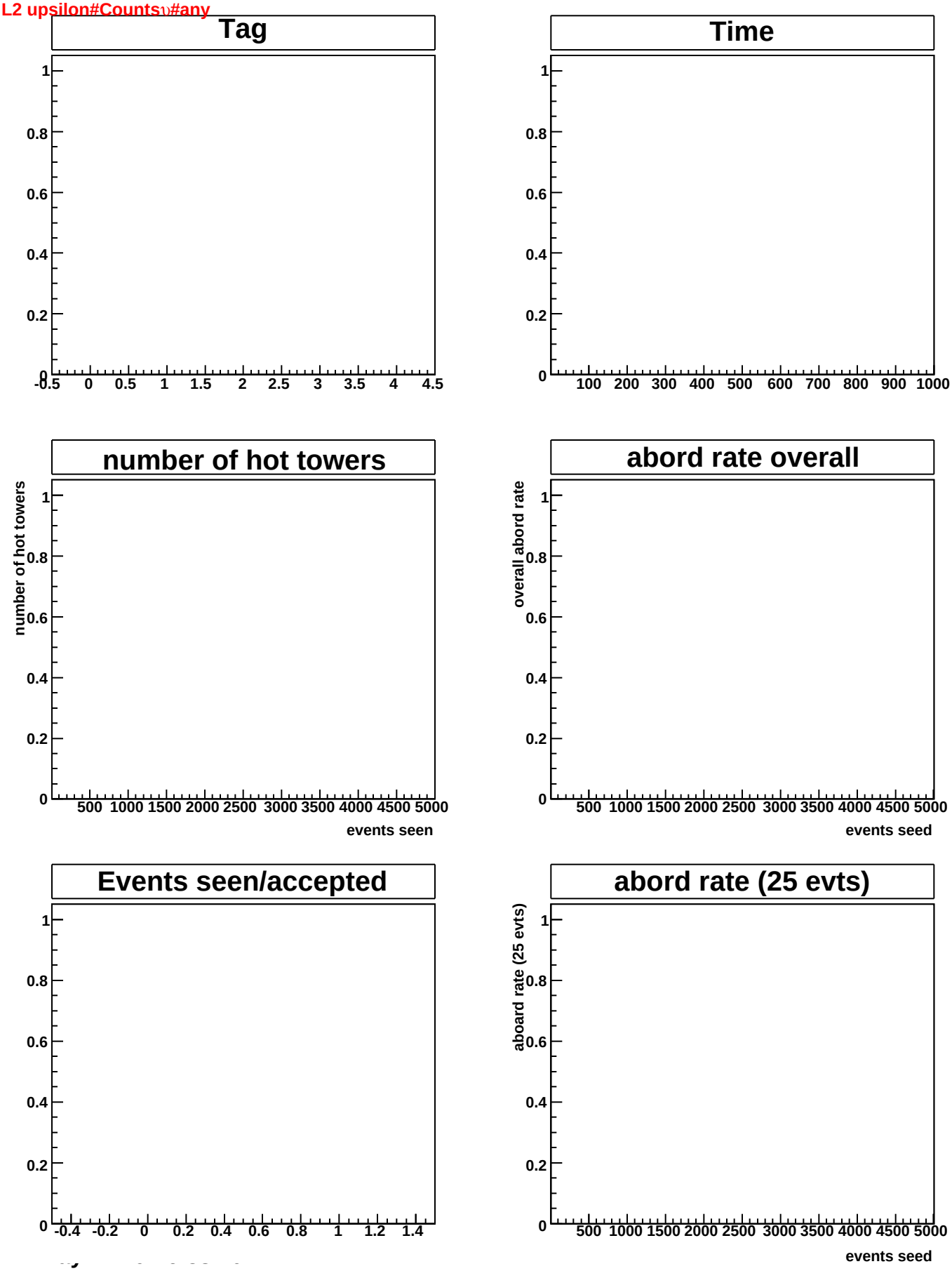

Channel_vs_ADC for PreShower Chain 47
h428_pmd_Chain_47
Entries 1586304
Mean 0
RMS 0


Channel_vs_ADC for PreShower Chain 48
h429_pmd_Chain_48
Entries 1586304
Mean 0
RMS 0

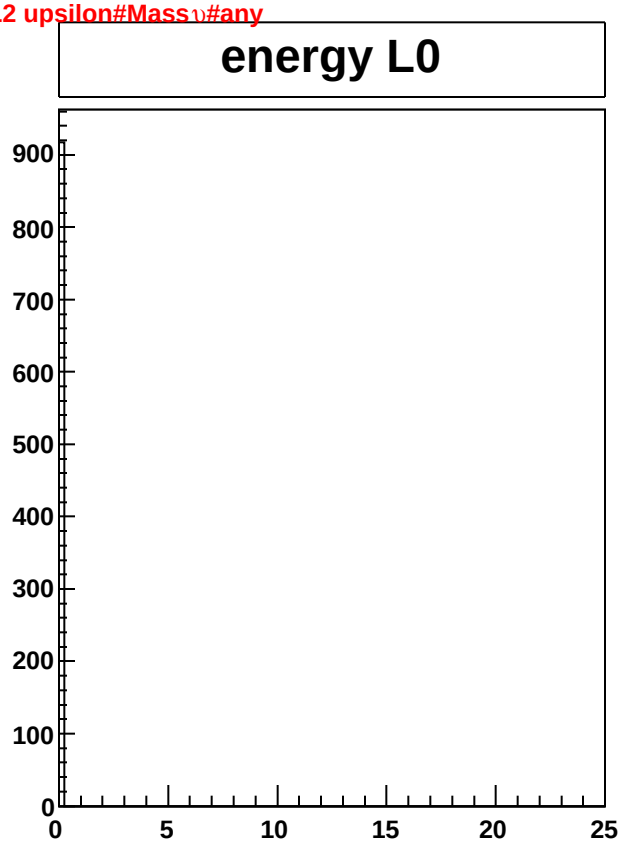

Chain vs Channel 2D
45
40
35
30
25
20
15
10
5
0
200
400
600
800
1000
1200
1400
1600

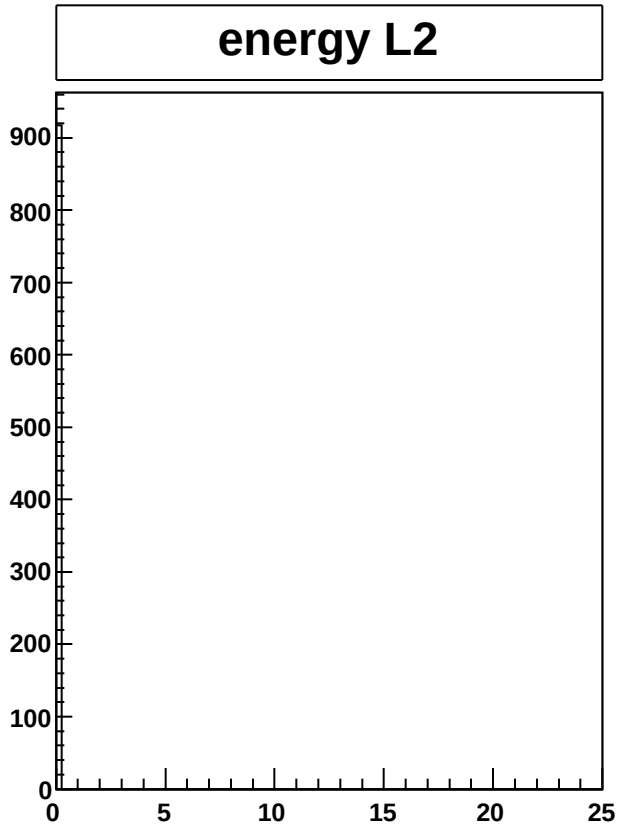

Chain vs Channel 2D (ADC weighted)
45
40
35
30
25
20
15
10
5
0
200
400
600
800
1000
1200
1400
1600
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0

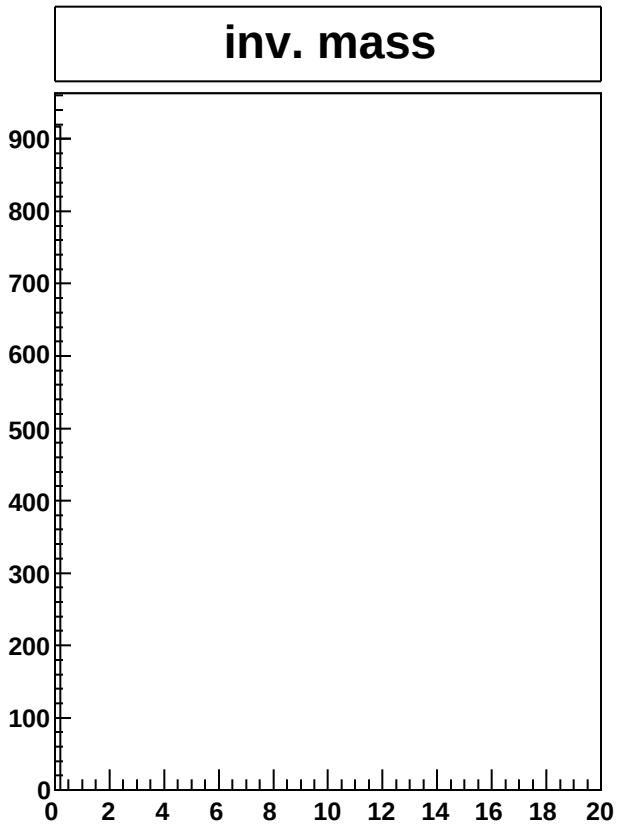
L2 upsilon#Countsυ#any

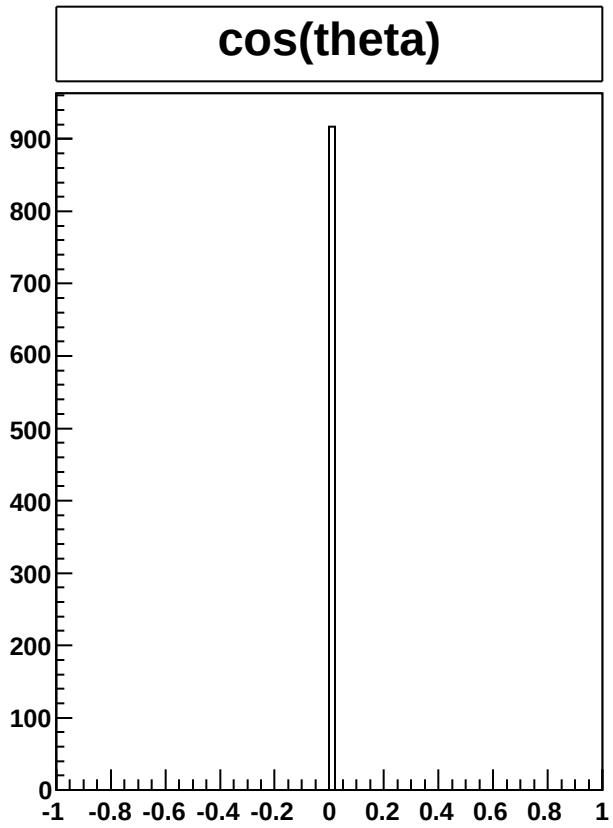


L2 upsilon#Massυ#any

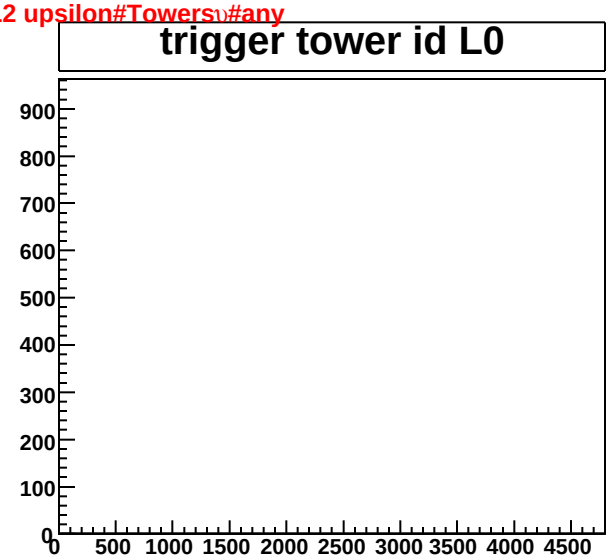


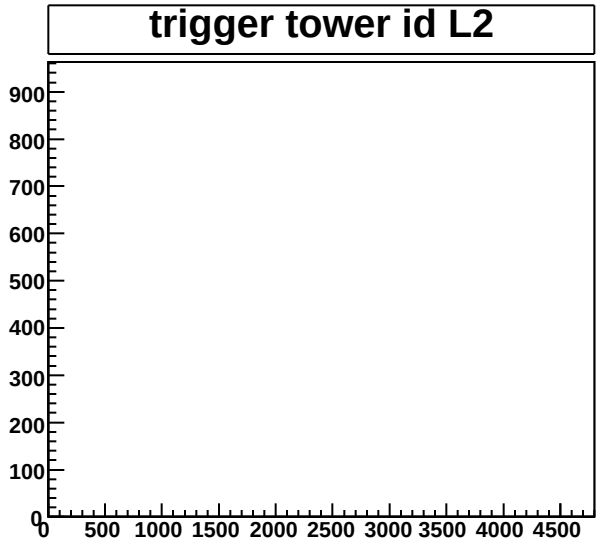


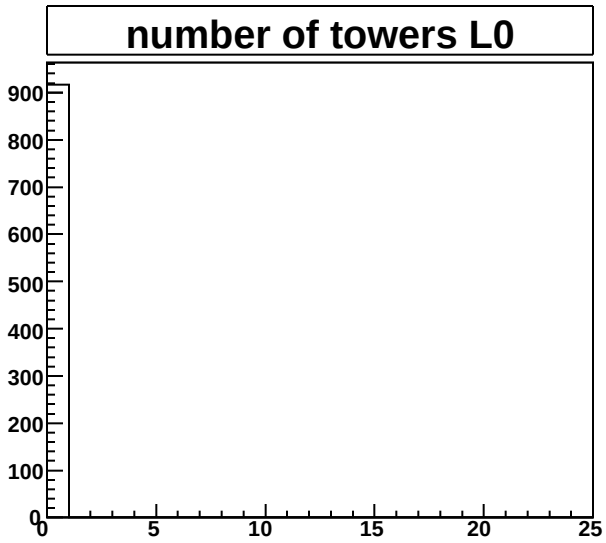


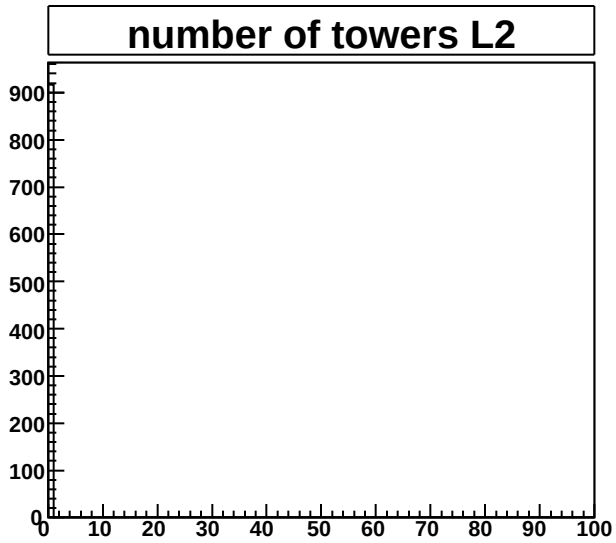


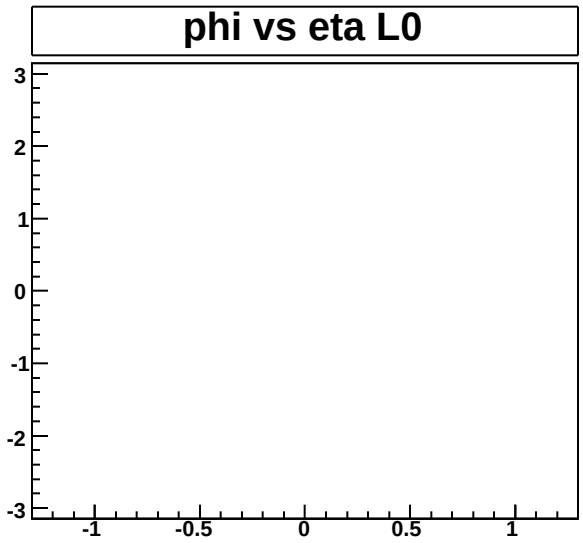
L2 upsilon#Towersυ#any

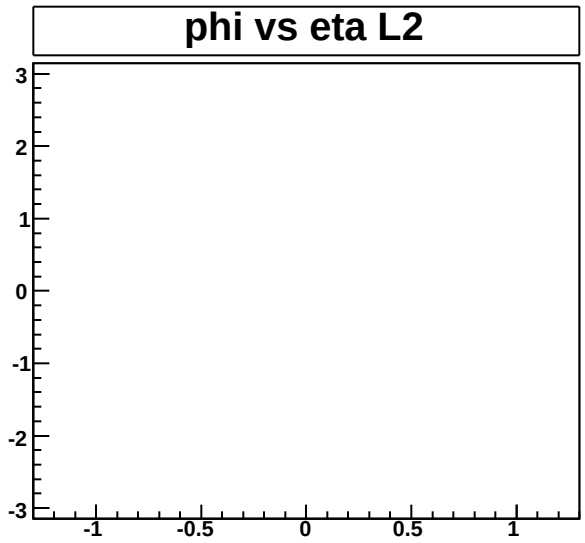






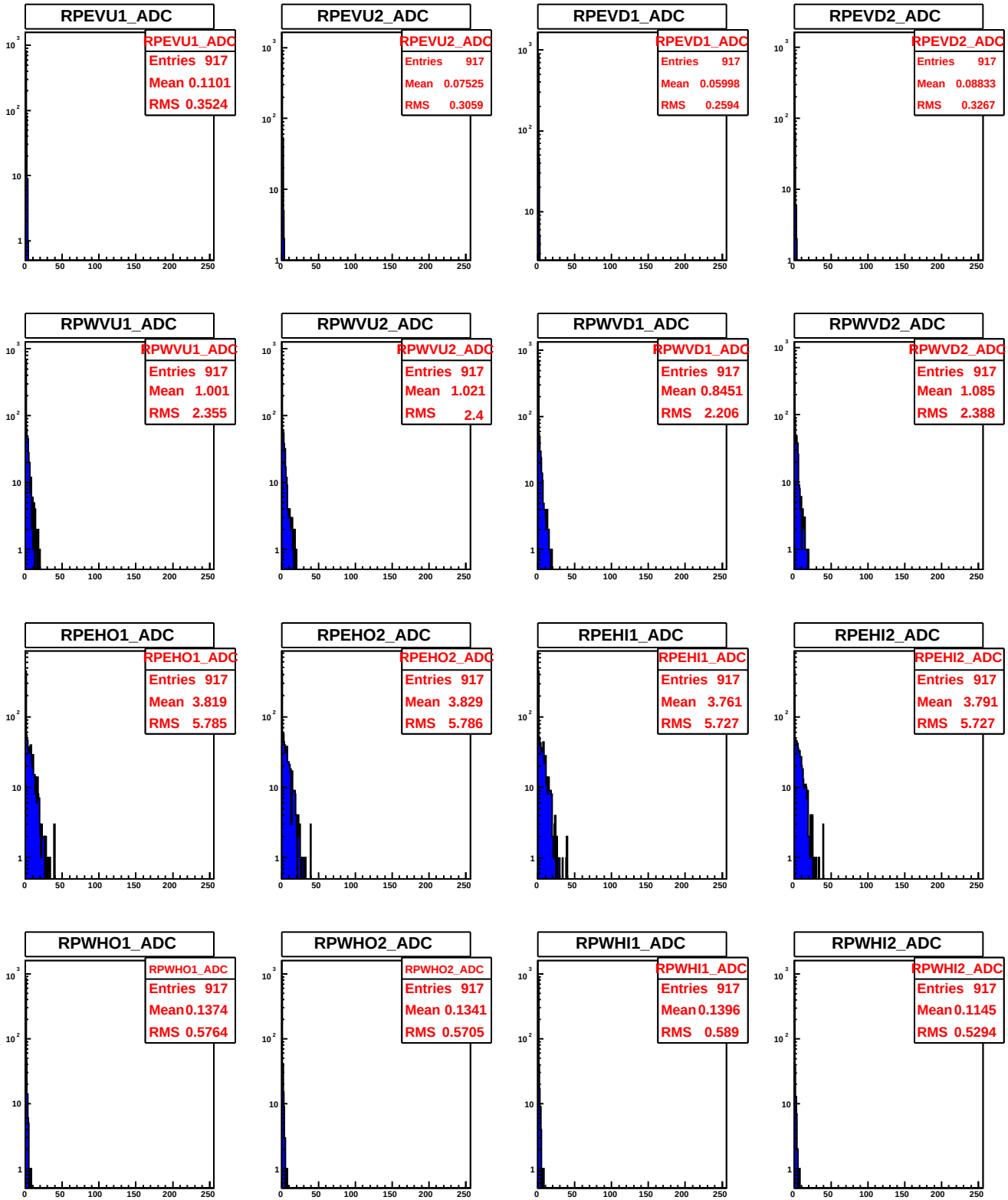






P2P#Trg1-16#any#trg

Run: #12147027



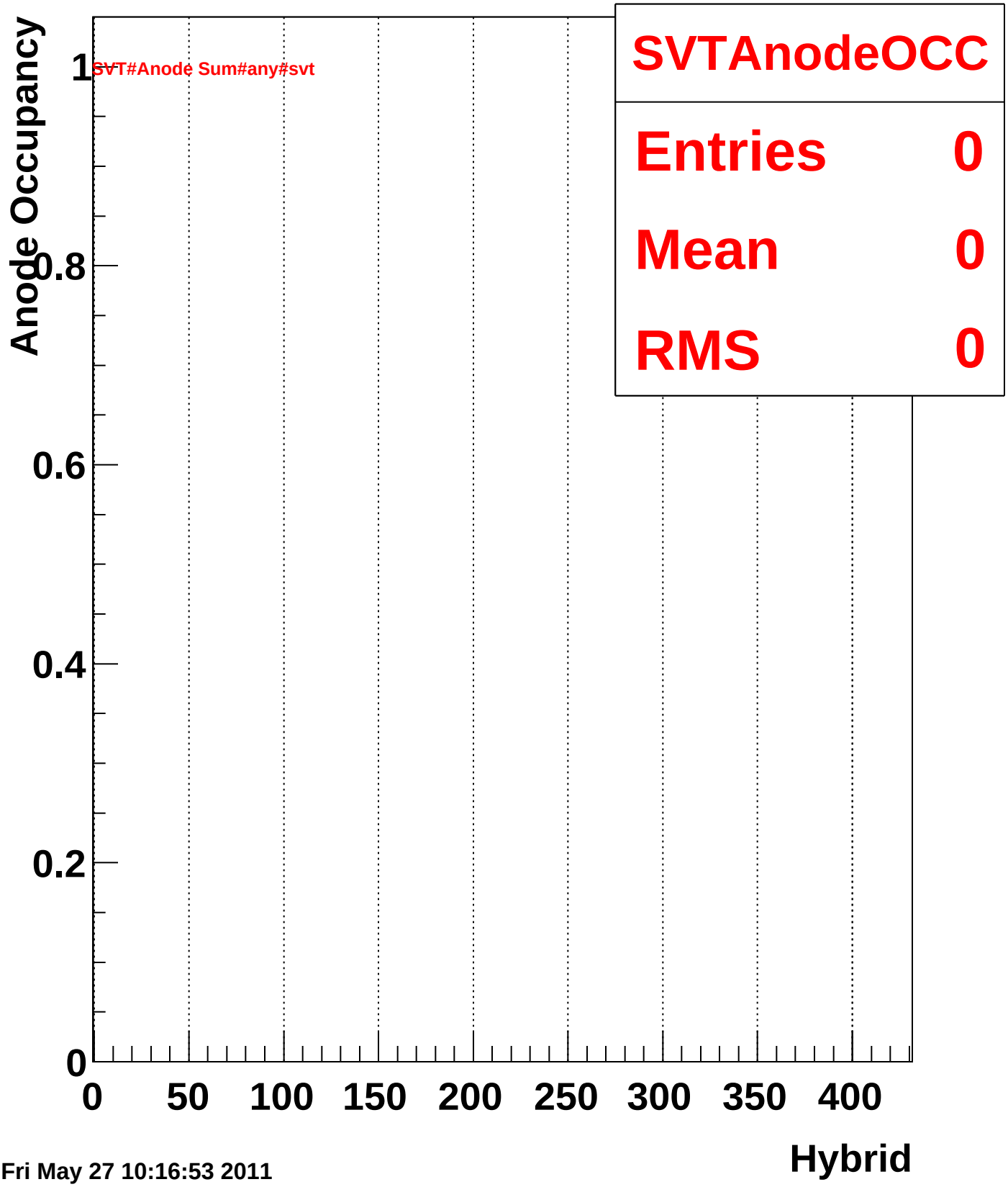
Fri May 27 10:16:53 2011

P2P#Trg17-32#any#trg

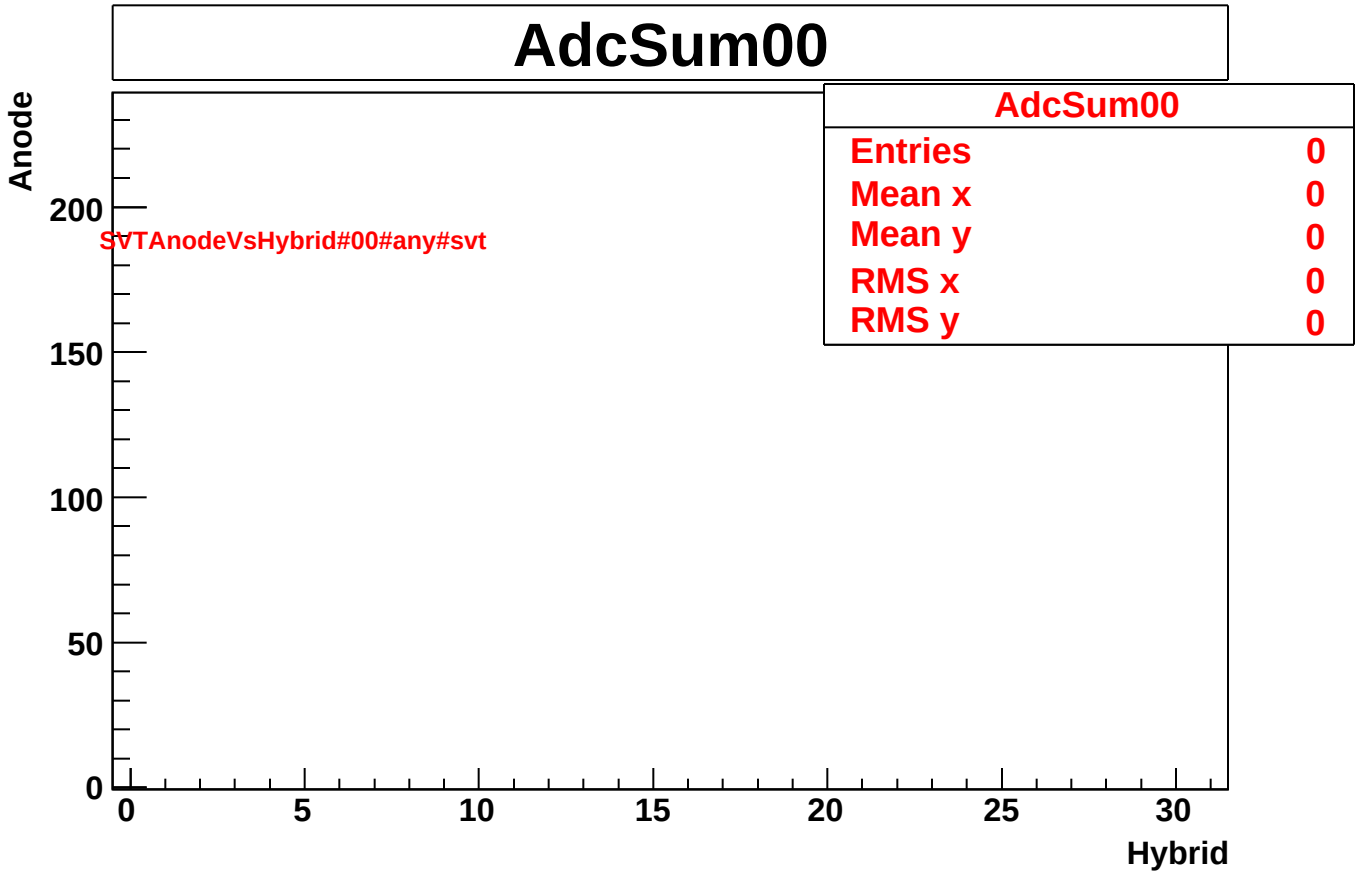
Run: #12147027

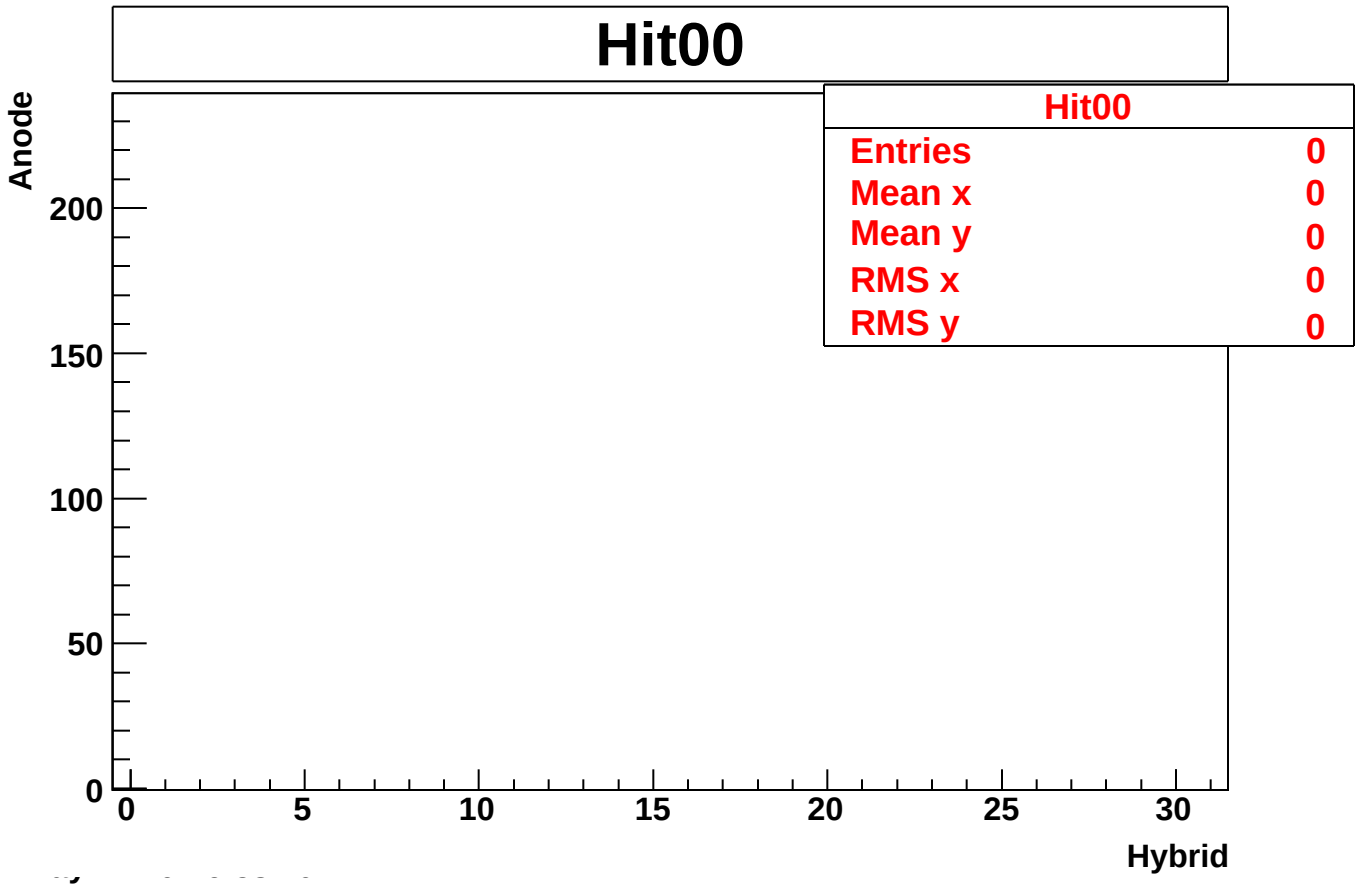
Fri May 27 10:16:53 2011

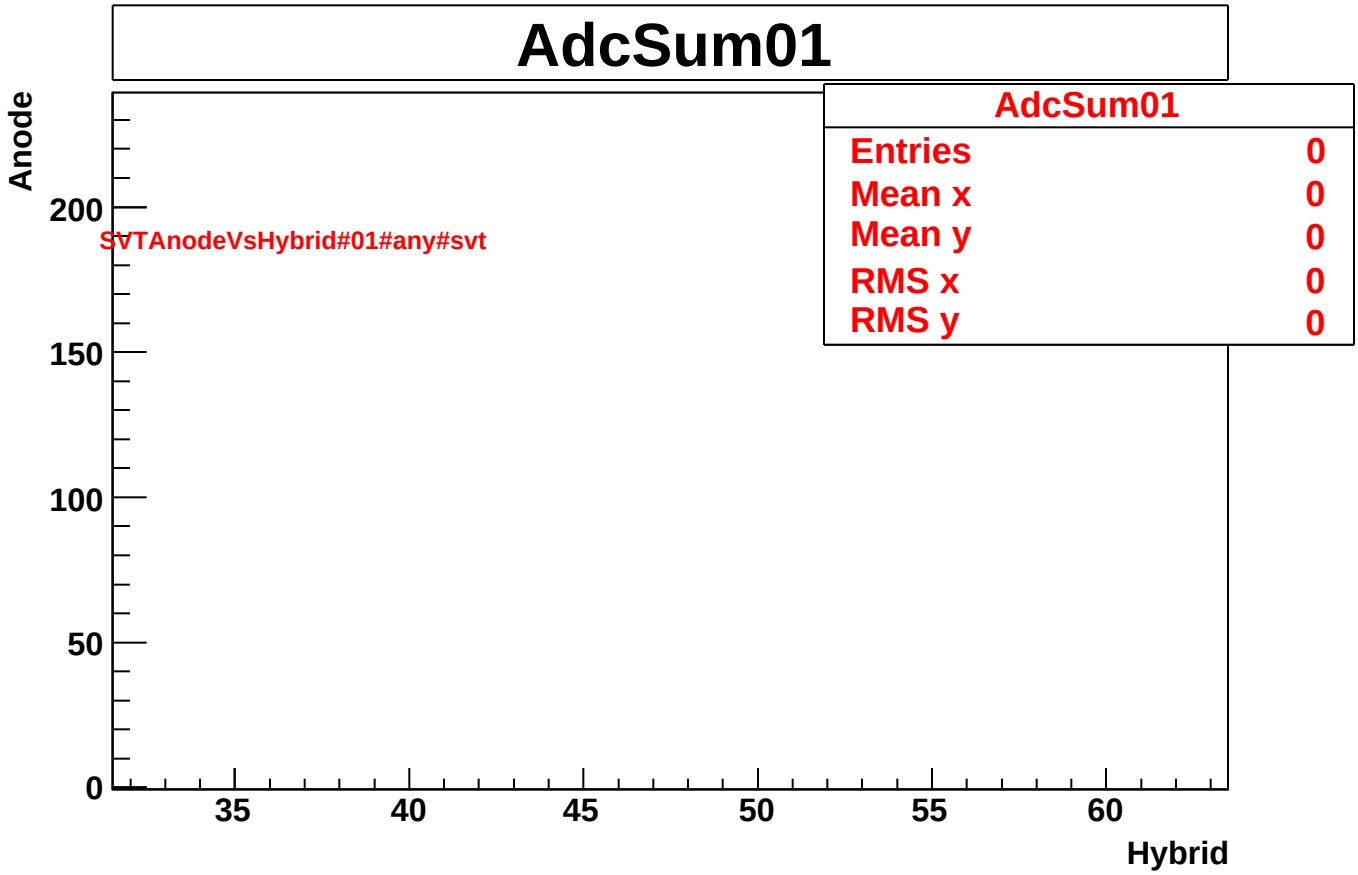
SVTAnodeOCC

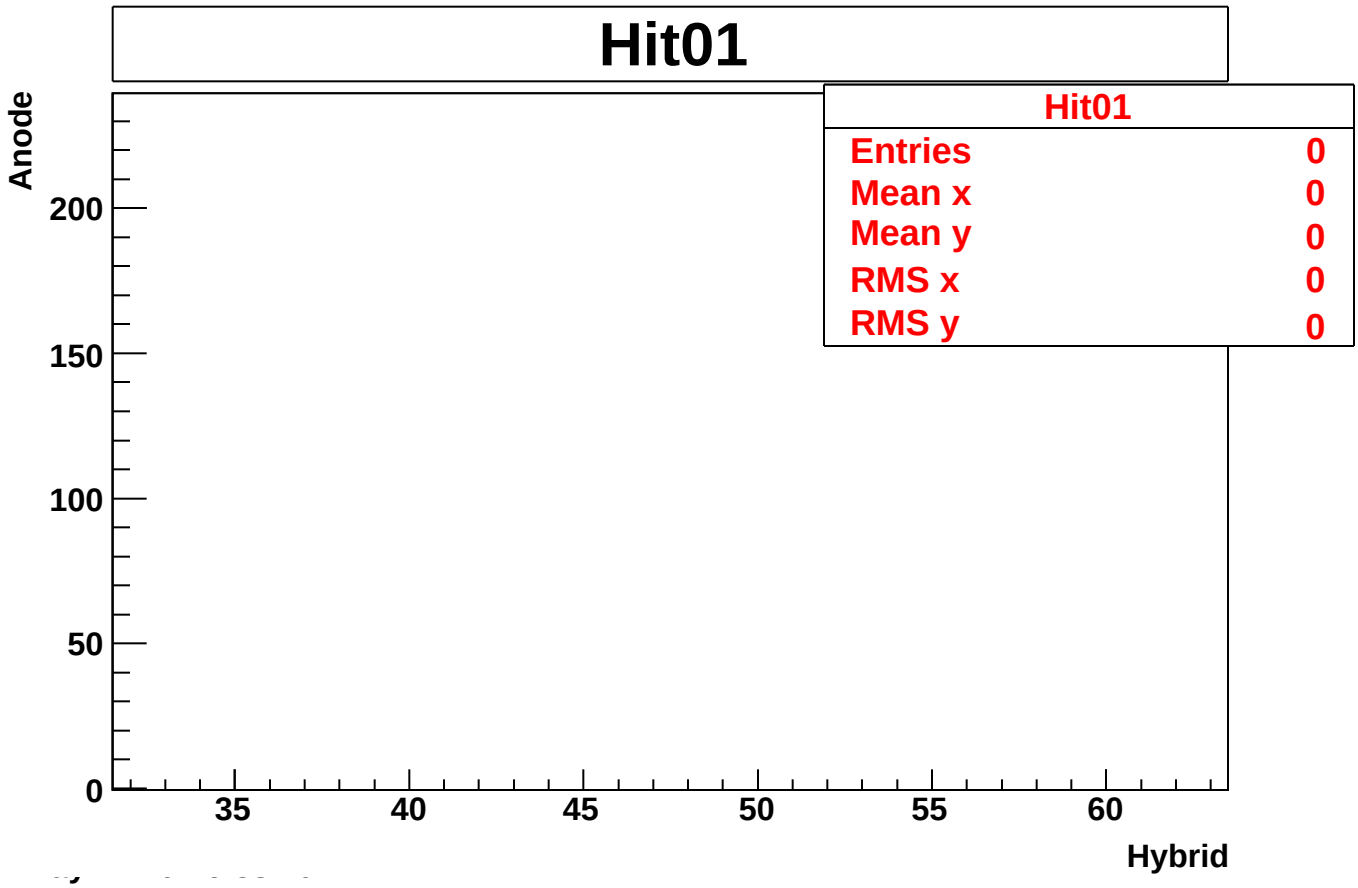


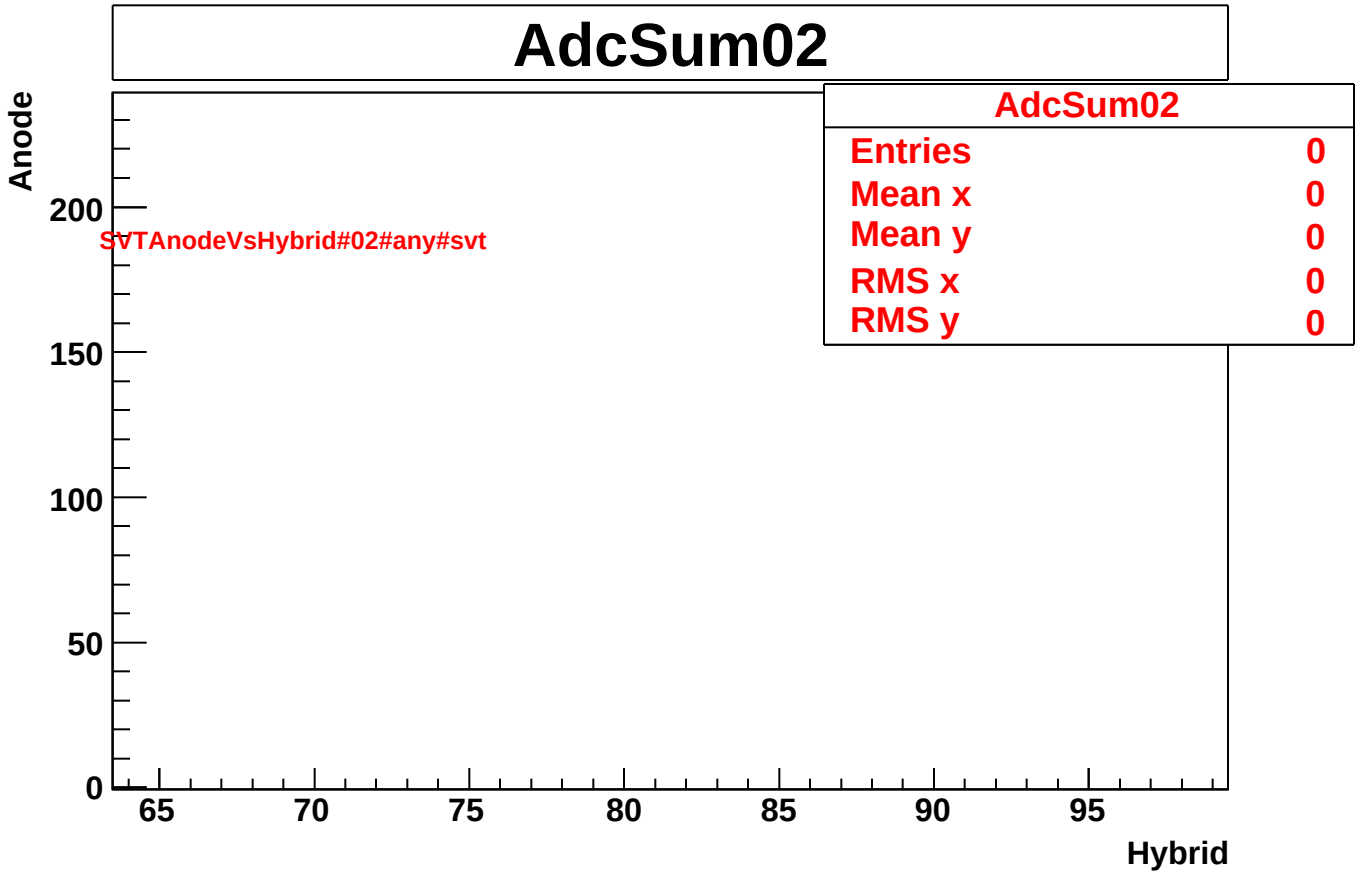
Fri May 27 10:16:53 2011

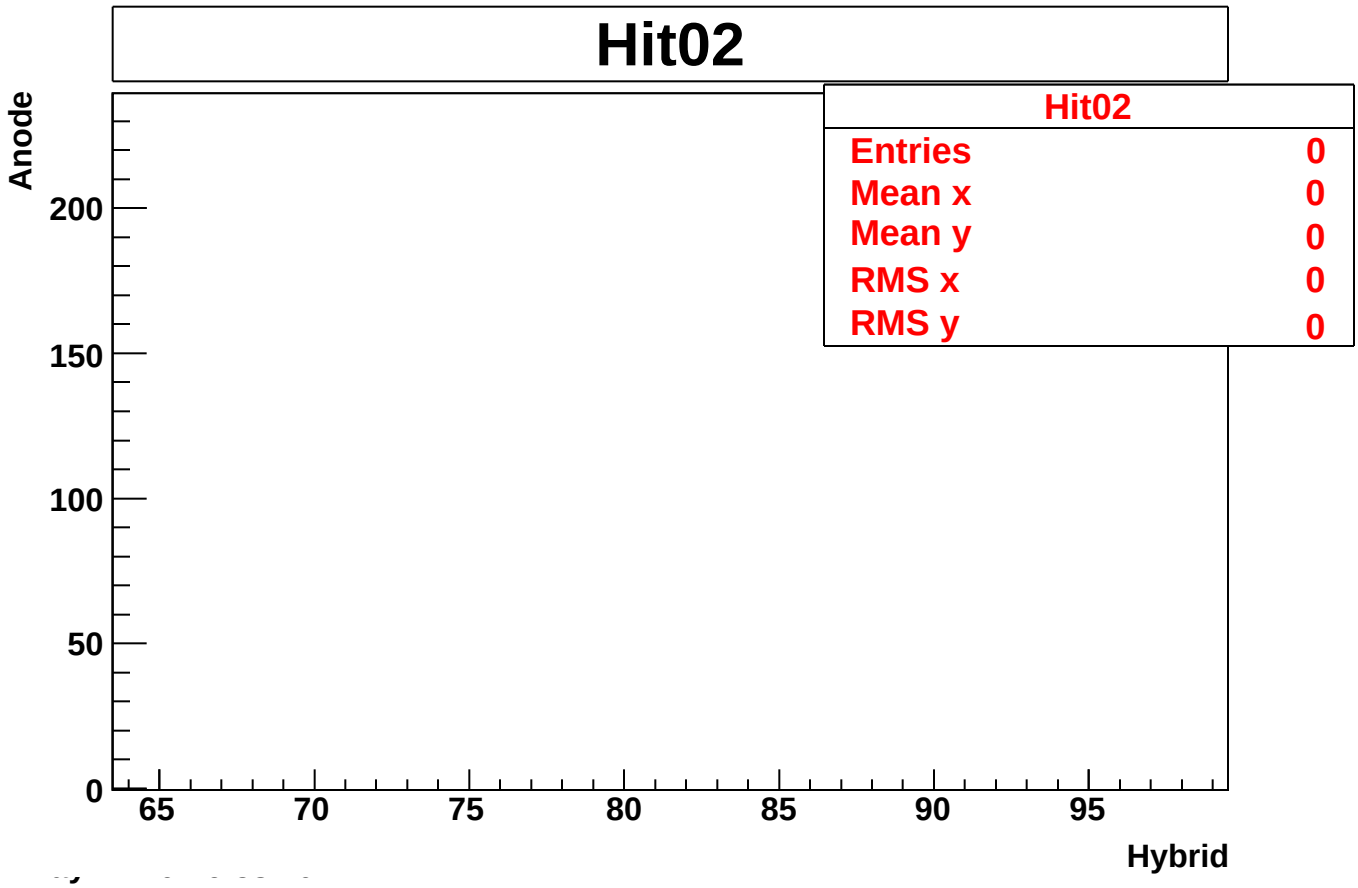

AdcSum00
AdcSum00
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#00#any#svt
Anode
0
50
100
150
200
0
5
10
15
20
25
30
Hybrid

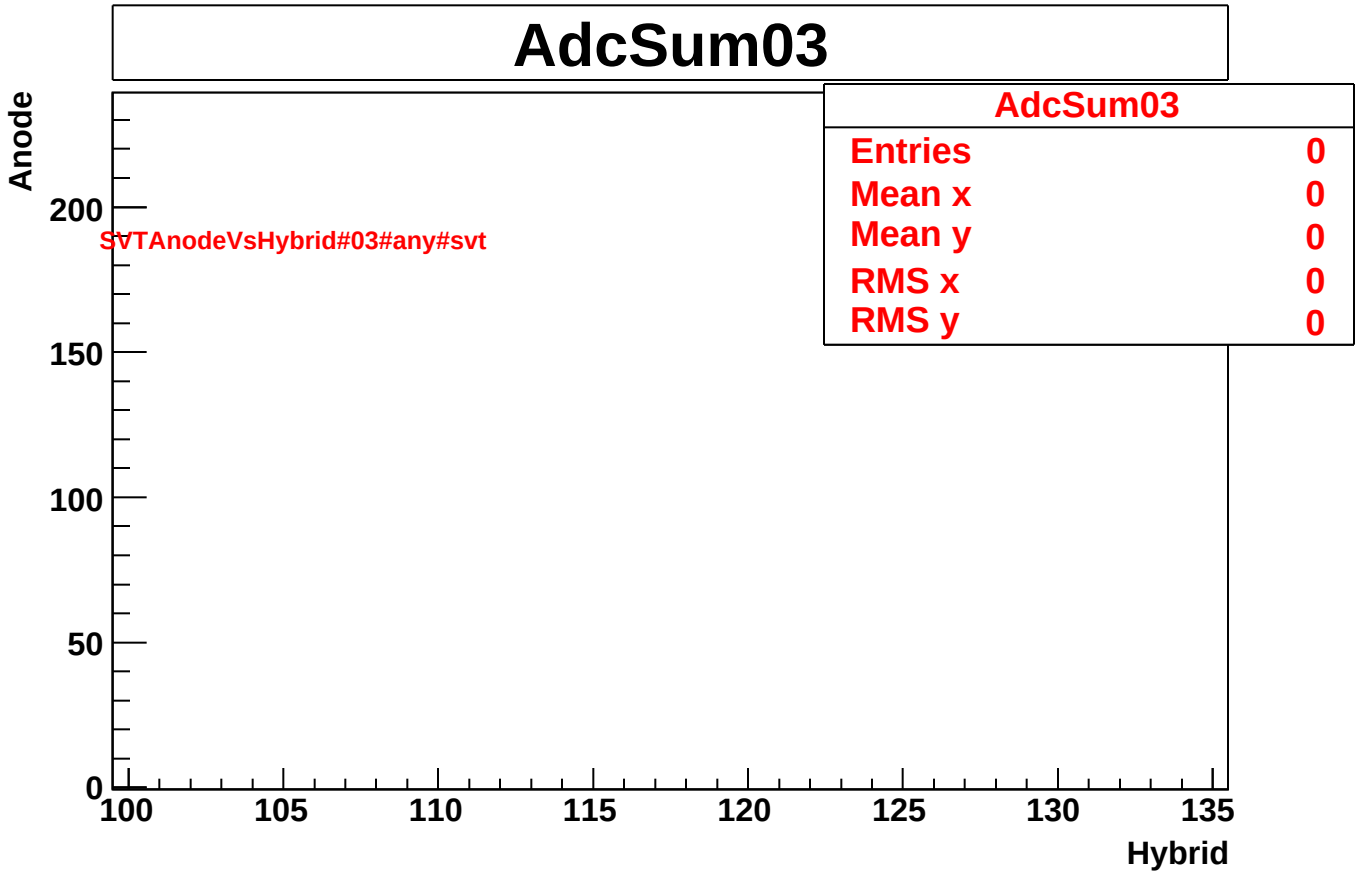

Hit00
Hit00
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
0
50
100
150
200
0
5
10
15
20
25
30
Hybrid

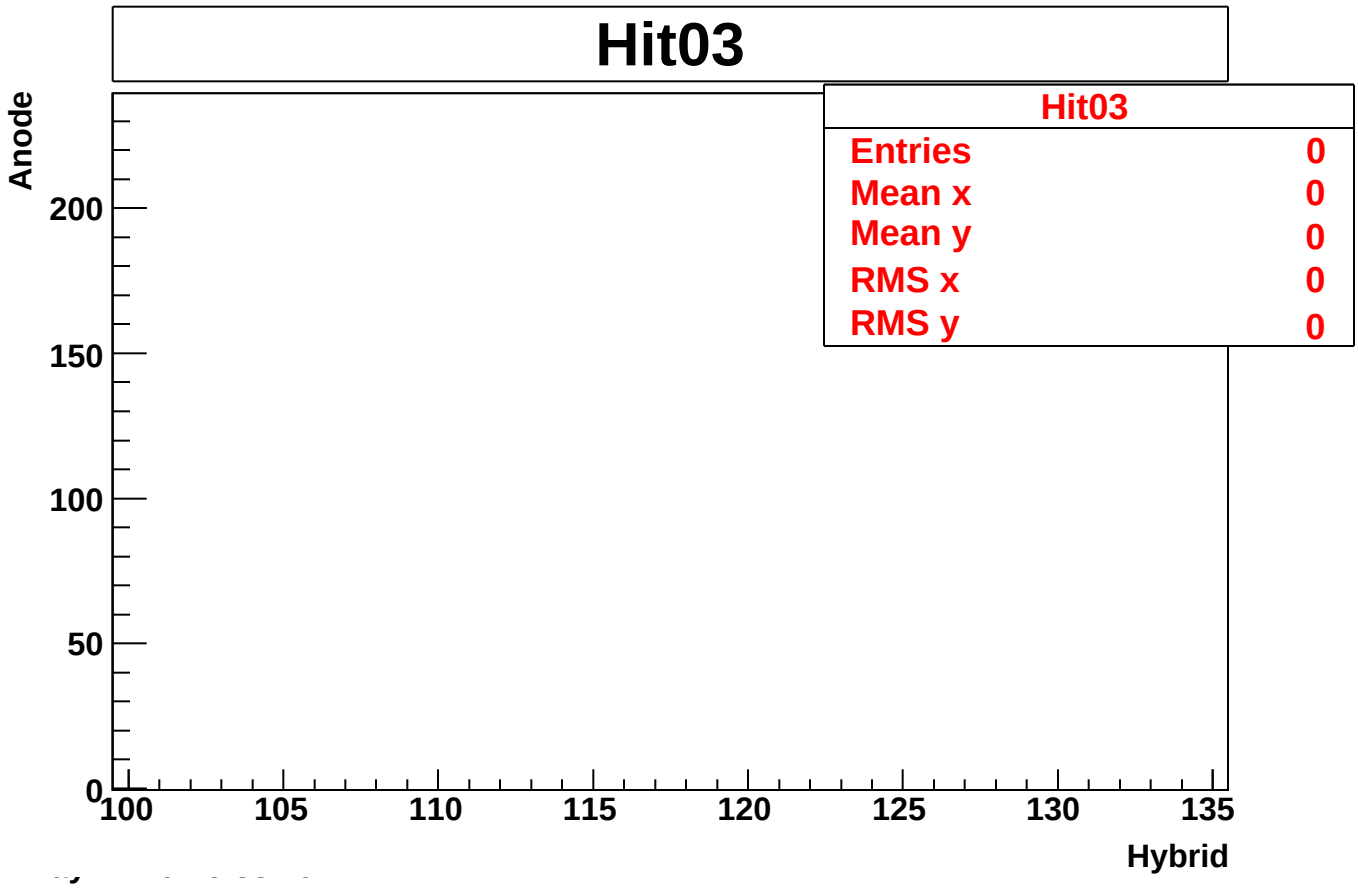

AdcSum01
AdcSum01
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#01#any#svt
Anode
200
150
100
50
0
35
40
45
50
55
60
Hybrid

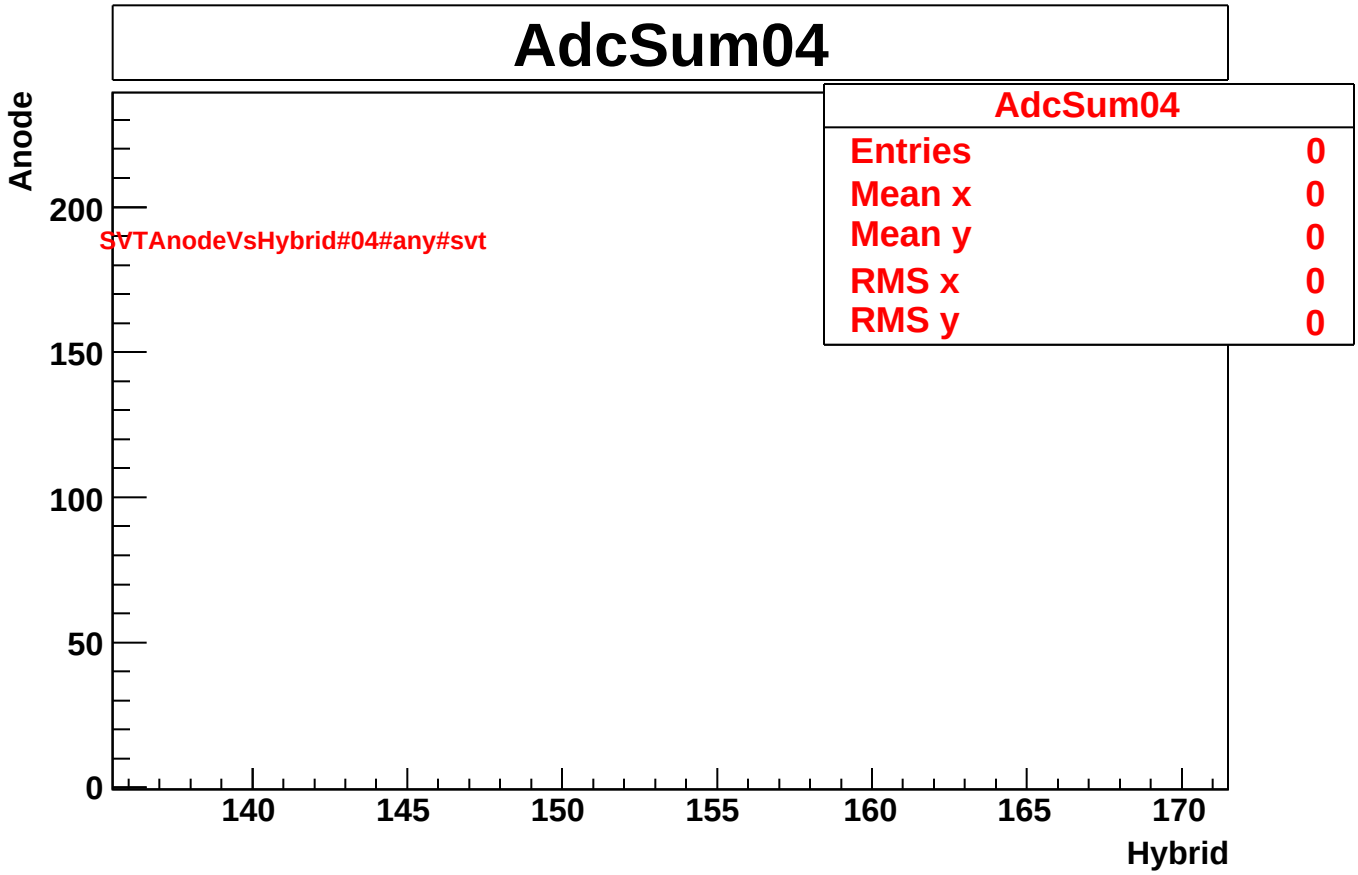

Hit01
Hit01
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
200
150
100
50
0
35
40
45
50
55
60
Hybrid

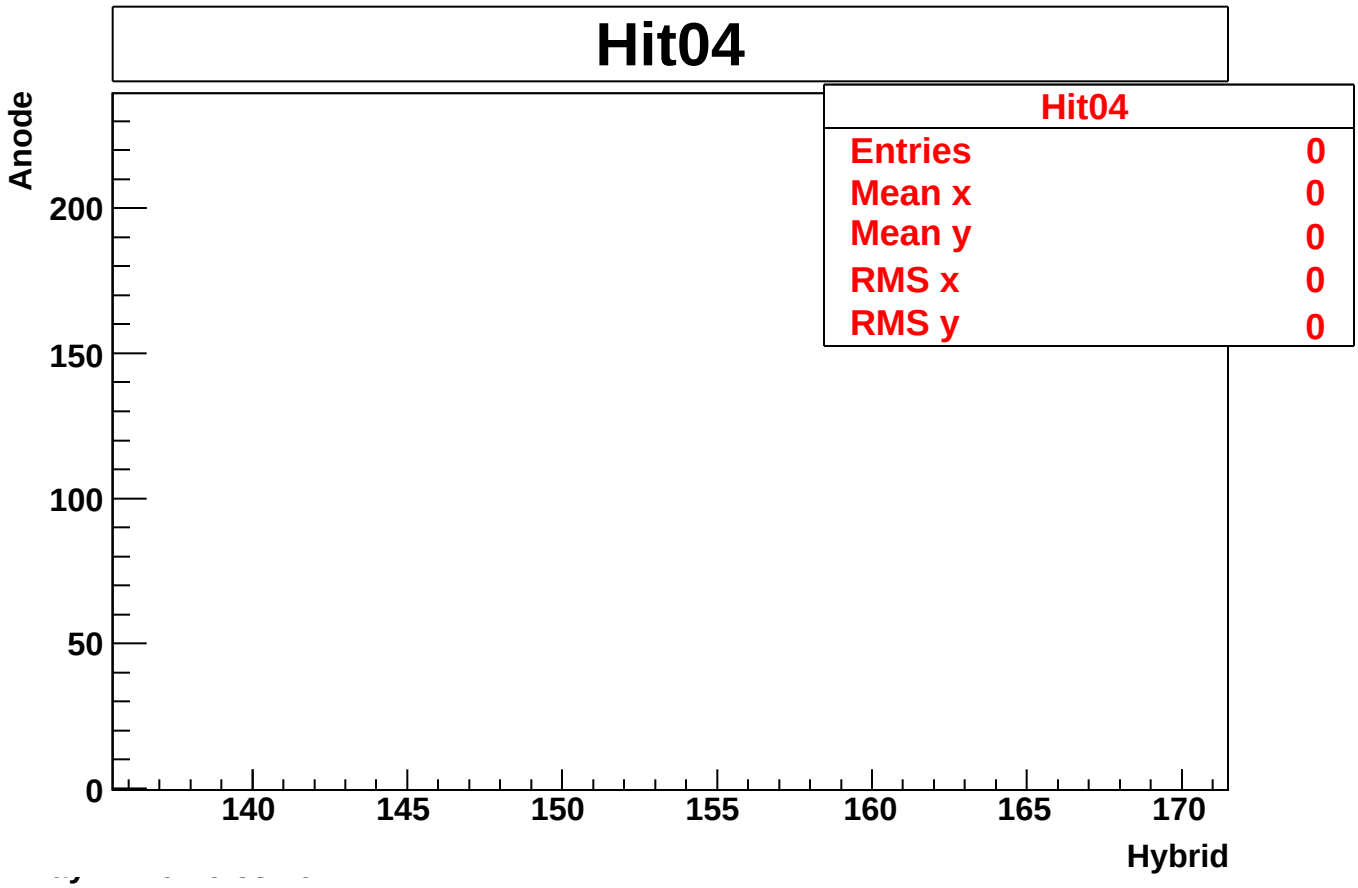

AdcSum02
AdcSum02
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#02#any#svt
Anode
200
150
100
50
0
65
70
75
80
85
90
95
Hybrid

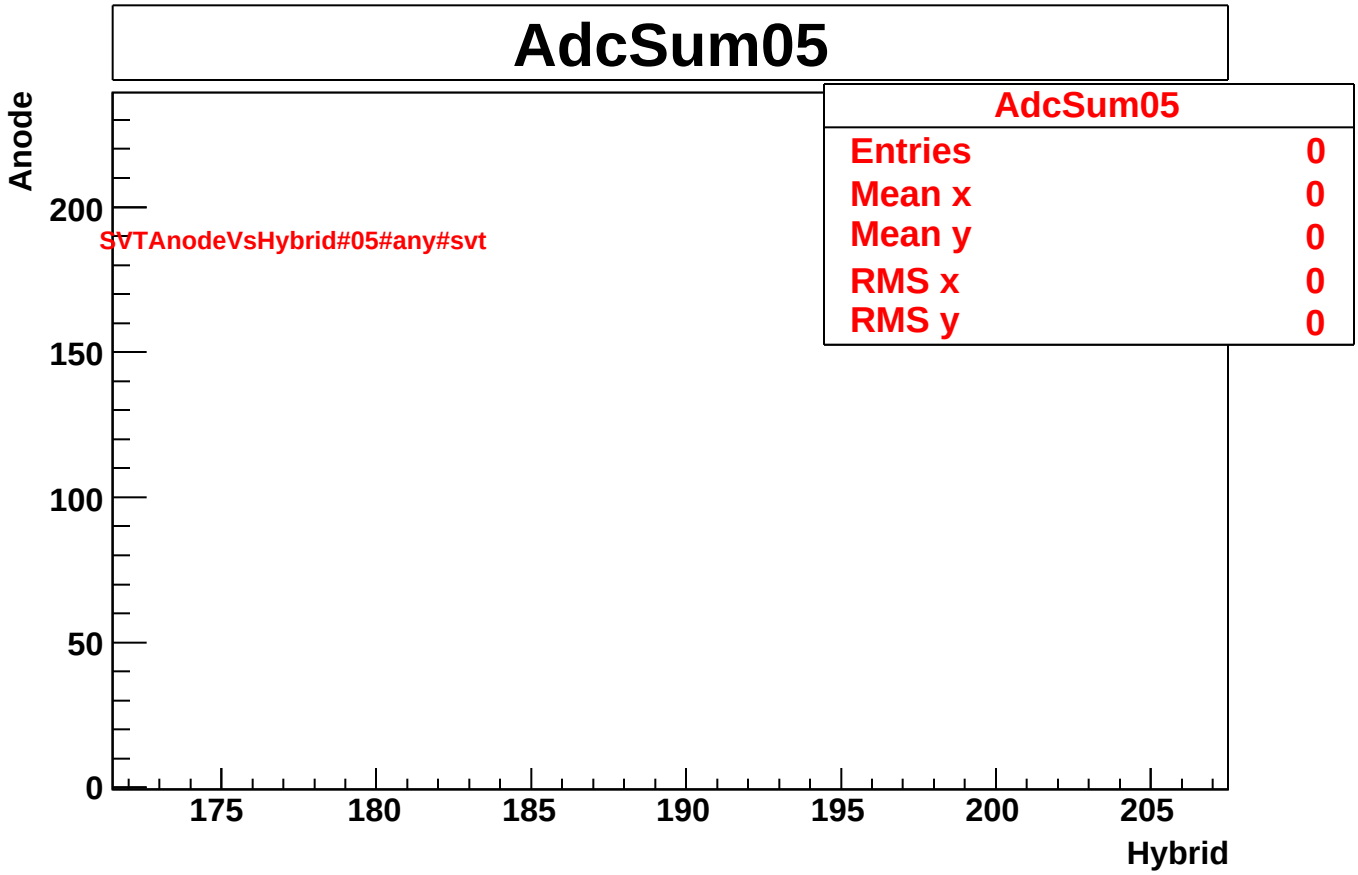

Hit02
Hit02
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
200
150
100
50
0
65
70
75
80
85
90
95
Hybrid

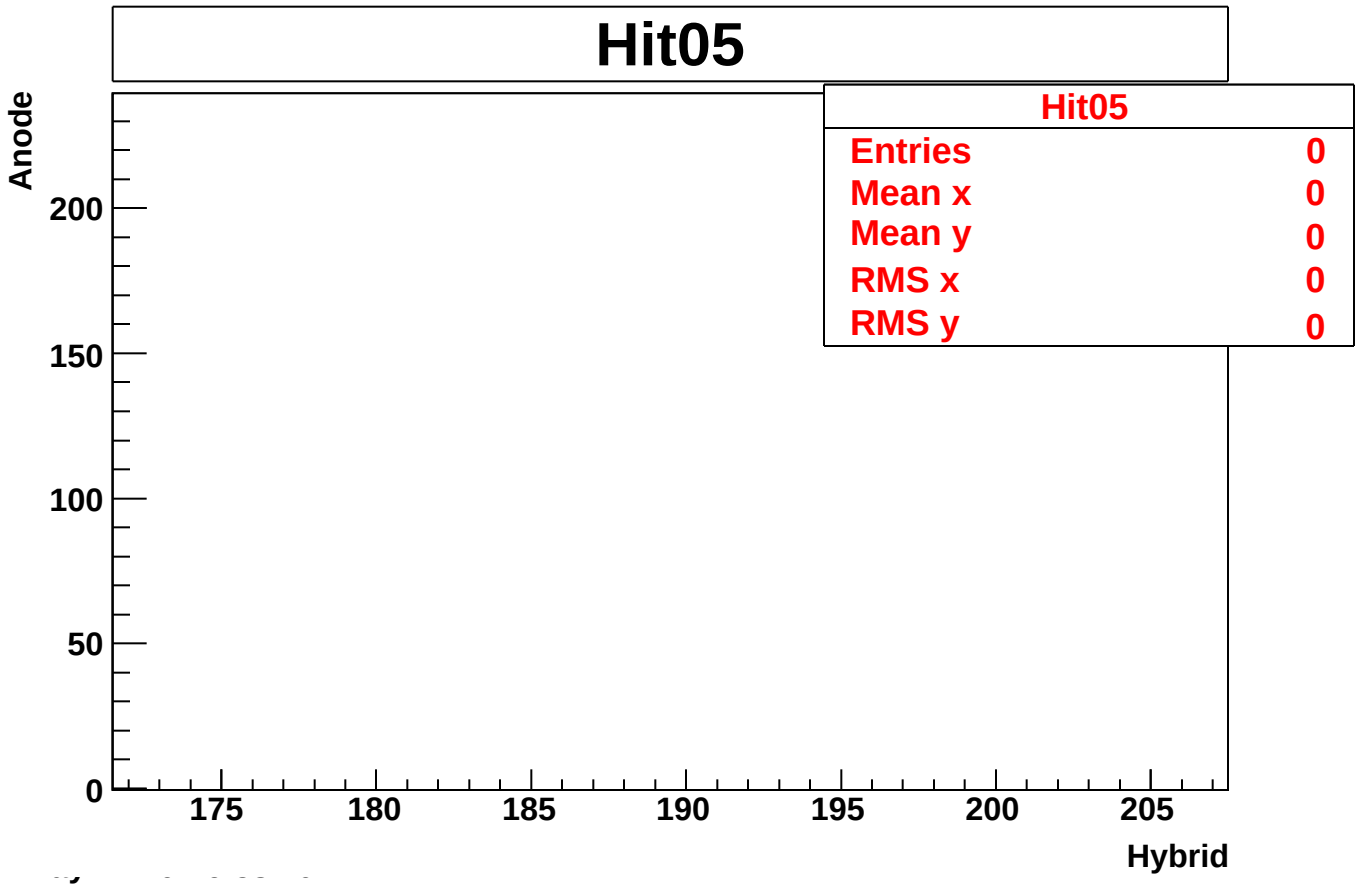

AdcSum03
AdcSum03
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#03#any#svt
Anode
0
50
100
150
200
100
105
110
115
120
125
130
135
Hybrid

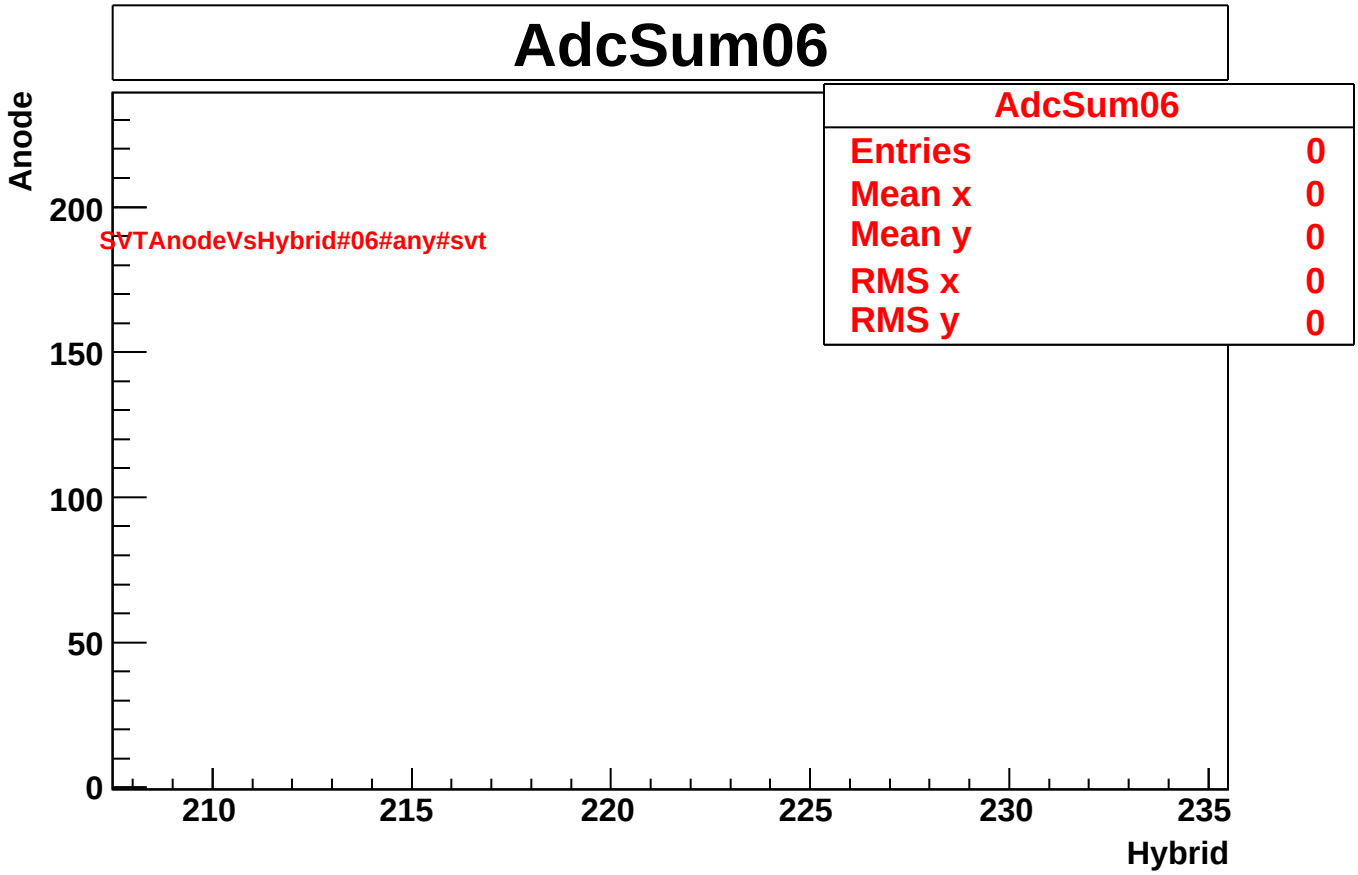

Hit03
Hit03
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
0
50
100
150
200
100
105
110
115
120
125
130
135
Hybrid

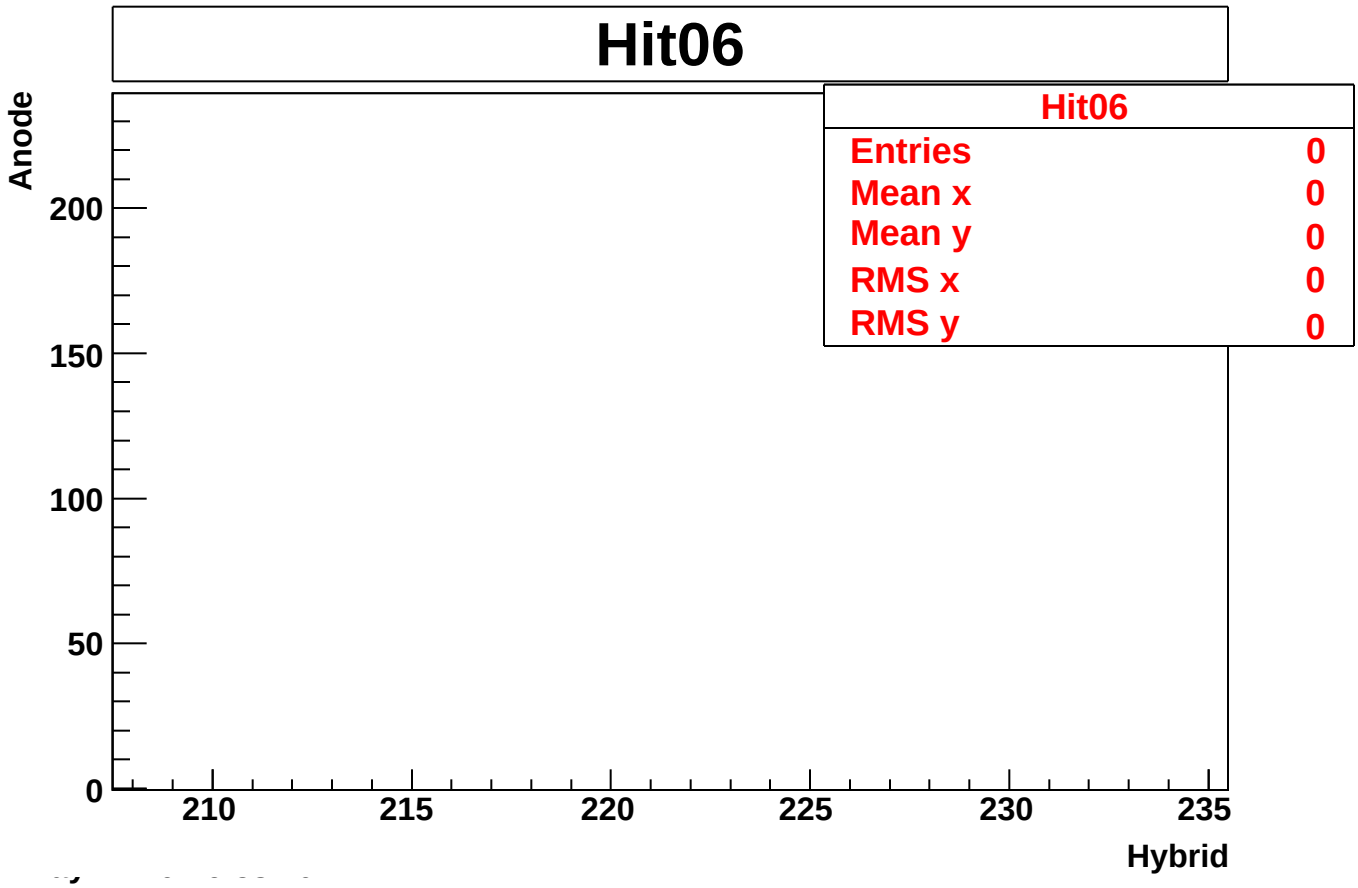

AdcSum04
AdcSum04
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#04#any#svt
Anode
200
150
100
50
0
140
145
150
155
160
165
170
Hybrid

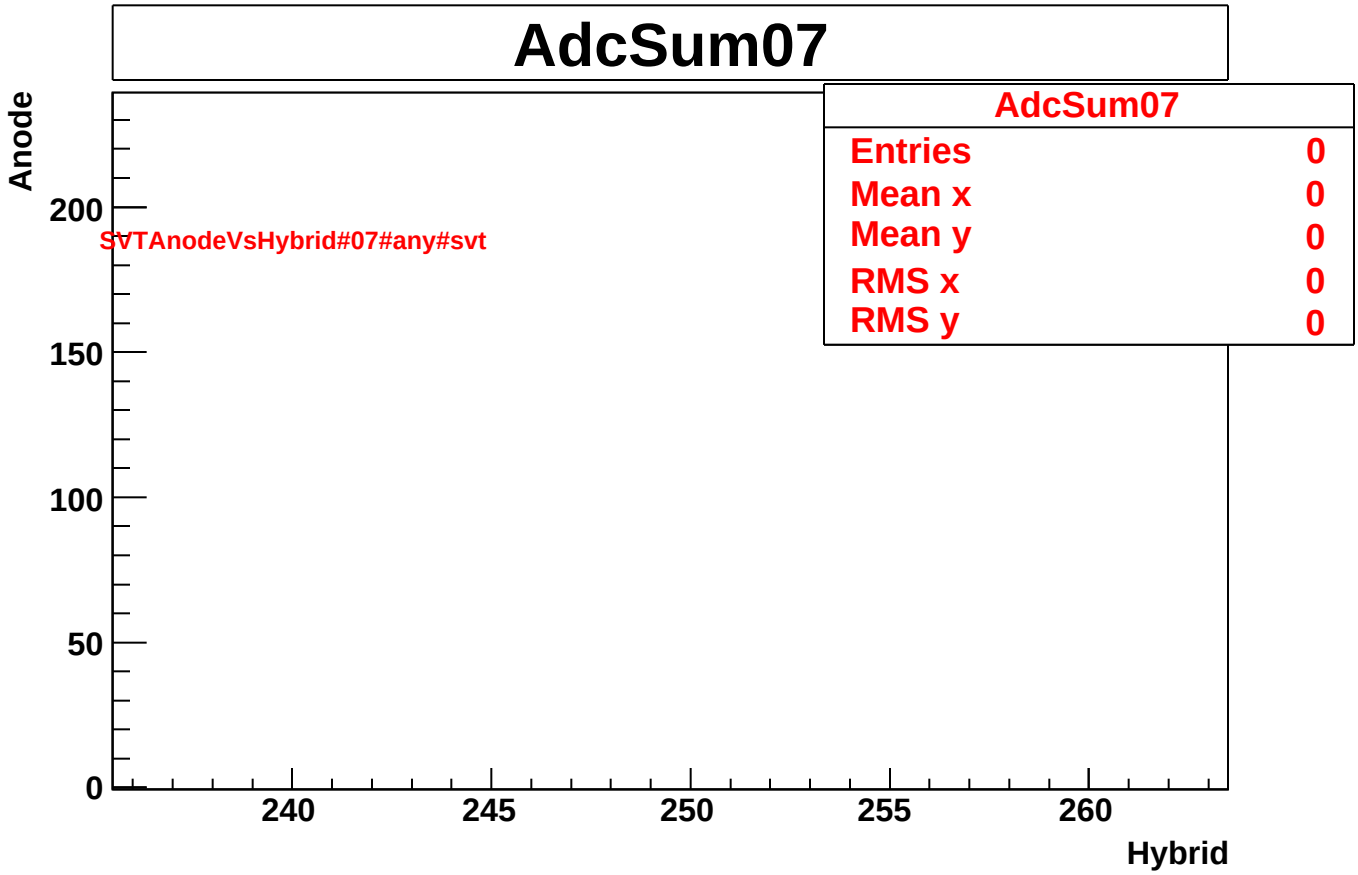

Hit04
Hit04
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
200
150
100
50
0
140
145
150
155
160
165
170
Hybrid

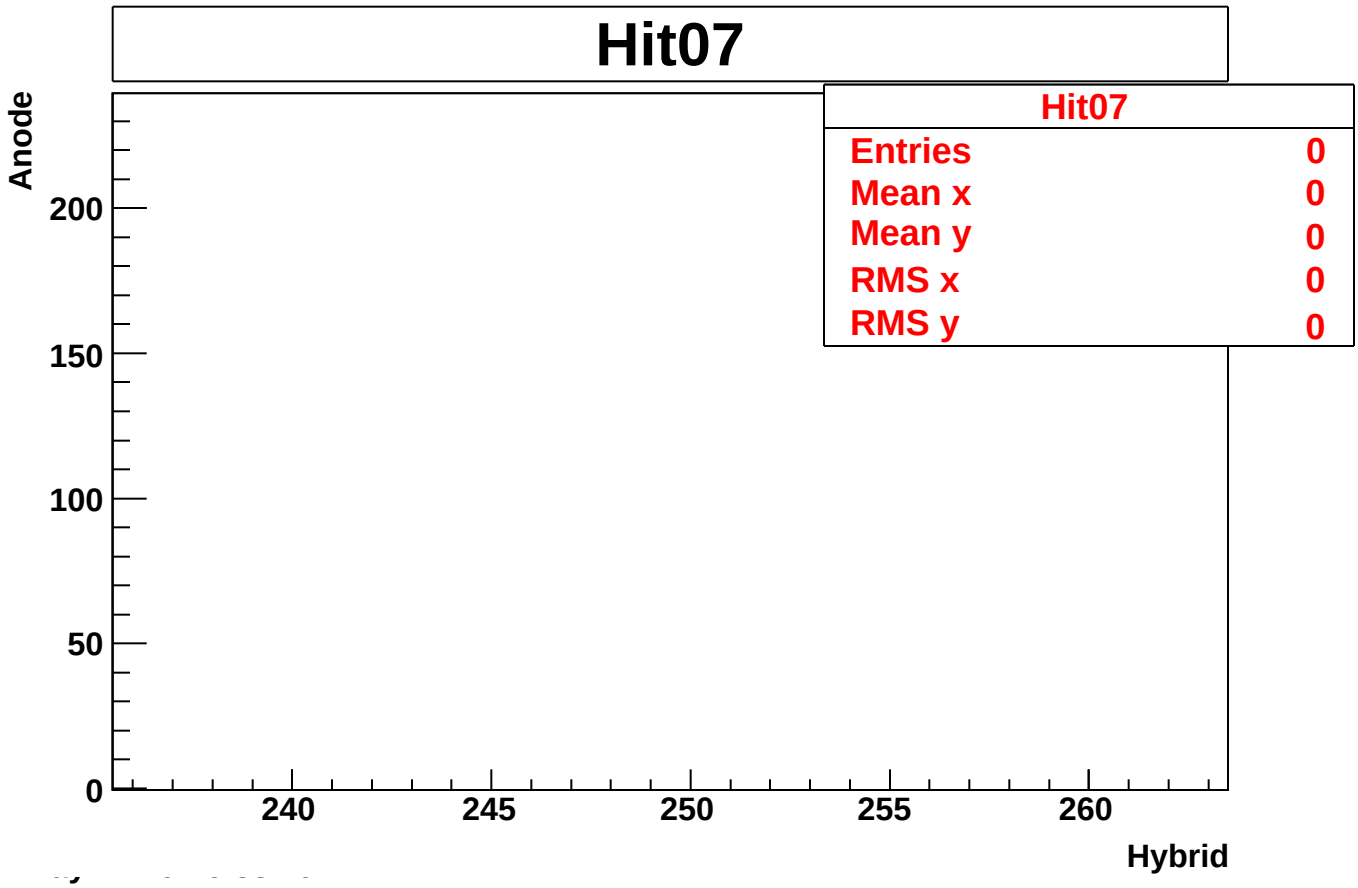

AdcSum05
AdcSum05
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#05#any#svt
Anode
0
50
100
150
200
175
180
185
190
195
200
205
Hybrid

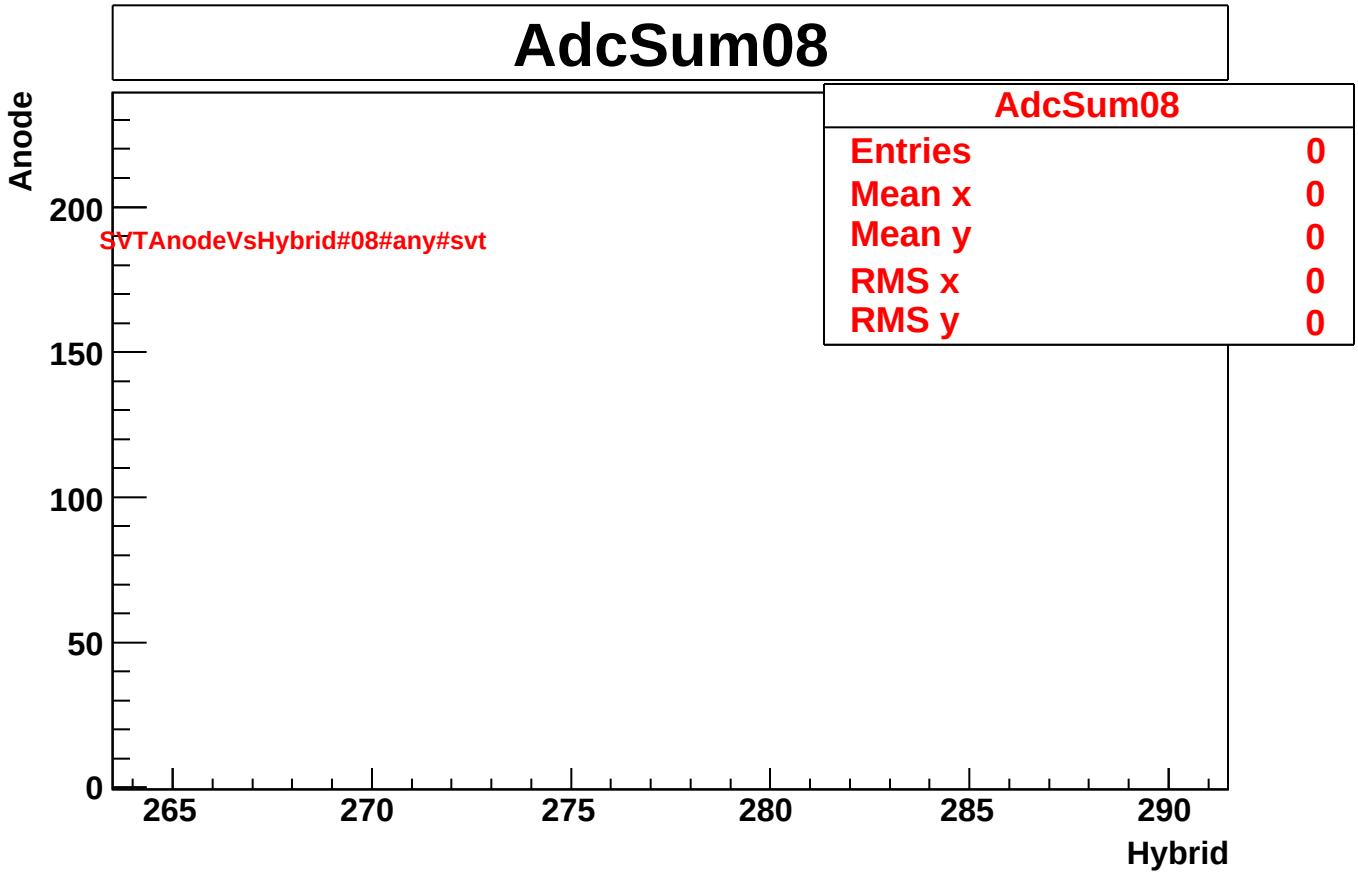

Hit05
Hit05
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
0
50
100
150
200
175
180
185
190
195
200
205
Hybrid

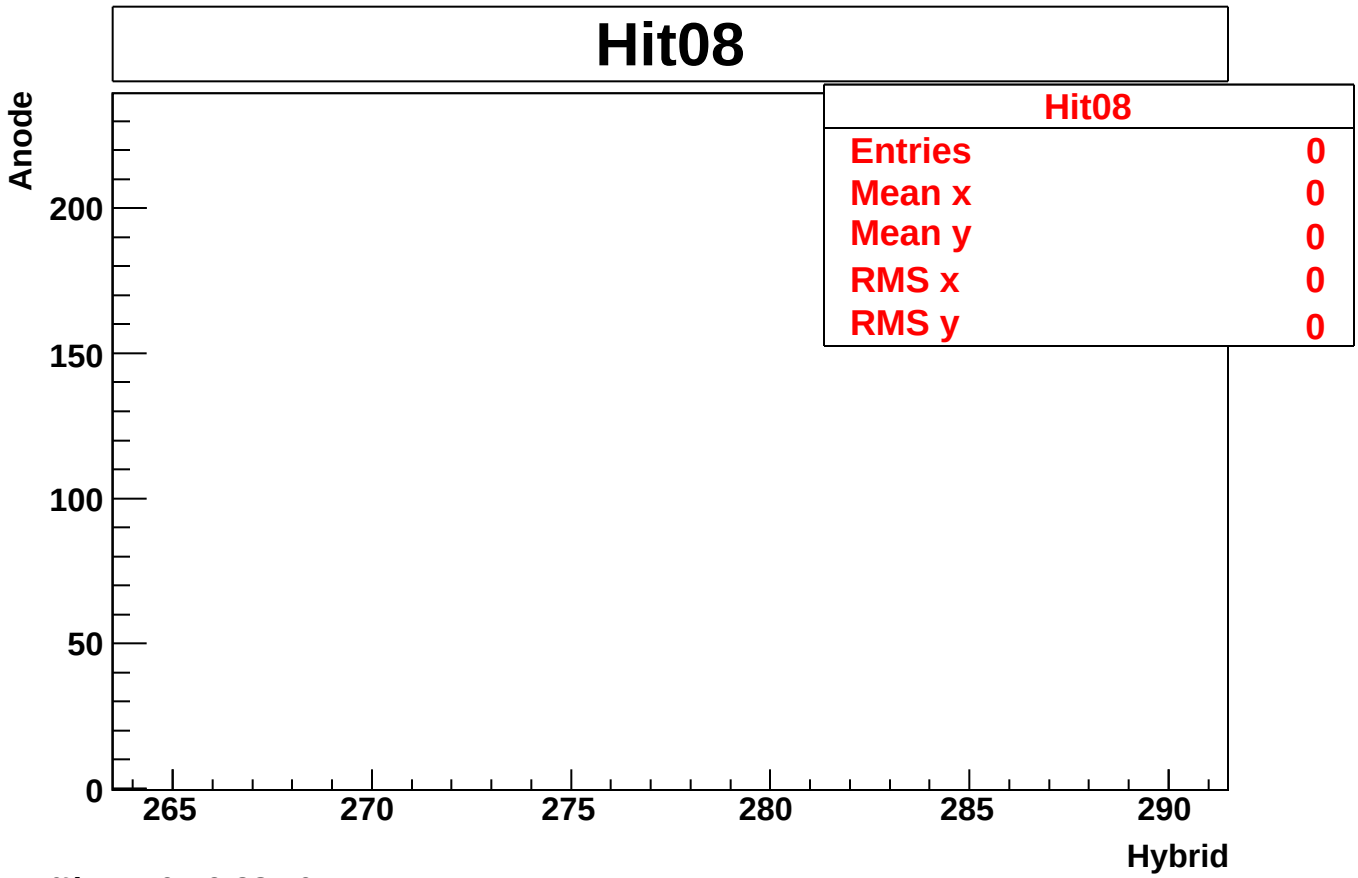

AdcSum06
AdcSum06
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#06#any#svt
Anode
0
50
100
150
200
210
215
220
225
230
235
Hybrid

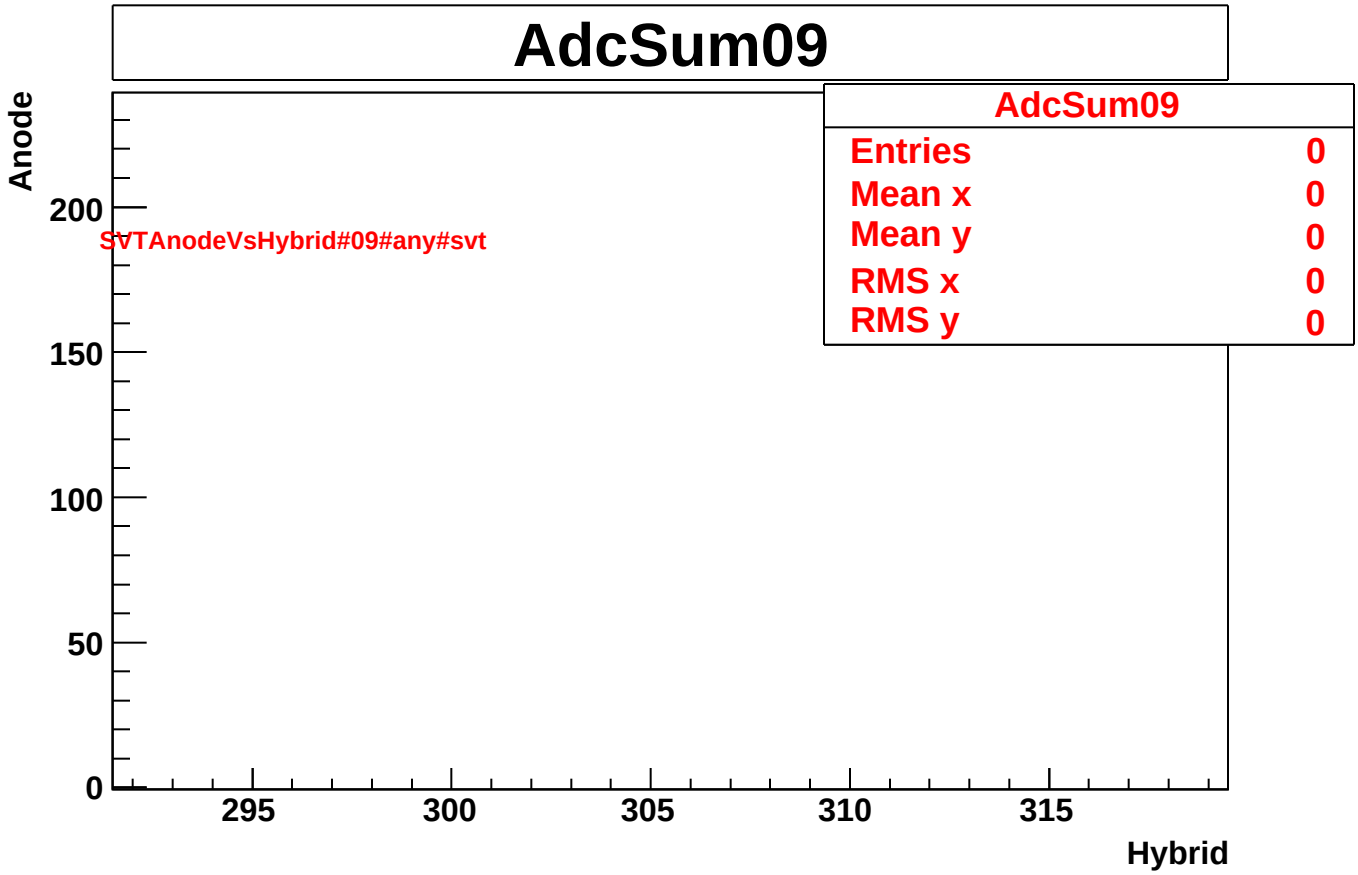

Hit06
Hit06
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
0
50
100
150
200
210
215
220
225
230
235
Hybrid

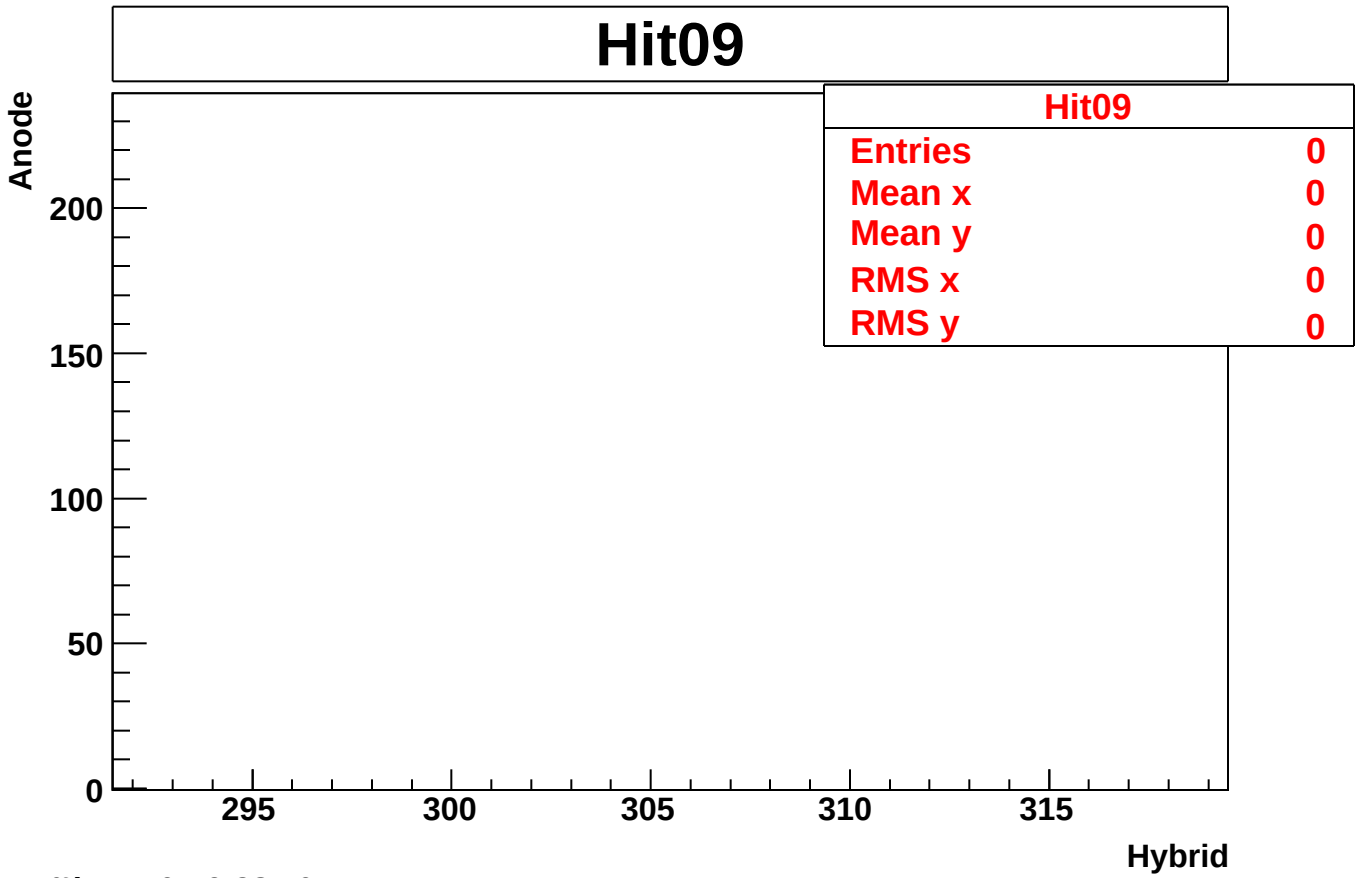

AdcSum07
AdcSum07
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#07#any#svt
Anode
200
150
100
50
0
240
245
250
255
260
Hybrid

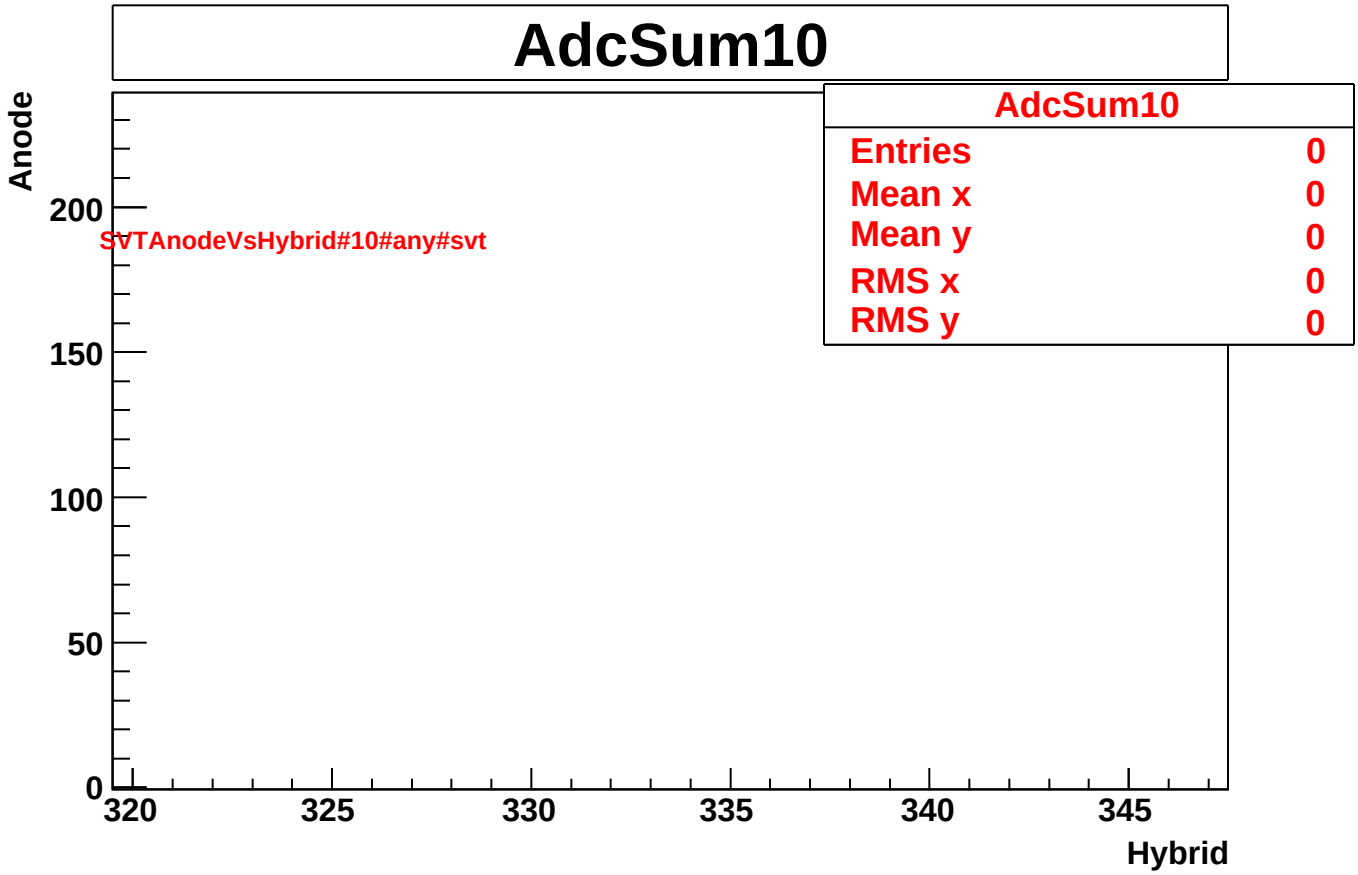

Hit07
Hit07
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
200
150
100
50
0
240
245
250
255
260
Hybrid

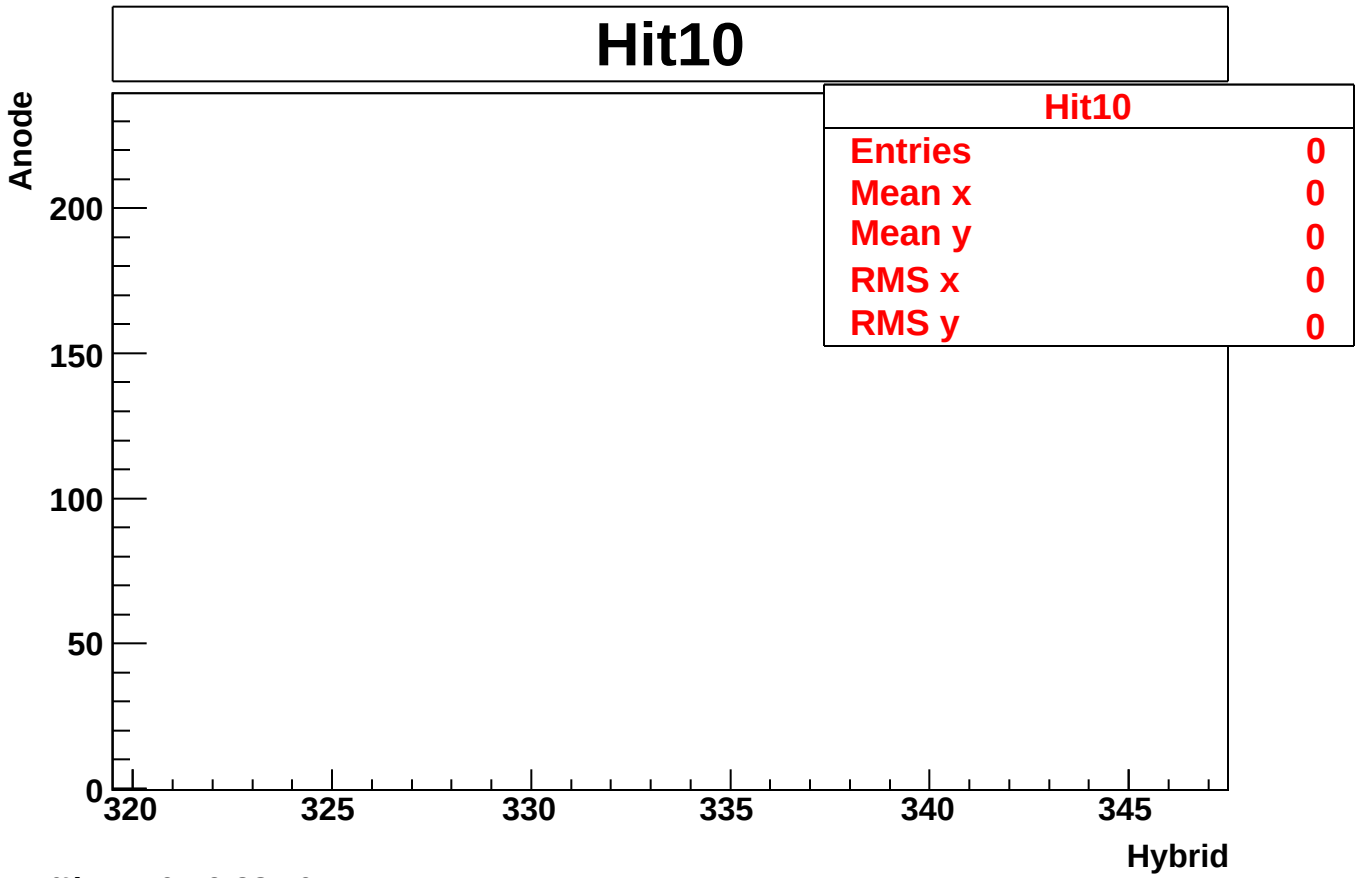

AdcSum08
AdcSum08
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#08#any#svt
Anode
0
50
100
150
200
265
270
275
280
285
290
Hybrid

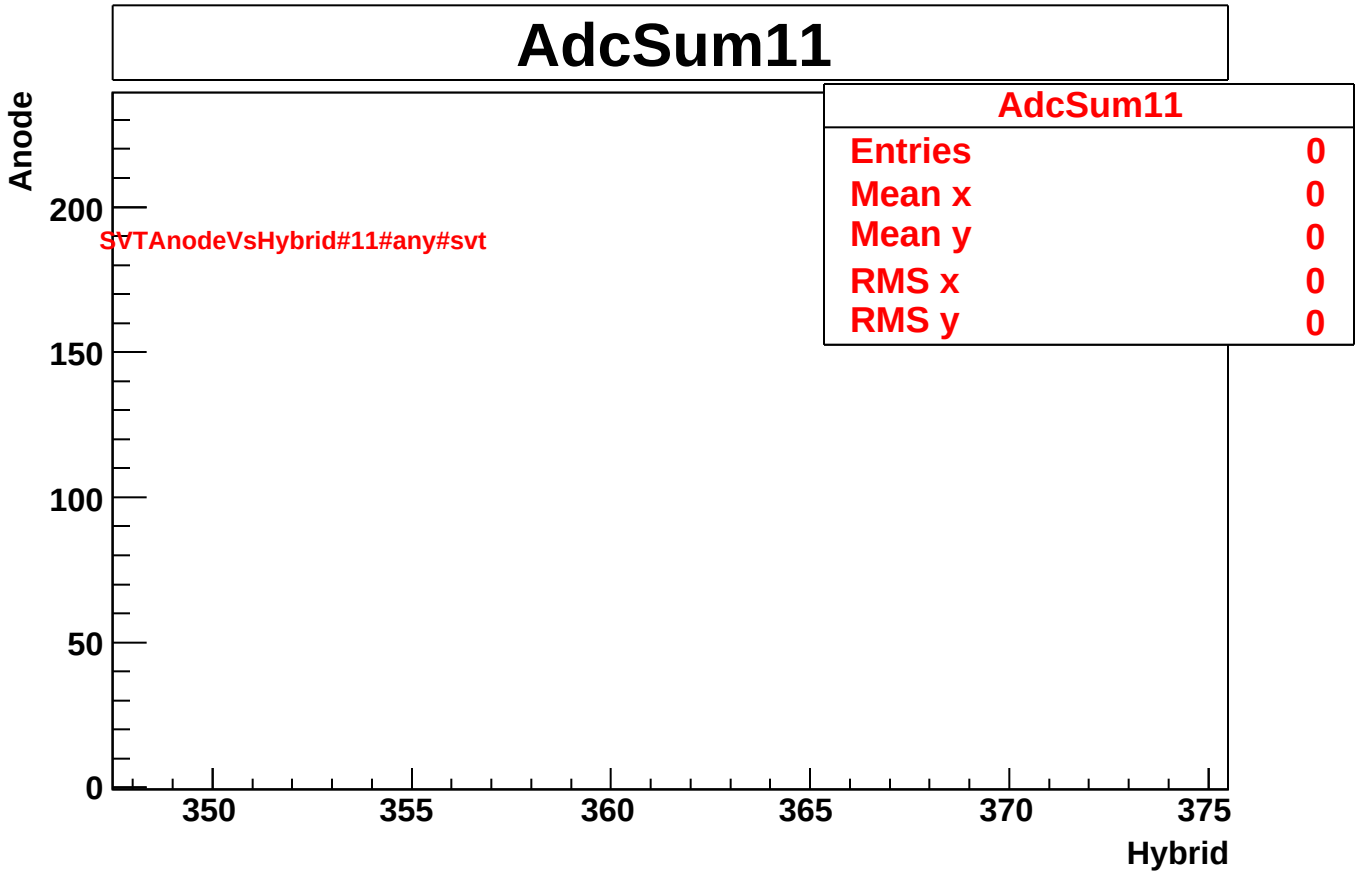

Hit08
Hit08
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
0
50
100
150
200
265
270
275
280
285
290
Hybrid

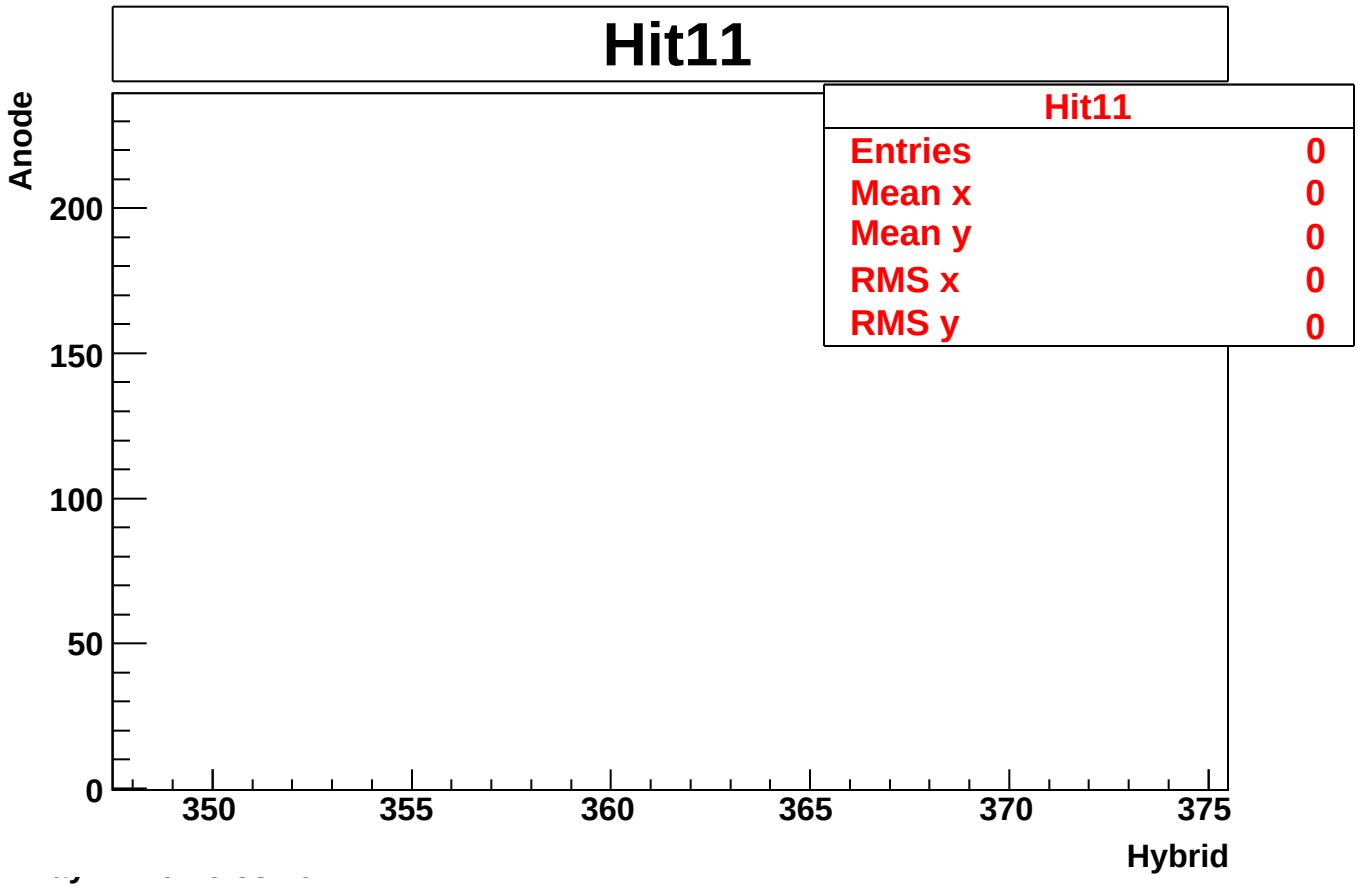

AdcSum09
AdcSum09
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#09#any#svt
Anode
200
150
100
50
0
295
300
305
310
315
Hybrid

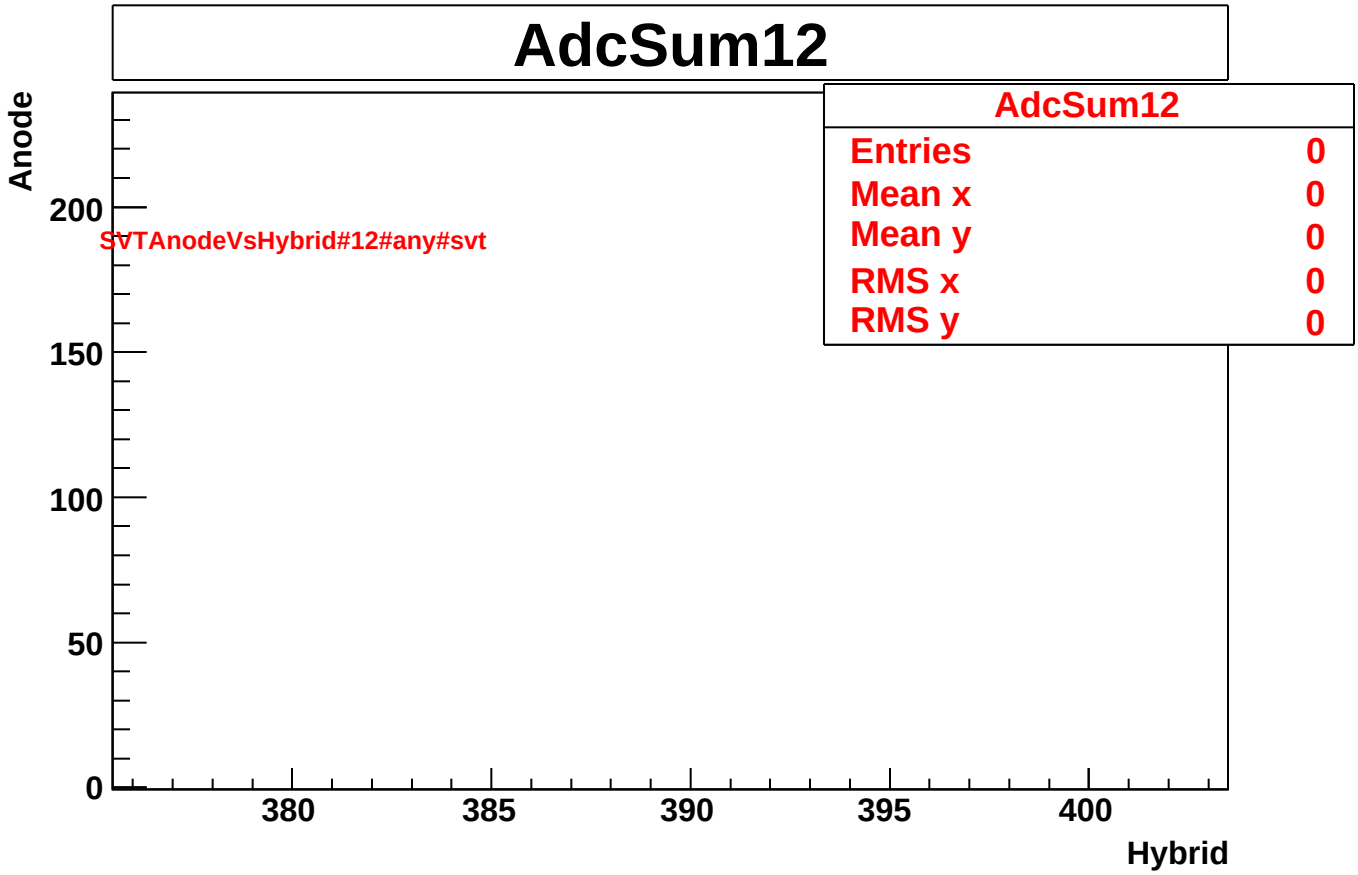

Hit09
Hit09
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
200
150
100
50
0
295
300
305
310
315
Hybrid

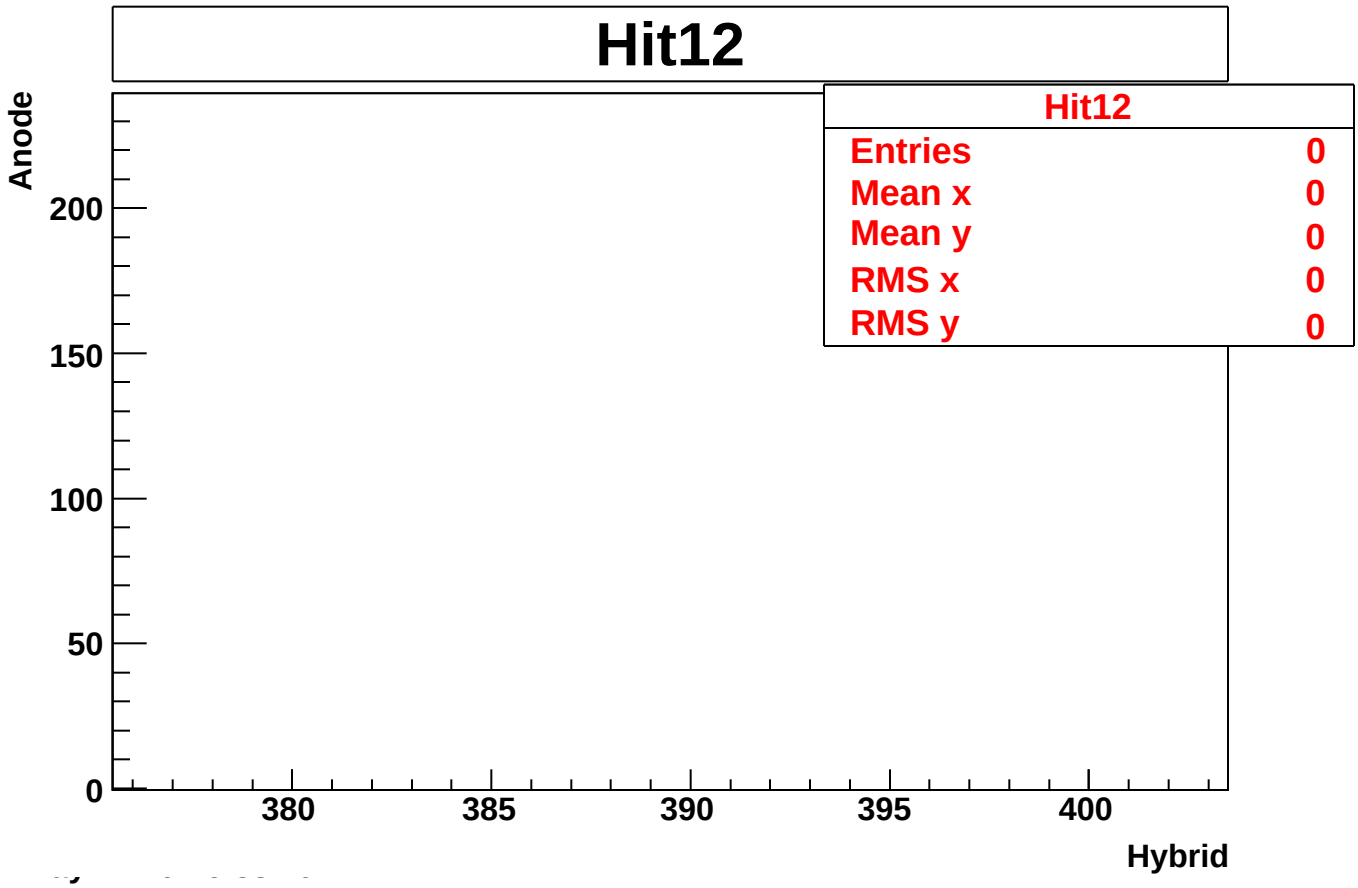

AdcSum10
AdcSum10
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#10#any#svt
Anode
0
50
100
150
200
320
325
330
335
340
345
Hybrid

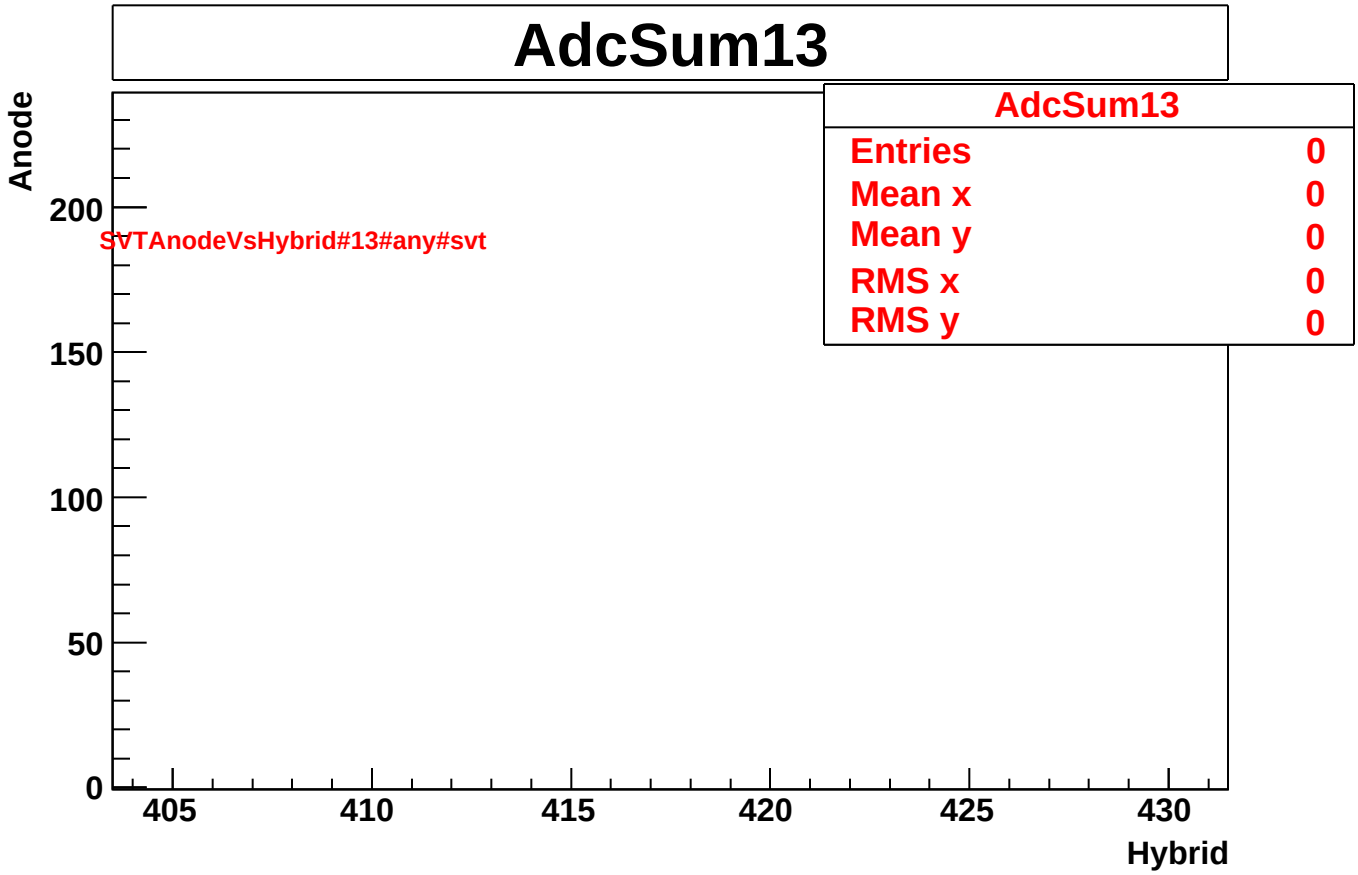

Hit10
Hit10
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
0
50
100
150
200
320
325
330
335
340
345
Hybrid

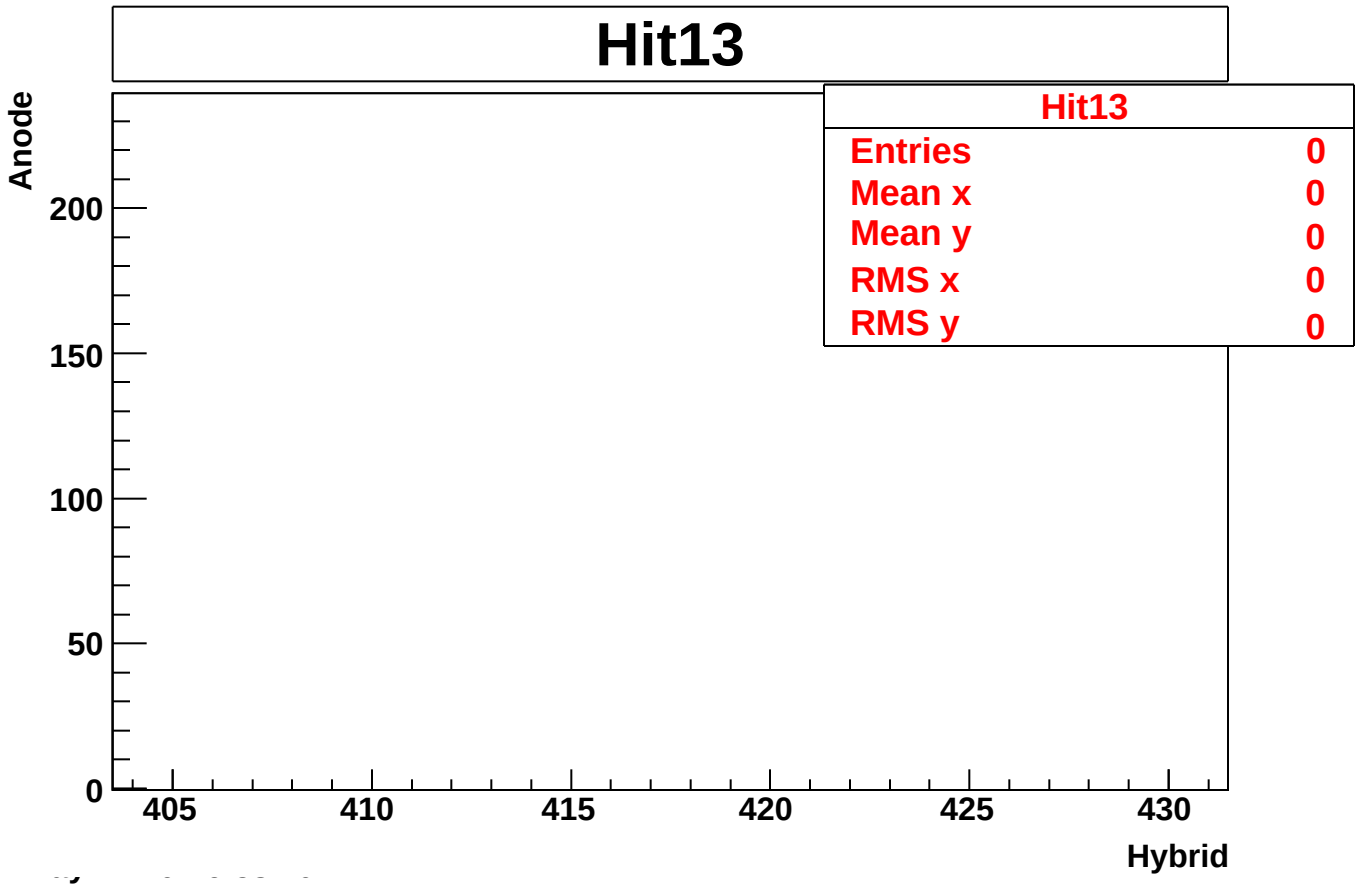

AdcSum11
AdcSum11
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#11#any#svt
Anode
200
150
100
50
0
350
355
360
365
370
375
Hybrid


Hit11
Hit11
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
200
150
100
50
0
350
355
360
365
370
375
Hybrid

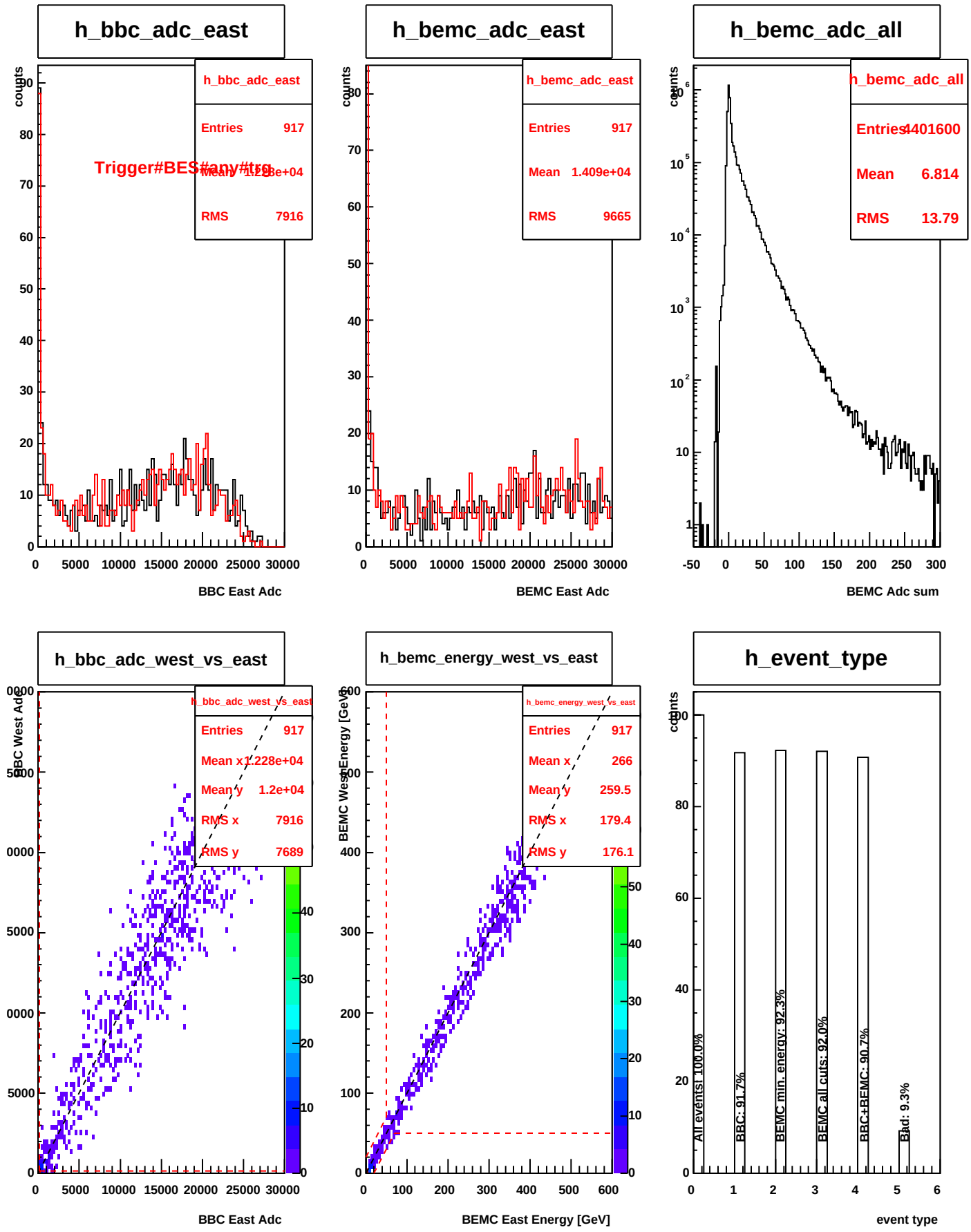

AdcSum12
AdcSum12
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#12#any#svt
Anode
200
150
100
50
0
380
385
390
395
400
Hybrid

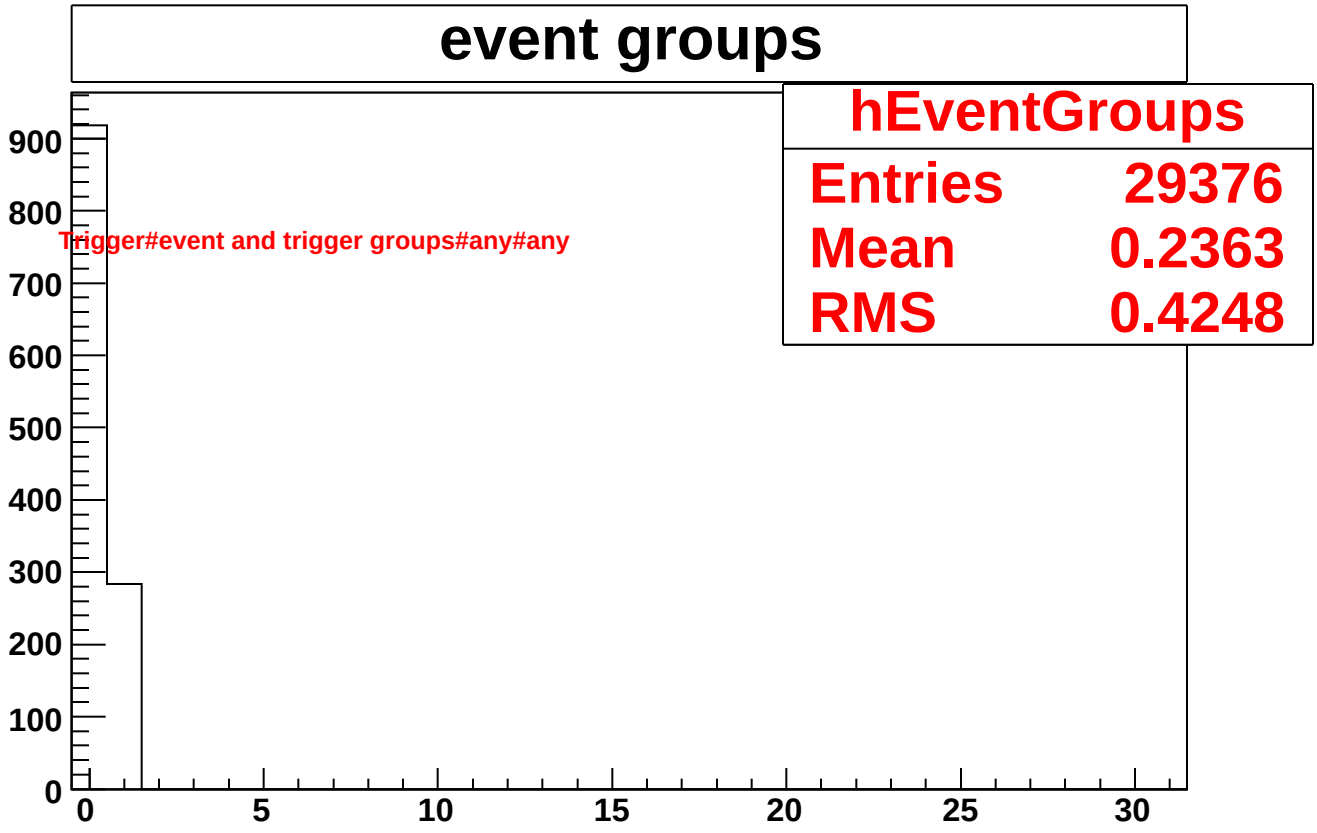

Hit12
Hit12
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
200
150
100
50
0
380
385
390
395
400
Hybrid

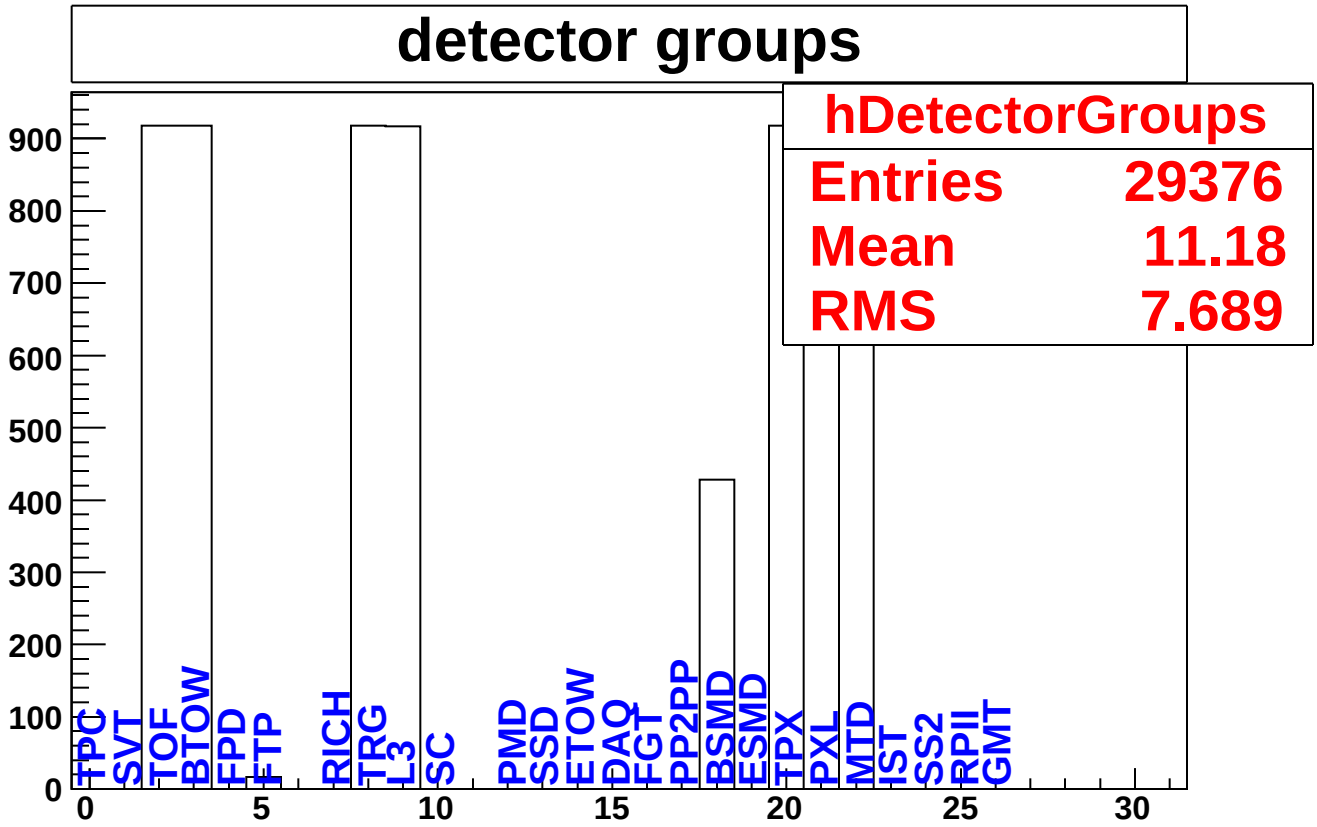

AdcSum13
AdcSum13
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#13#any#svt
Anode
0
50
100
150
200
405
410
415
420
425
430
Hybrid

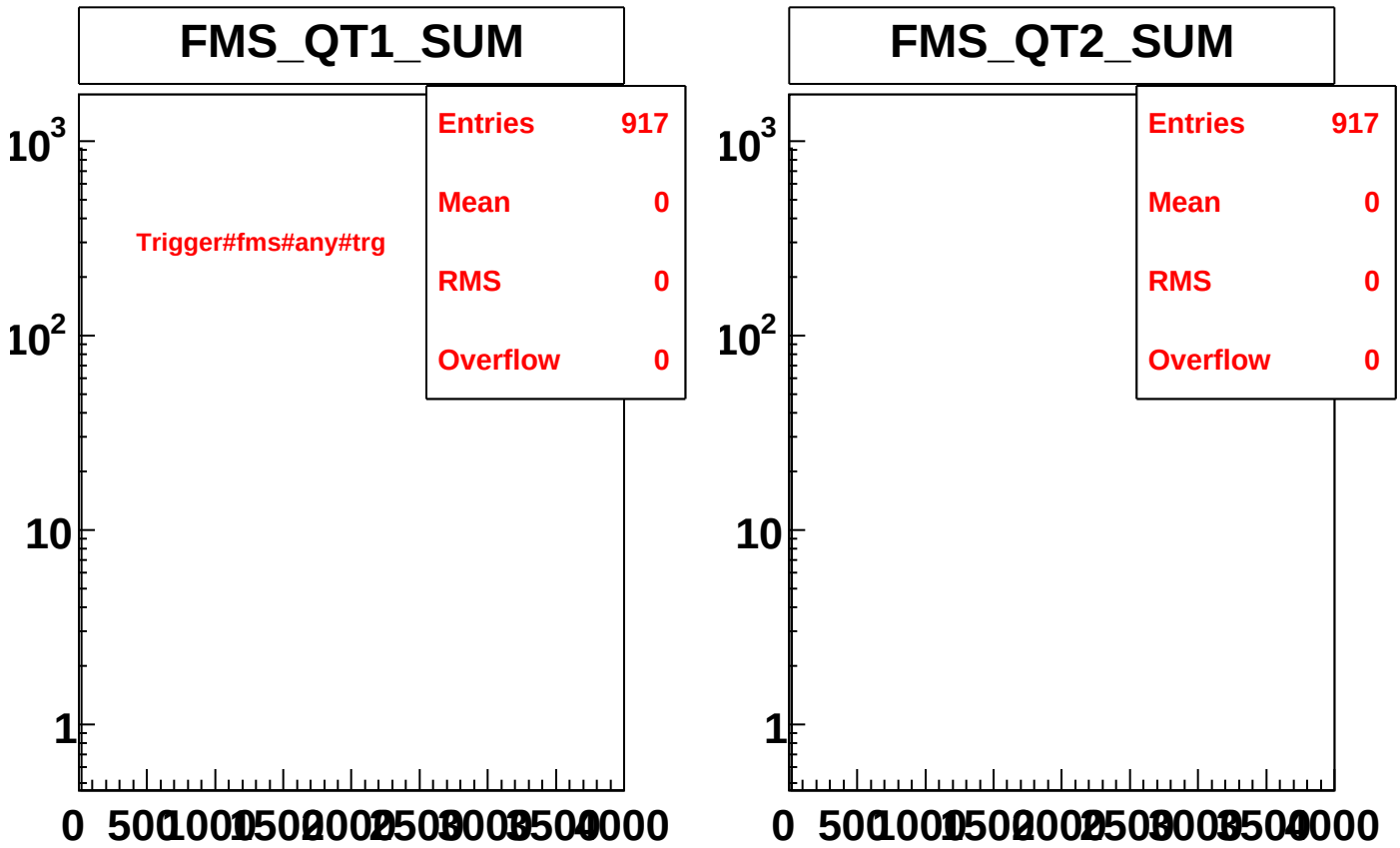

Hit13
Hit13
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
0
50
100
150
200
405
410
415
420
425
430
Hybrid

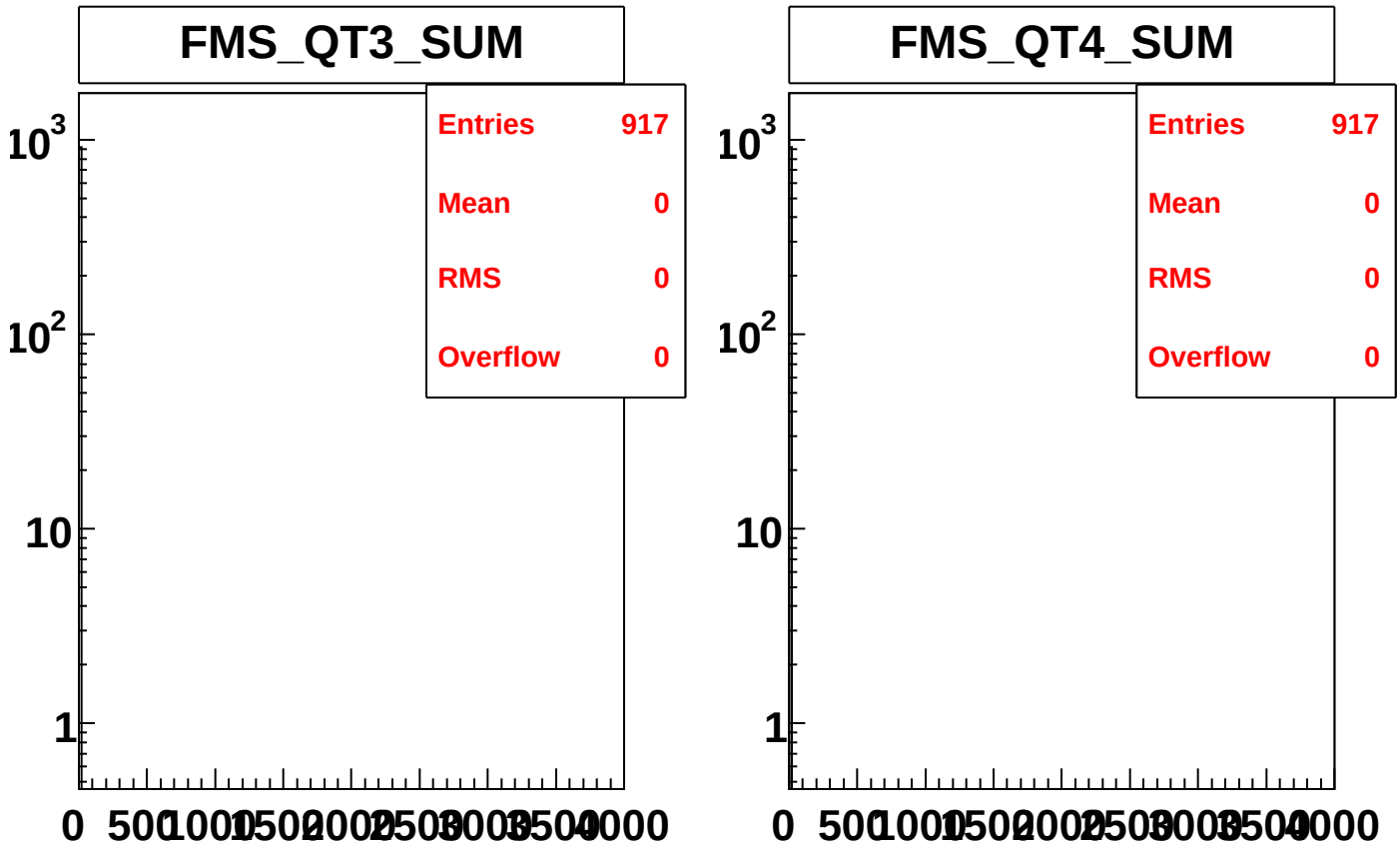
Ssd#Pedestal#pedestal#ssd

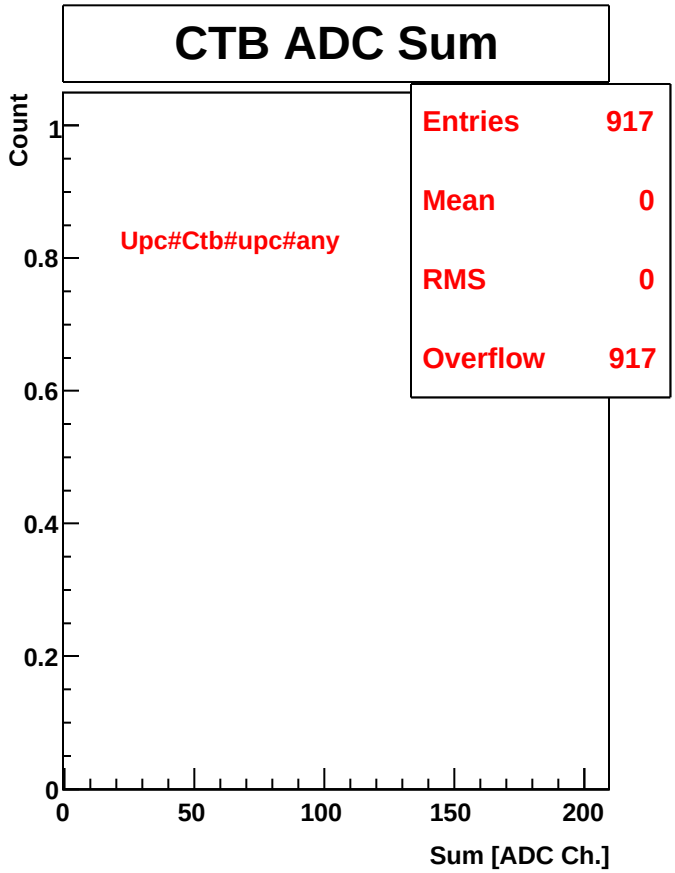

h_bbc_adc_east
h_bbc_adc_east
Entries 917
Mean 1.228e+04
RMS 7916
Trigger#BES#any#trg
counts
BBC East Adc
h_bemc_adc_east
h_bemc_adc_east
Entries 917
Mean 1.409e+04
RMS 9665
counts
BEMC East Adc
h_bemc_adc_all
h_bemc_adc_all
Entries 1401600
Mean 6.814
RMS 13.79
counts
BEMC Adc sum
h_bbc_adc_west_vs_east
h_bbc_adc_west_vs_east
Entries 917
Mean x 1.228e+04
Mean y 1.2e+04
RMS x 7916
RMS y 7689
BBC West Adc
BBC East Adc
h_bemc_energy_west_vs_east
h_bemc_energy_west_vs_east
Entries 917
Mean x 266
Mean y 259.5
RMS x 179.4
RMS y 176.1
BEMC West Energy [GeV]
BEMC East Energy [GeV]
h_event_type
counts
All events: 100.0%
BBC: 91.7%
BEMC min. energy: 92.3%
BEMC all cuts: 92.0%
BBC+BEMC: 90.7%
Bad: 9.3%
event type

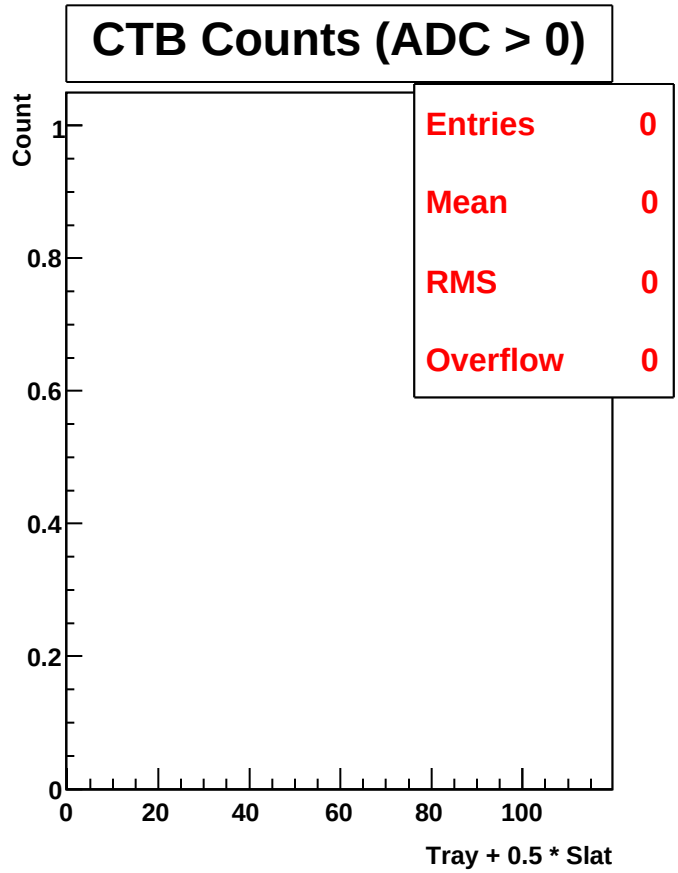

event groups
hEventGroups
Entries 29376
Mean 0.2363
RMS 0.4248
Trigger#event and trigger groups#any#any
0
100
200
300
400
500
600
700
800
900
0
5
10
15
20
25
30

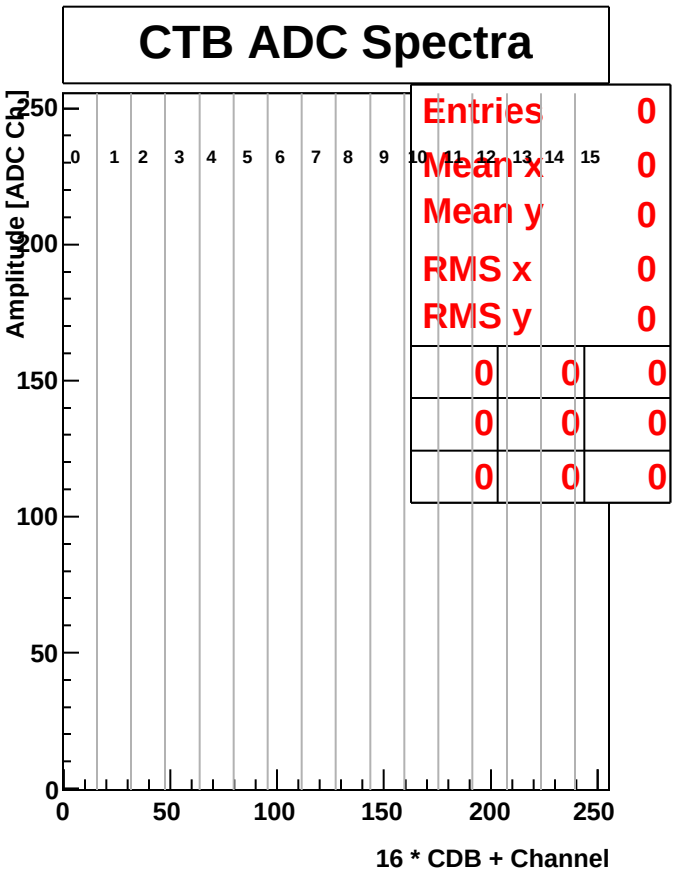

detector groups
hDetectorGroups
Entries 29376
Mean 11.18
RMS 7.689
TPC
SVT
TOF
BTOW
FPD
FTP
RICH
TRG
L3
SC
PMD
SSD
ETOW
DAQ
FGT
PP2PP
BSMD
ESMD
TPX
PXL
MTD
IST
SS2
RPII
GMT
0
100
200
300
400
500
600
700
800
900
0
5
10
15
20
25
30

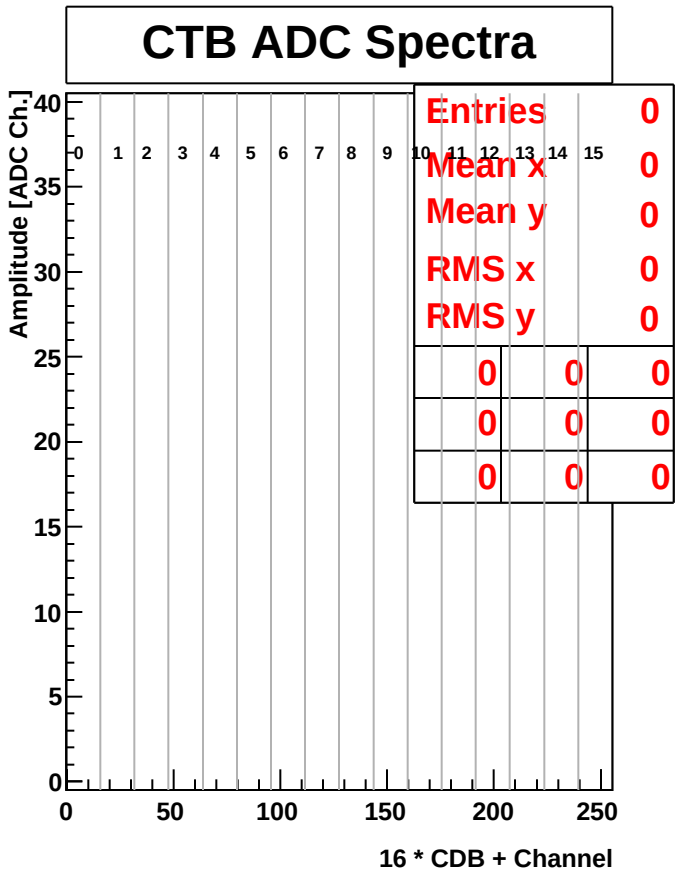

FMS_QT1_SUM
Entries 917
Mean 0
RMS 0
Overflow 0
Trigger#fms#any#trg
FMS_QT2_SUM
Entries 917
Mean 0
RMS 0
Overflow 0

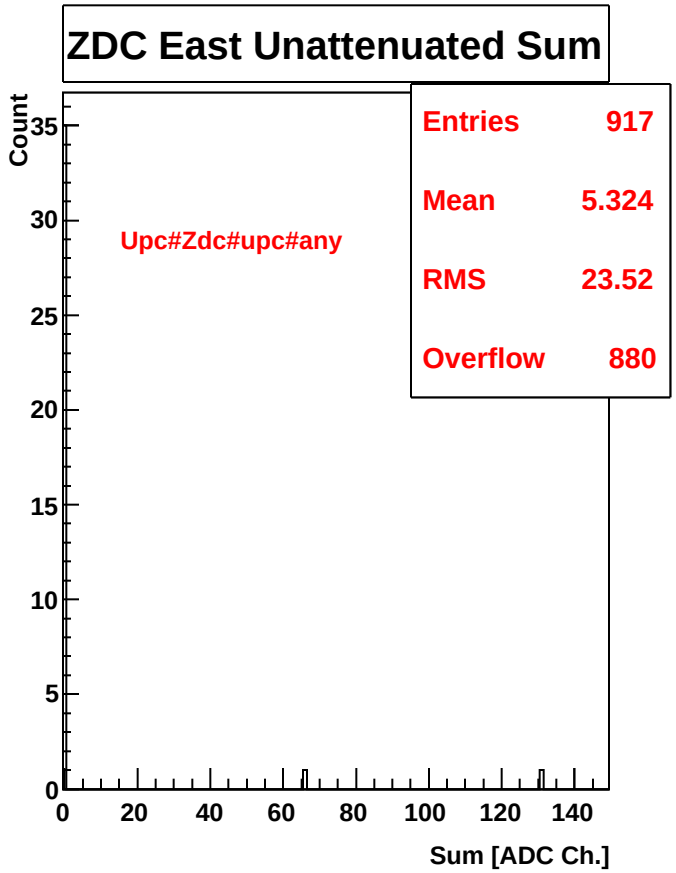

FMS_QT3_SUM
Entries 917
Mean 0
RMS 0
Overflow 0
FMS_QT4_SUM
Entries 917
Mean 0
RMS 0
Overflow 0

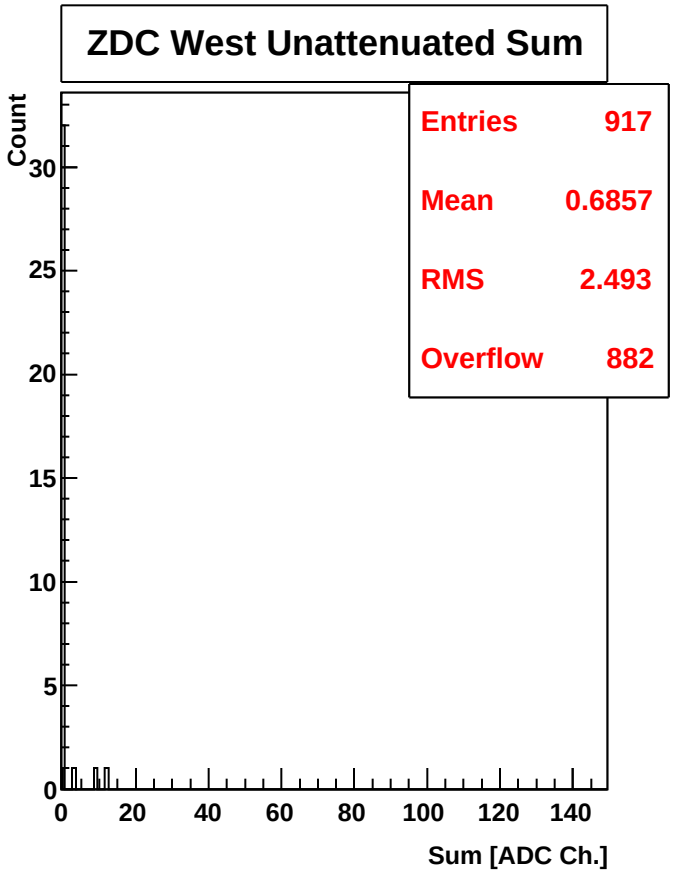

CTB ADC Sum
Count
Upc#Ctb#upc#any
Entries 917
Mean 0
RMS 0
Overflow 917
Sum [ADC Ch.]

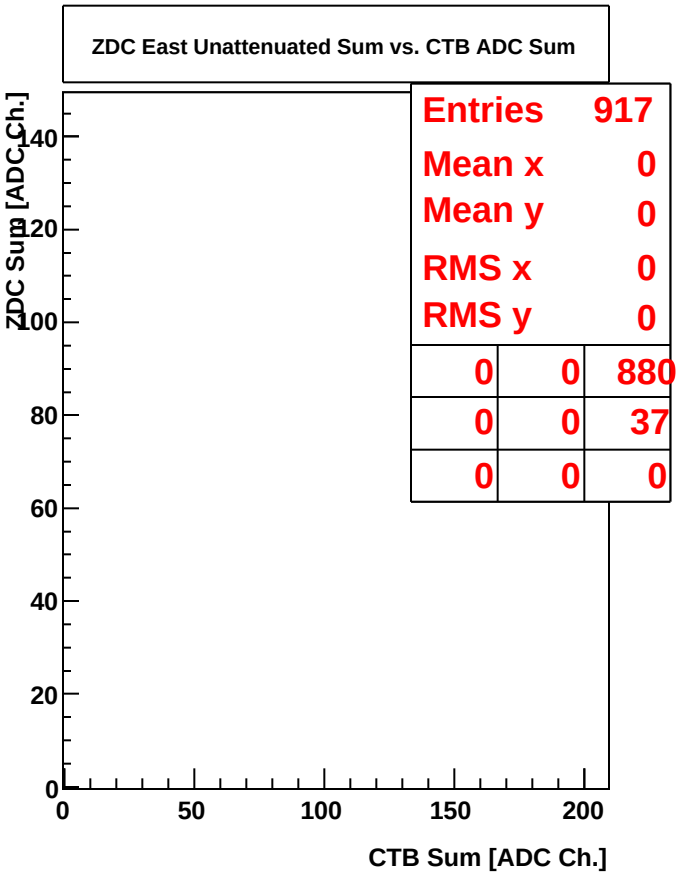

CTB Counts (ADC > 0)
Count
Entries 0
Mean 0
RMS 0
Overflow 0
Tray + 0.5 * Slat

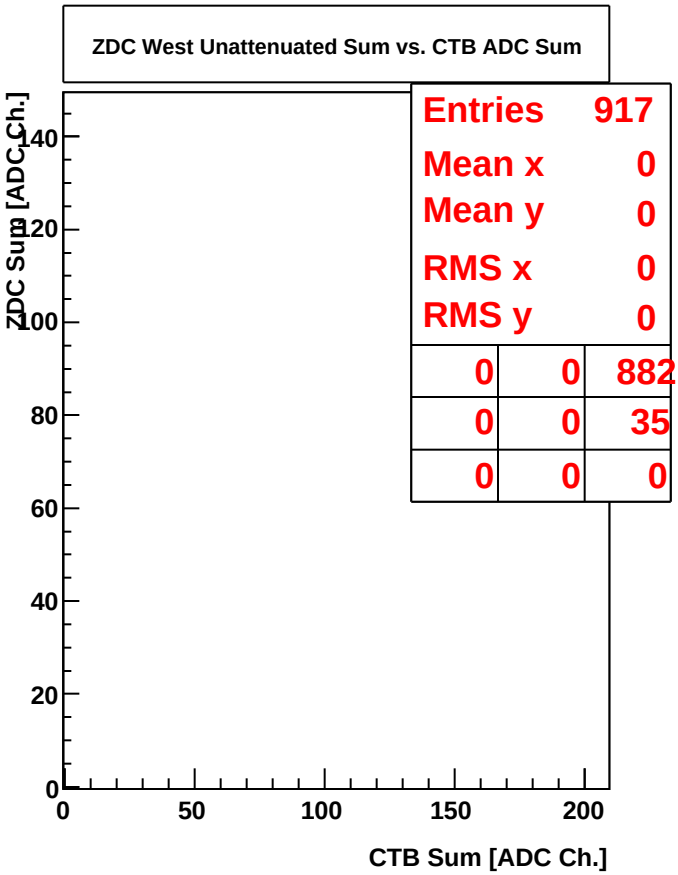

CTB ADC Spectra
Amplitude [ADC Ch.]
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
16 * CDB + Channel


CTB ADC Spectra
Amplitude [ADC Ch.]
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
16 * CDB + Channel


ZDC East Unattenuated Sum
Entries 917
Mean 5.324
RMS 23.52
Overflow 880
Upc#Zdc#upc#any
Count
Sum [ADC Ch.]


ZDC West Unattenuated Sum
Entries 917
Mean 0.6857
RMS 2.493
Overflow 882
Count
Sum [ADC Ch.]


ZDC East Unattenuated Sum vs. CTB ADC Sum
Entries 917
Mean x 0
Mean y 0
RMS x 0
RMS y 0
0 0 880
0 0 37
0 0 0
ZDC Sum [ADC Ch.]
CTB Sum [ADC Ch.]


ZDC West Unattenuated Sum vs. CTB ADC Sum
Entries 917
Mean x 0
Mean y 0
RMS x 0
RMS y 0
0 0 882
0 0 35
0 0 0
ZDC Sum [ADC Ch.]
CTB Sum [ADC Ch.]