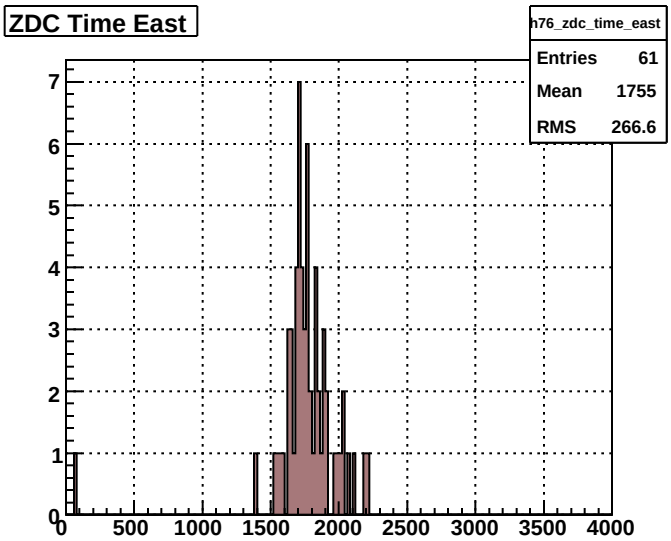
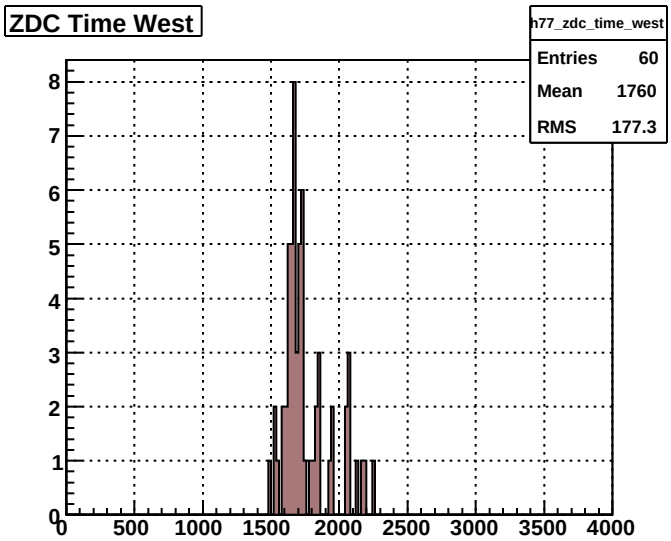
Run: #12130023

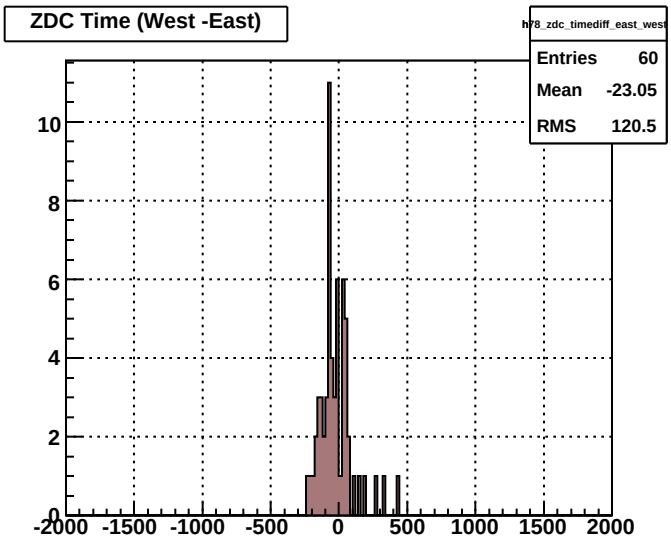
TriggerBits: 11111111 11111111 11111111 11111111

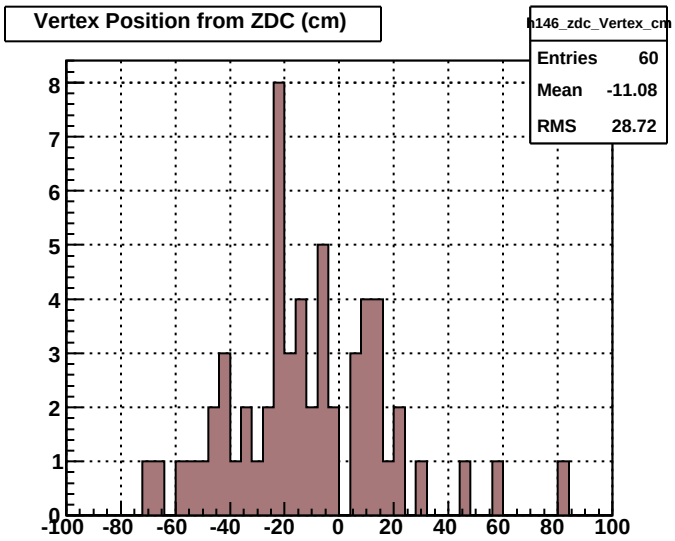
DetectorBits: 00000000 01010100 00000011 00001100

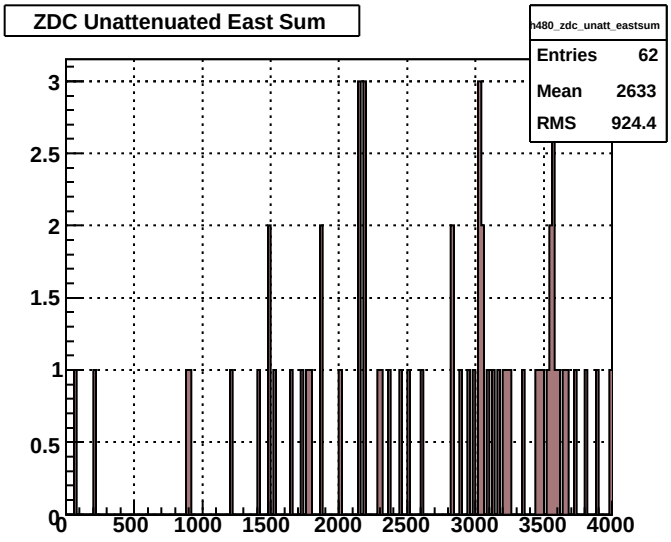
Tue May 10 08:45:20 2011

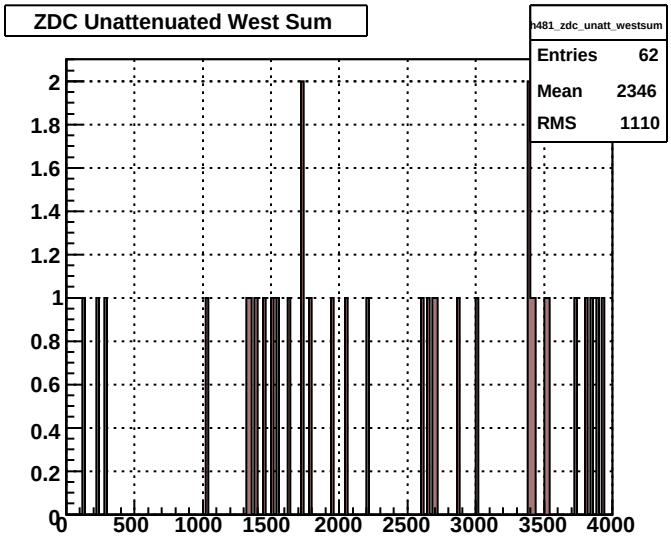

ZDC Time East
h76_zdc_time_east
Entries 61
Mean 1755
RMS 266.6

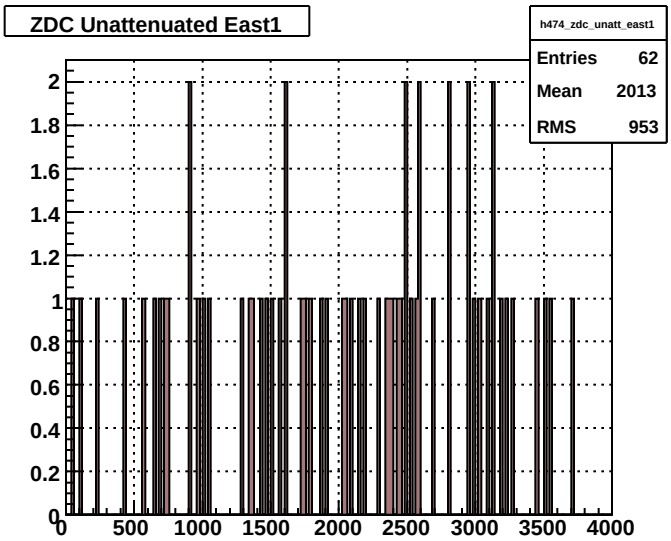

ZDC Time West
h77_zdc_time_west
Entries 60
Mean 1760
RMS 177.3

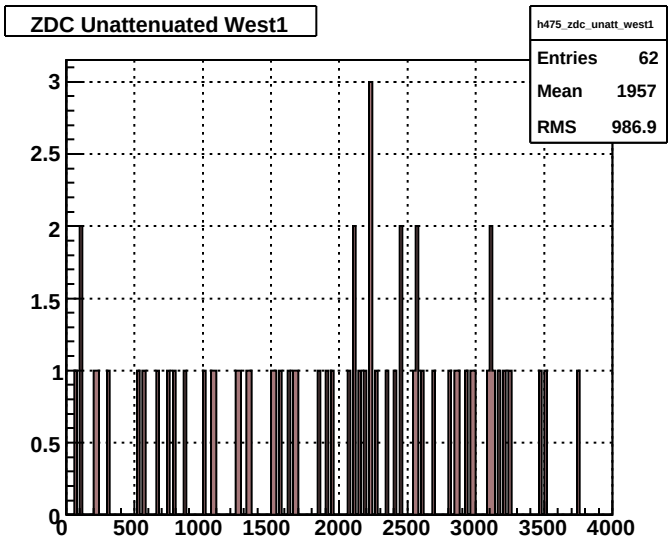

ZDC Time (West -East)
h78_zdc_timediff_east_west
Entries 60
Mean -23.05
RMS 120.5

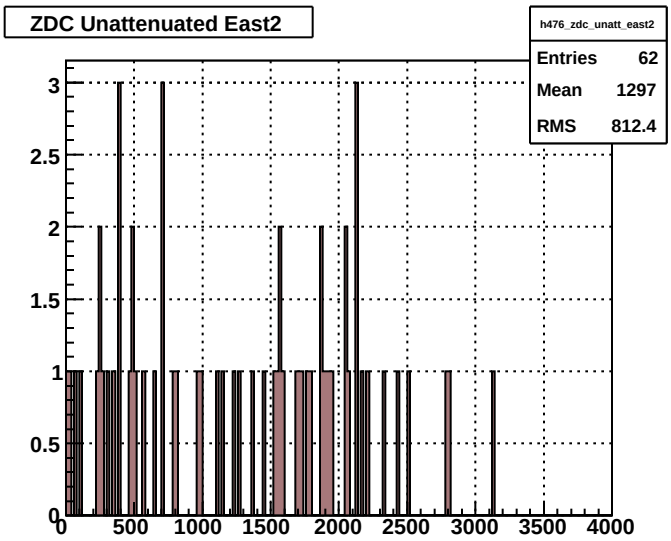

Vertex Position from ZDC (cm)
h146_zdc_Vertex_cm
Entries 60
Mean -11.08
RMS 28.72

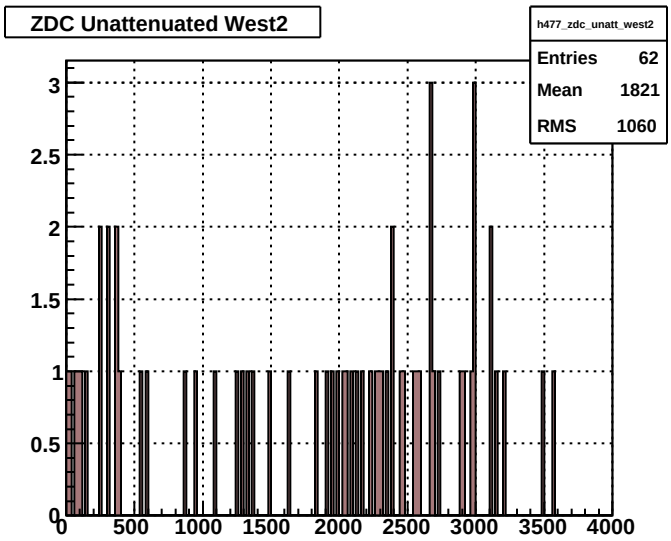

ZDC Unattenuated East Sum
h480_zdc_unatt_eastsum
Entries 62
Mean 2633
RMS 924.4

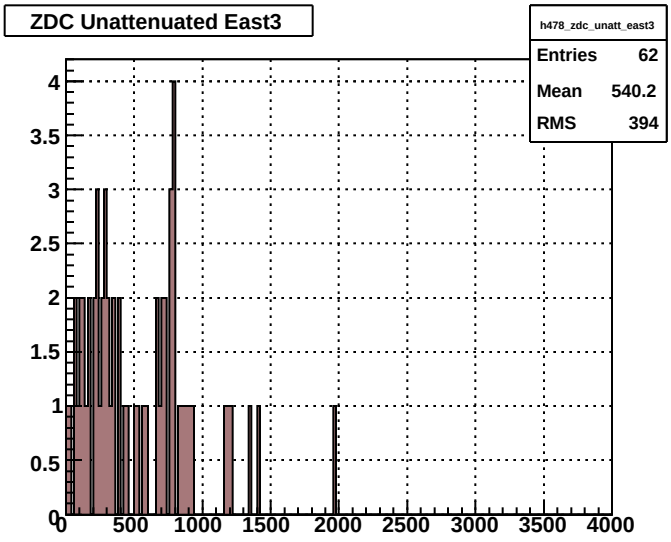

ZDC Unattenuated West Sum
h481_zdc_unatt_westsum
Entries 62
Mean 2346
RMS 1110

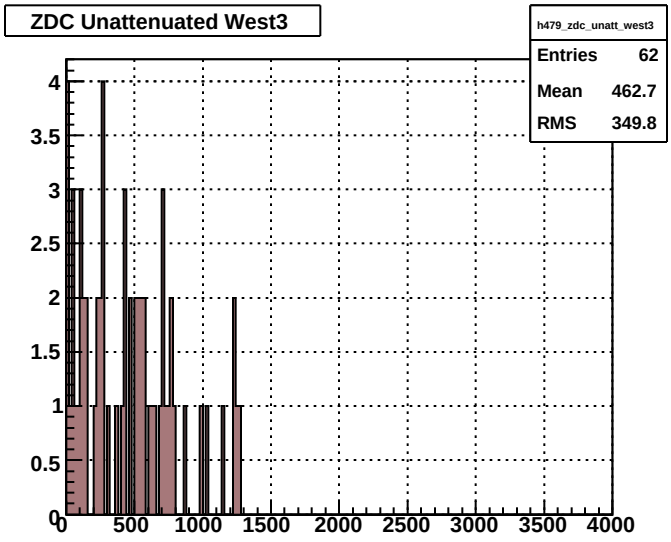

ZDC Unattenuated East1
h474_zdc_unatt_east1
Entries 62
Mean 2013
RMS 953

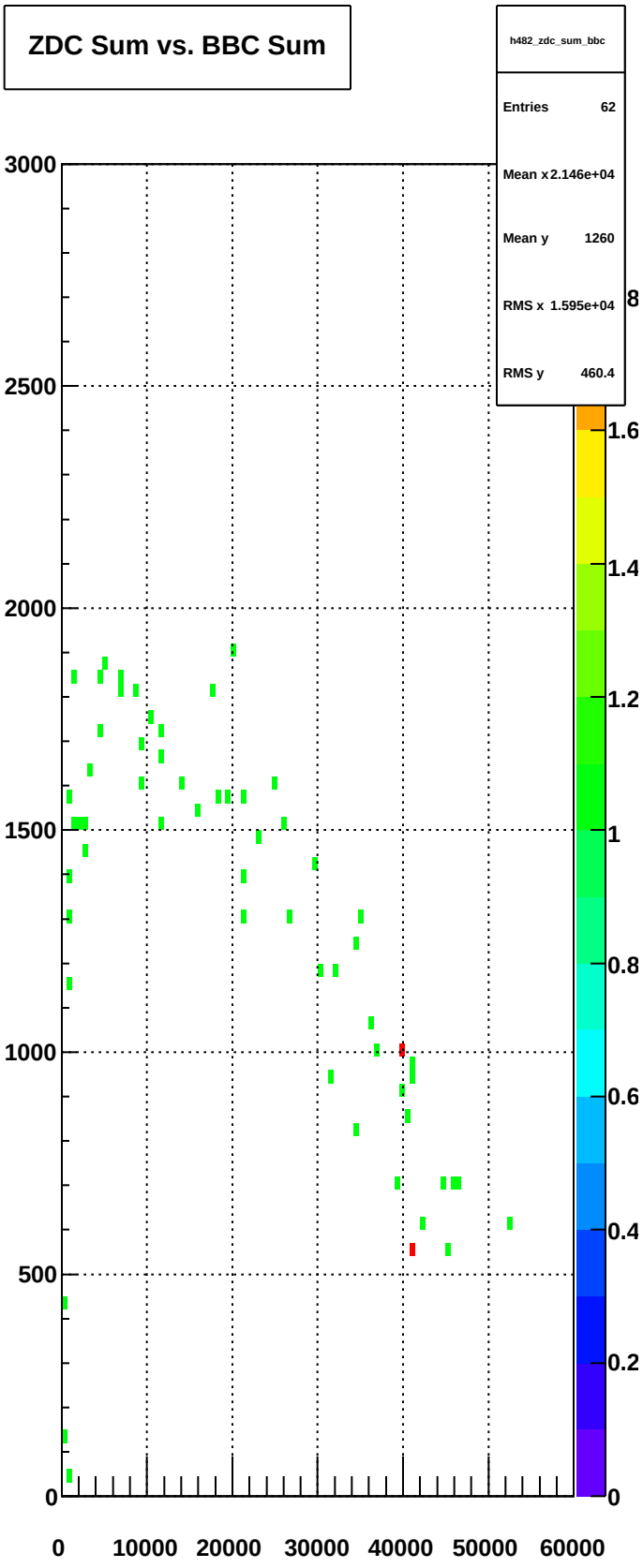

ZDC Unattenuated West1
h475_zdc_unatt_west1
Entries 62
Mean 1957
RMS 986.9

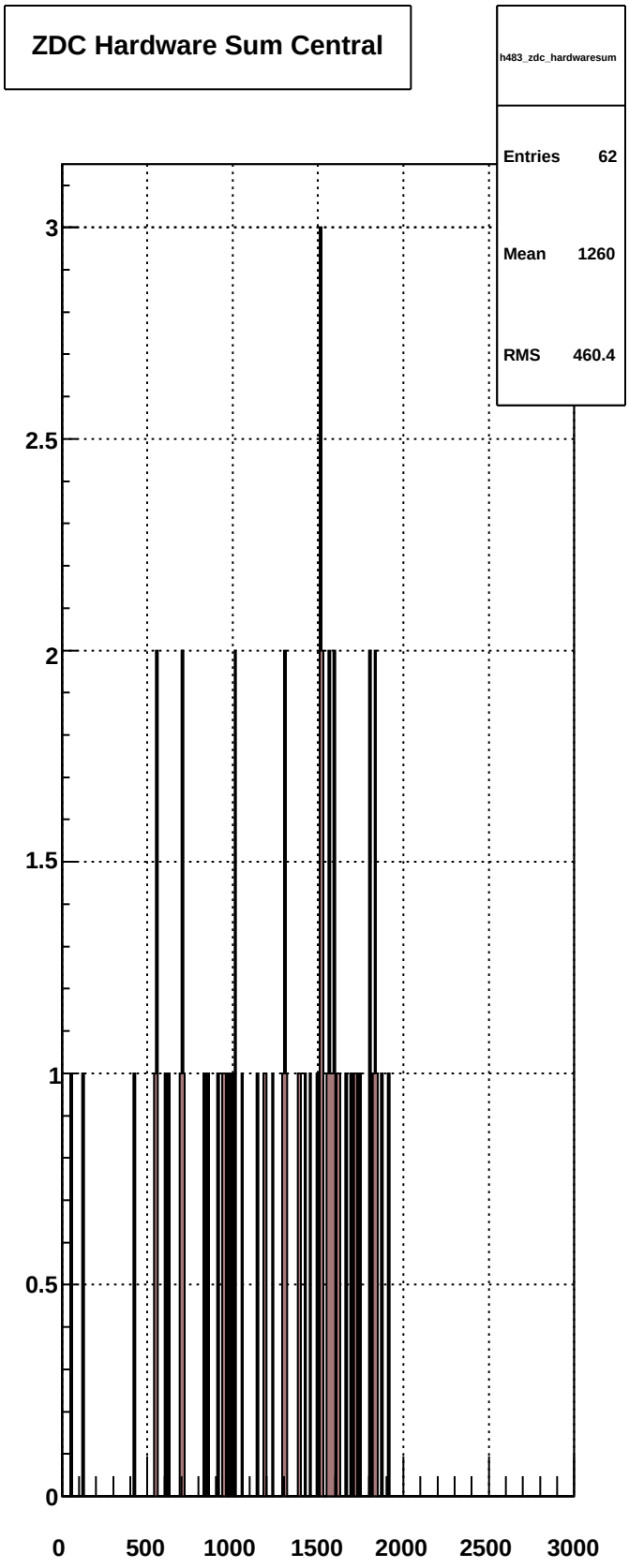

ZDC Unattenuated East2
h476_zdc_unatt_east2
Entries 62
Mean 1297
RMS 812.4

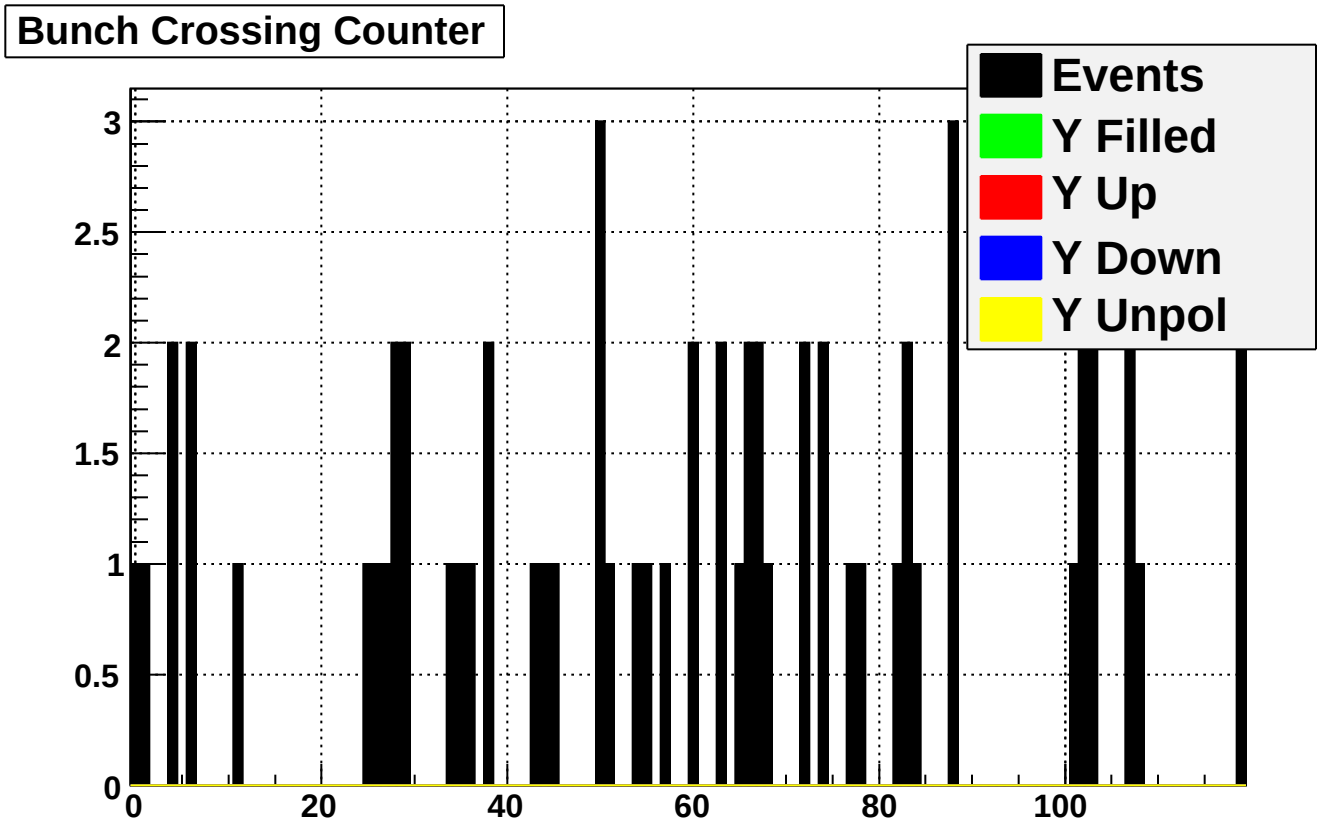

ZDC Unattenuated West2
h477_zdc_unatt_west2
Entries 62
Mean 1821
RMS 1060

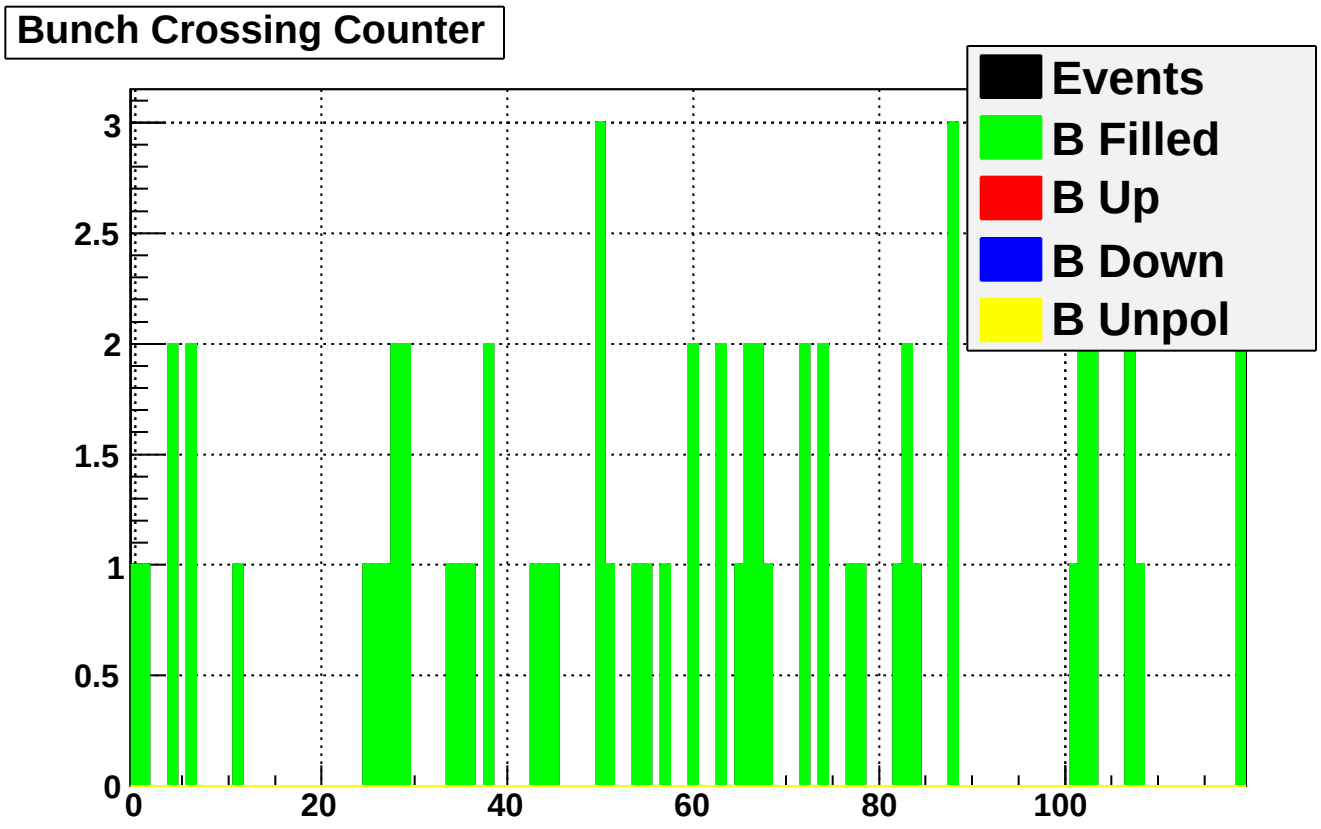

ZDC Unattenuated East3
h478_zdc_unatt_east3
Entries 62
Mean 540.2
RMS 394

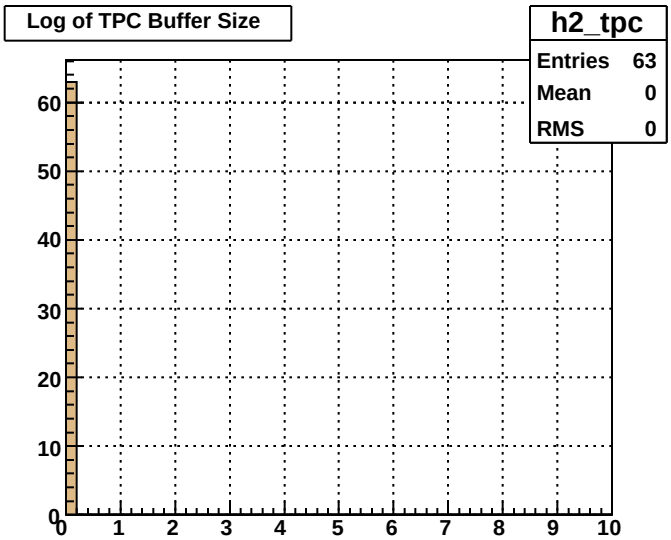

ZDC Unattenuated West3
h479_zdc_unatt_west3
Entries 62
Mean 462.7
RMS 349.8

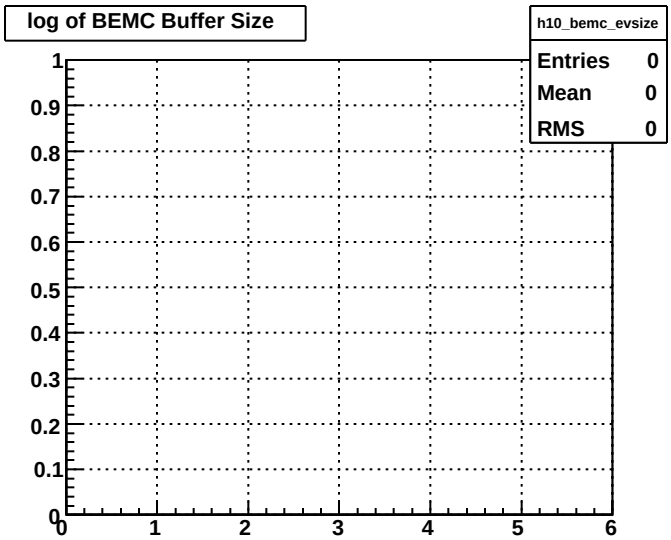

ZDC Sum vs. BBC Sum
h482_zdc_sum_bbc
Entries 62
Mean x 2.146e+04
Mean y 1260
RMS x 1.595e+04
RMS y 460.4
3000
2500
2000
1500
1000
500
0
0
10000
20000
30000
40000
50000
60000
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

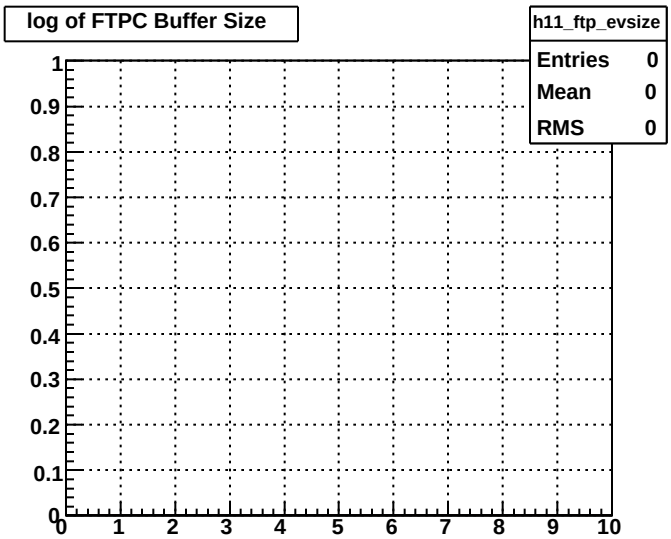

ZDC Hardware Sum Central
h483_zdc_hardwaresum
Entries 62
Mean 1260
RMS 460.4
3
2.5
2
1.5
1
0.5
0
0
500
1000
1500
2000
2500
3000

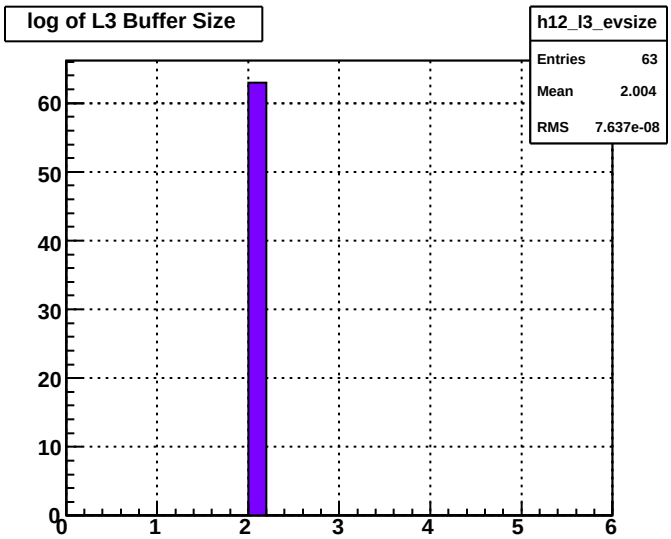

Bunch Crossing Counter
Events
Y Filled
Y Up
Y Down
Y Unpol
0
0.5
1
1.5
2
2.5
3
0
20
40
60
80
100

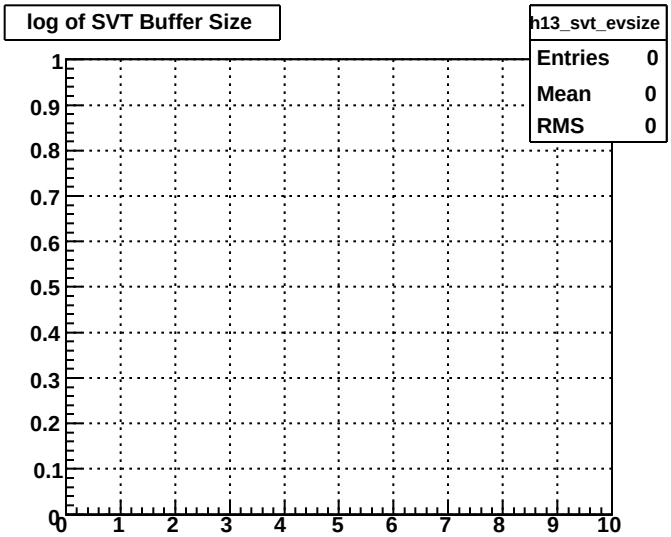

Bunch Crossing Counter
Events
B Filled
B Up
B Down
B Unpol
0
0.5
1
1.5
2
2.5
3
0
20
40
60
80
100

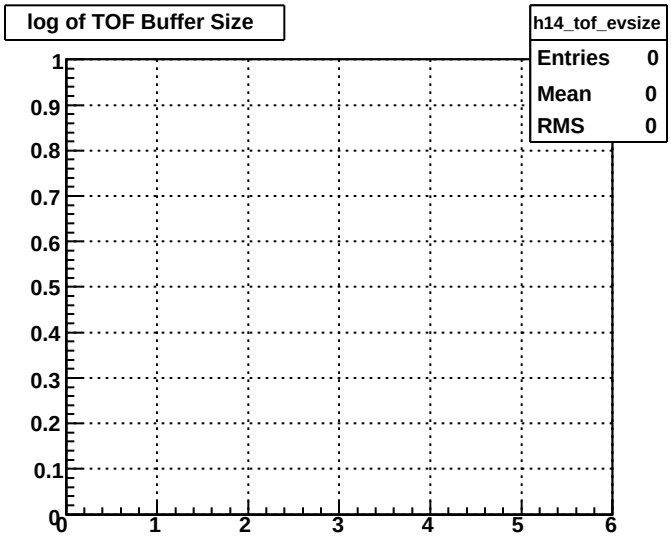

Log of TPC Buffer Size
h2_tpc
Entries 63
Mean 0
RMS 0

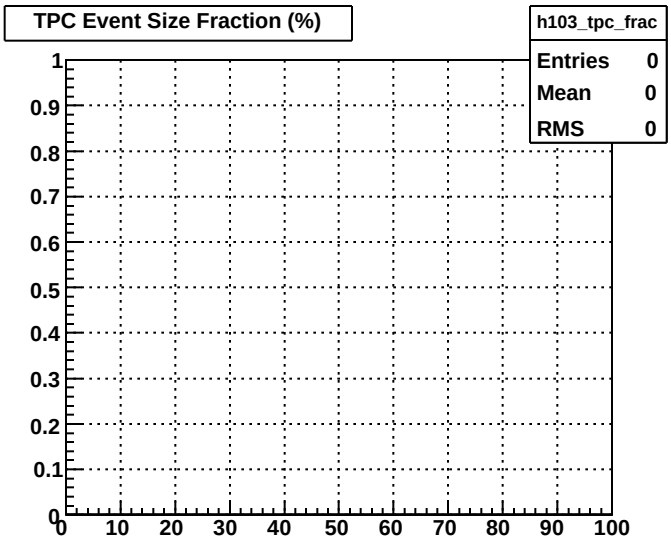

log of BEMC Buffer Size
h10_bemc_evsize
Entries 0
Mean 0
RMS 0

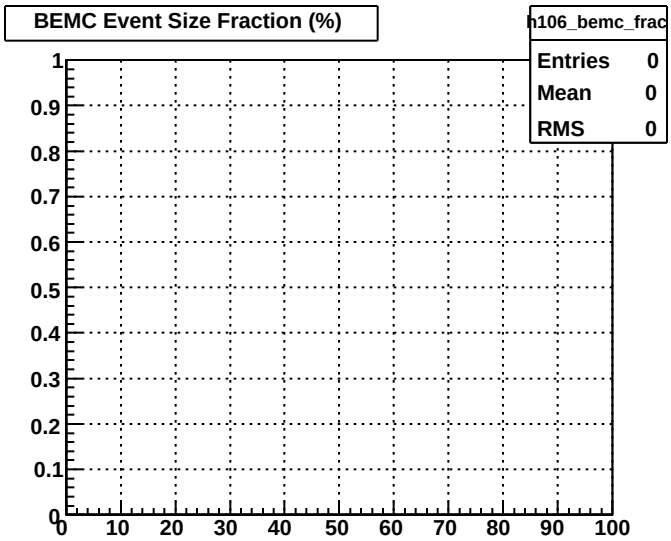

log of FTPC Buffer Size
h11_ftp_evsize
Entries 0
Mean 0
RMS 0

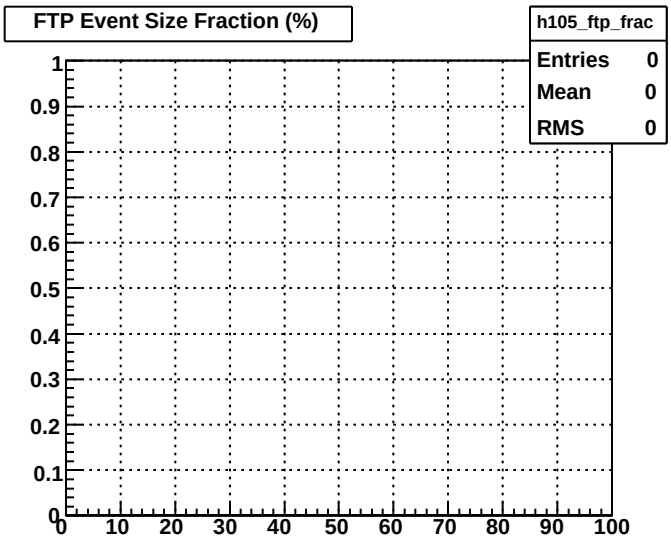

log of L3 Buffer Size
h12_l3_evsize
Entries 63
Mean 2.004
RMS 7.637e-08

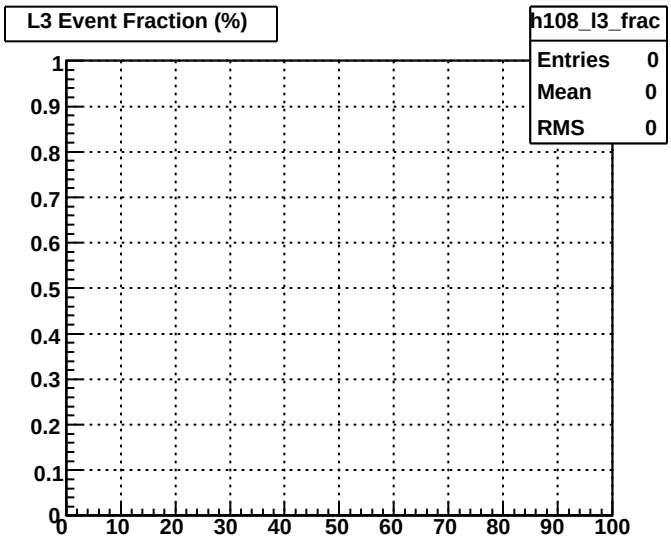

log of SVT Buffer Size
h13_svt_evsize
Entries 0
Mean 0
RMS 0

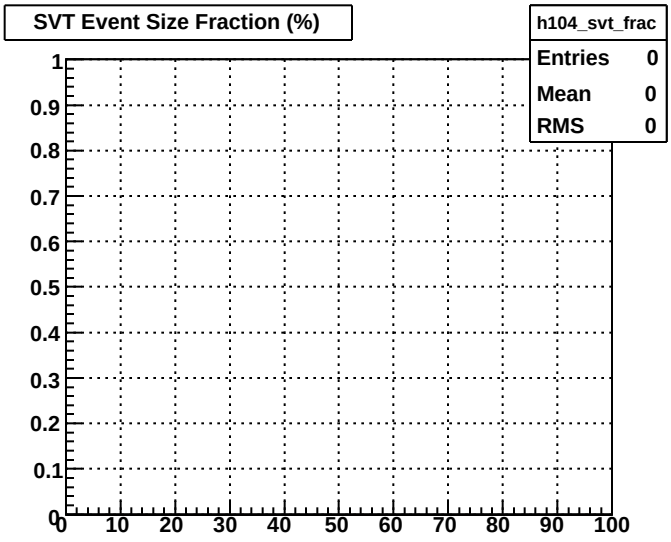

log of TOF Buffer Size
h14_tof_evsize
Entries 0
Mean 0
RMS 0

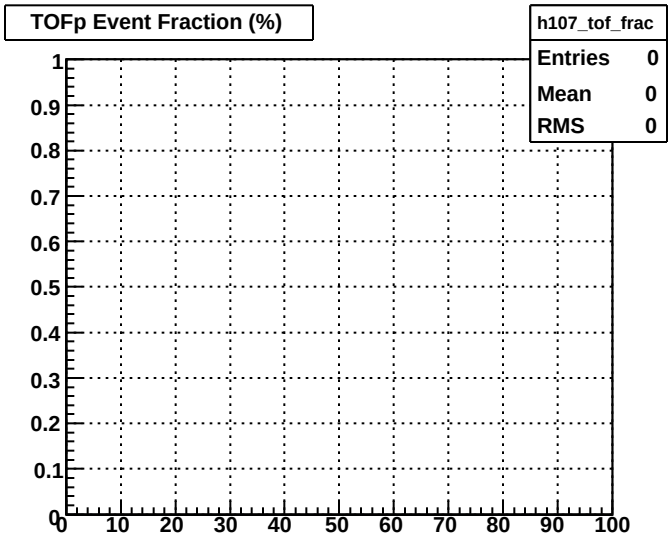

TPC Event Size Fraction (%)
h103_tpc_frac
Entries 0
Mean 0
RMS 0


BEMC Event Size Fraction (%)
h106_bemc_frac
Entries 0
Mean 0
RMS 0

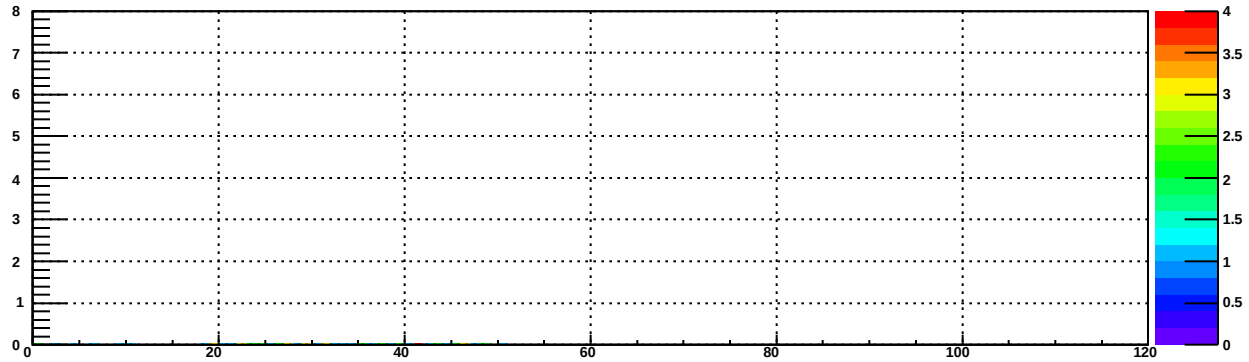

FTP Event Size Fraction (%)
h105_ftp_frac
Entries 0
Mean 0
RMS 0


L3 Event Fraction (%)
h108_l3_frac
Entries 0
Mean 0
RMS 0

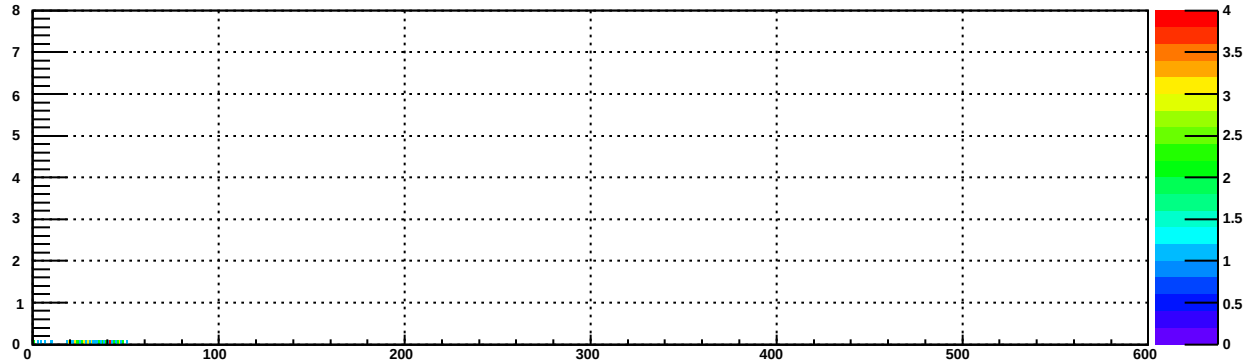

SVT Event Size Fraction (%)
h104_svt_frac
Entries 0
Mean 0
RMS 0


TOFp Event Fraction (%)
h107_tof_frac
Entries 0
Mean 0
RMS 0

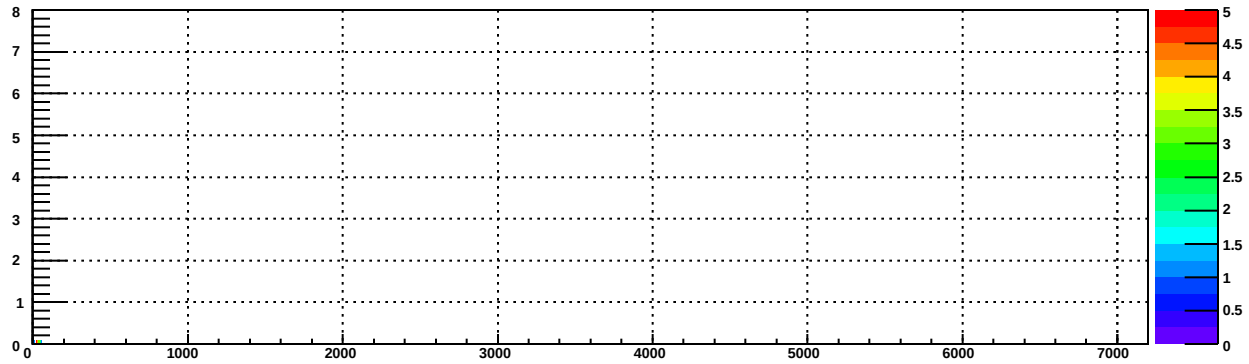
Event Size (Log10) vs time (sec)



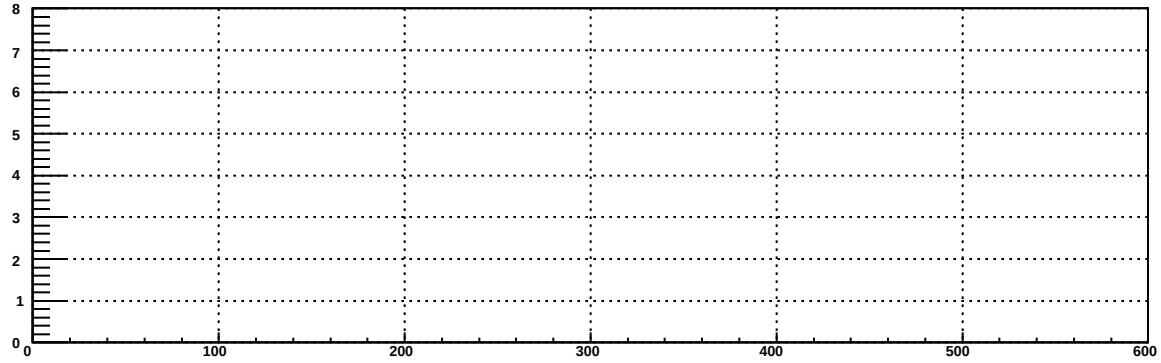
Event Size (Log10) vs time (sec)

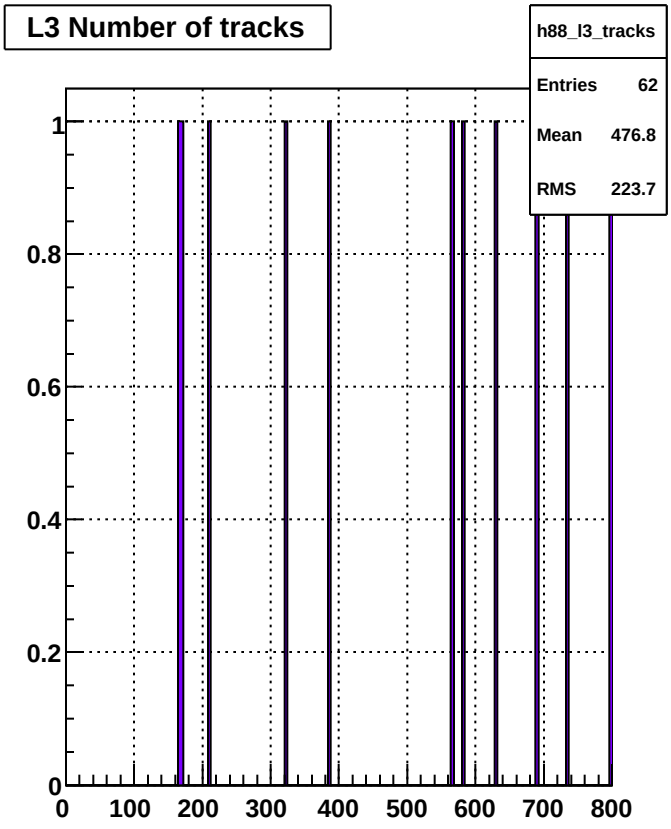


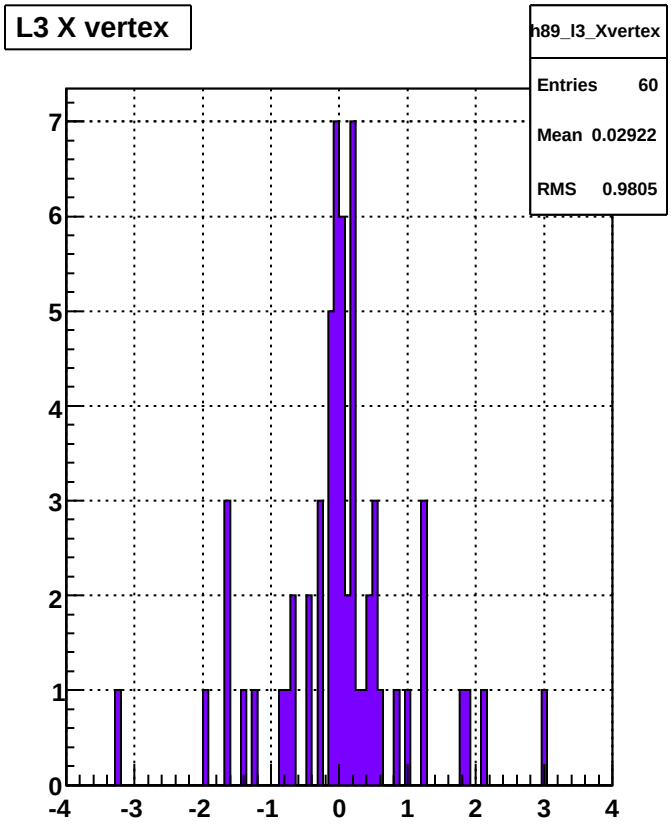
Event Size (Log10) vs time(sec)

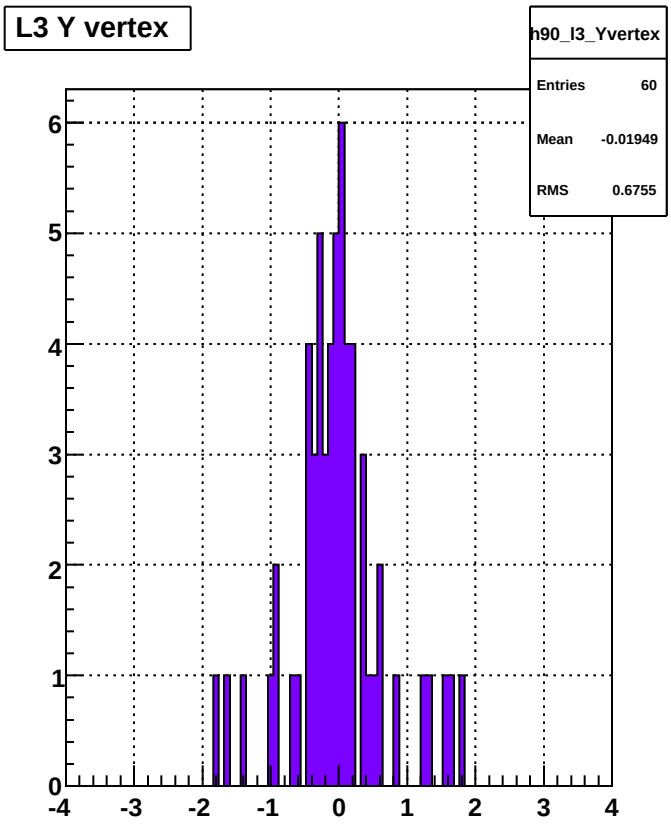


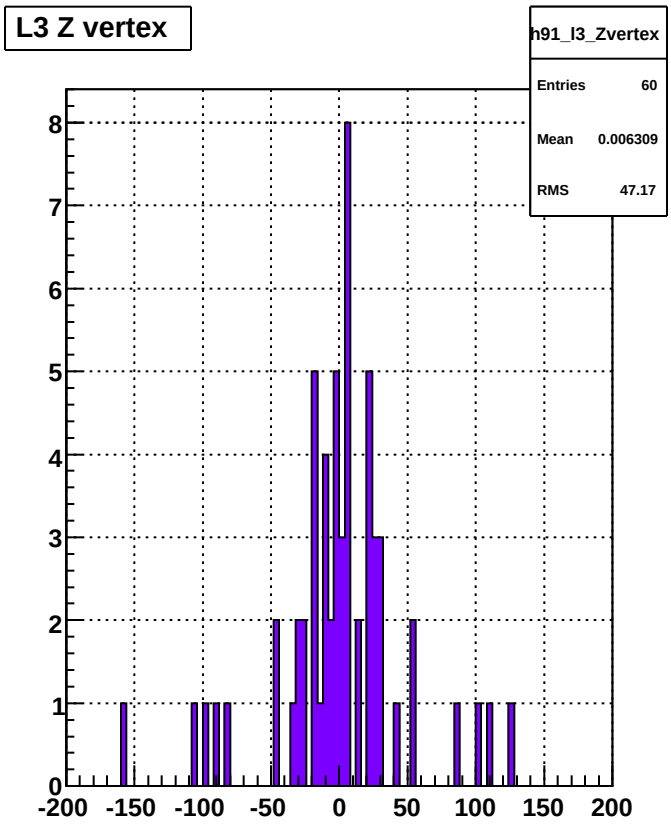
FTPC Event Size vs time(sec)



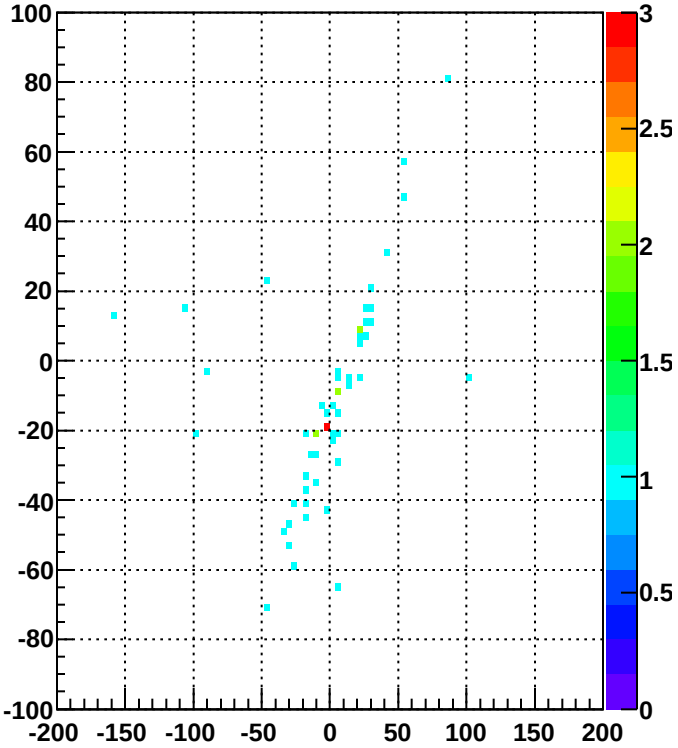

L3 Number of tracks
h88_l3_tracks
Entries 62
Mean 476.8
RMS 223.7


L3 X vertex
h89_l3_Xvertex
Entries 60
Mean 0.02922
RMS 0.9805

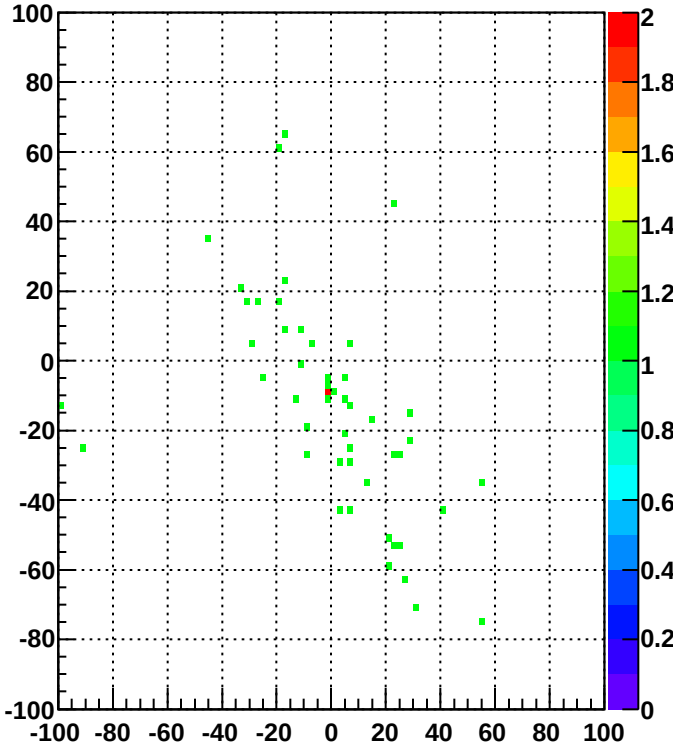

L3 Y vertex
h90_l3_Yvertex
Entries 60
Mean -0.01949
RMS 0.6755


L3 Z vertex
h91_l3_Zvertex
Entries 60
Mean 0.006309
RMS 47.17

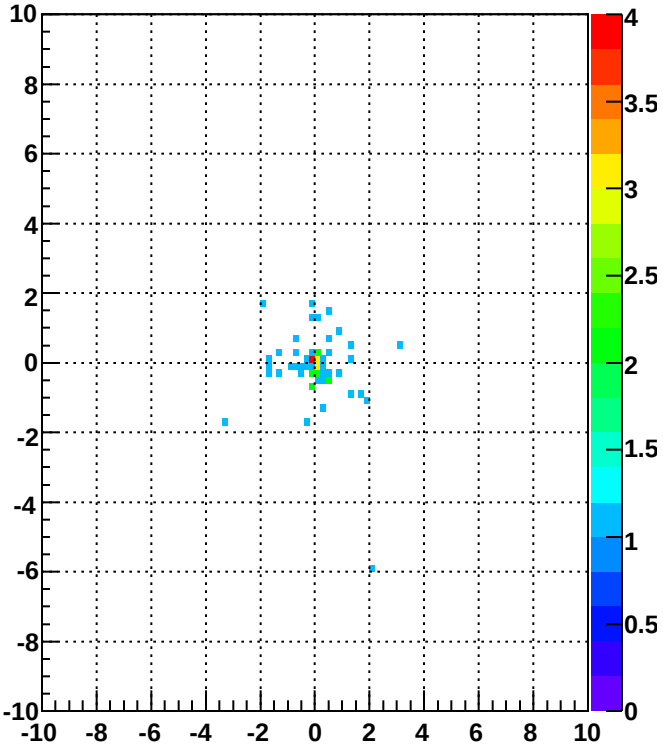
ZDC Vertex vs L3 Vertex



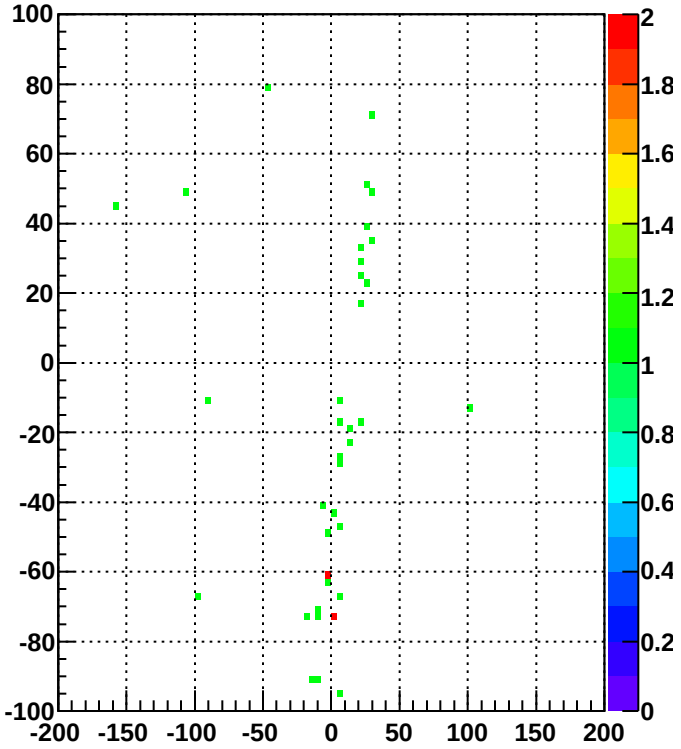
BBC Vertex vs L3 Vertex

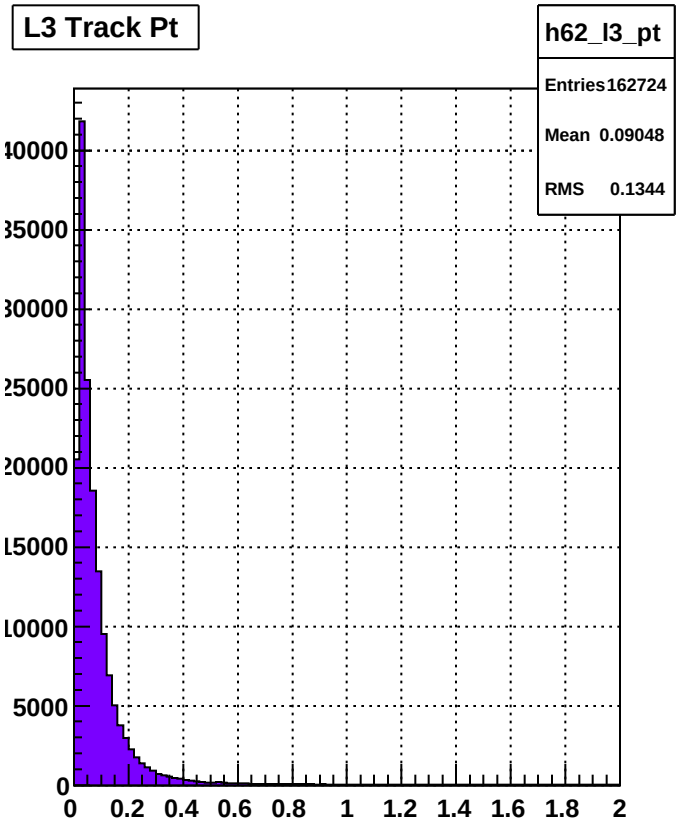


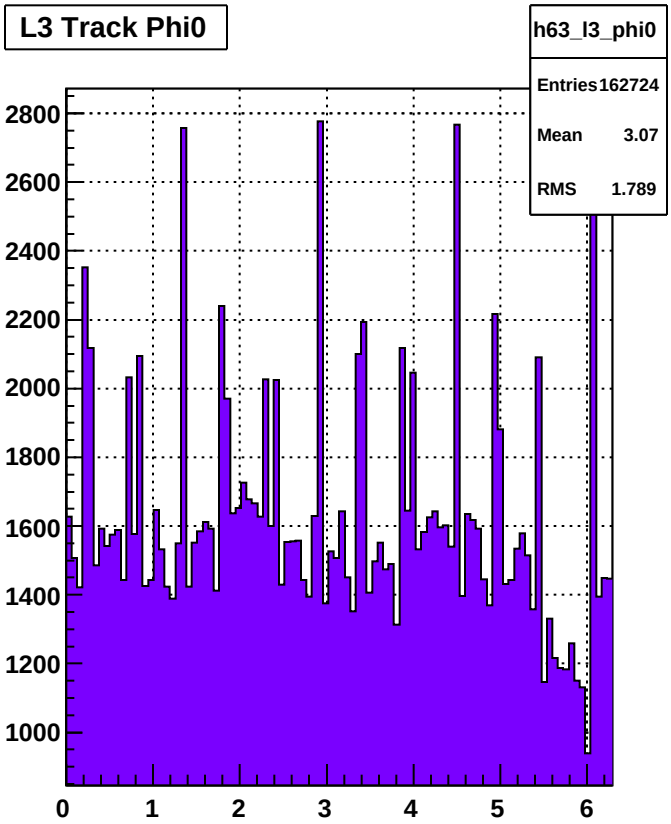
L3 Vertex Y vs X

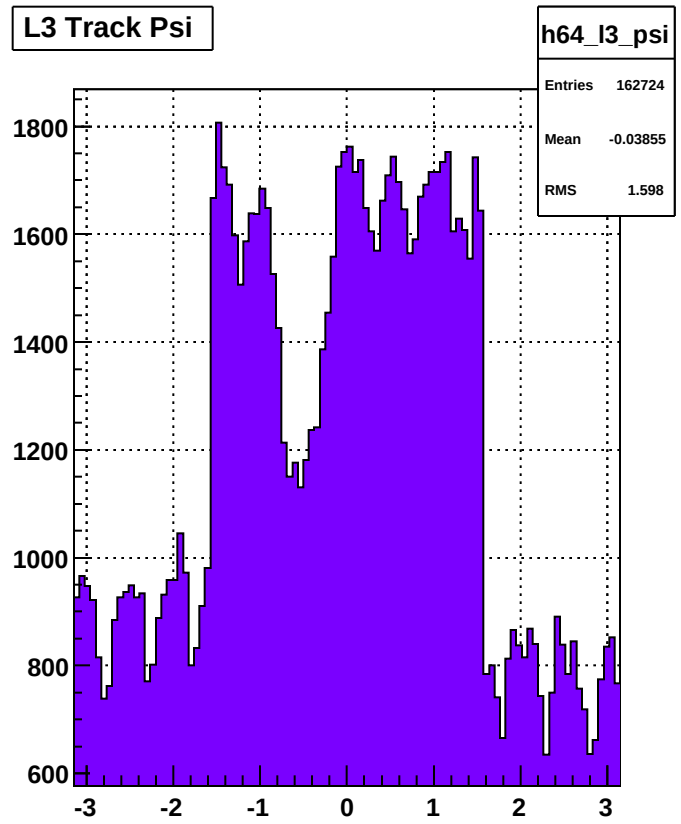


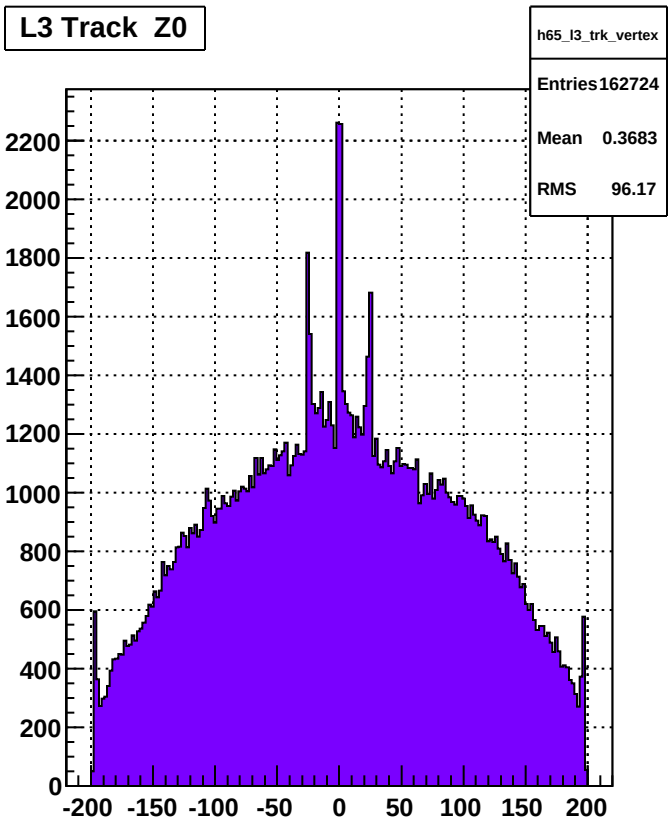
L3 Vertex-Z vs ZDC Timing difference

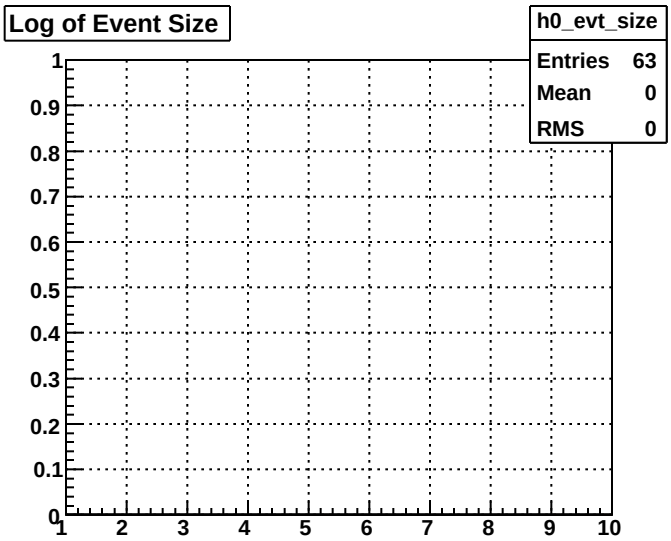


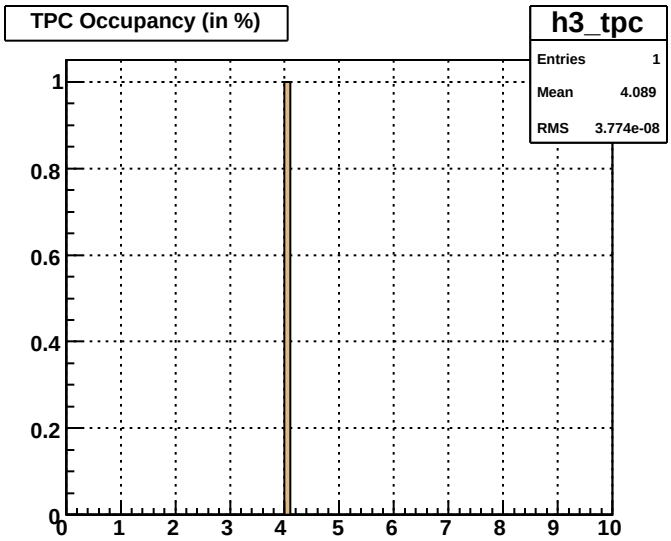

L3 Track Pt
h62_l3_pt
Entries 162724
Mean 0.09048
RMS 0.1344
40000
35000
30000
25000
20000
15000
10000
5000
0
0 0.2 0.4 0.6 0.8 1 1.2 1.4 1.6 1.8 2

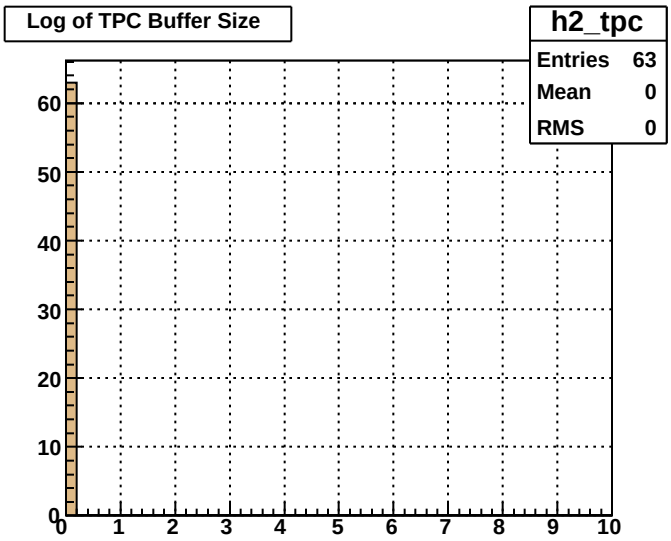

L3 Track Phi0
h63_l3_phi0
Entries 162724
Mean 3.07
RMS 1.789
2800
2600
2400
2200
2000
1800
1600
1400
1200
1000
0 1 2 3 4 5 6

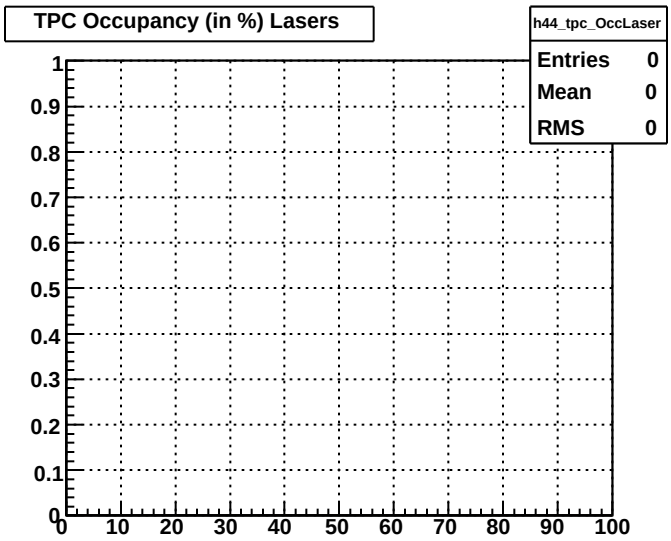

L3 Track Psi
h64_l3_psi
Entries 162724
Mean -0.03855
RMS 1.598
1800
1600
1400
1200
1000
800
600
-3 -2 -1 0 1 2 3

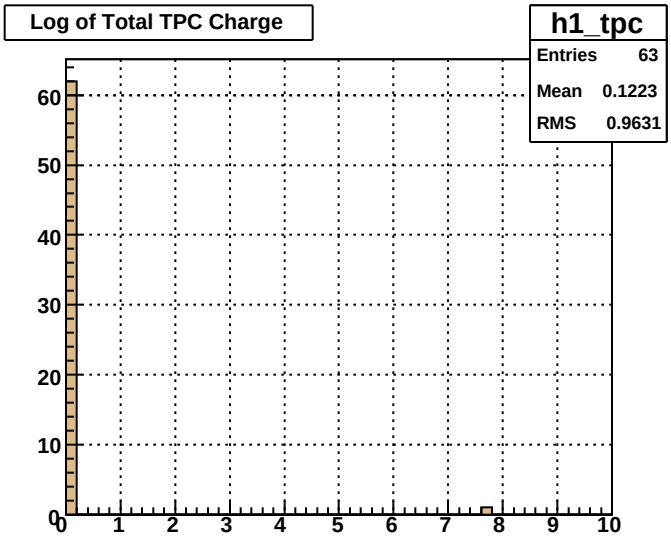

L3 Track Z0
h65_l3_trk_vertex
Entries 162724
Mean 0.3683
RMS 96.17
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
-200 -150 -100 -50 0 50 100 150 200

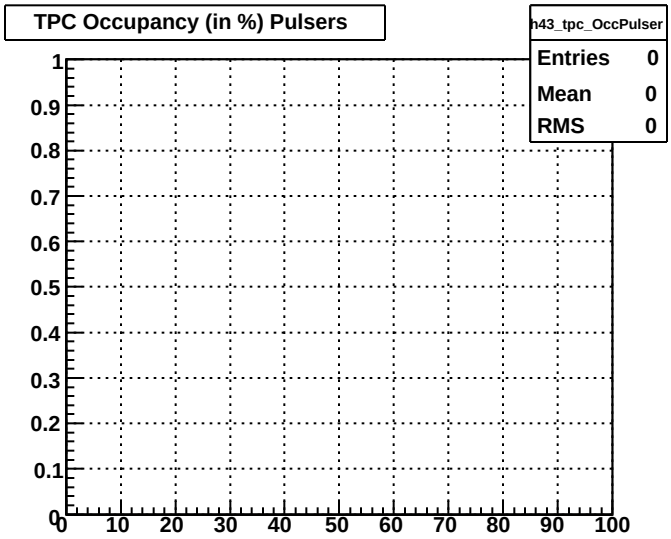

Log of Event Size
h0_evt_size
Entries 63
Mean 0
RMS 0


TPC Occupancy (in %)
h3_tpc
Entries 1
Mean 4.089
RMS 3.774e-08

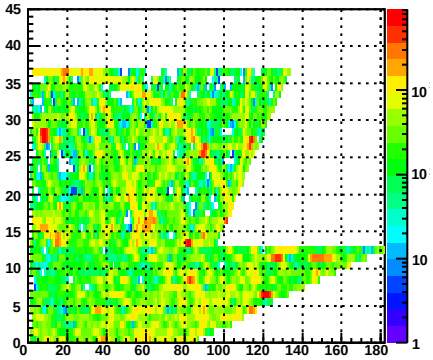

Log of TPC Buffer Size
h2_tpc
Entries 63
Mean 0
RMS 0


TPC Occupancy (in %) Lasers
h44_tpc_OccLaser
Entries 0
Mean 0
RMS 0

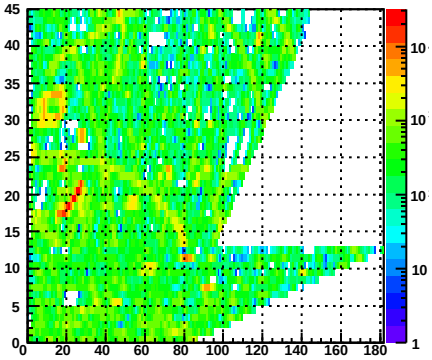

Log of Total TPC Charge
h1_tpc
Entries 63
Mean 0.1223
RMS 0.9631


TPC Occupancy (in %) Pulsers
h43_tpc_OccPulser
Entries 0
Mean 0
RMS 0

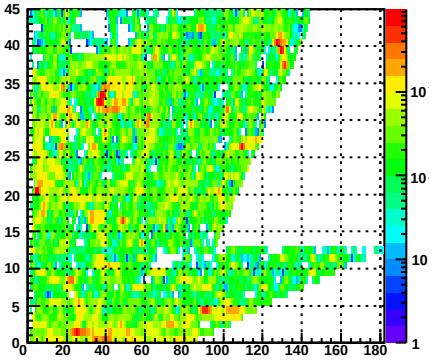
Sec. 1 charge per pad



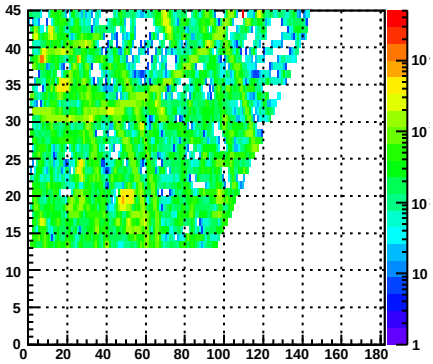
Sec. 2 charge per pad



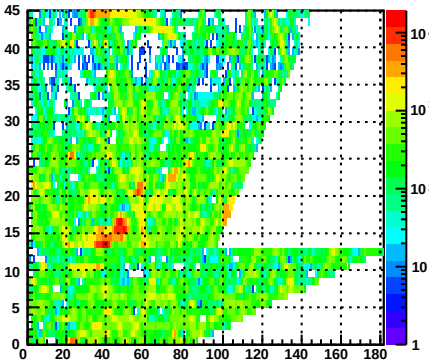
Sec. 3 charge per pad



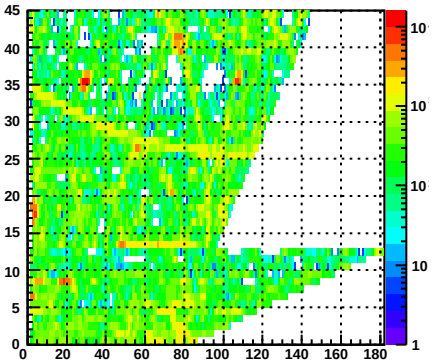
Sec. 4 charge per pad



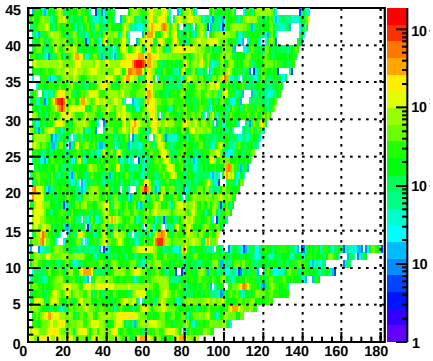
Sec. 5 charge per pad



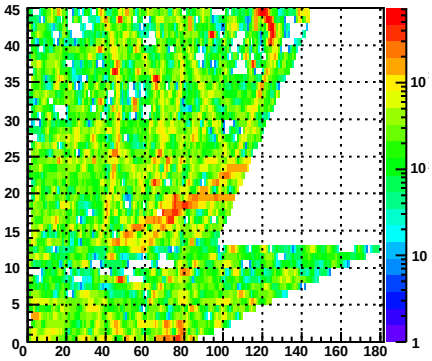
Sec. 6 charge per pad



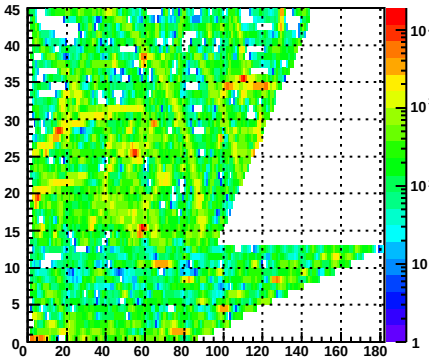
Sec. 7 charge per pad



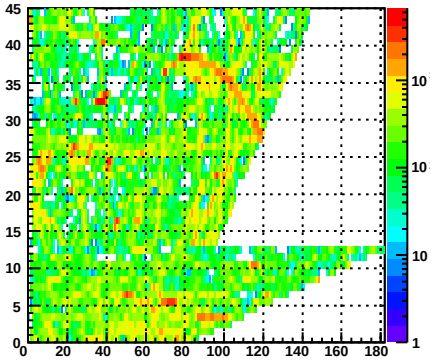
Sec. 8 charge per pad



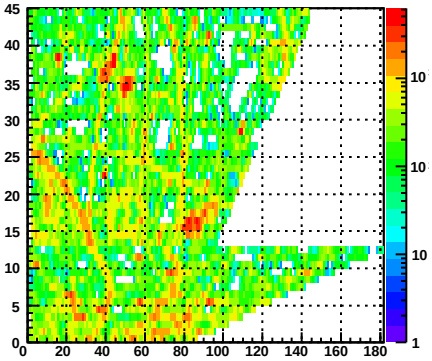
Sec. 9 charge per pad



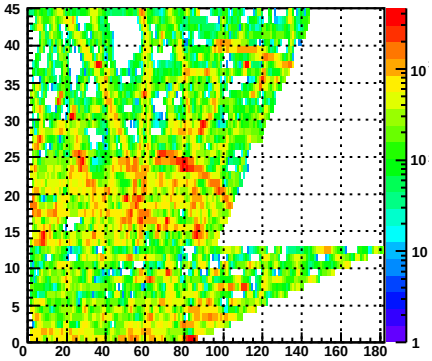
Sec. 10 charge per pad



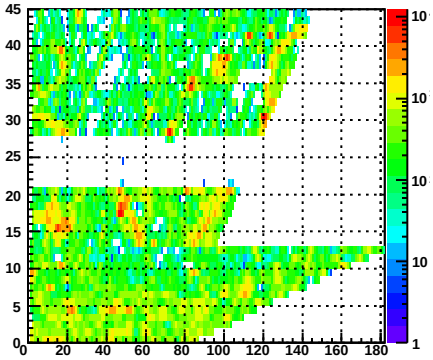
Sec. 11 charge per pad



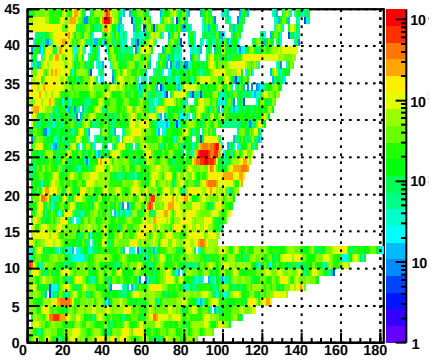
Sec. 12 charge per pad



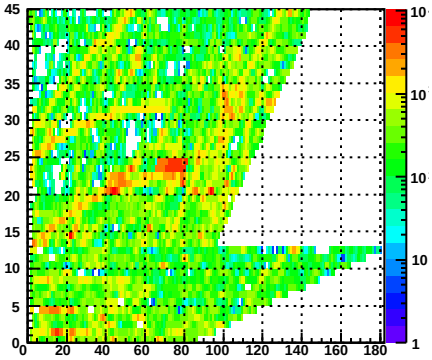
Sec. 13 charge per pad



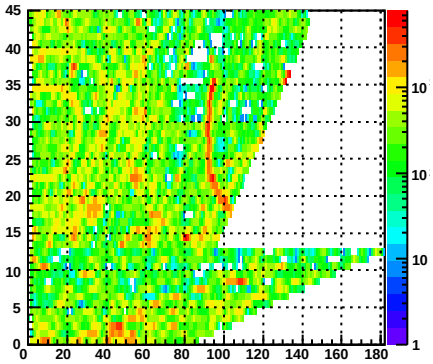
Sec. 14 charge per pad



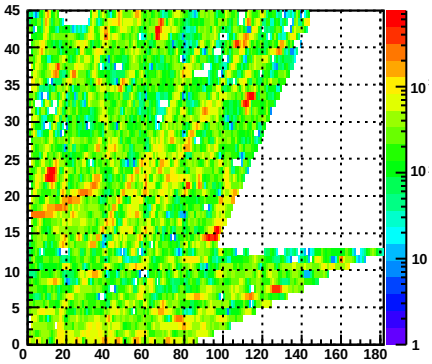
Sec. 15 charge per pad



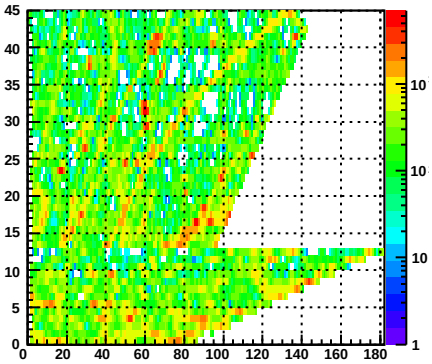
Sec. 16 charge per pad



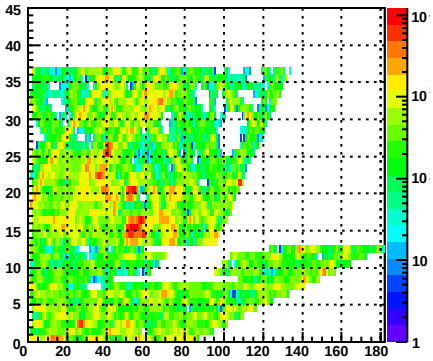
Sec. 17 charge per pad



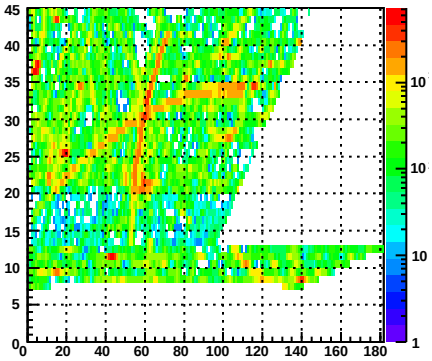
Sec. 18 charge per pad



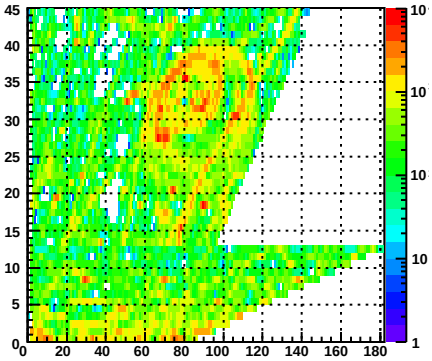
Sec. 19 charge per pad



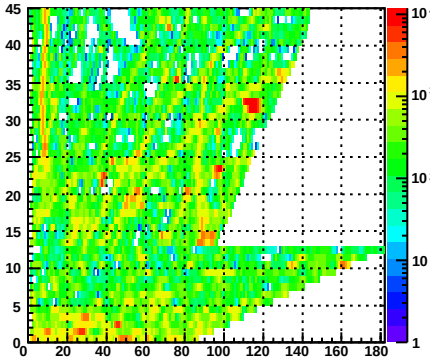
Sec. 20 charge per pad



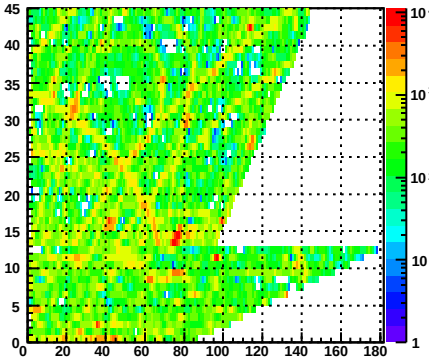
Sec. 21 charge per pad



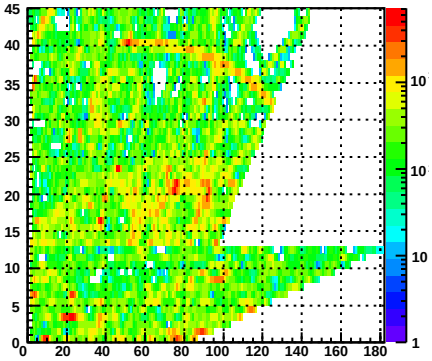
Sec. 22 charge per pad

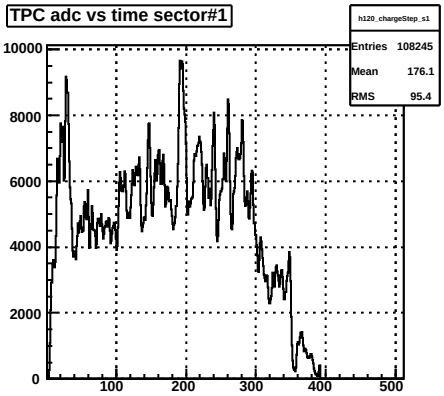


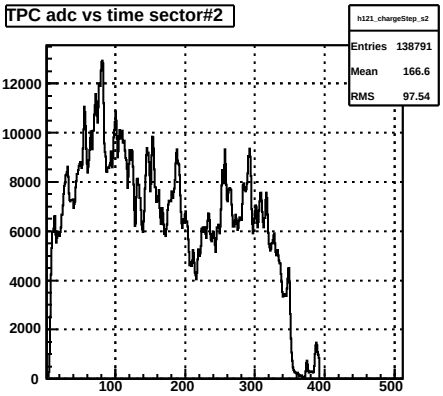
Sec. 23 charge per pad

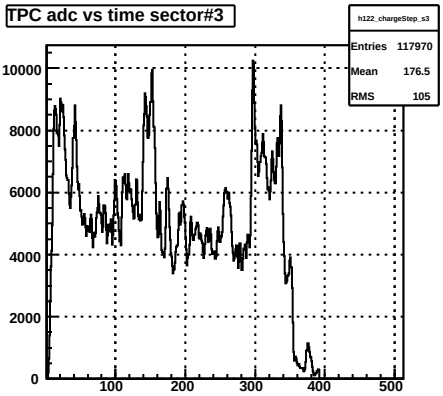


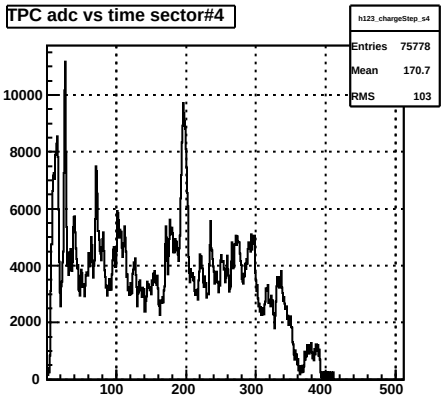
Sec. 24 charge per pad

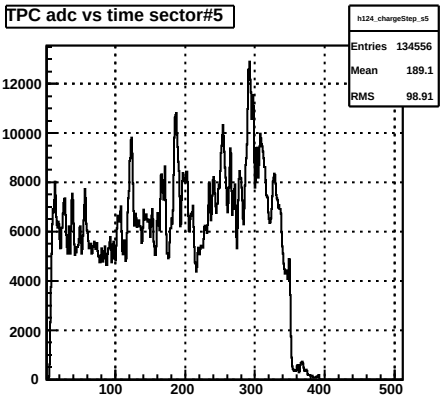


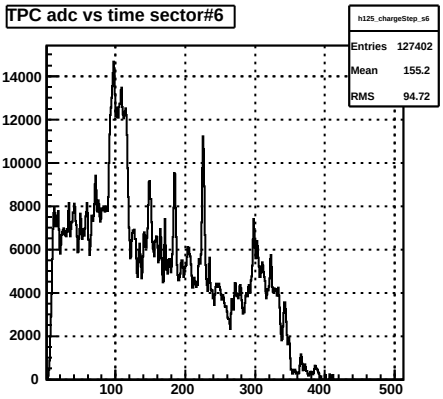

TPC adc vs time sector#1
h120_chargeStep_s1
Entries 108245
Mean 176.1
RMS 95.4

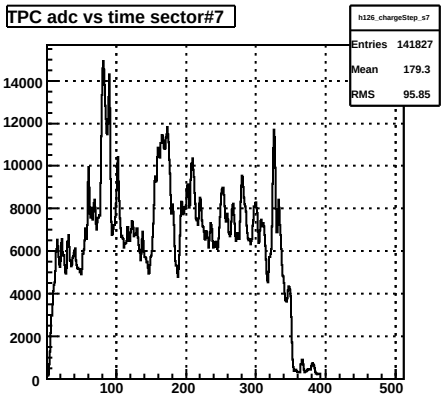

TPC adc vs time sector#2
h121_chargeStep_s2
Entries 138791
Mean 166.6
RMS 97.54

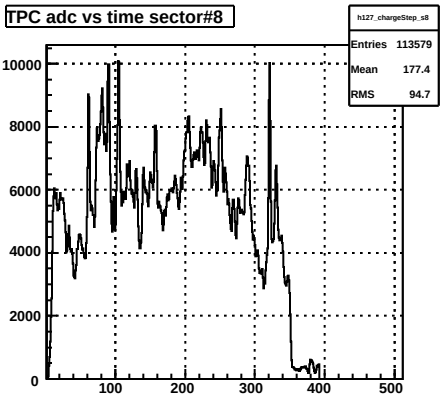

TPC adc vs time sector#3
h122_chargeStep_s3
Entries 117970
Mean 176.5
RMS 105

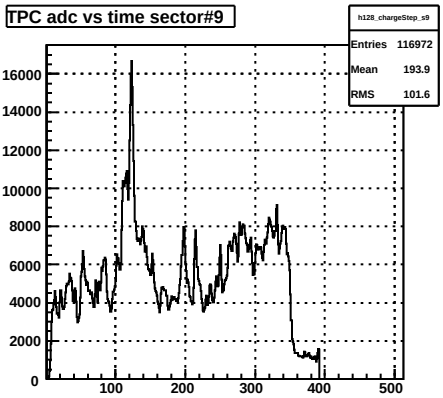

TPC adc vs time sector#4
h123_chargeStep_s4
Entries 75778
Mean 170.7
RMS 103

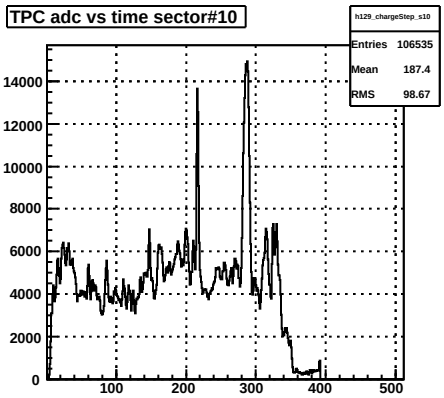

TPC adc vs time sector#5
h124_chargeStep_s5
Entries 134556
Mean 189.1
RMS 98.91

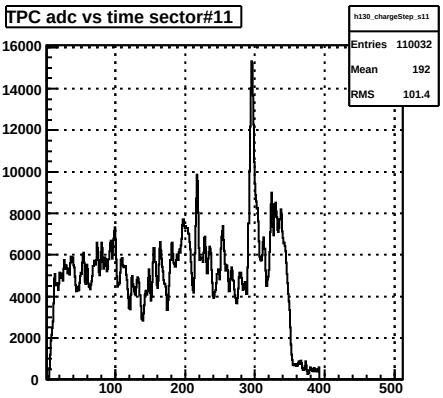

TPC adc vs time sector#6
h125_chargeStep_s6
Entries 127402
Mean 155.2
RMS 94.72

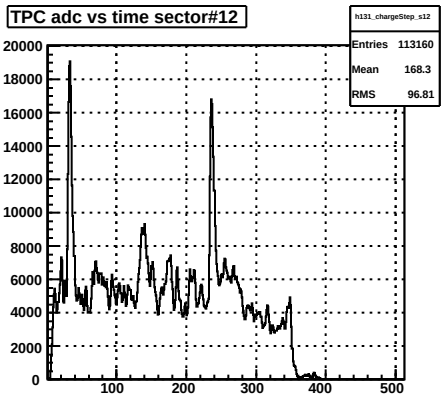

TPC adc vs time sector#7
h126_chargeStep_s7
Entries 141827
Mean 179.3
RMS 95.85

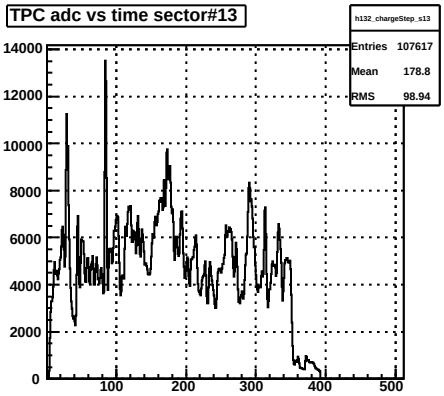

TPC adc vs time sector#8
h127_chargeStep_s8
Entries 113579
Mean 177.4
RMS 94.7

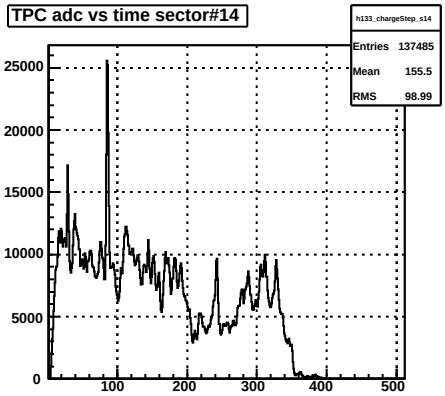

TPC adc vs time sector#9
h128_chargeStep_s9
Entries 116972
Mean 193.9
RMS 101.6

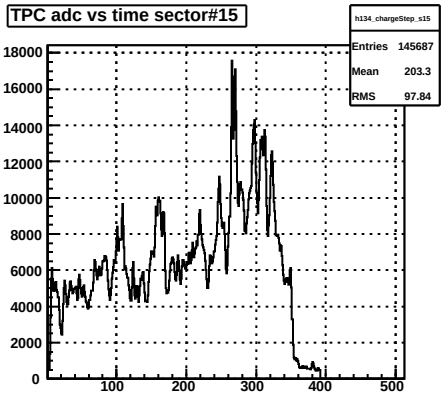

TPC adc vs time sector#10
h129_chargeStep_s10
Entries 106535
Mean 187.4
RMS 98.67

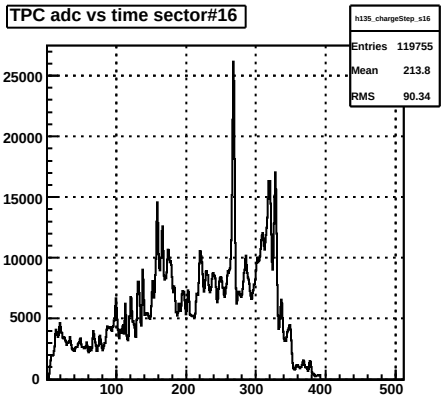

TPC adc vs time sector#11
h130_chargeStep_s11
Entries 110032
Mean 192
RMS 101.4

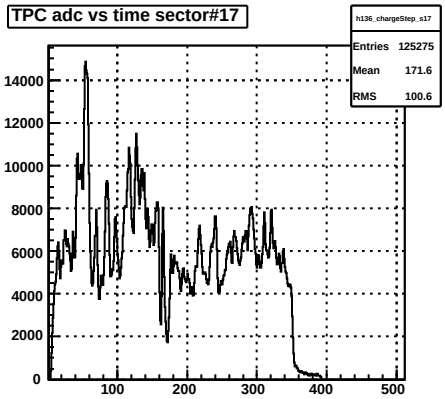

TPC adc vs time sector#12
h131_chargeStep_s12
Entries 113160
Mean 168.3
RMS 96.81

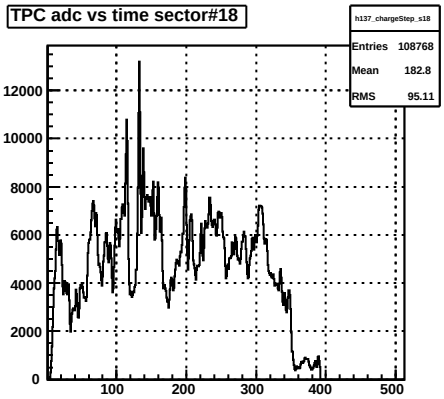

TPC adc vs time sector#13
h132_chargeStep_s13
Entries 107617
Mean 178.8
RMS 98.94

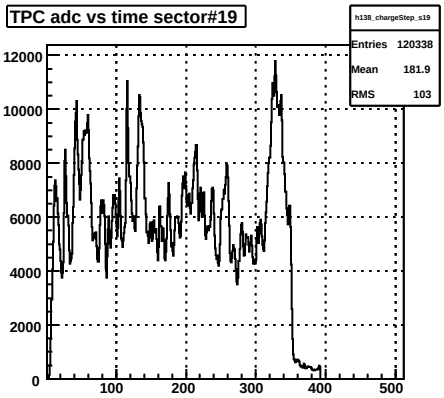

TPC adc vs time sector#14
h133_chargeStep_s14
Entries 137485
Mean 155.5
RMS 98.99

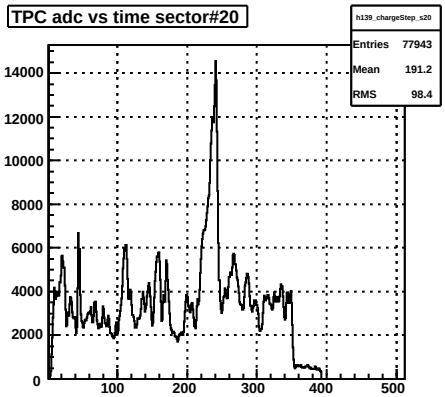

TPC adc vs time sector#15
h134_chargeStep_s15
Entries 145687
Mean 203.3
RMS 97.84

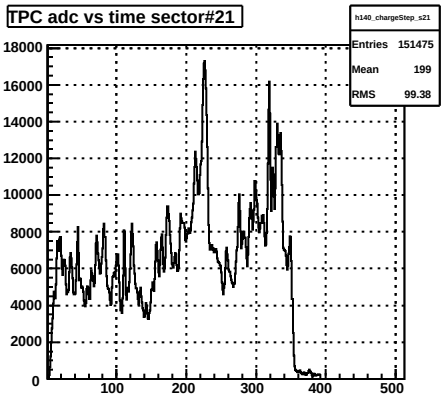

TPC adc vs time sector#16
h135_chargeStep_s16
Entries 119755
Mean 213.8
RMS 90.34

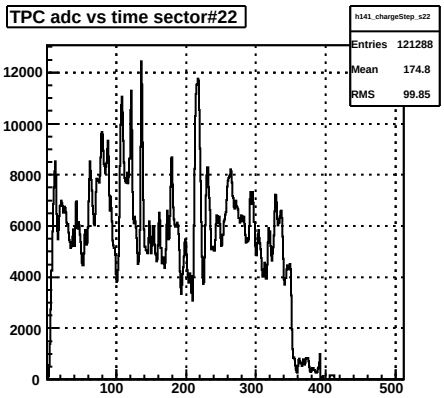

TPC adc vs time sector#17
h136_chargeStep_s17
Entries 125275
Mean 171.6
RMS 100.6

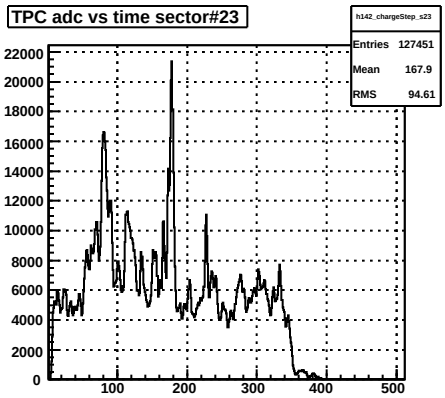

TPC adc vs time sector#18
h137_chargeStep_s18
Entries 108768
Mean 182.8
RMS 95.11

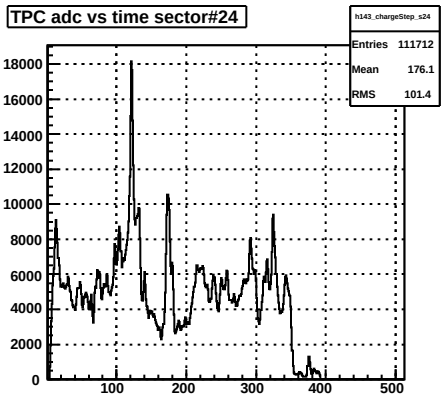

TPC adc vs time sector#19
h138_chargeStep_s19
Entries 120338
Mean 181.9
RMS 103

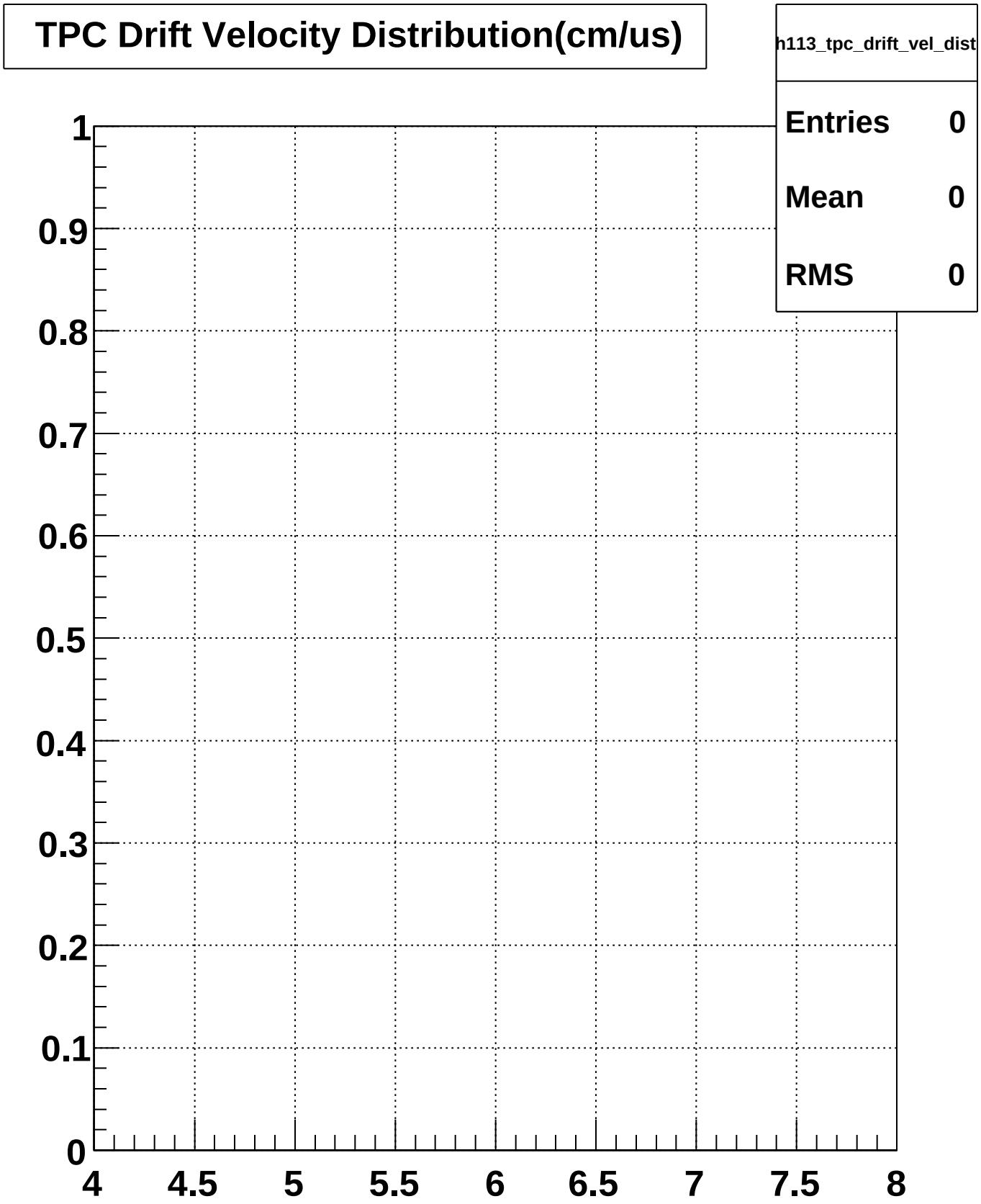

TPC adc vs time sector#20
h139_chargeStep_s20
Entries 77943
Mean 191.2
RMS 98.4

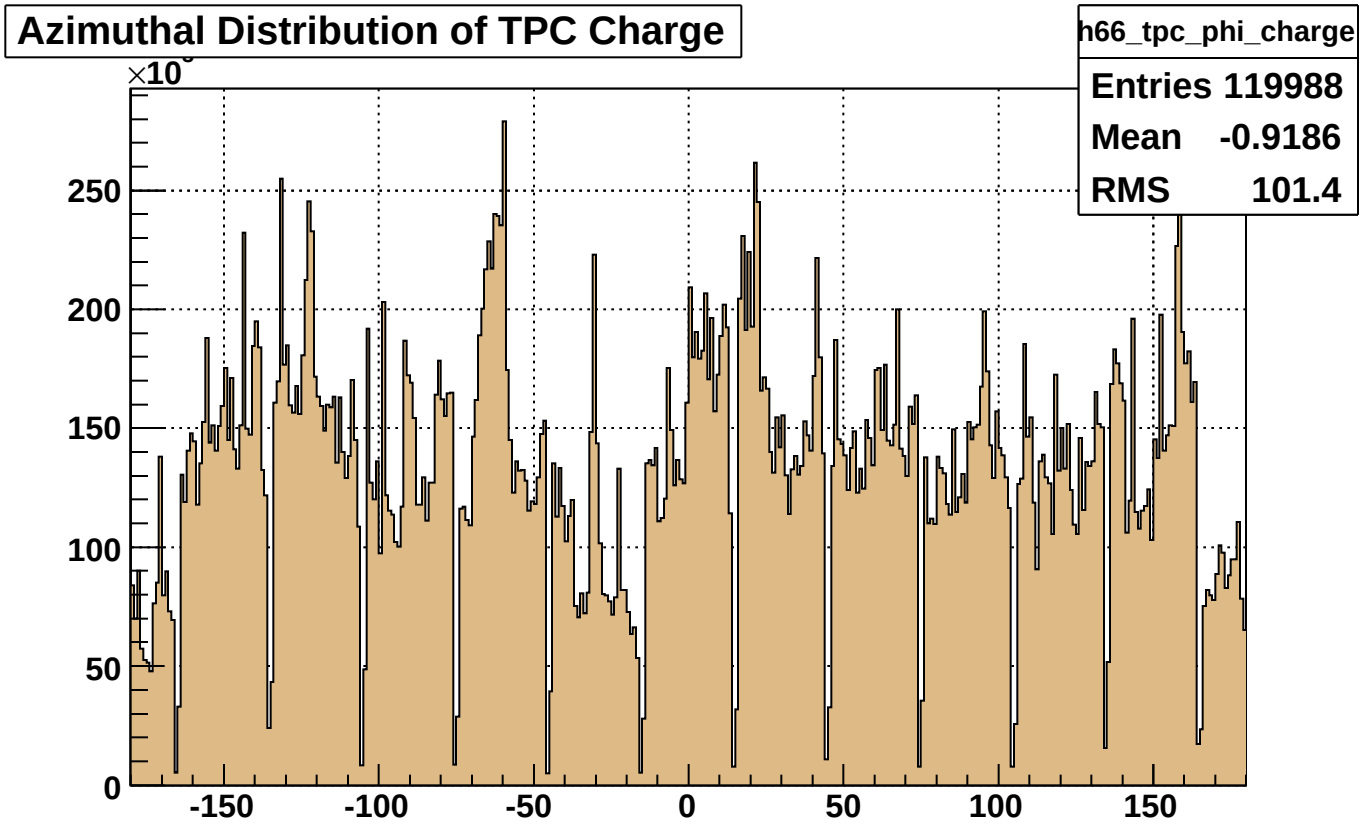

TPC adc vs time sector#21
h140_chargeStep_s21
Entries 151475
Mean 199
RMS 99.38

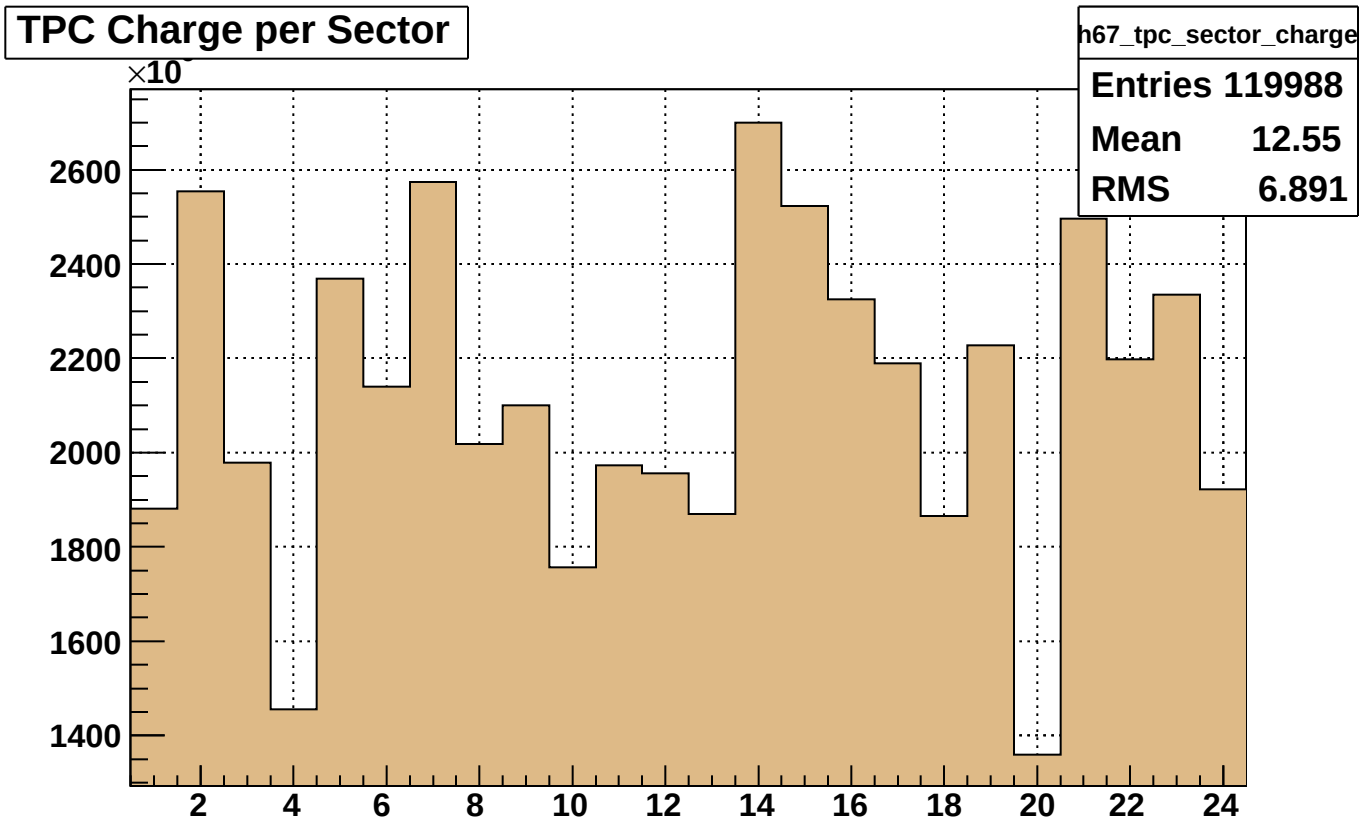

TPC adc vs time sector#22
h141_chargeStep_s22
Entries 121288
Mean 174.8
RMS 99.85


TPC adc vs time sector#23
h142_chargeStep_s23
Entries 127451
Mean 167.9
RMS 94.61


TPC adc vs time sector#24
h143_chargeStep_s24
Entries 111712
Mean 176.1
RMS 101.4


TPC Drift Velocity Distribution(cm/us)
h113_tpc_drift_vel_dist
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
4
4.5
5
5.5
6
6.5
7
7.5
8


Azimuthal Distribution of TPC Charge
h66_tpc_phi_charge
Entries 119988
Mean -0.9186
RMS 101.4
×10
250
200
150
100
50
0
-150
-100
-50
0
50
100
150


TPC Charge per Sector
h67_tpc_sector_charge
Entries 119988
Mean 12.55
RMS 6.891
×10
2600
2400
2200
2000
1800
1600
1400
2
4
6
8
10
12
14
16
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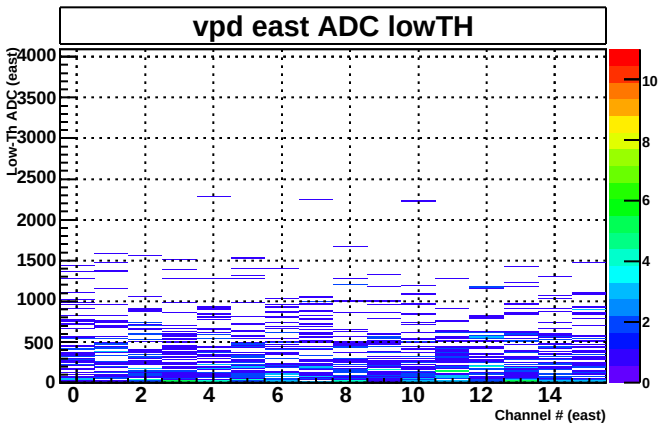
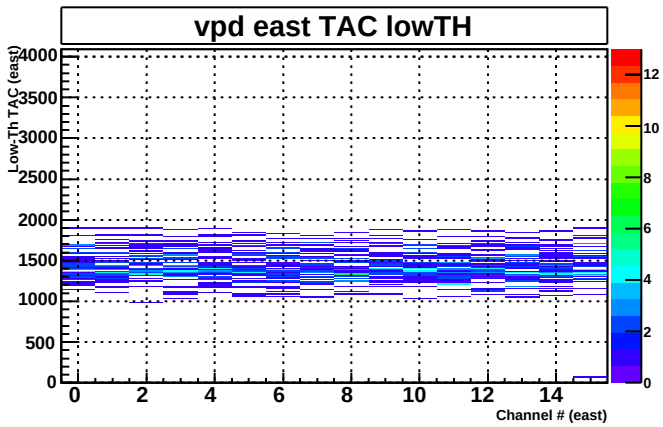
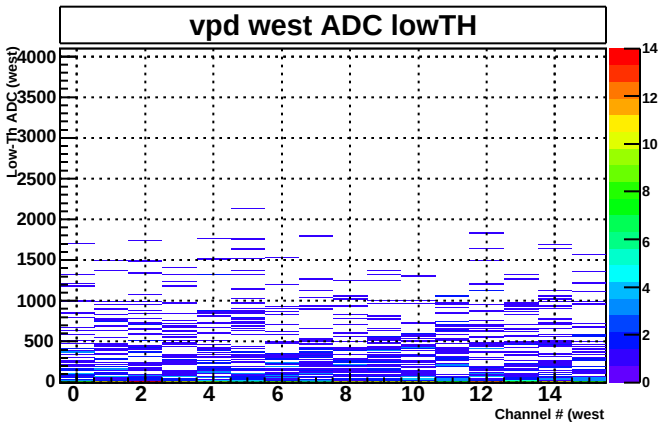
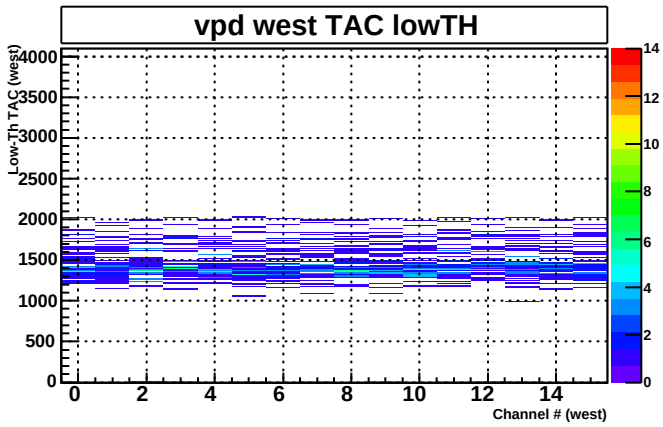
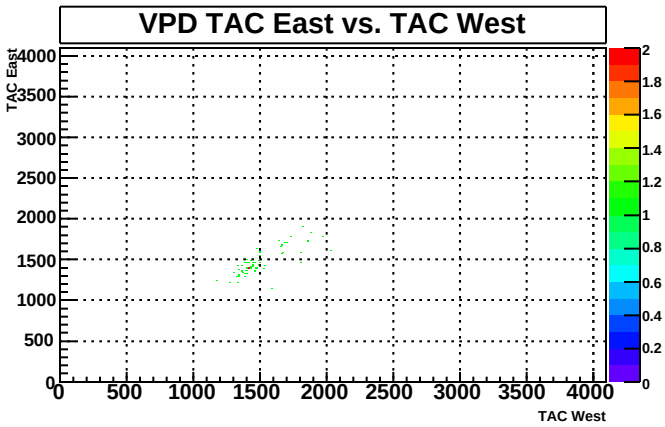
22
24

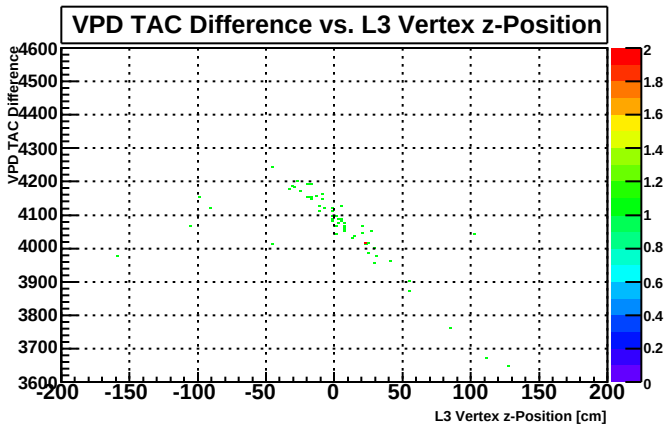

vpd east ADC lowTH
Low-Th ADC (east)
Channel # (east)

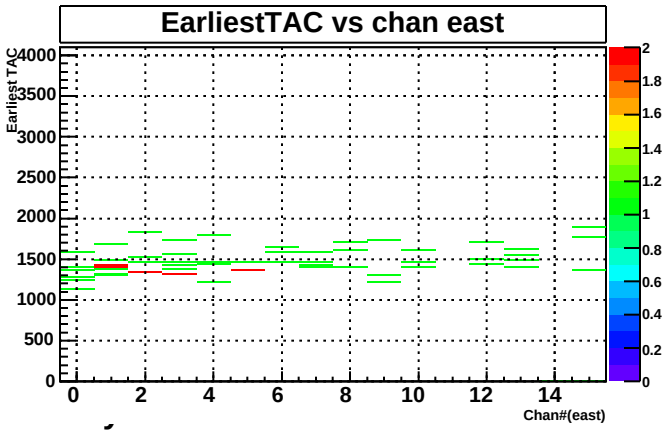

vpd east TAC lowTH
Low-Th TAC (east)
Channel # (east)

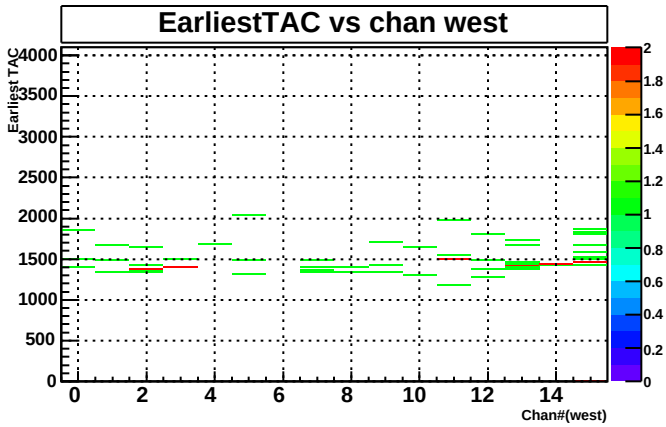

vpd west ADC lowTH
Low-Th ADC (west)
Channel # (west

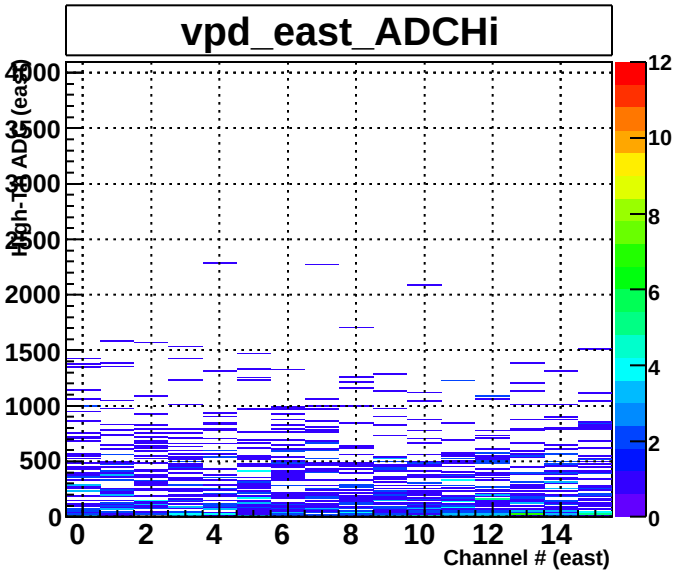

vpd west TAC lowTH
Low-Th TAC (west)
Channel # (west)

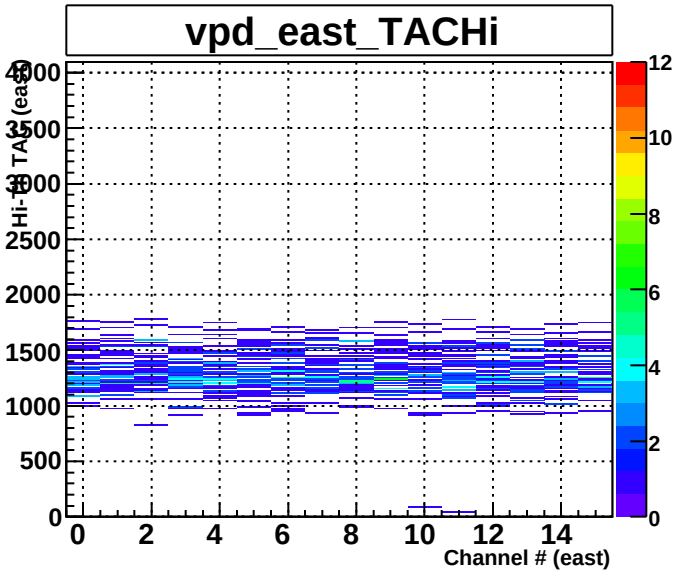

VPD TAC East vs. TAC West
TAC East
TAC West

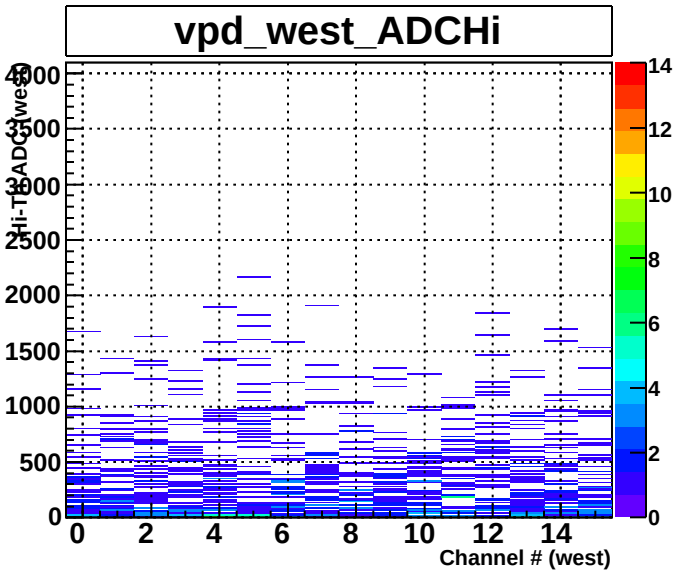

VPD TAC Difference vs. L3 Vertex z-Position
VPD TAC Difference
L3 Vertex z-Position [cm]

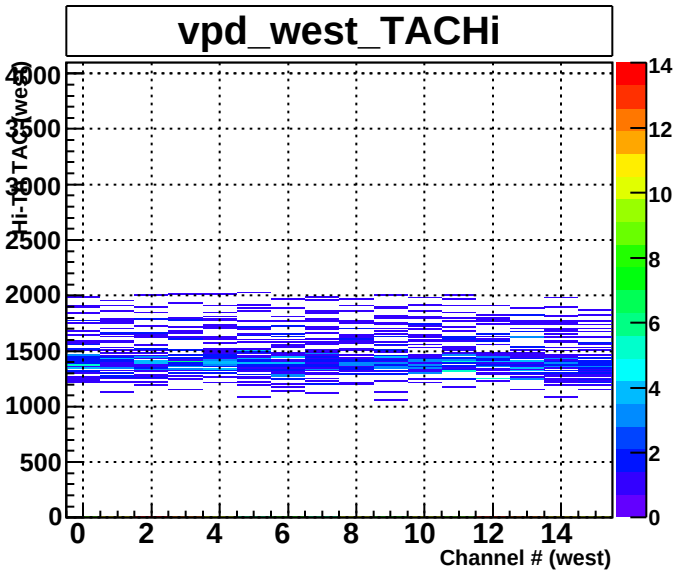

EarliestTAC vs chan east
Earliest TAC
Chan#(east)

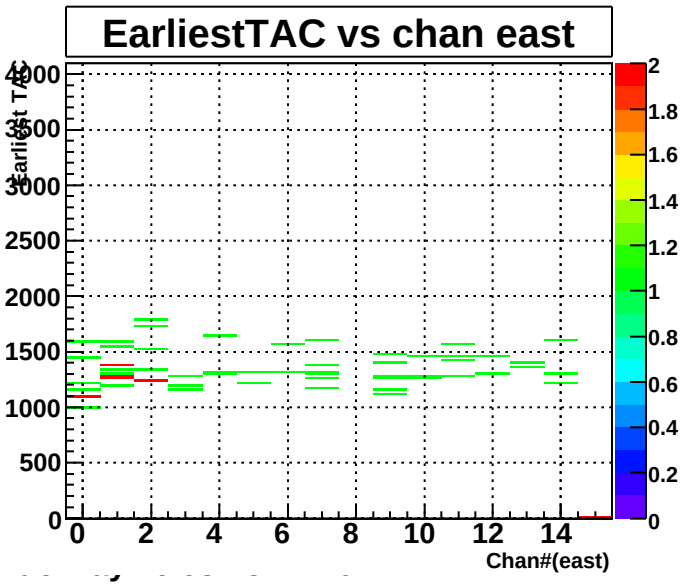

EarliestTAC vs chan west
Earliest TAC
Chan#(west)

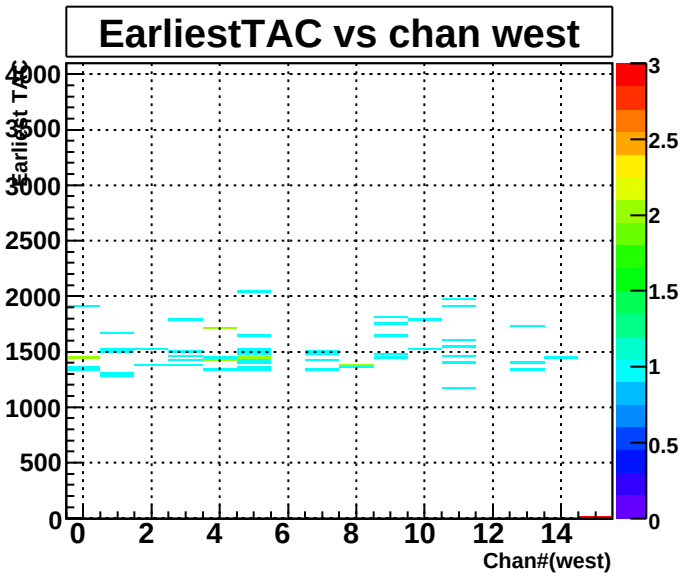

vpd_east_ADCHi
High-Hi ADC (east)
Channel # (east)

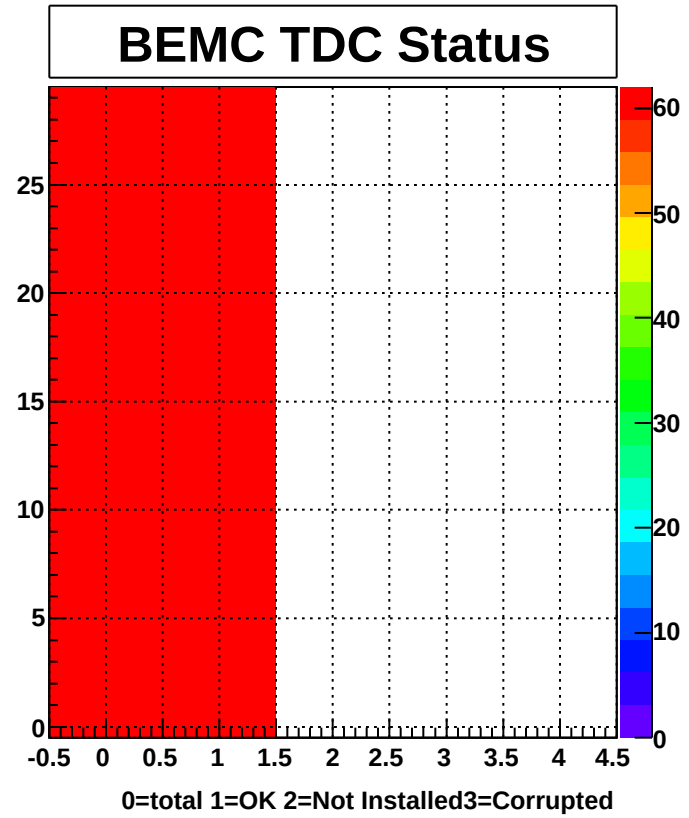

vpd_east_TACHi
Hi-Hi TAC (east)
Channel # (east)

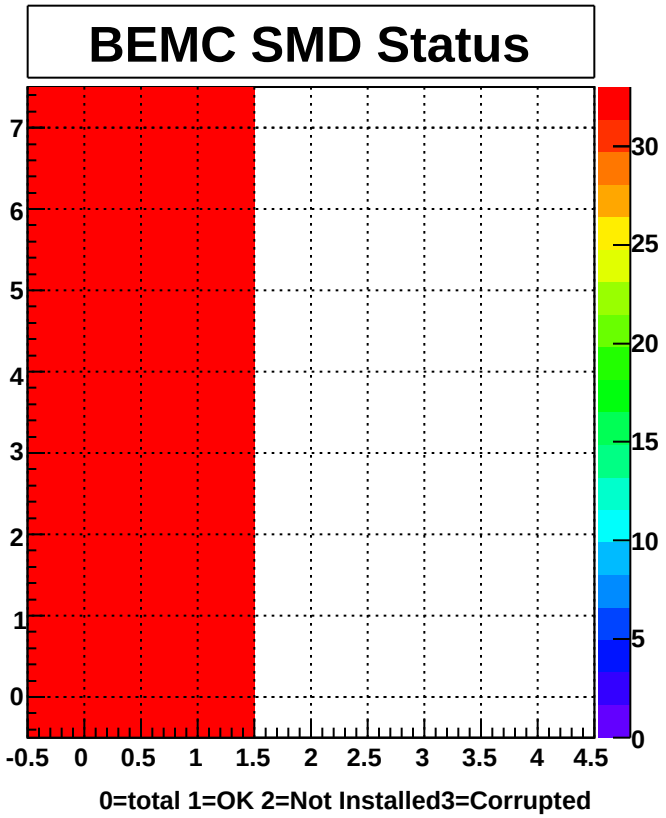

vpd_west_ADCHi
Hi-Hi ADC (west)
Channel # (west)

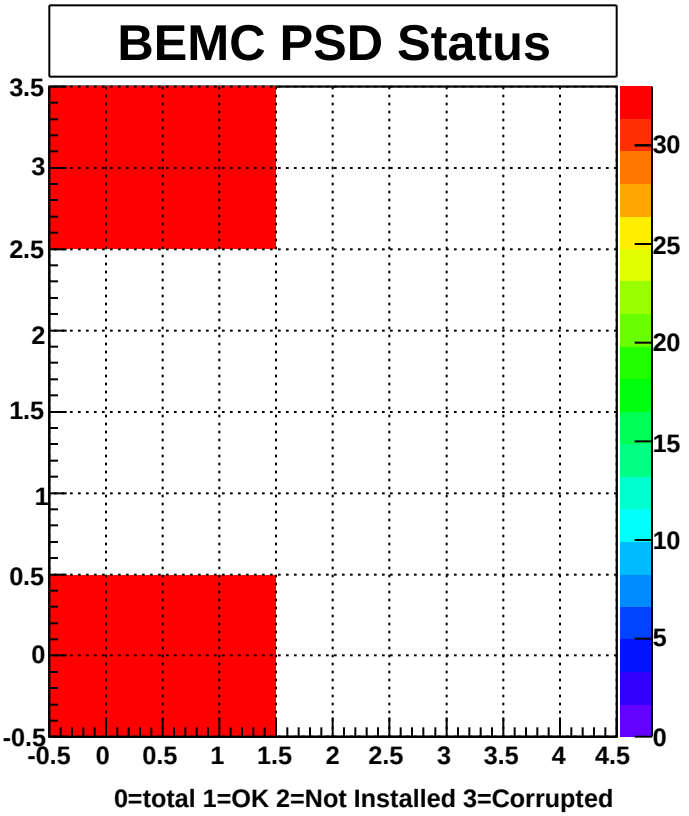

vpd_west_TACHi
Hi-Hi TAC (west)
Channel # (west)

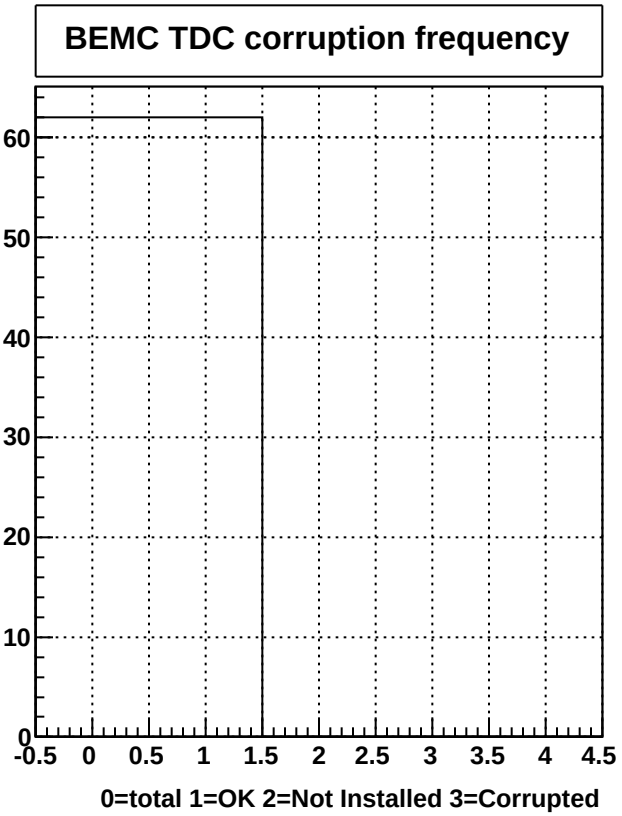

EarliestTAC vs chan east
Earliest TAC
Chan#(east)


EarliestTAC vs chan west
Earliest TAC
Chan#(west)









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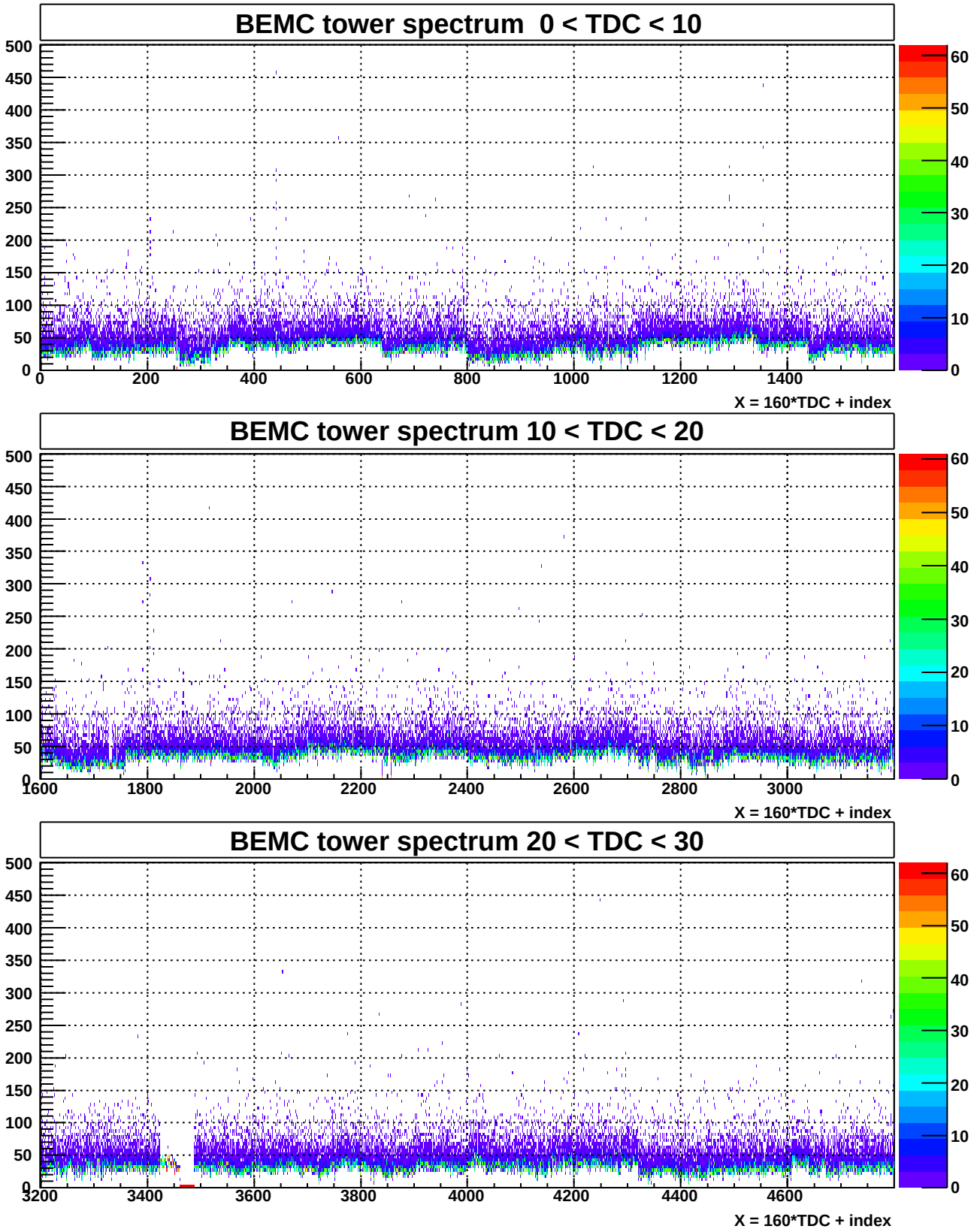

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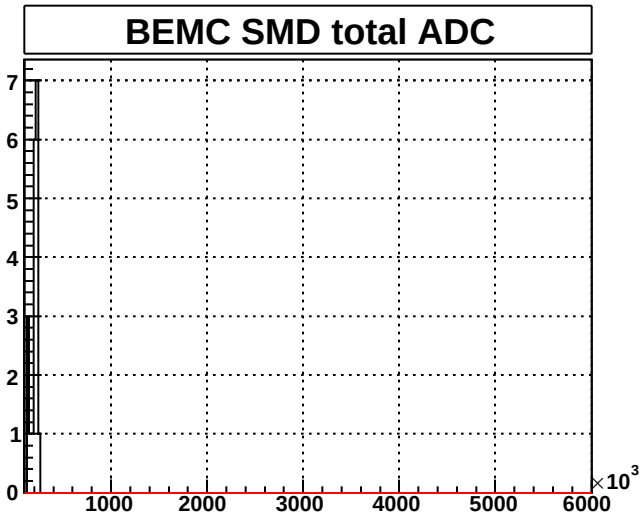

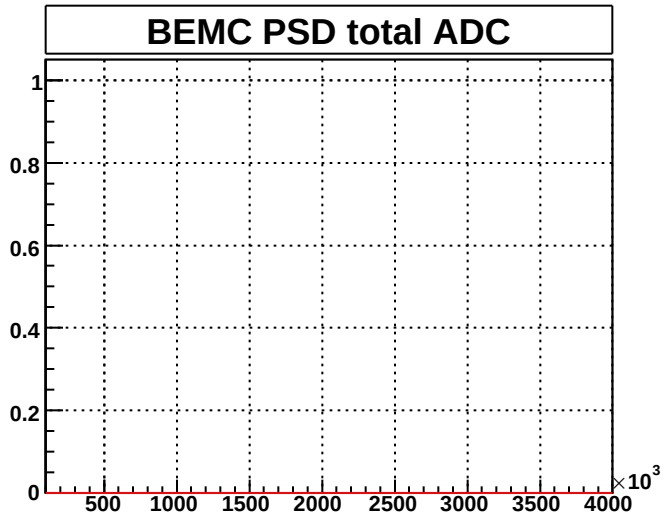

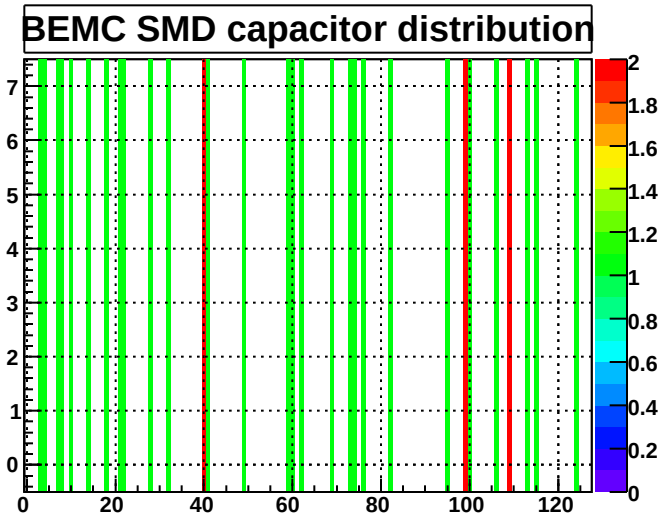

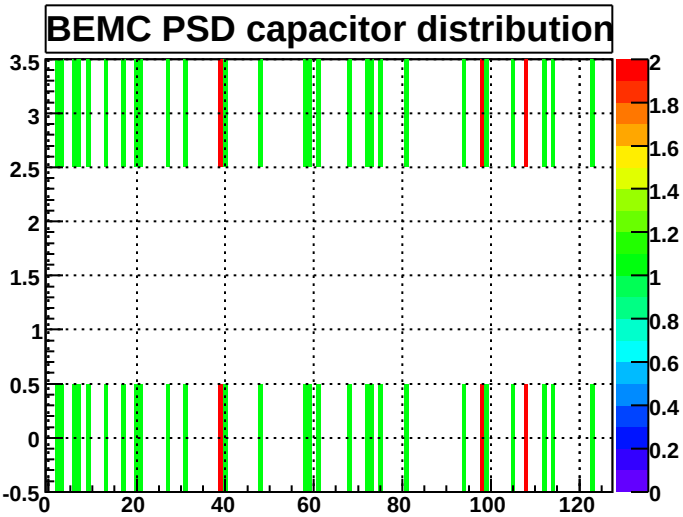

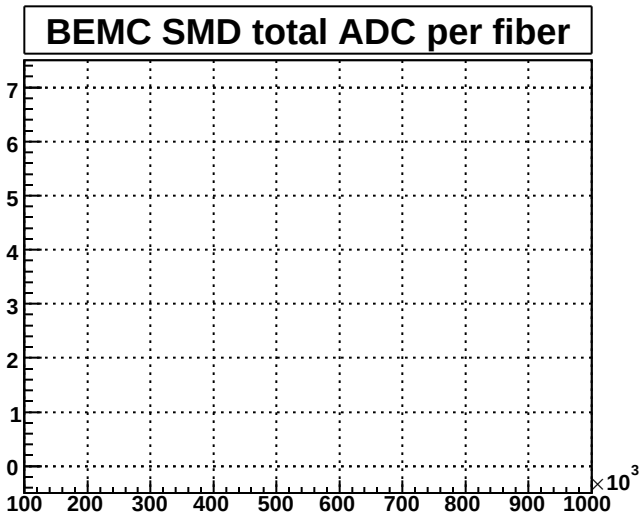

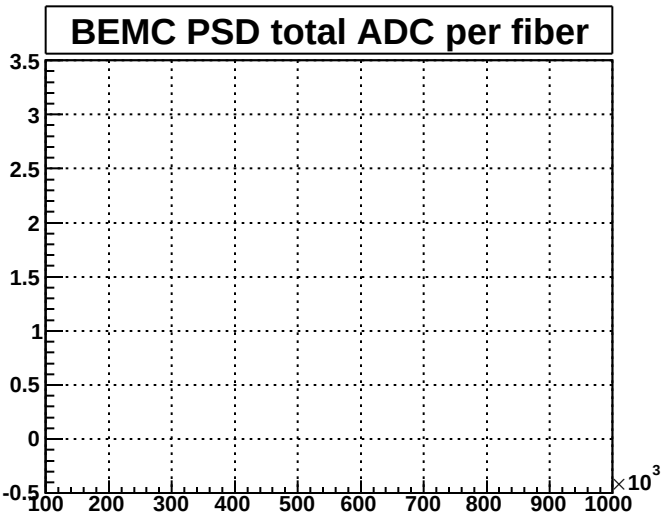

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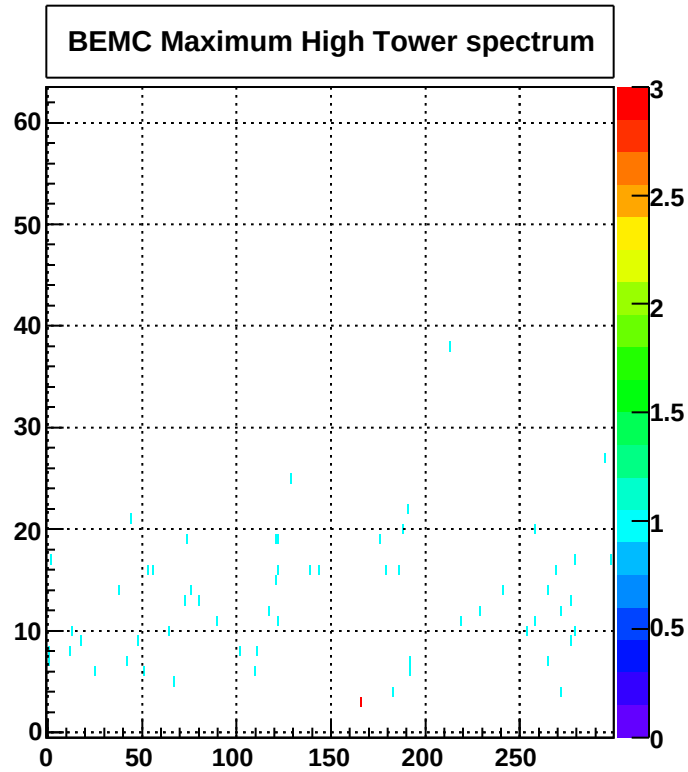

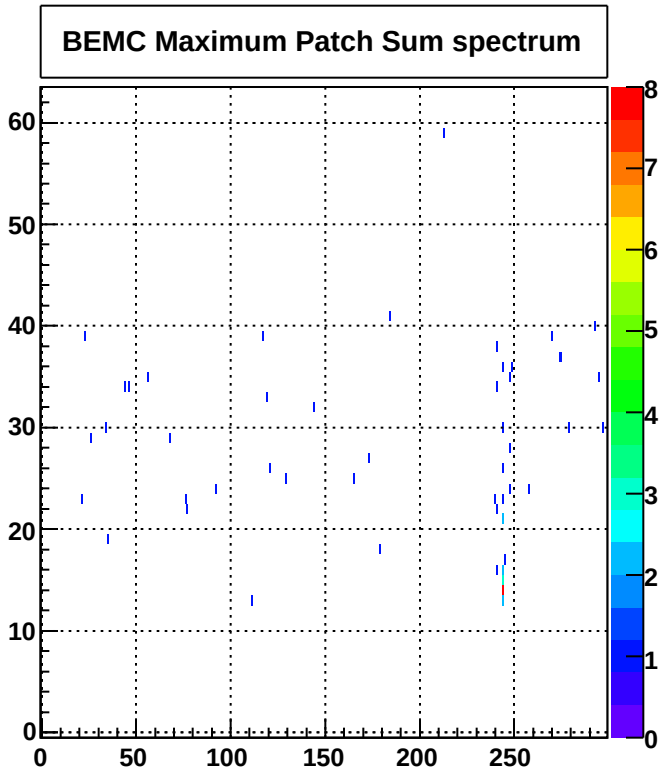

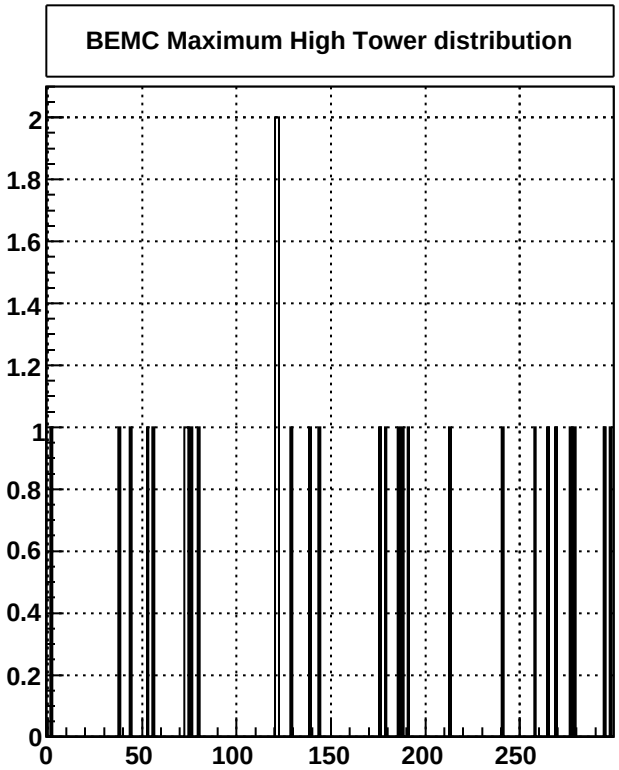

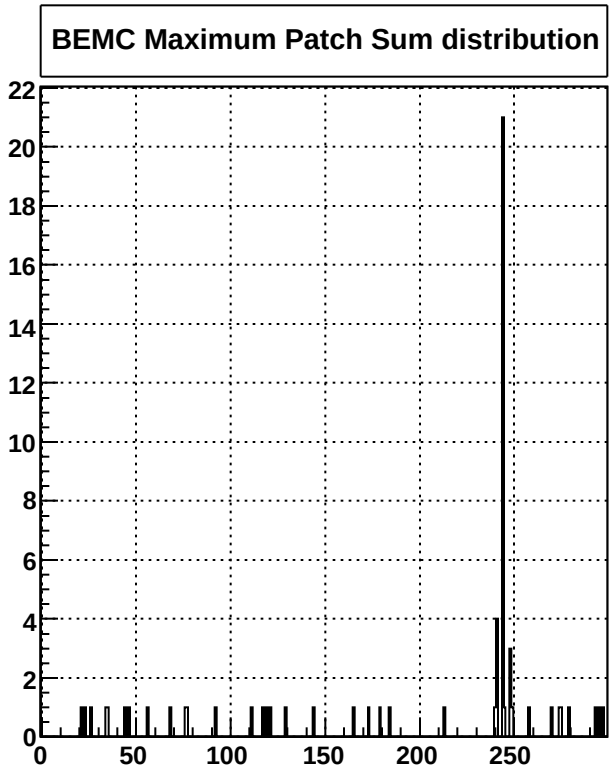

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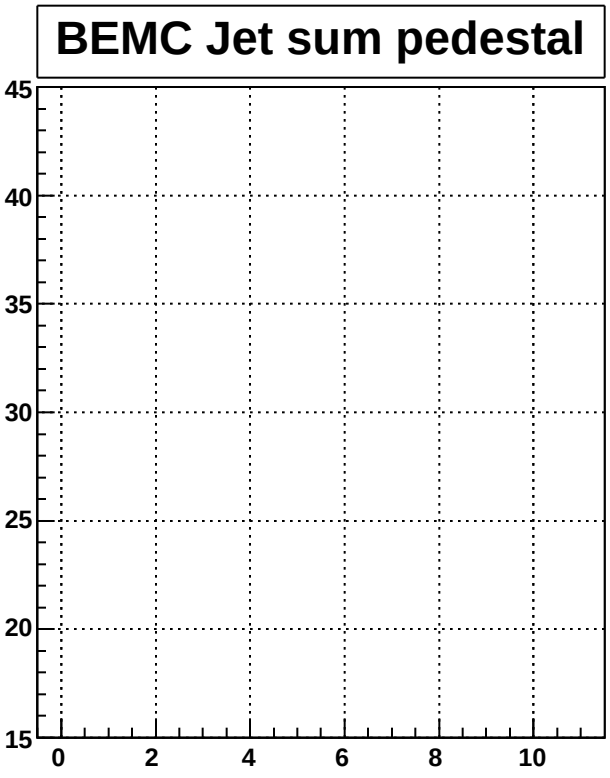

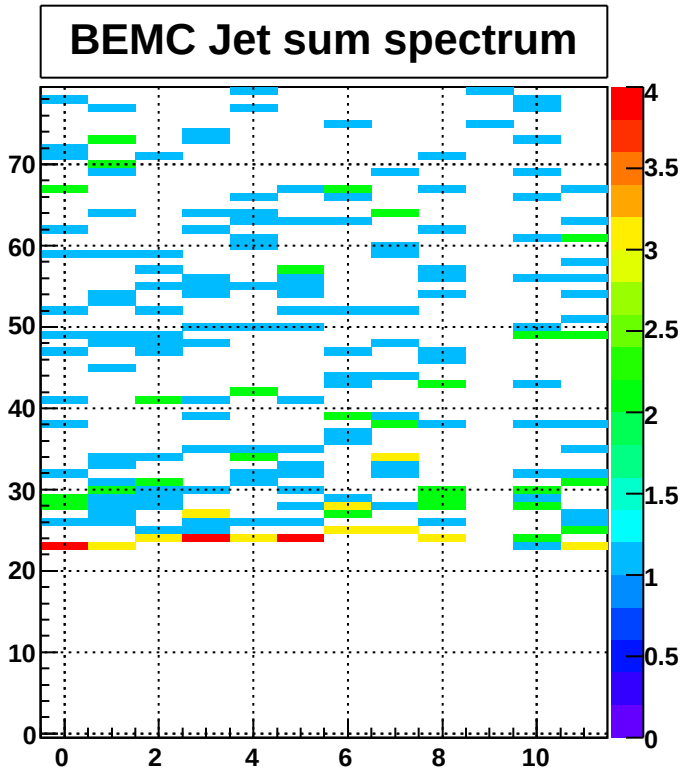

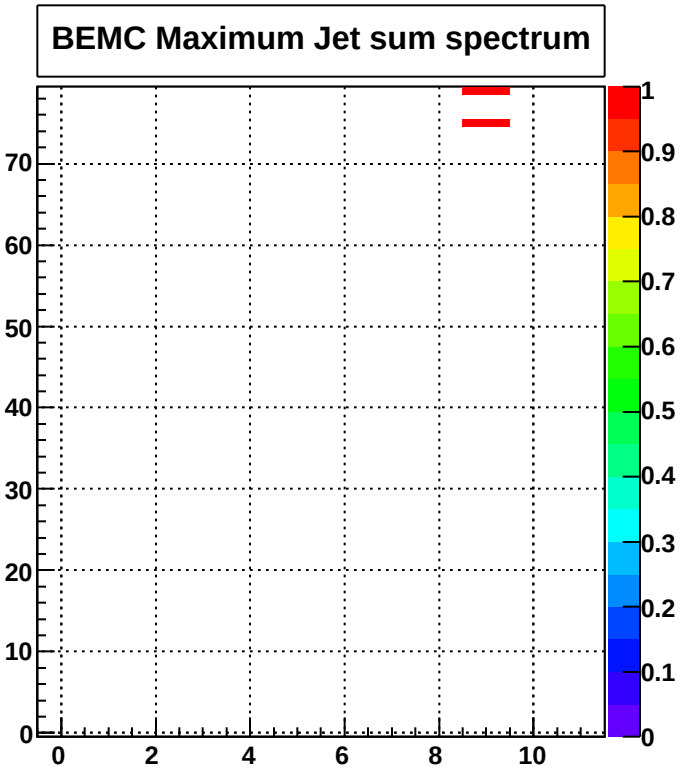

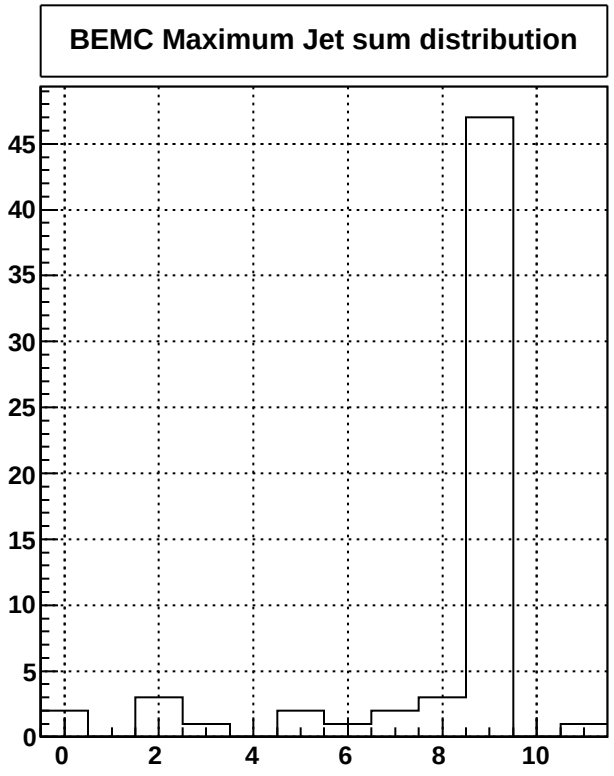

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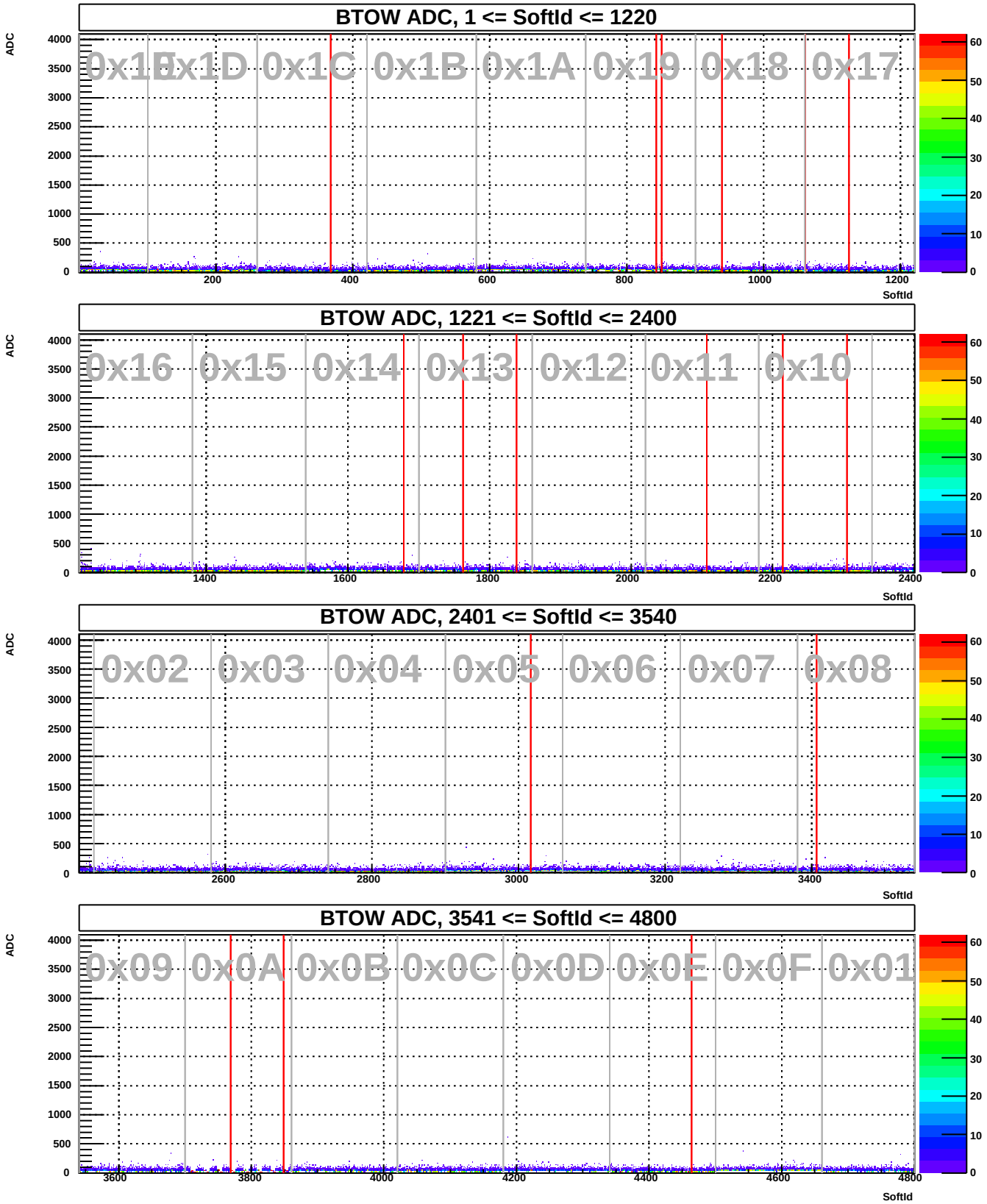
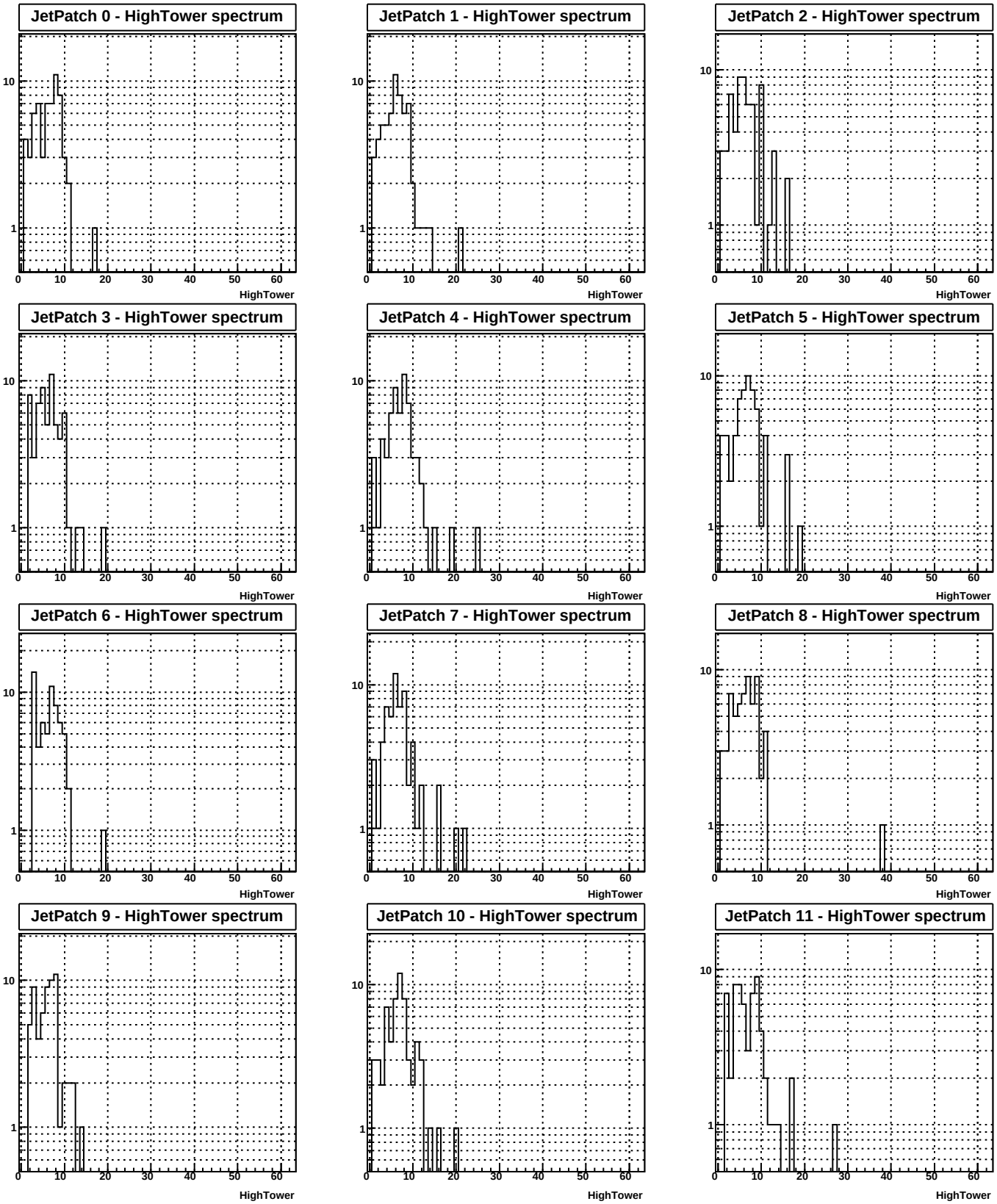
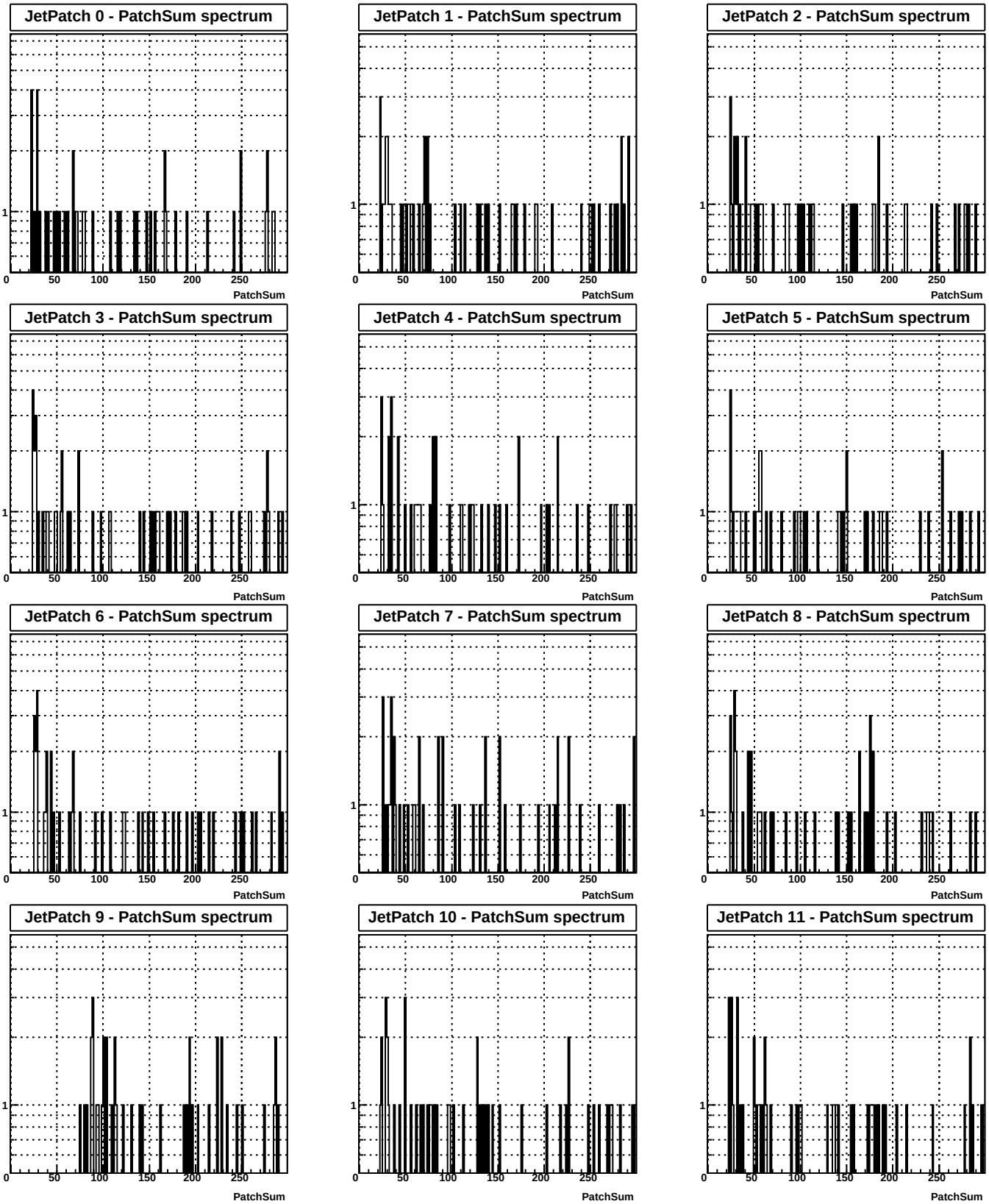


Tue May 10 08:45:24 2011



Tue May 10 08:45:25 2011



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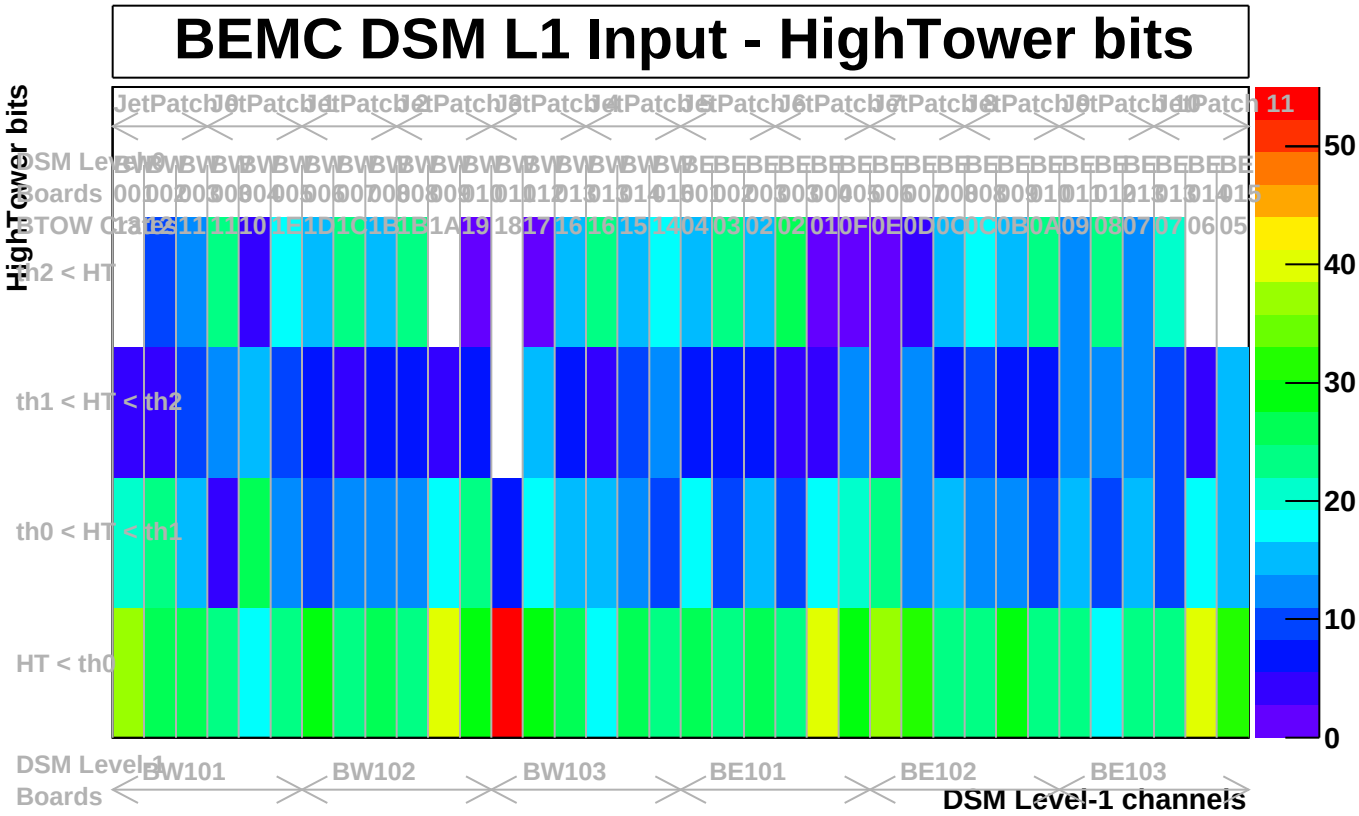

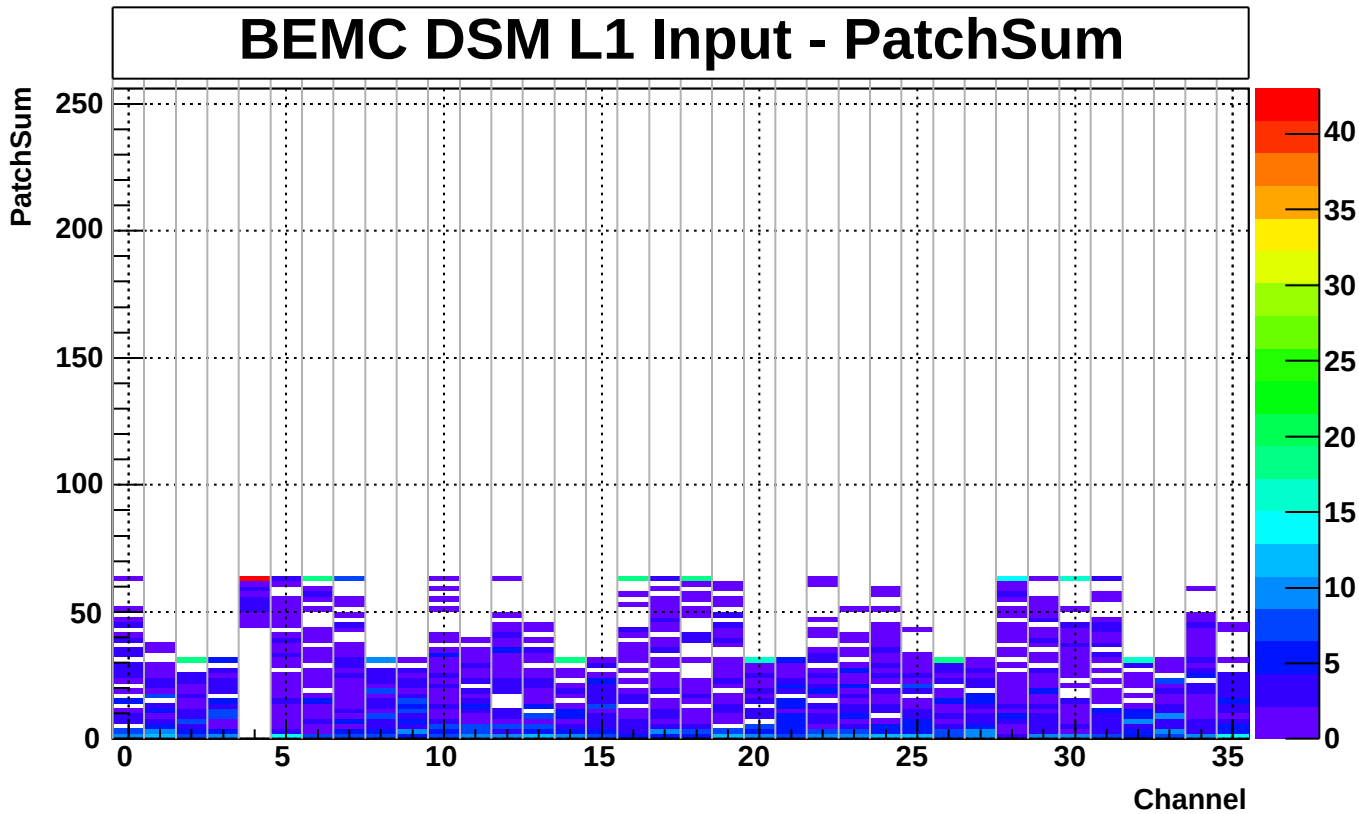

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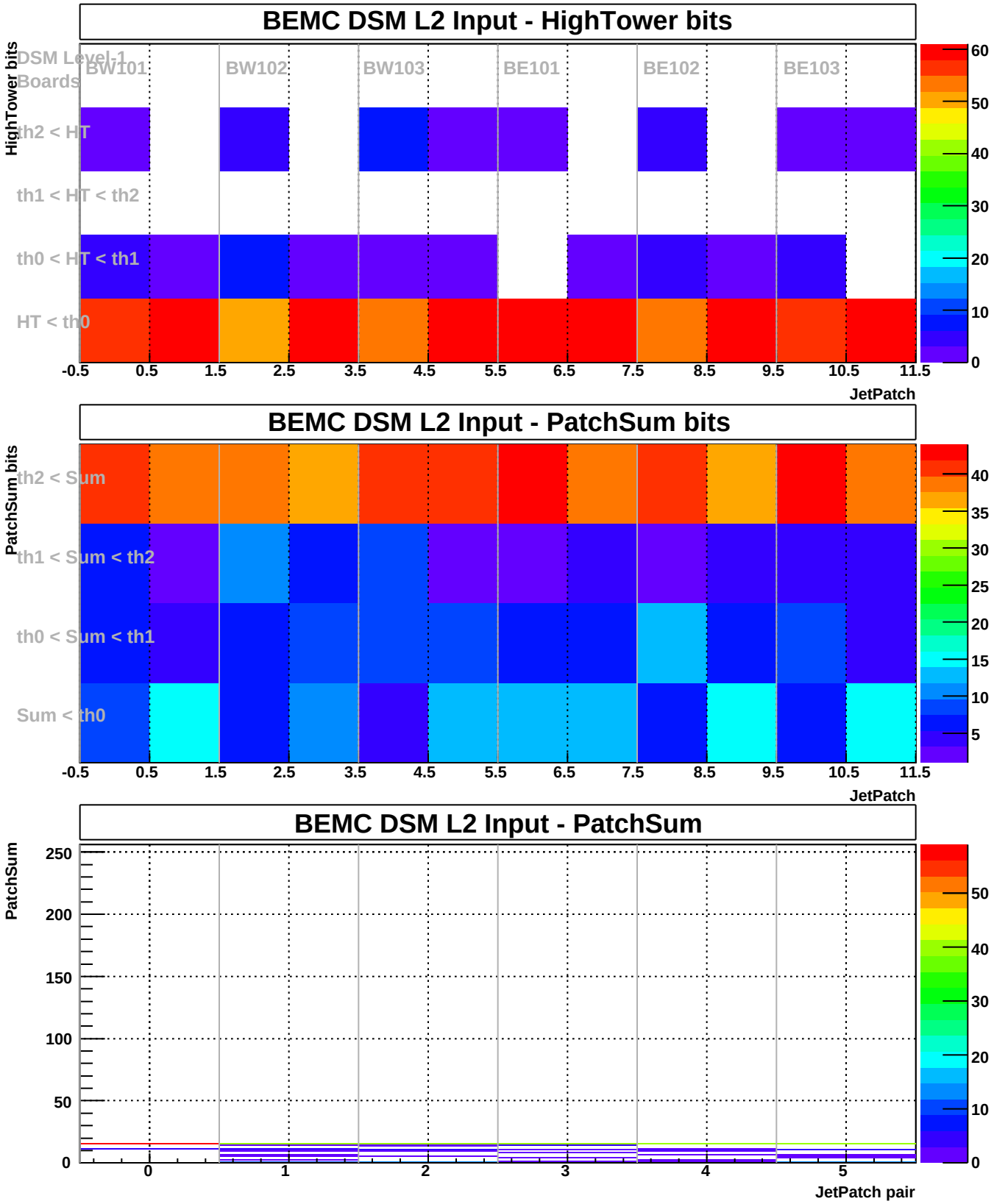

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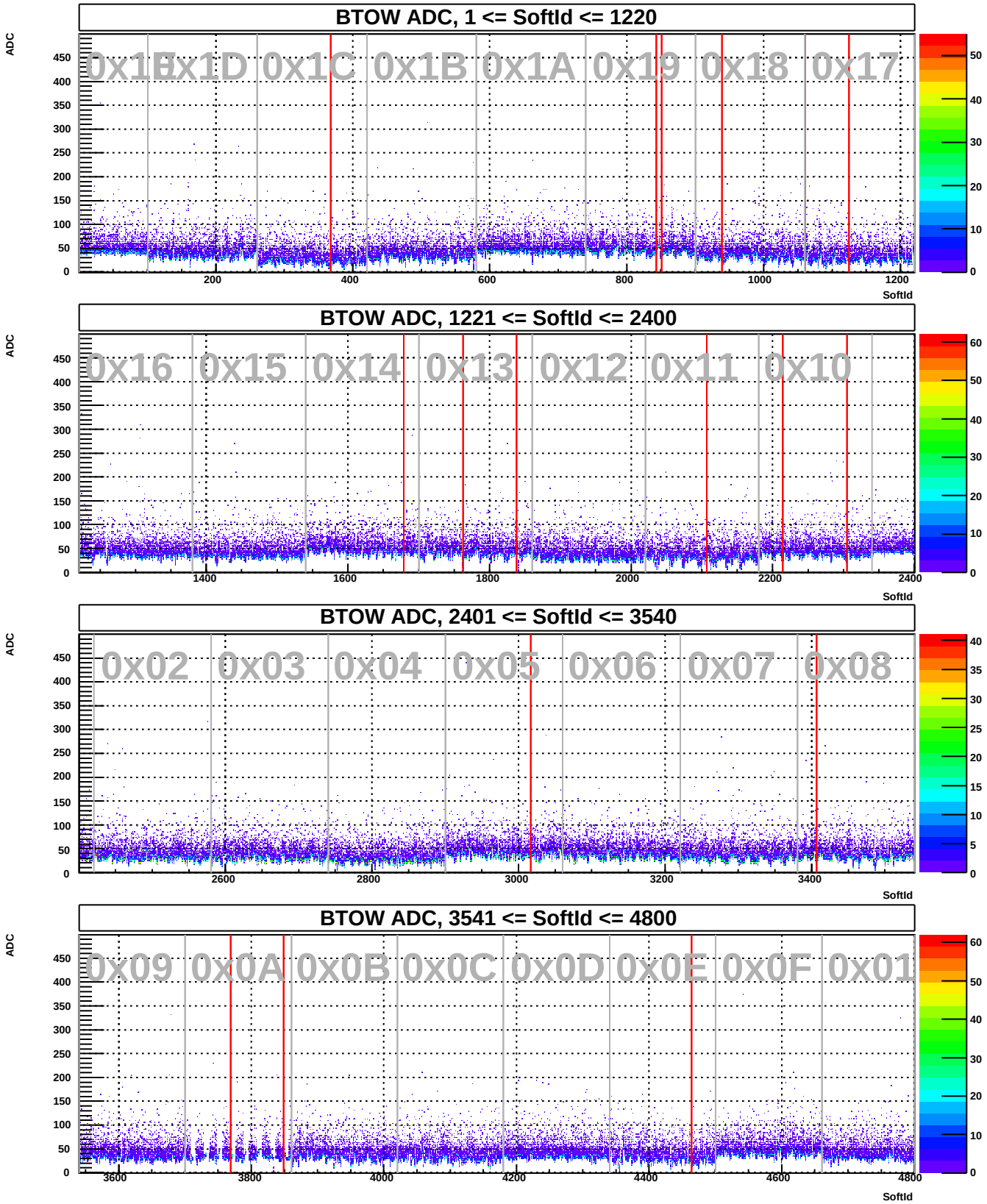
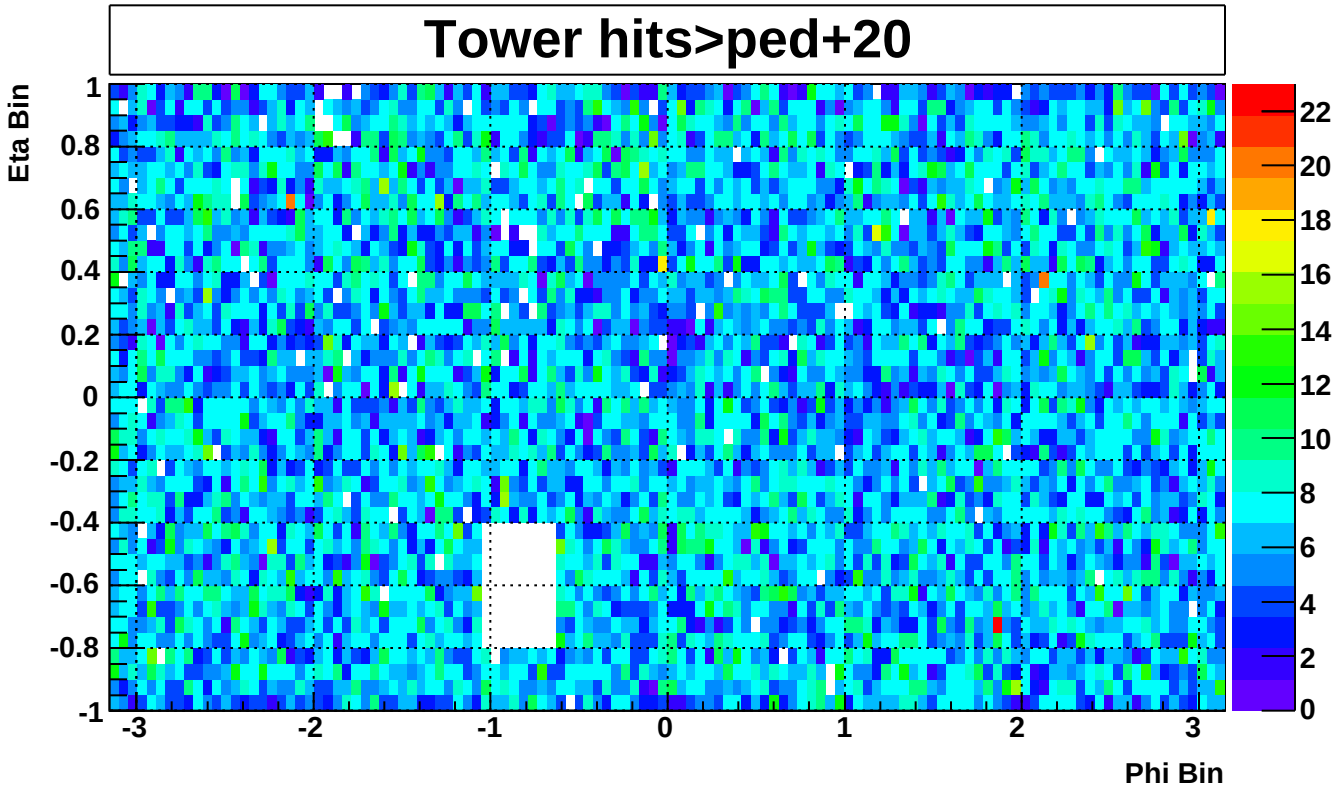
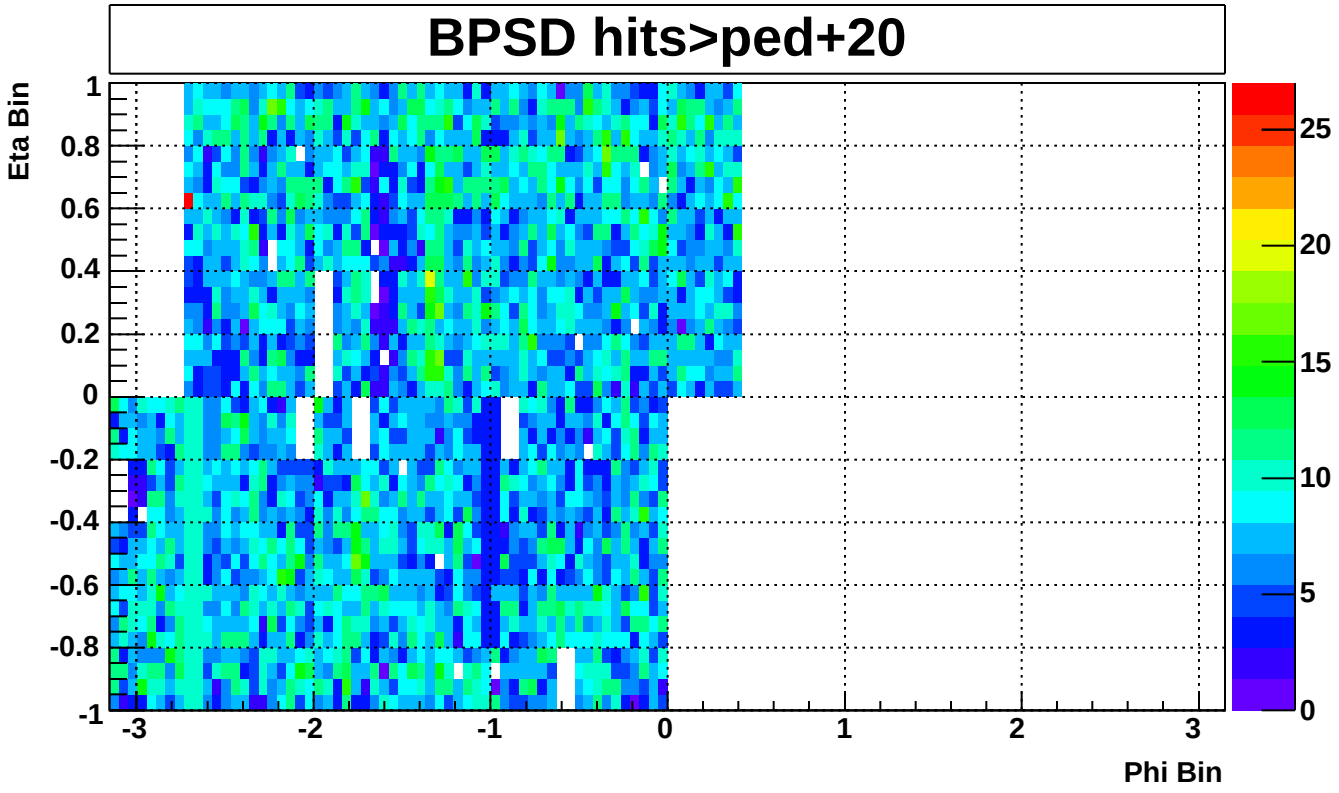
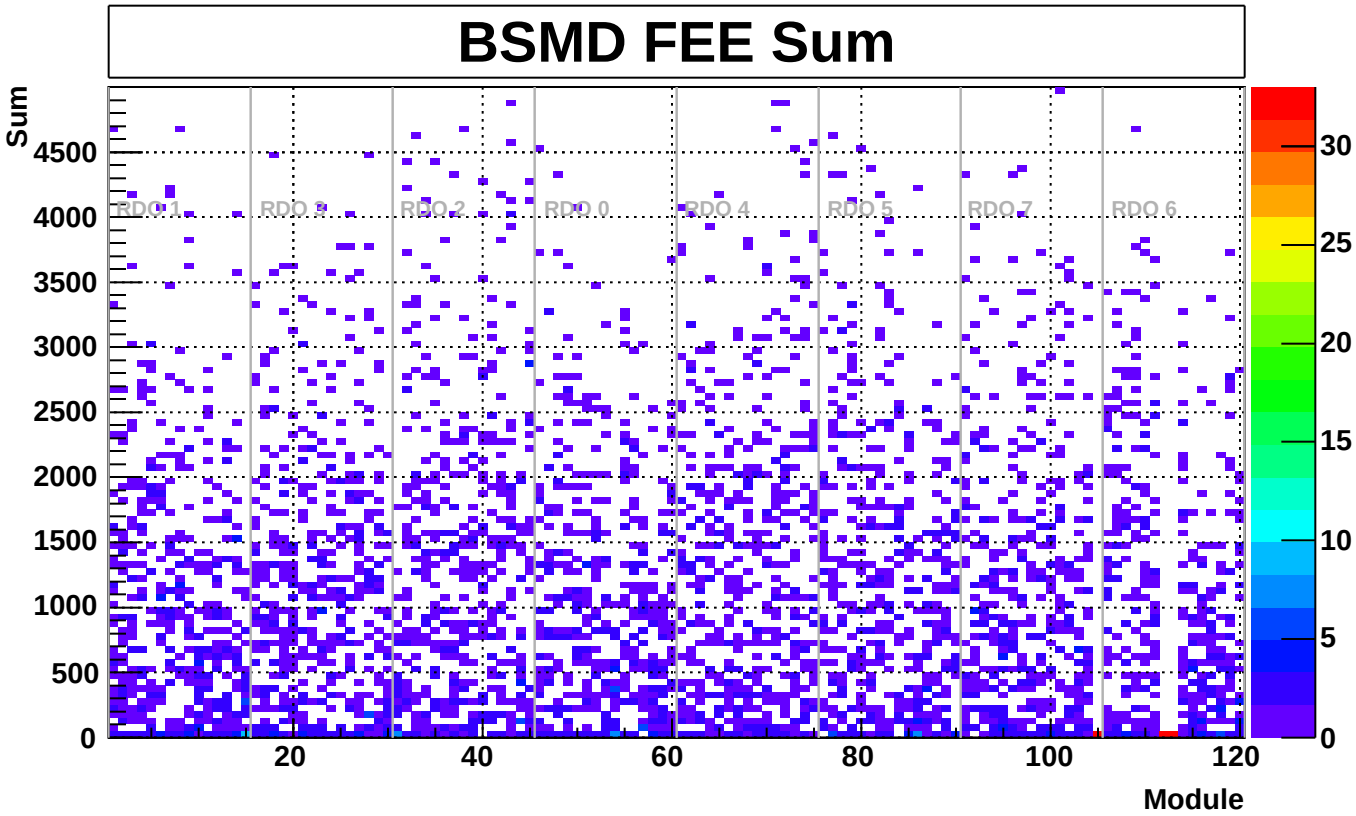


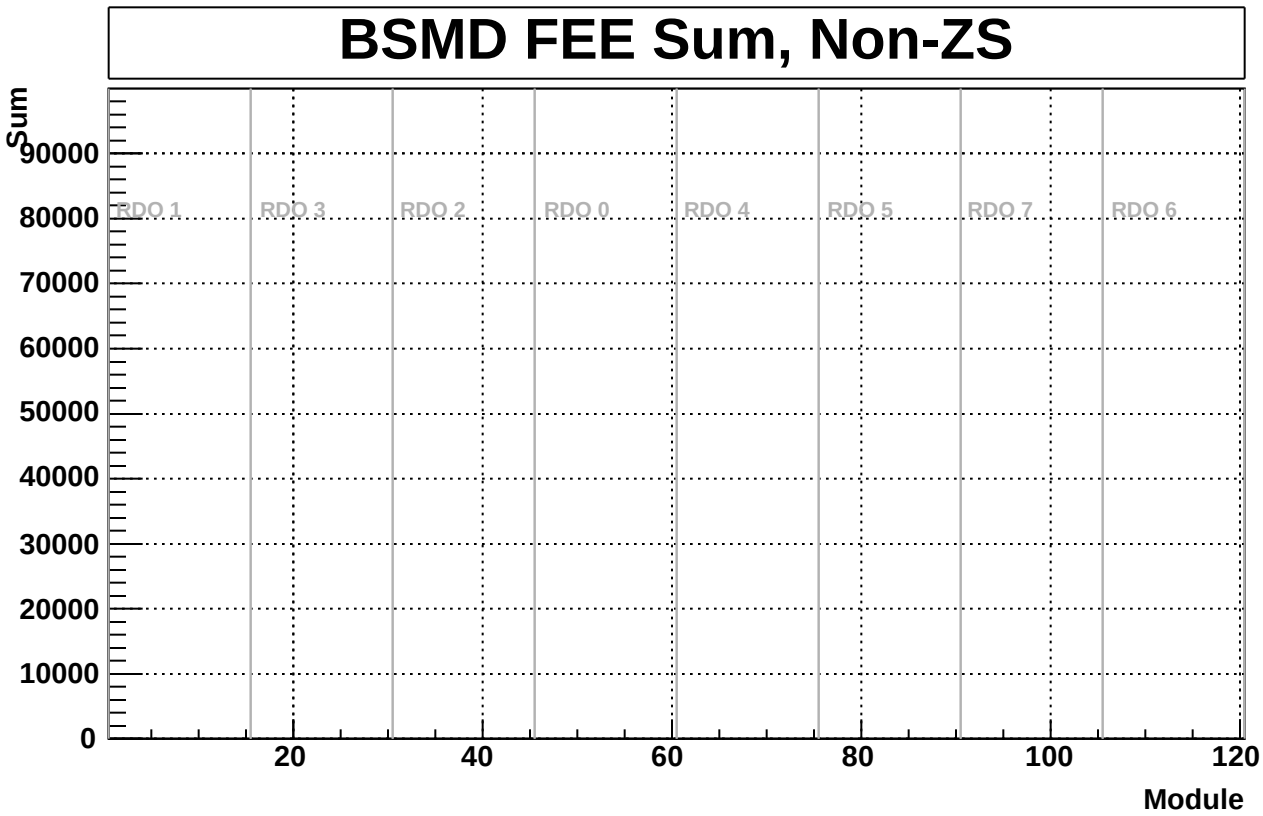
Tue May 10 08:45:26 2011





Tue May 10 08:45:27 2011





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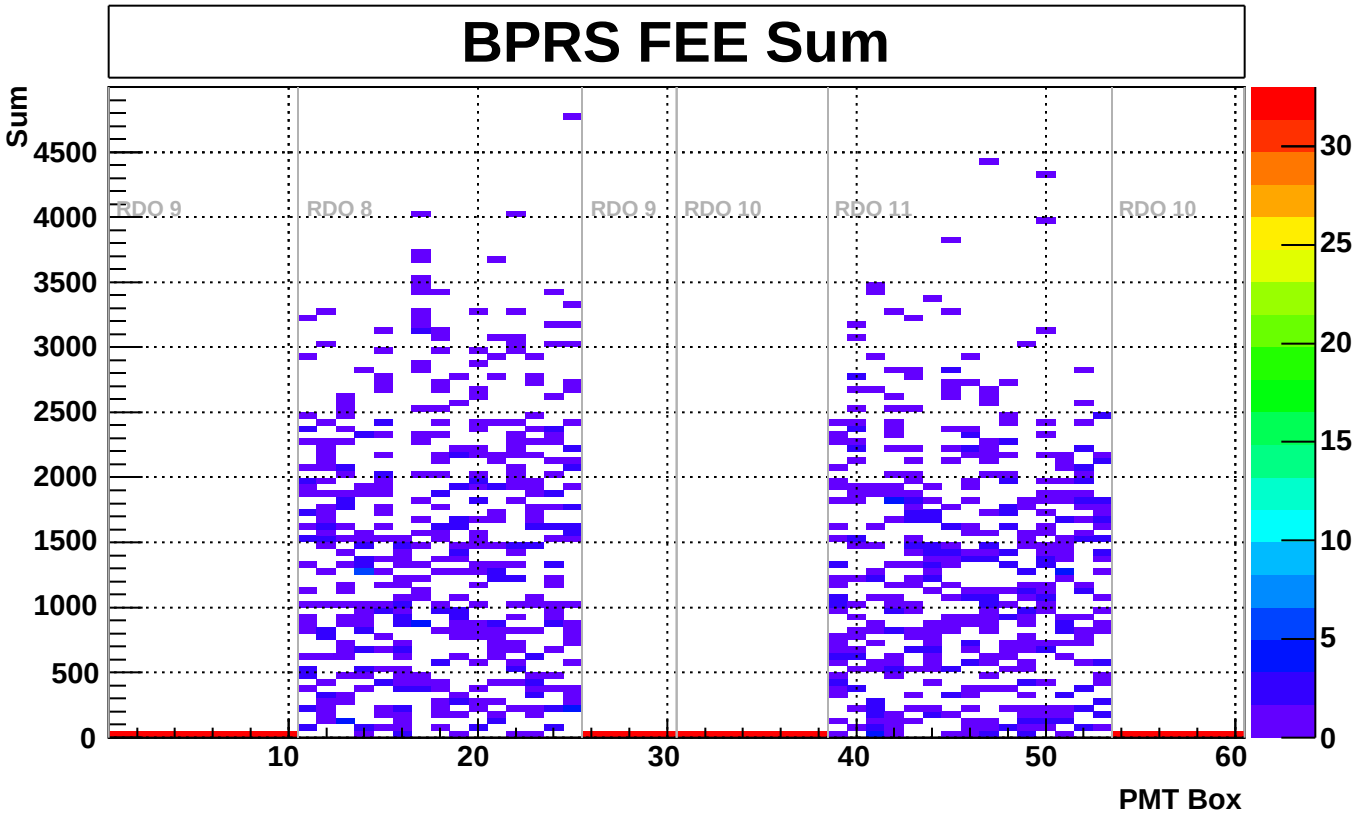
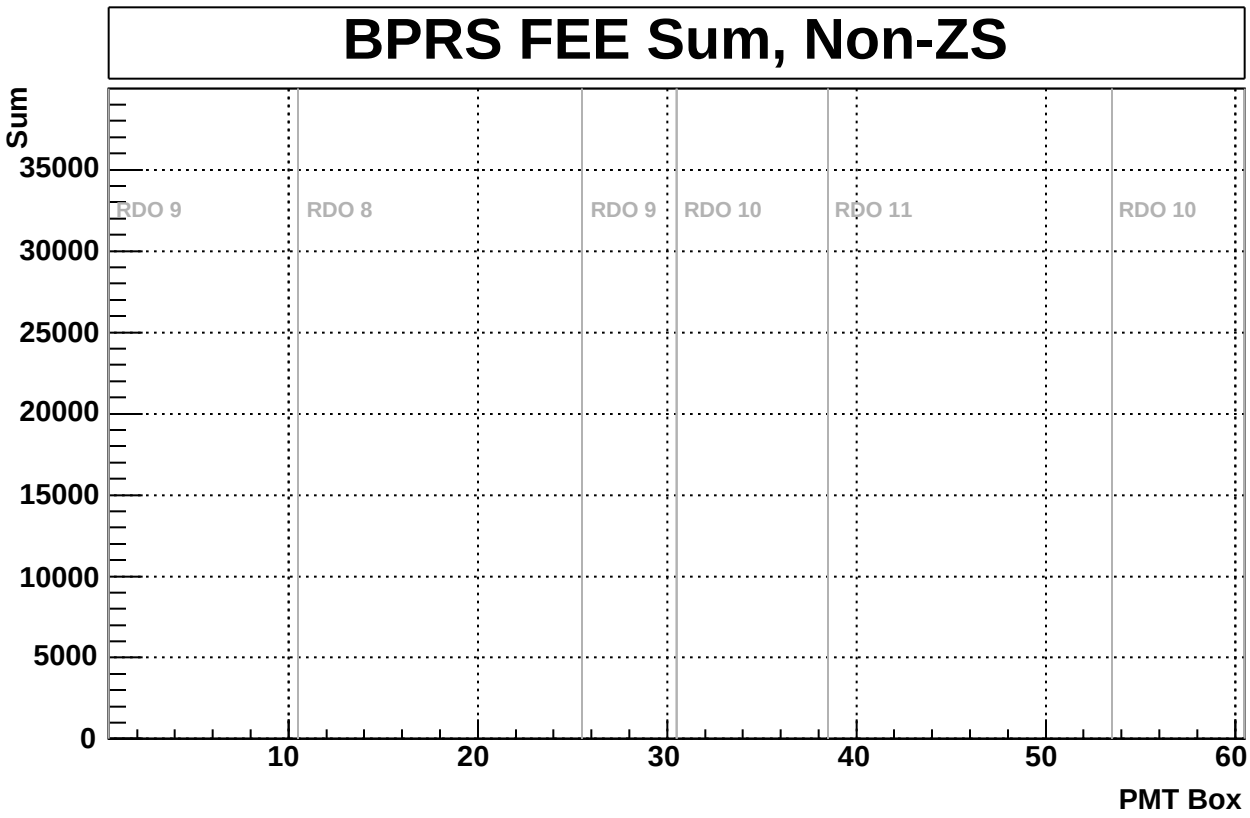
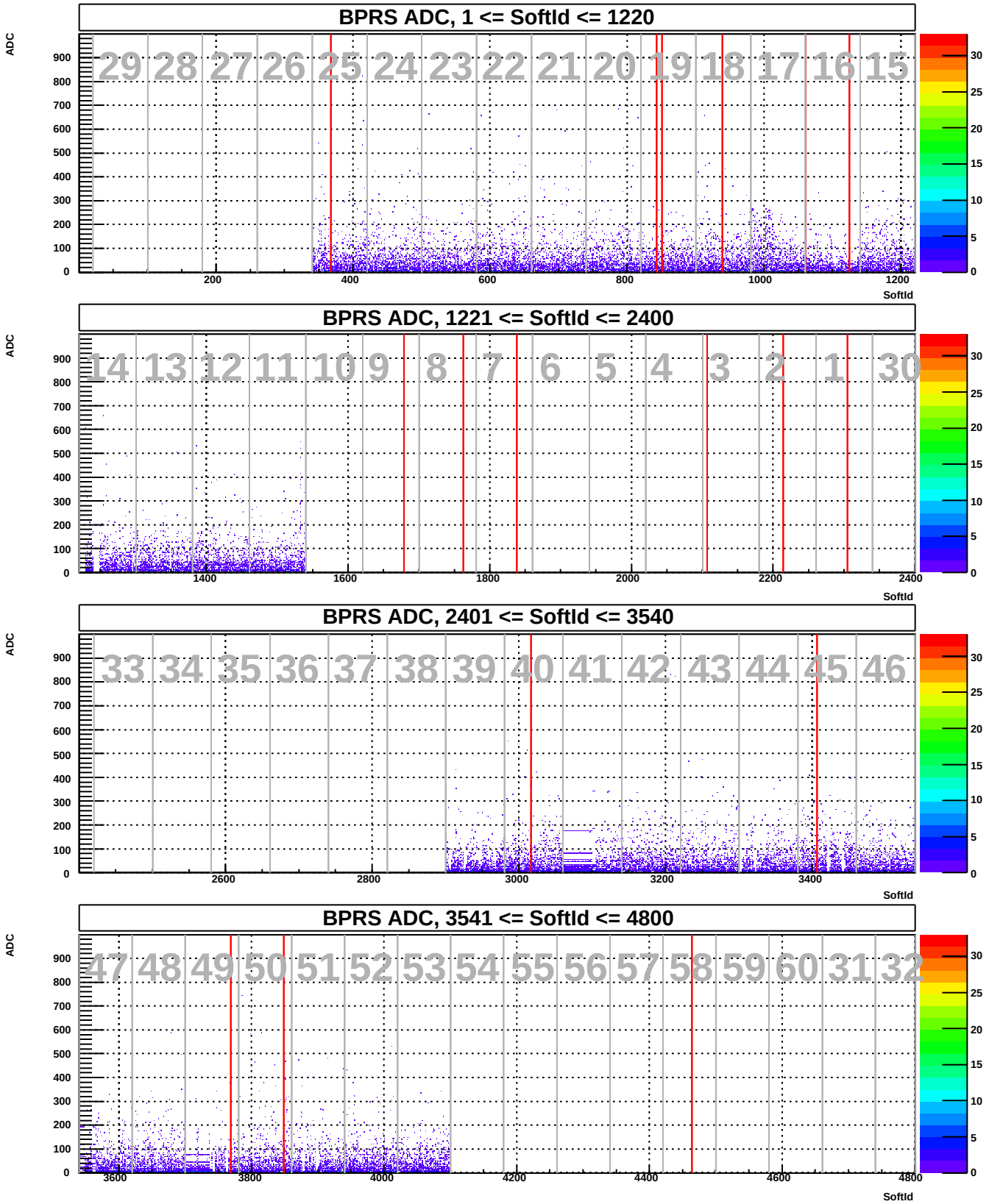




Tue May 10 08:45:30 2011



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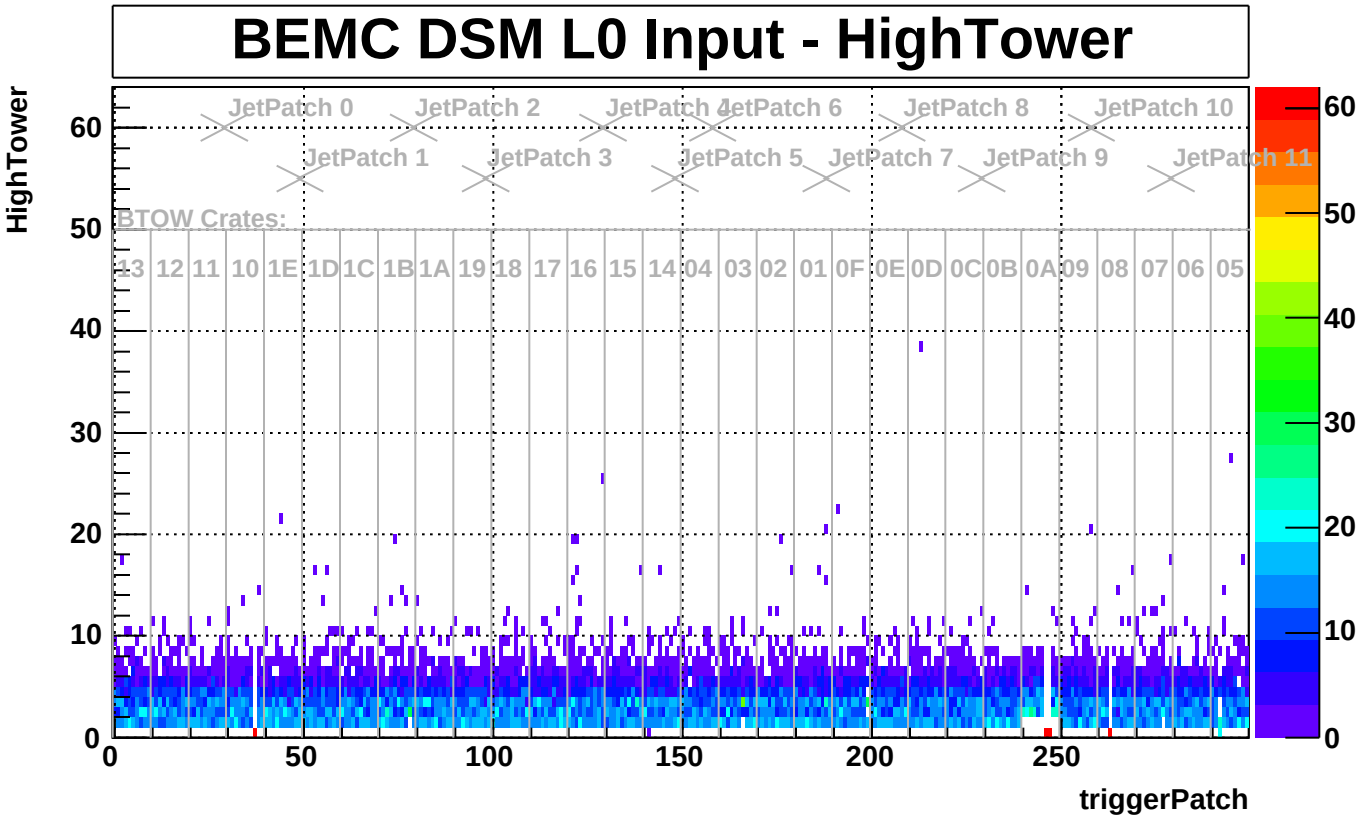

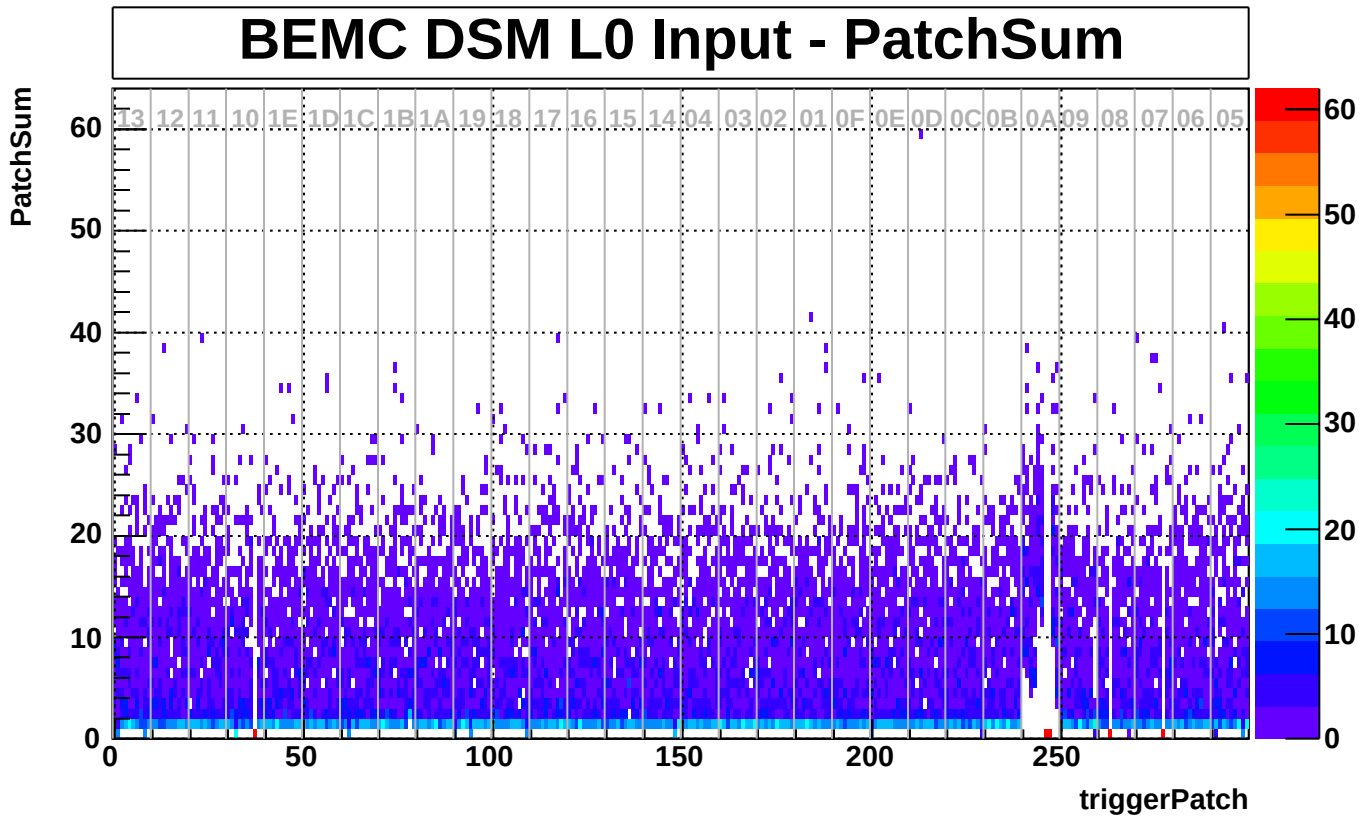

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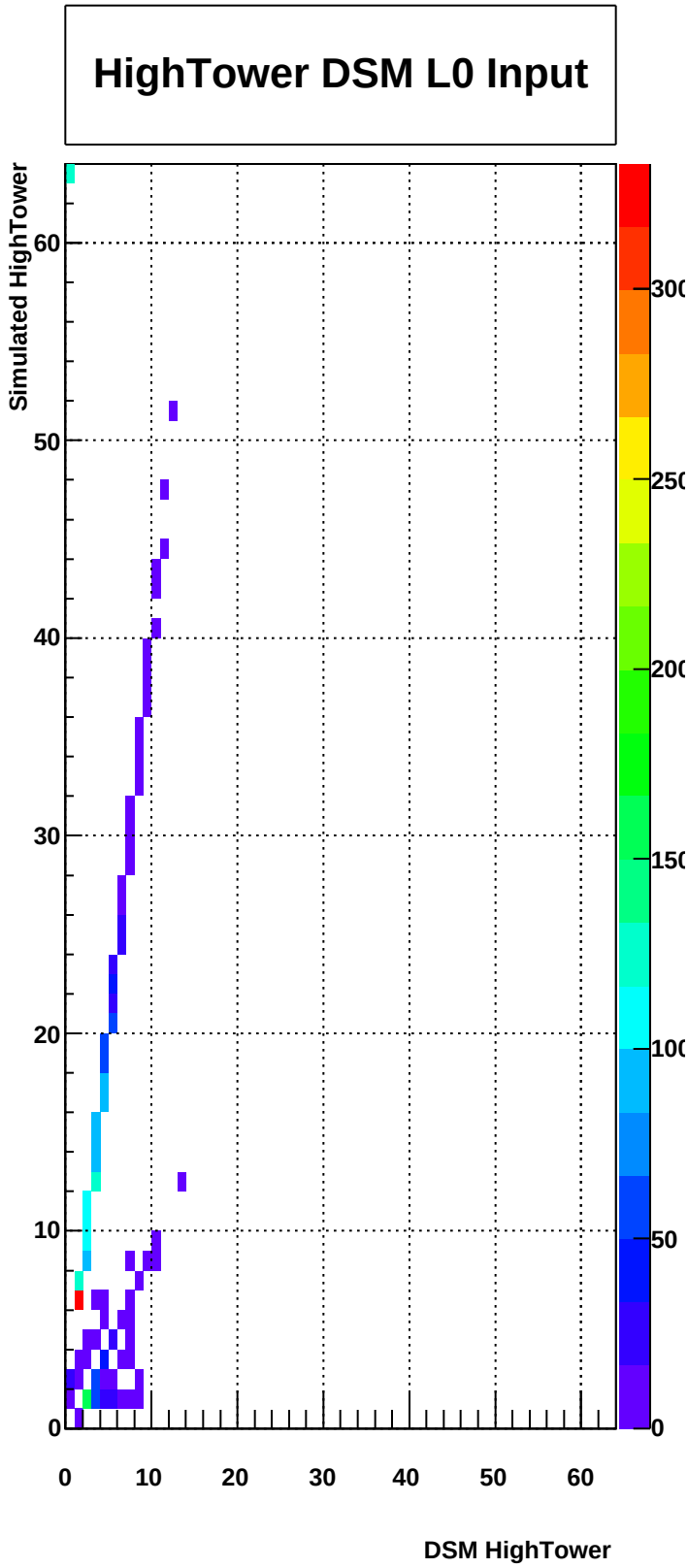

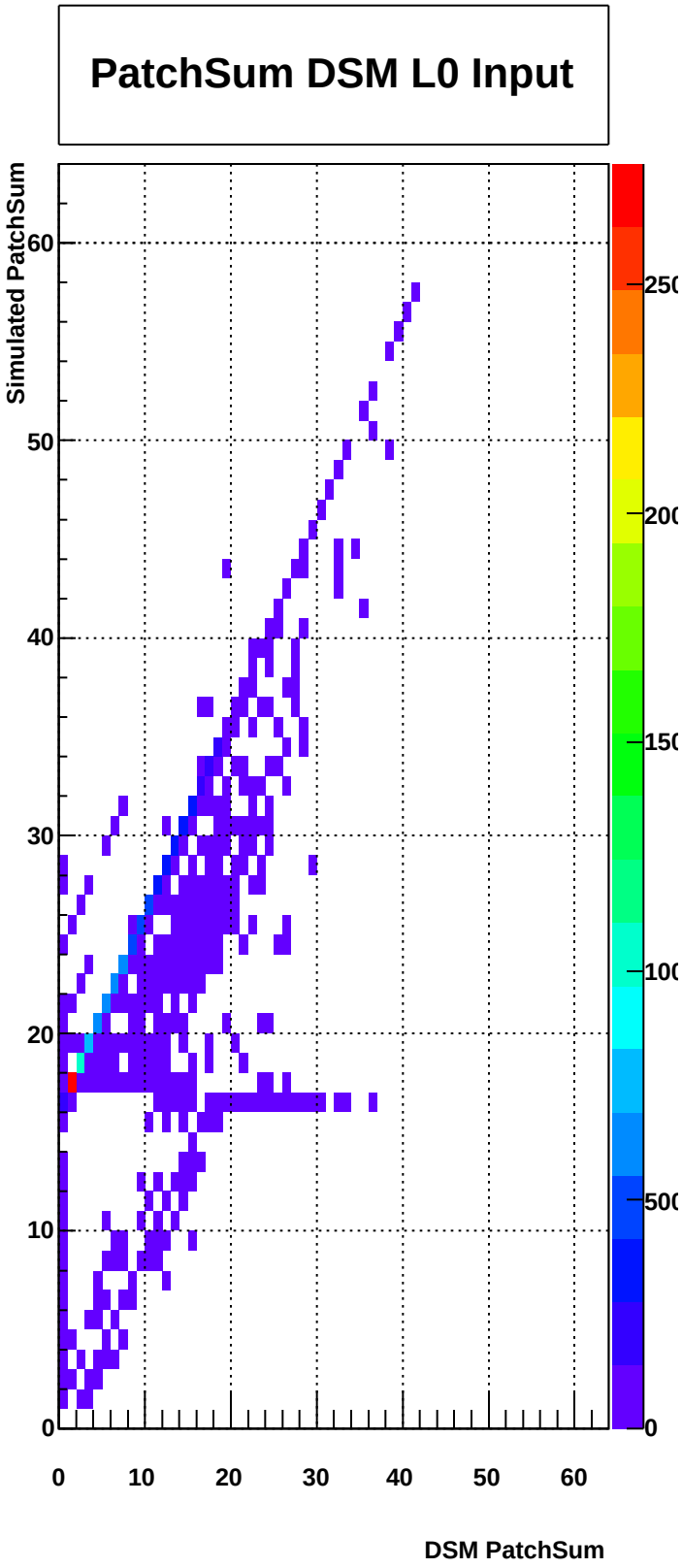

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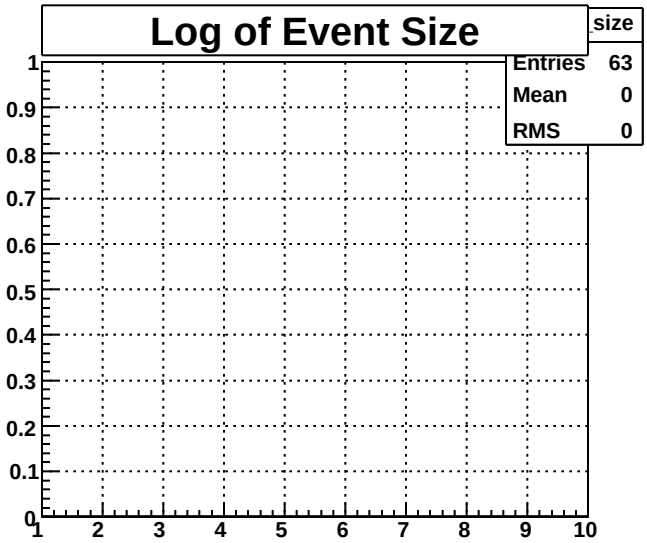
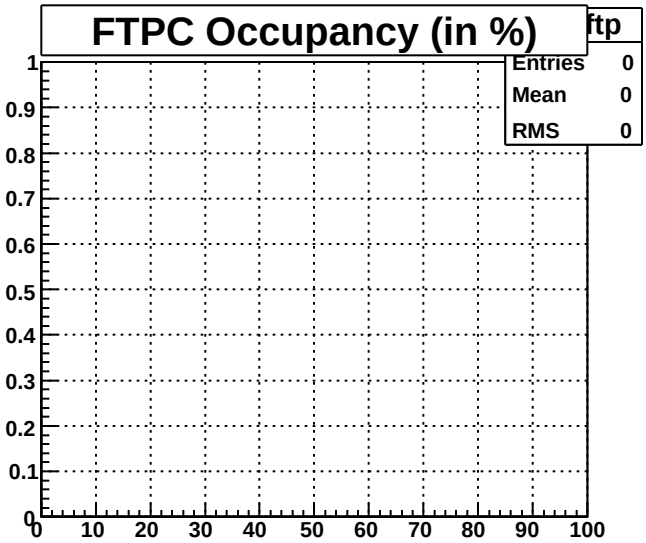
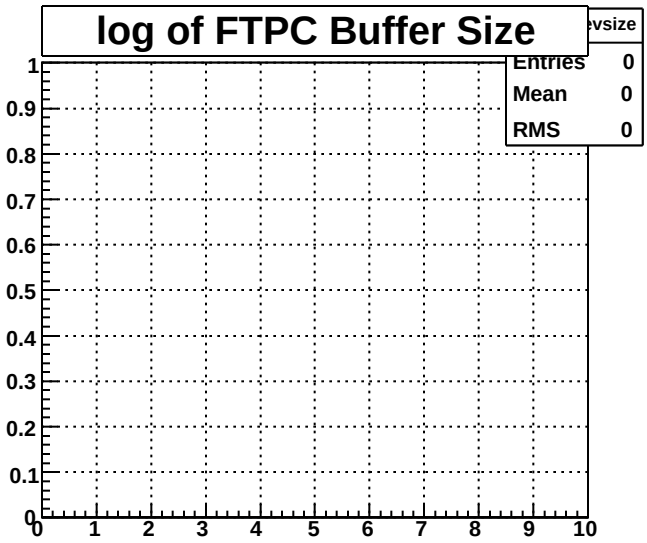

Log of Event Size
size
Entries 63
Mean 0
RMS 0


FTPC Occupancy (in %)
ftp
Entries 0
Mean 0
RMS 0


log of FTPC Buffer Size
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Entries 0
Mean 0
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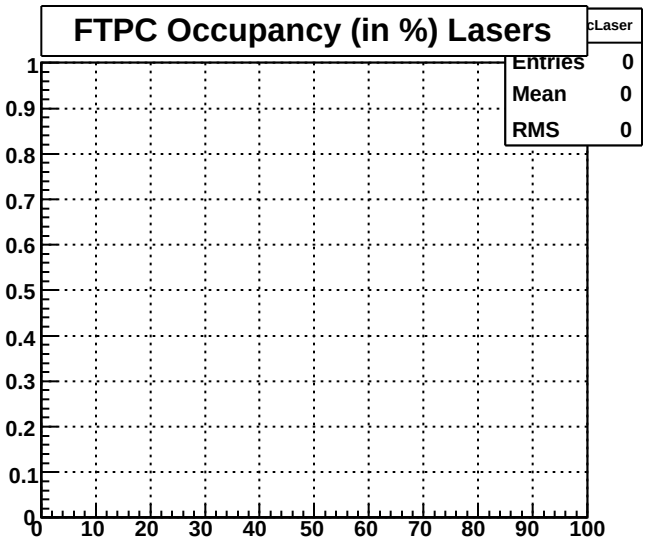
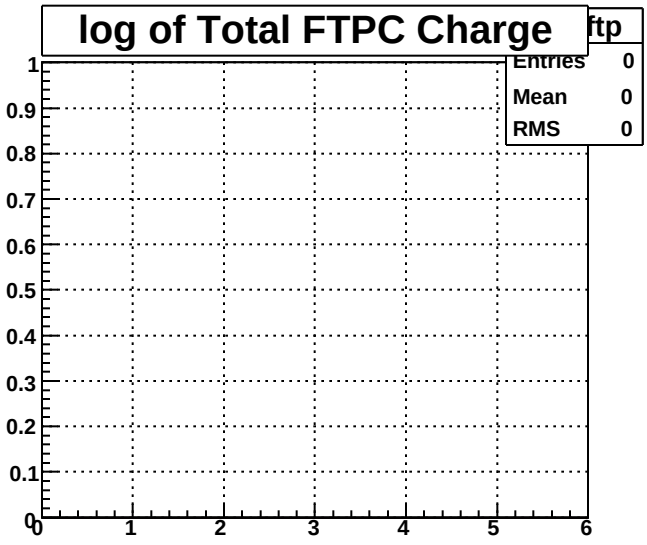
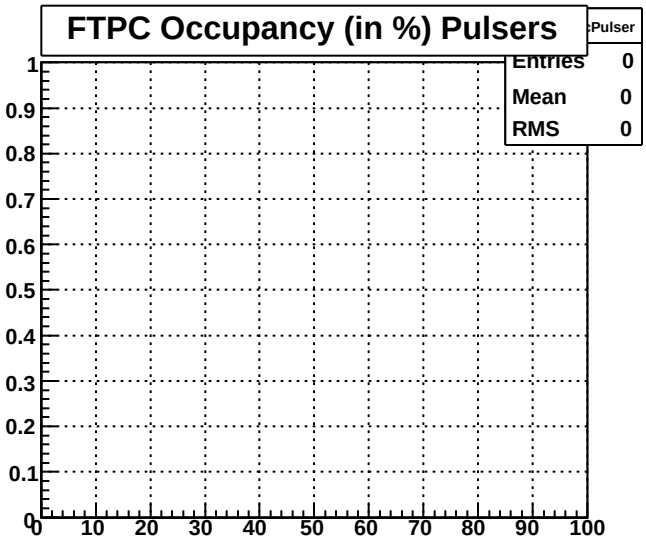
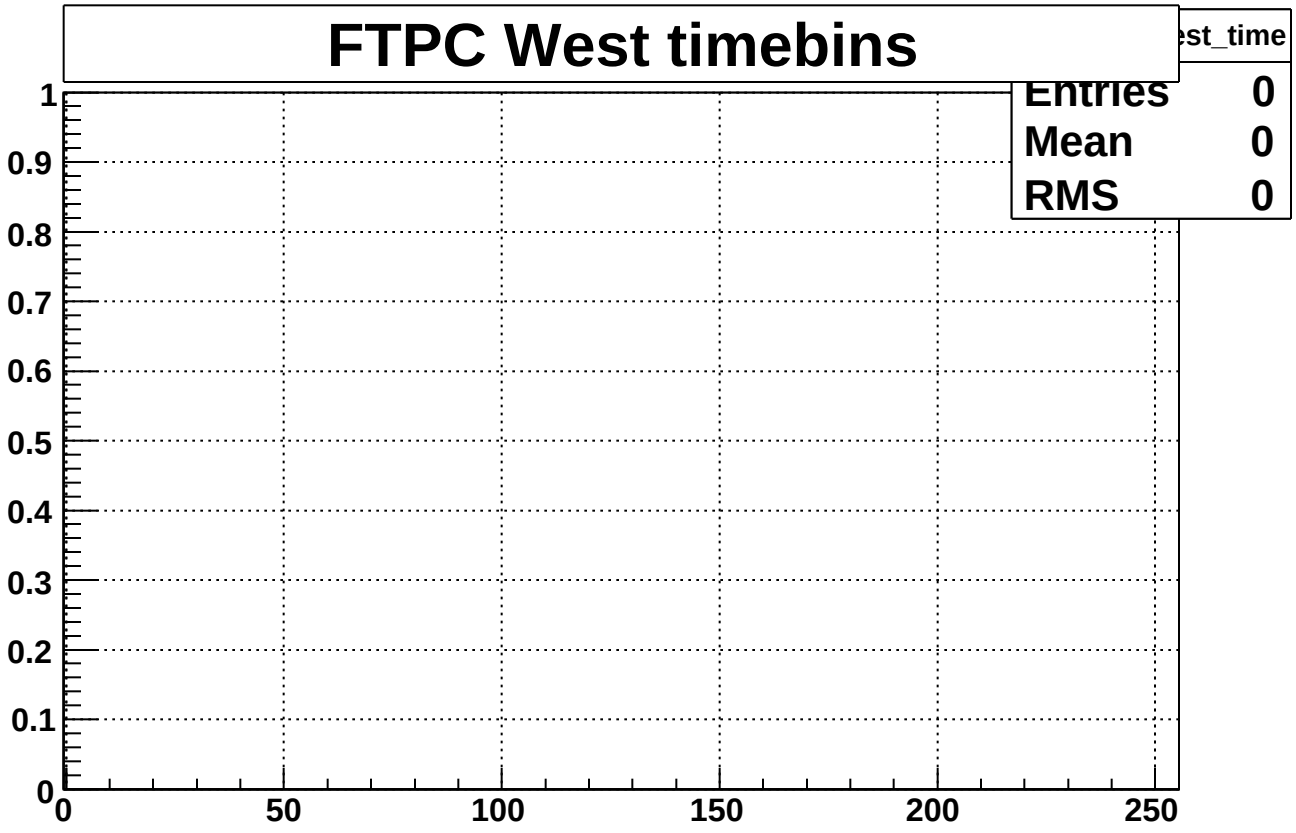
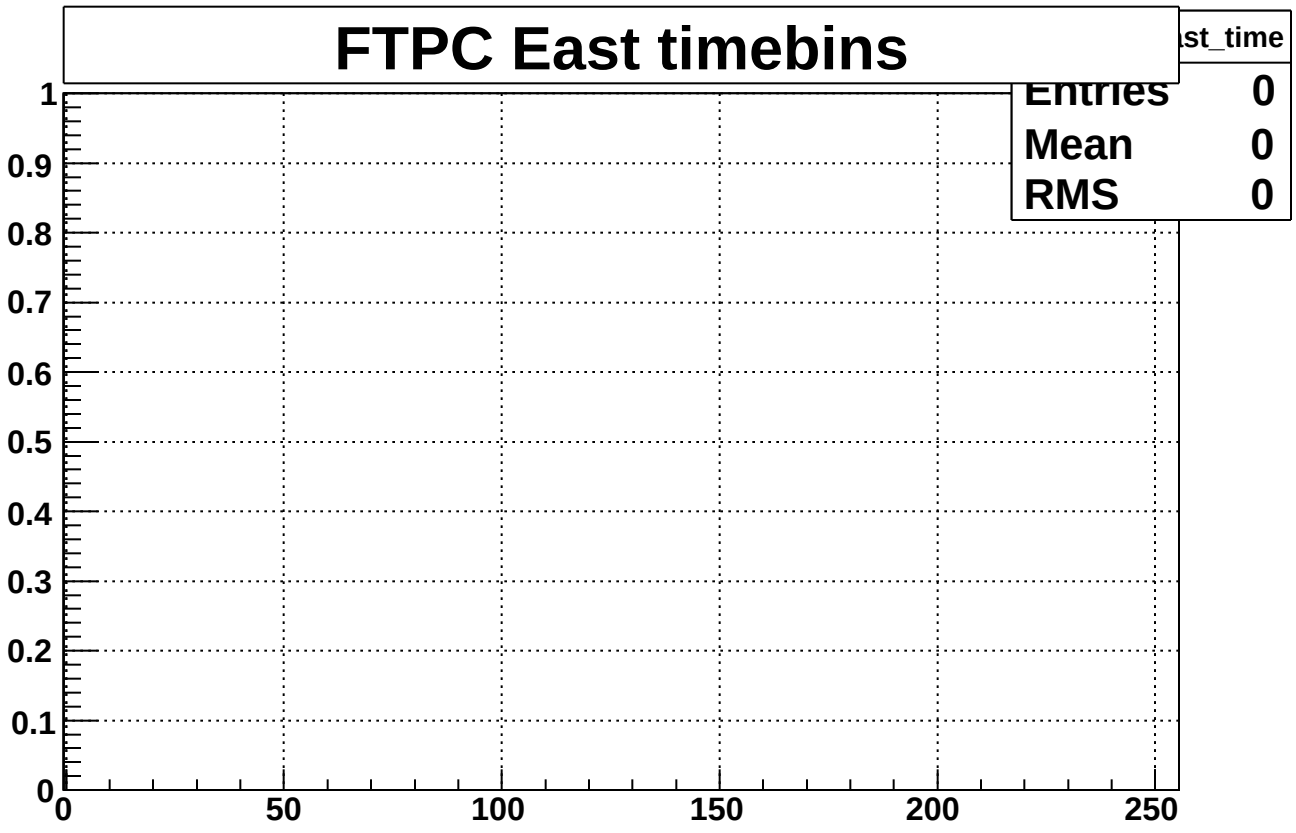
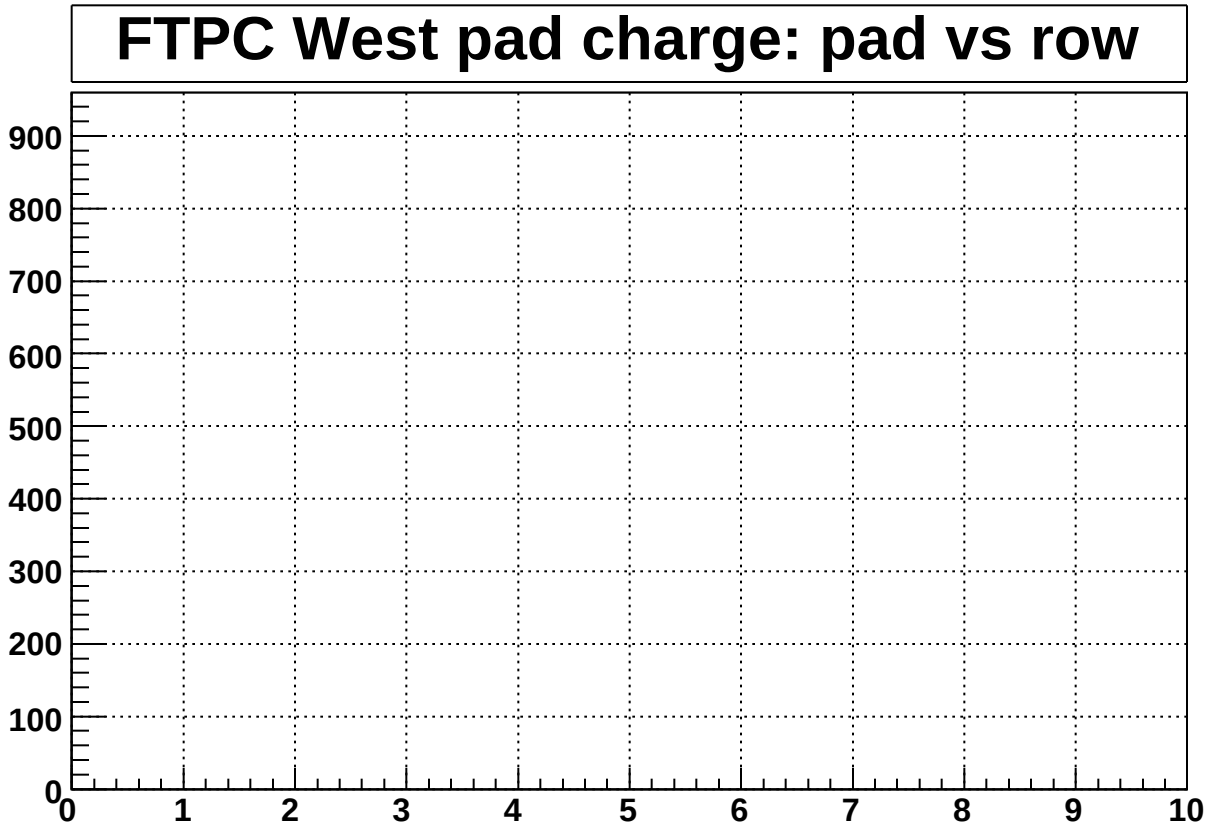

FTPC Occupancy (in %) Lasers
cLaser
Entries 0
Mean 0
RMS 0

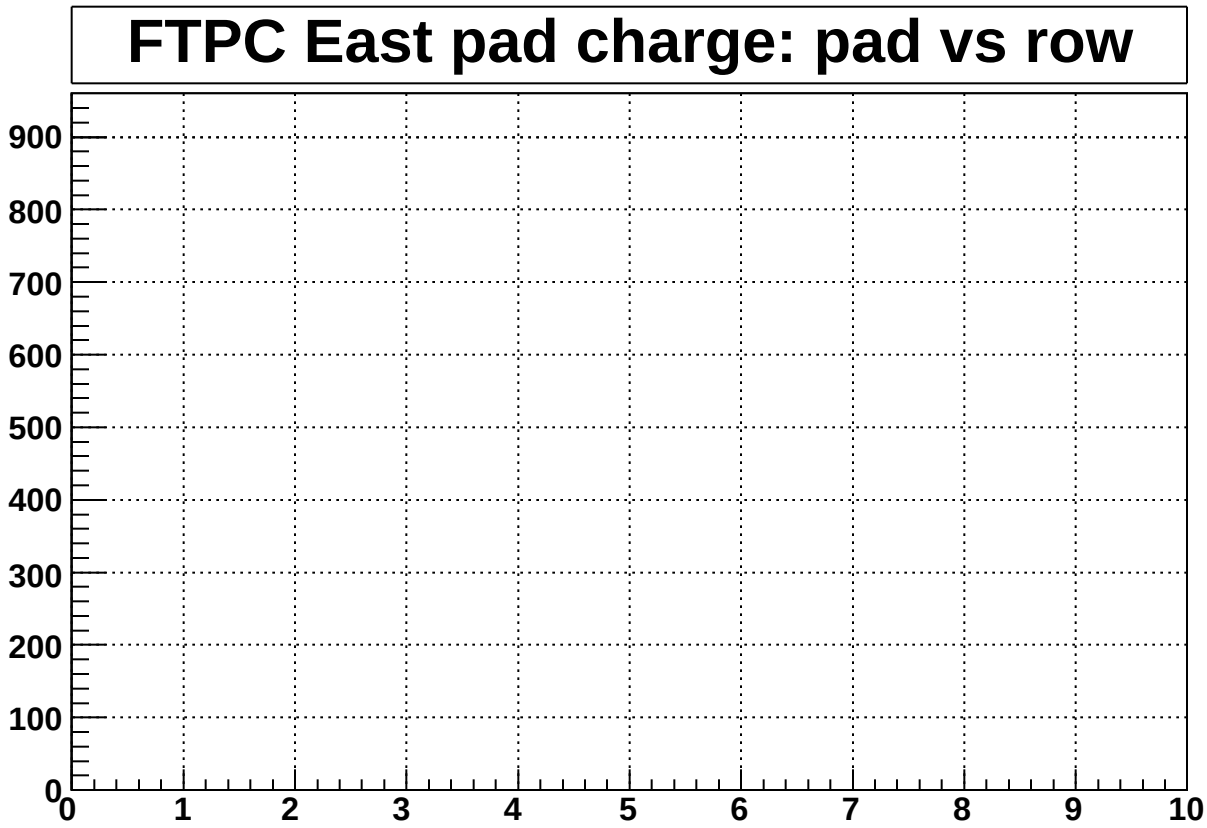

log of Total FTPC Charge
ftp
Entries 0
Mean 0
RMS 0

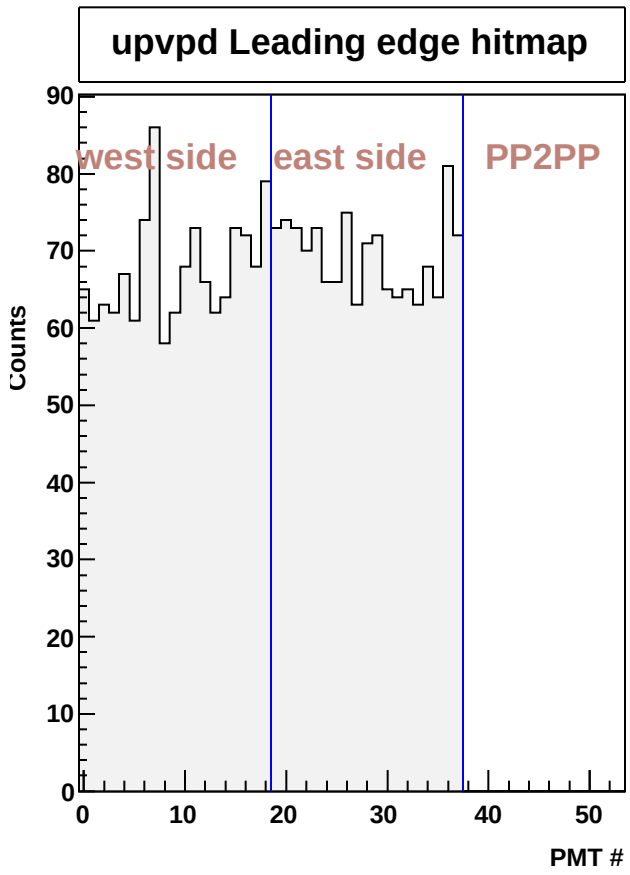

FTPC Occupancy (in %) Pulsers
cPulser
Entries 0
Mean 0
RMS 0

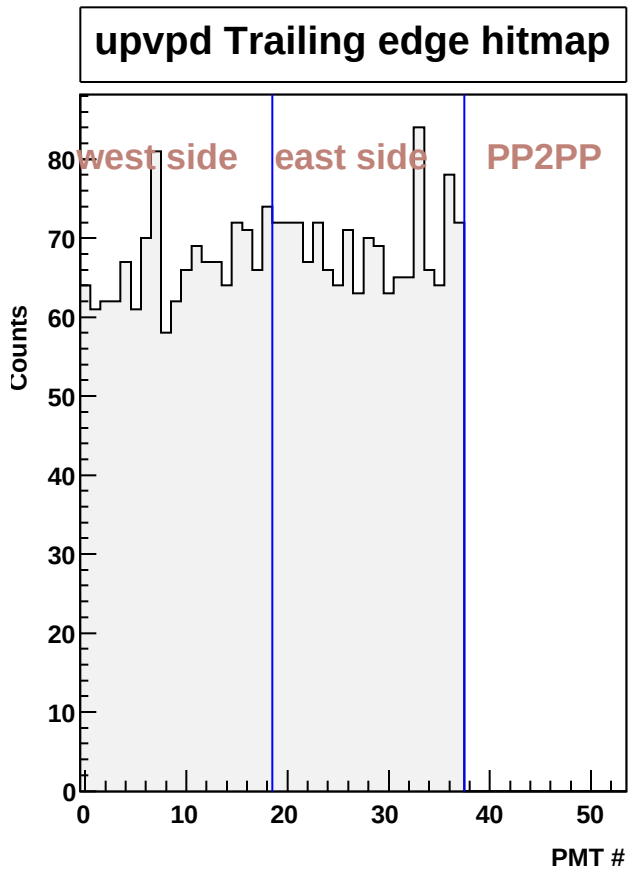

FTPC West timebins
est_time
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
0
50
100
150
200
250

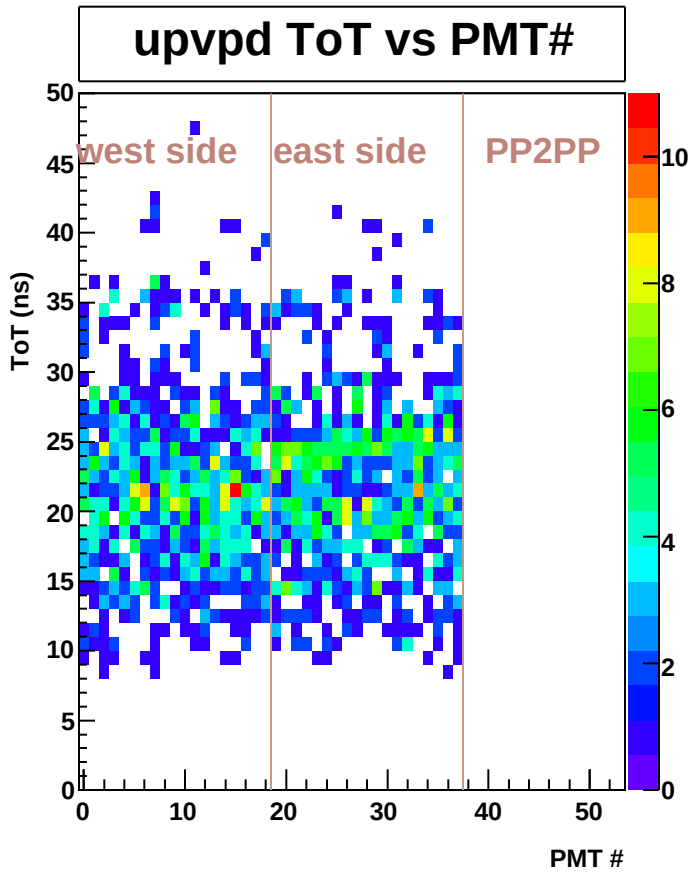

FTPC East timebins
st_time
Entries 0
Mean 0
RMS 0
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0
0
50
100
150
200
250

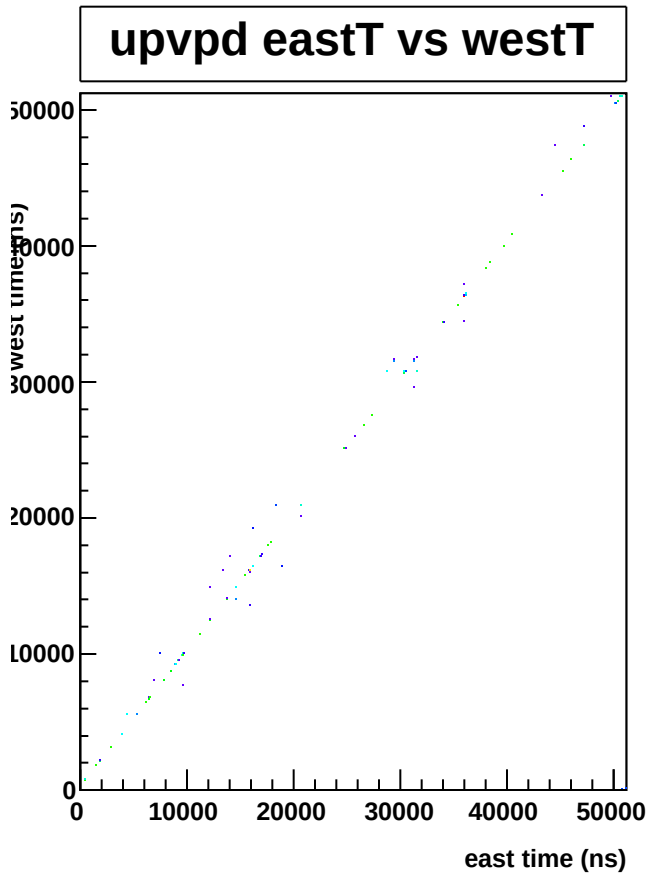

FTPC West pad charge: pad vs row
900
800
700
600
500
400
300
200
100
0
0
1
2
3
4
5
6
7
8
9
10

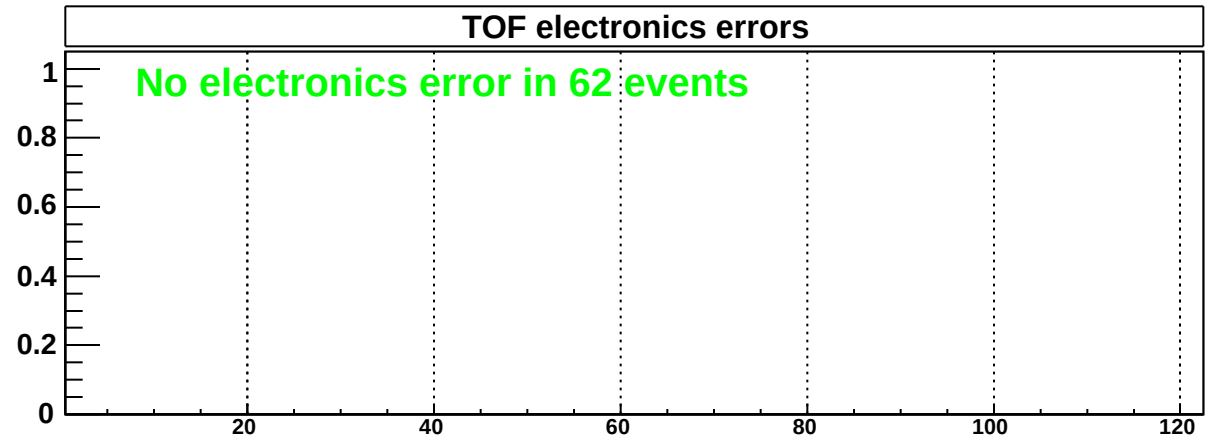

FTPC East pad charge: pad vs row
900
800
700
600
500
400
300
200
100
0
0
1
2
3
4
5
6
7
8
9
10

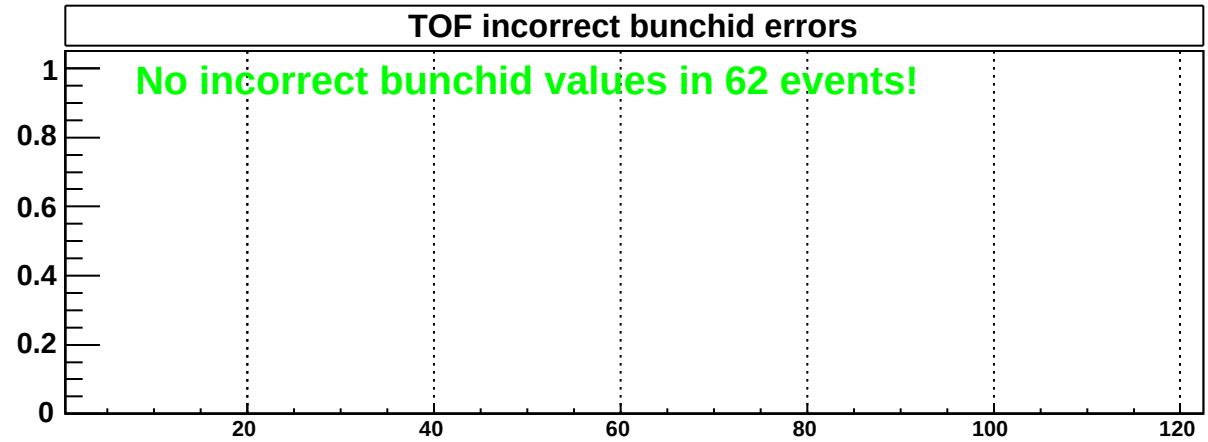

upvpd Leading edge hitmap
west side
east side
PP2PP
Counts
PMT #

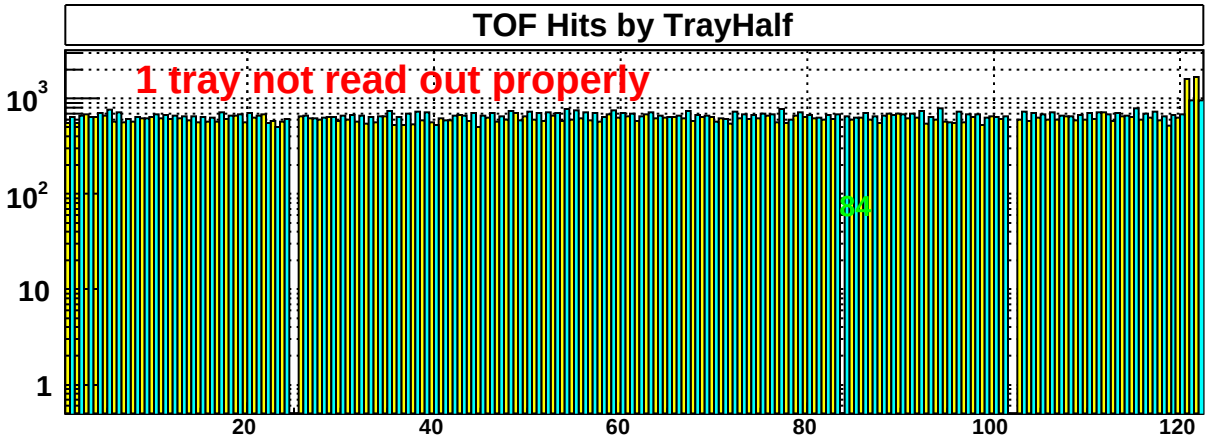

upvpd Trailing edge hitmap
west side
east side
PP2PP
Counts
PMT #

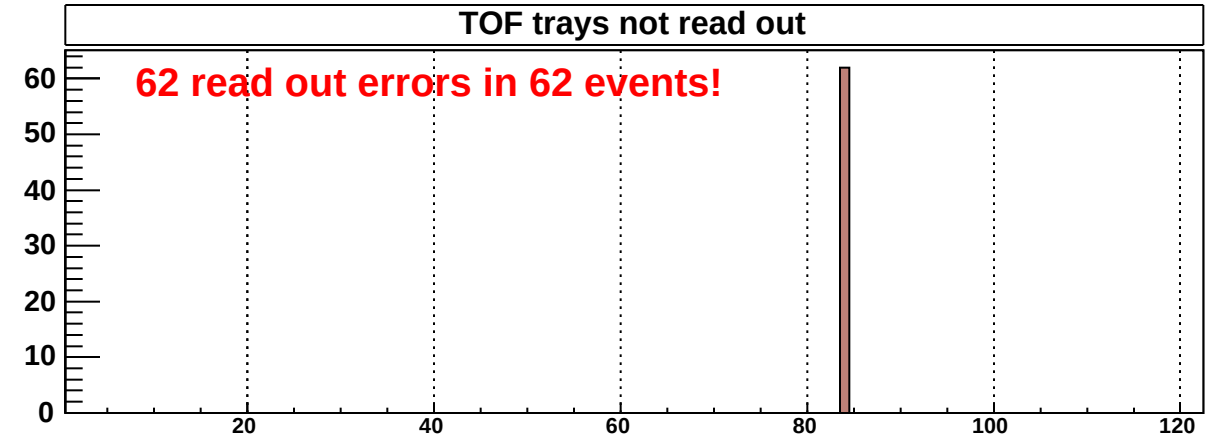

upvpd ToT vs PMT#
west side
east side
PP2PP
ToT (ns)
PMT #

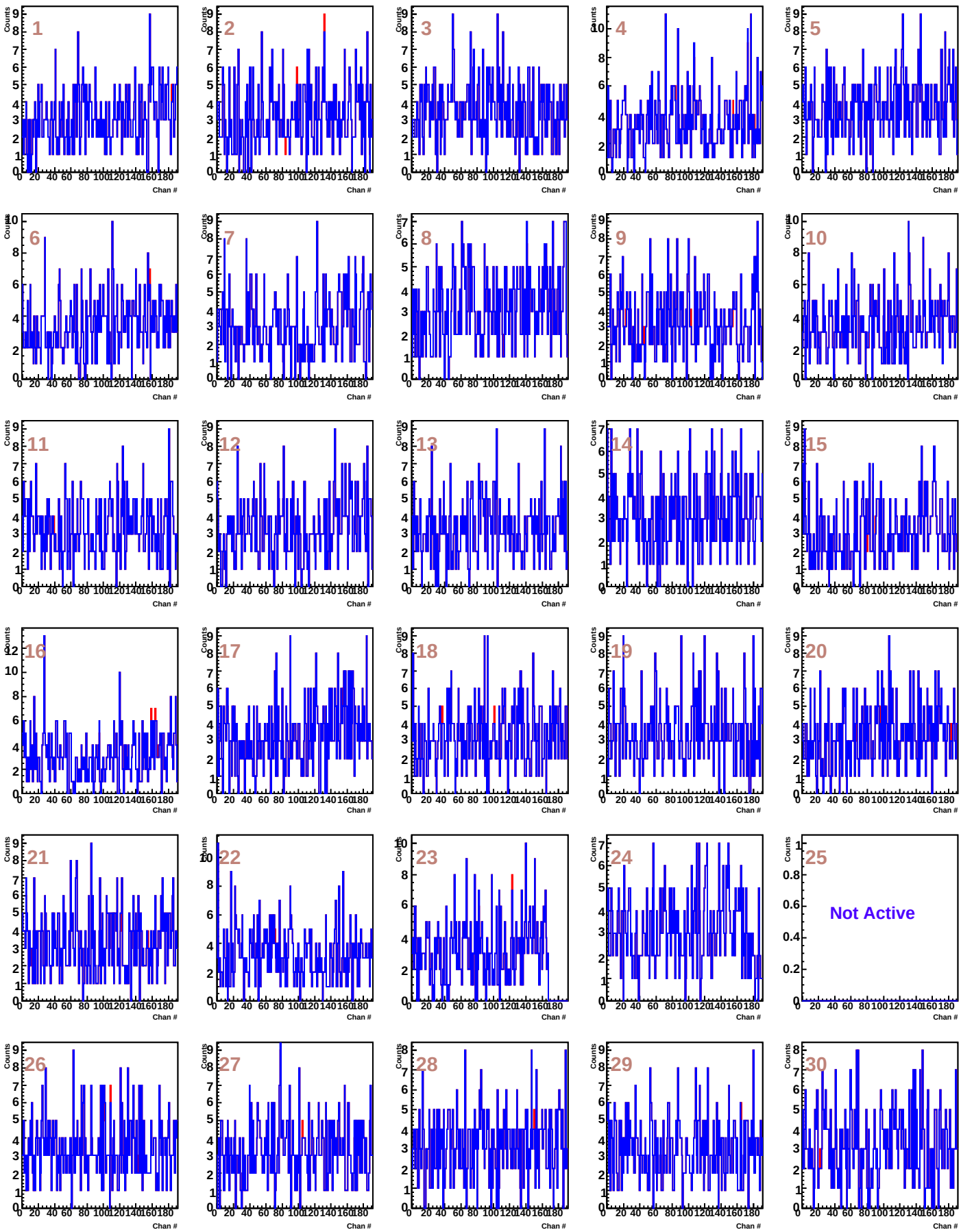

upvpd eastT vs westT
east time (ns)


TOF electronics errors
No electronics error in 62 events
1
0.8
0.6
0.4
0.2
0
20
40
60
80
100
120


TOF incorrect bunchid errors
No incorrect bunchid values in 62 events!
1
0.8
0.6
0.4
0.2
0
20
40
60
80
100
120


TOF Hits by TrayHalf
1 tray not read out properly
64
10^3
10^2
10
1
20
40
60
80
100
120


TOF trays not read out
62 read out errors in 62 events!
60
50
40
30
20
10
0
20
40
60
80
100
120


1
2
3
4
5
6
7
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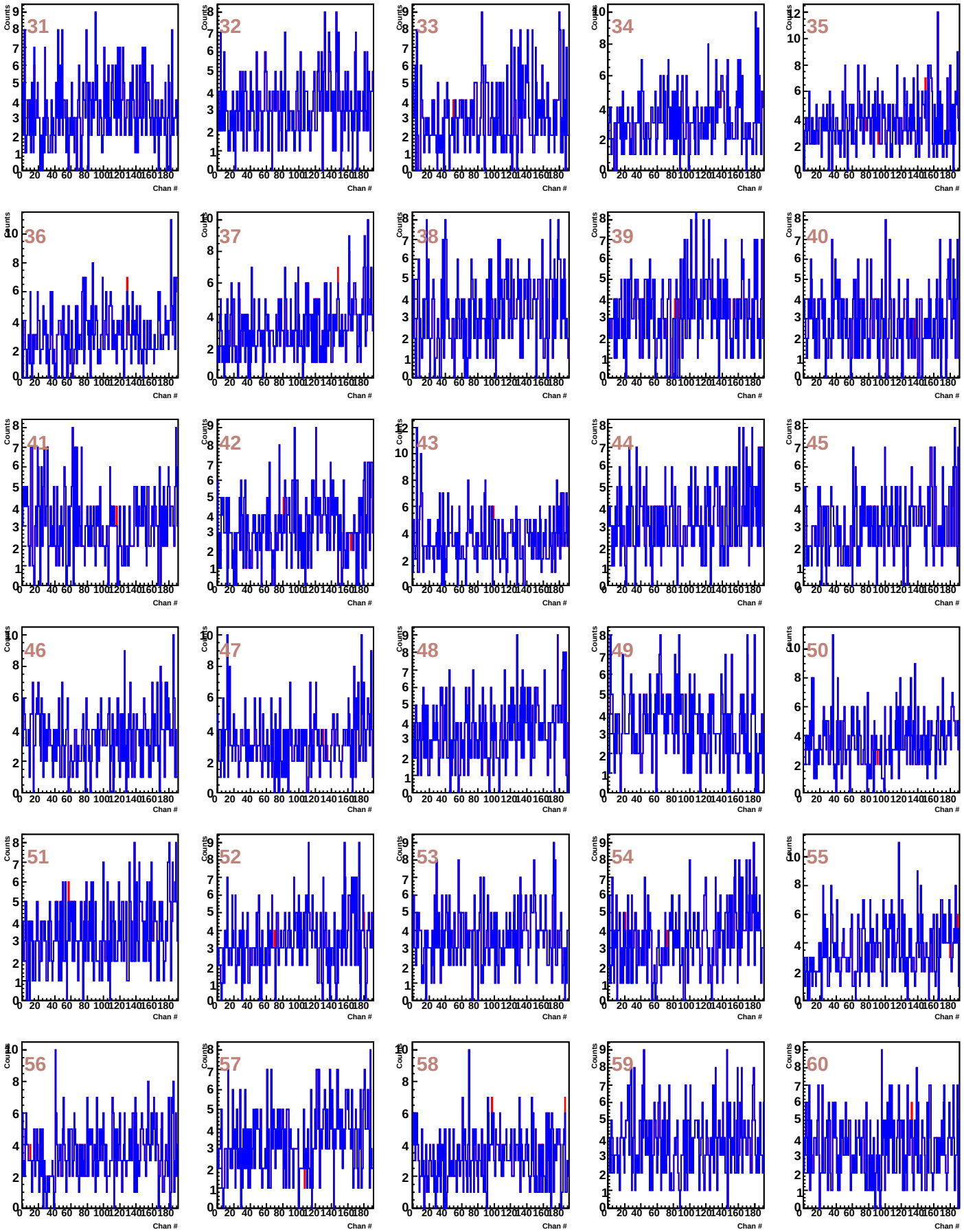
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23
24
25
Not Active
26
27
28
29
30
Counts
Chan #


31
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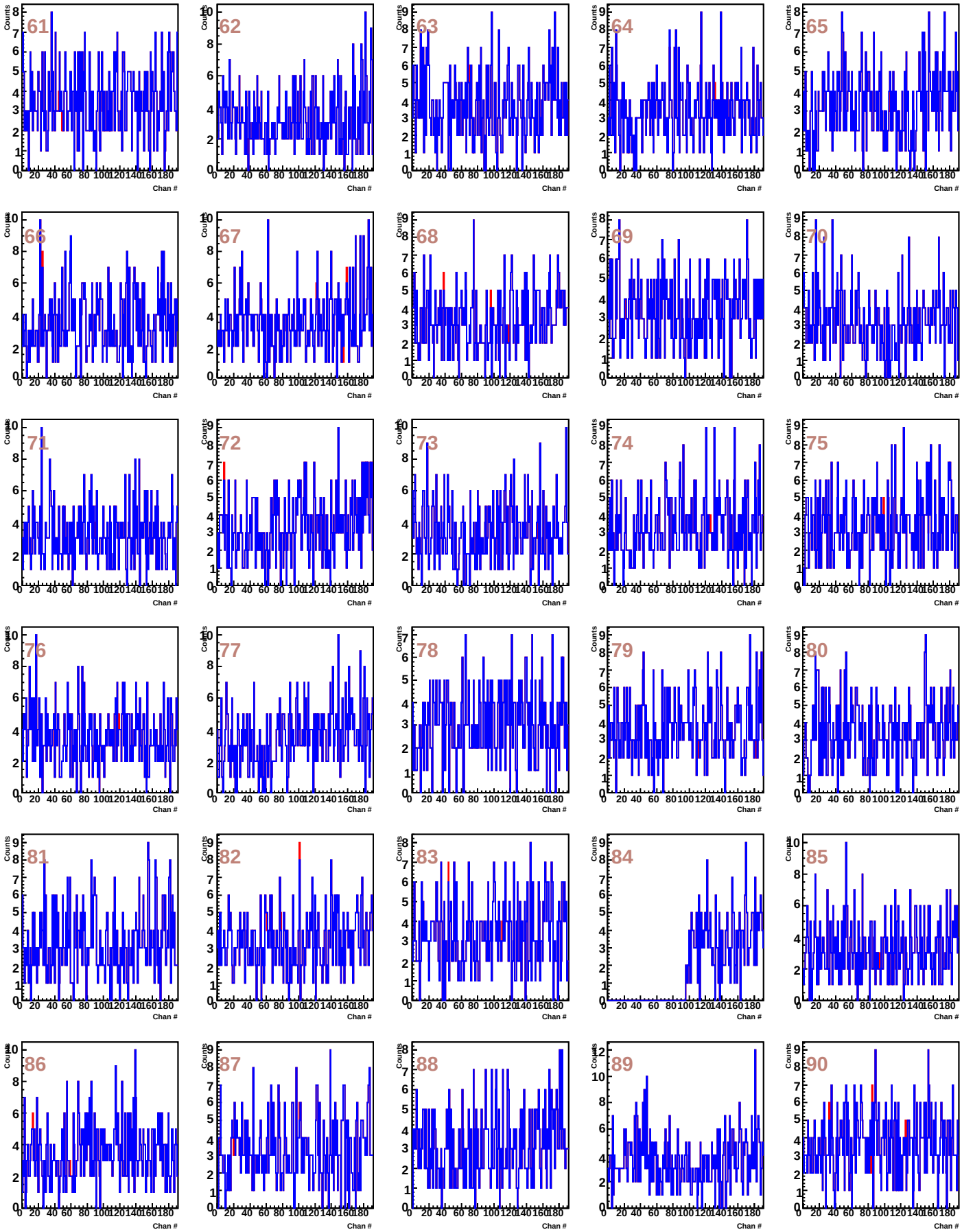
46
47
48
49
50
51
52
53
54
55
56
57
58
59
60
Counts
Chan #


61
62
63
64
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72
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77
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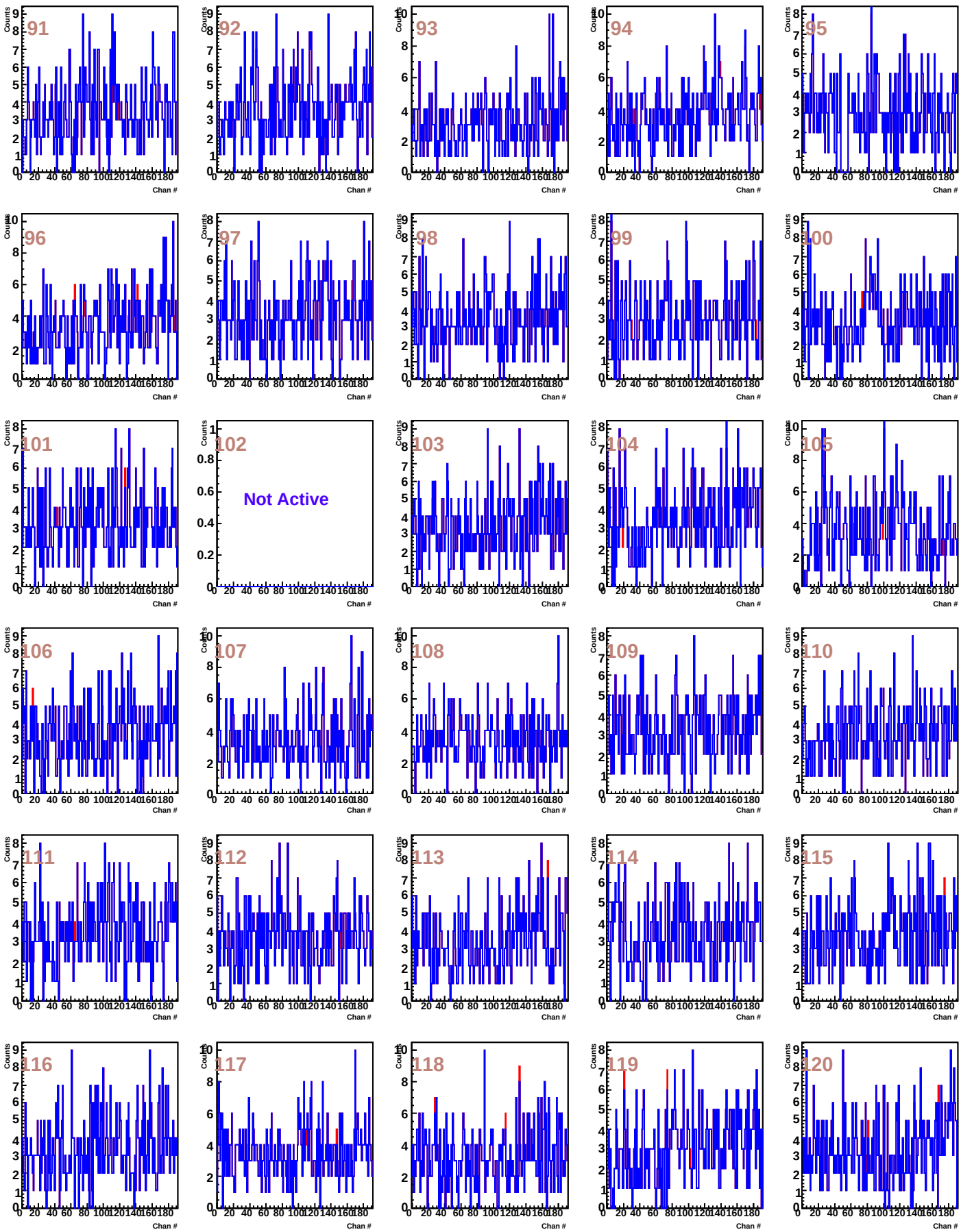
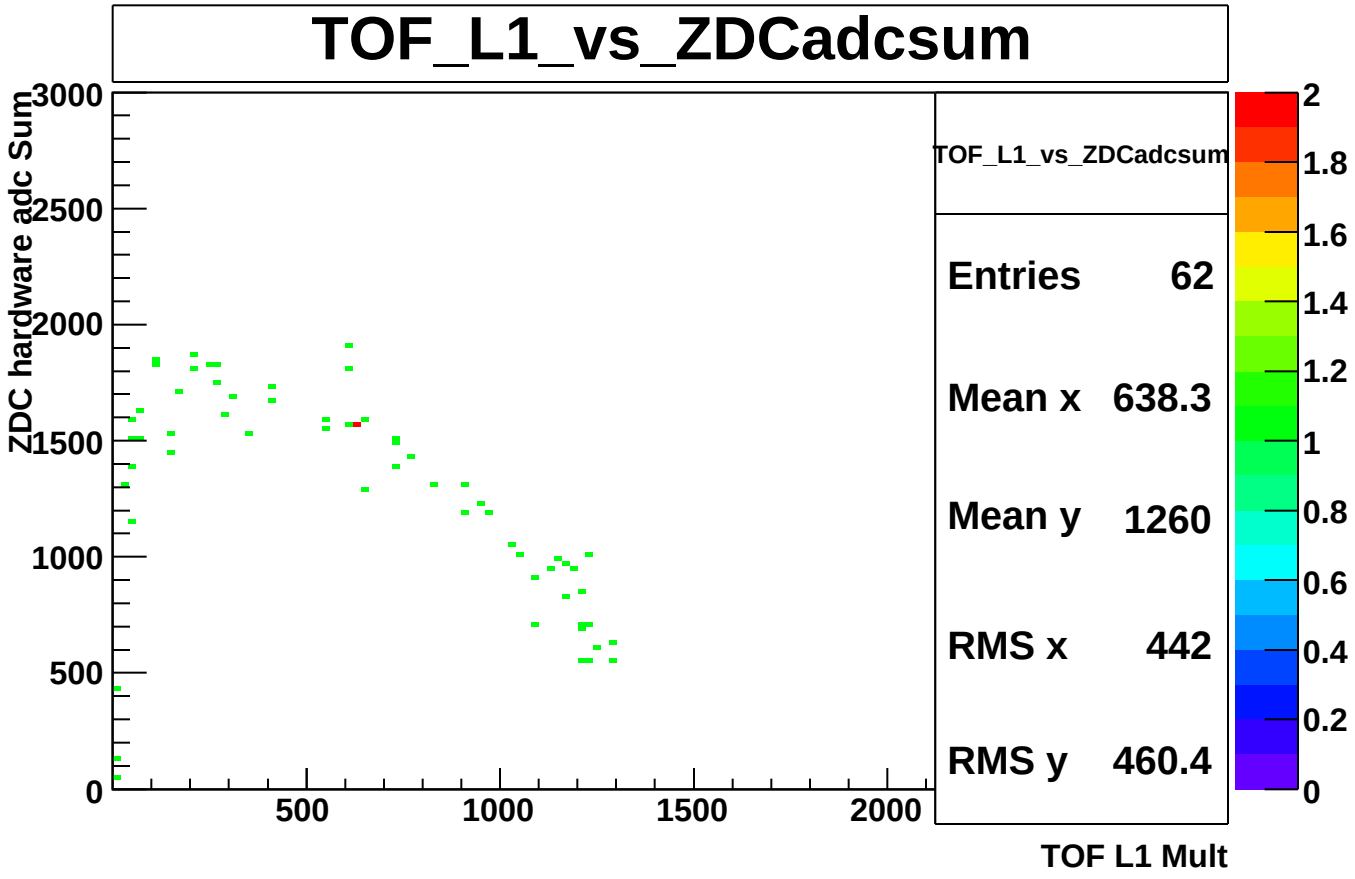
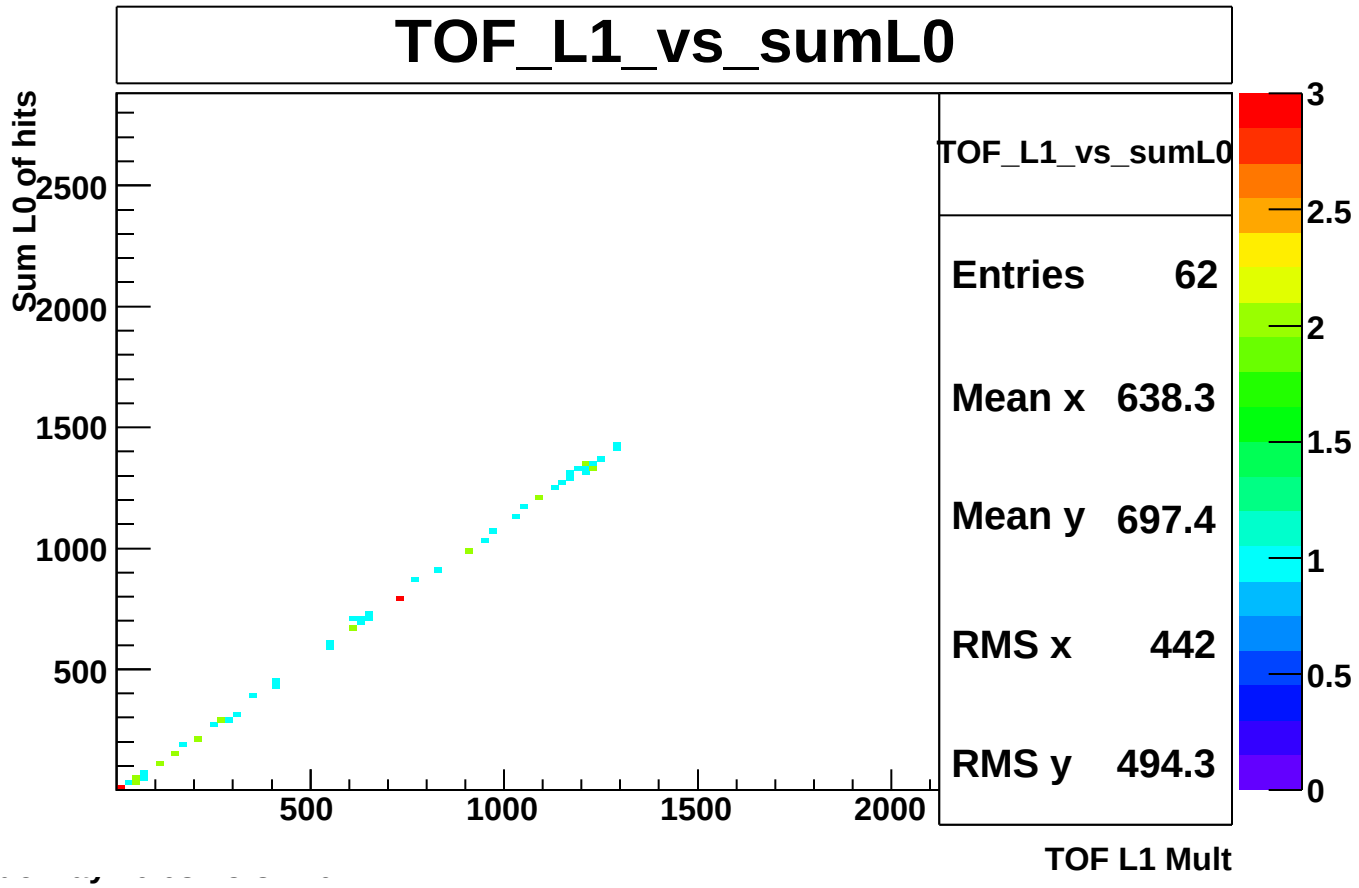
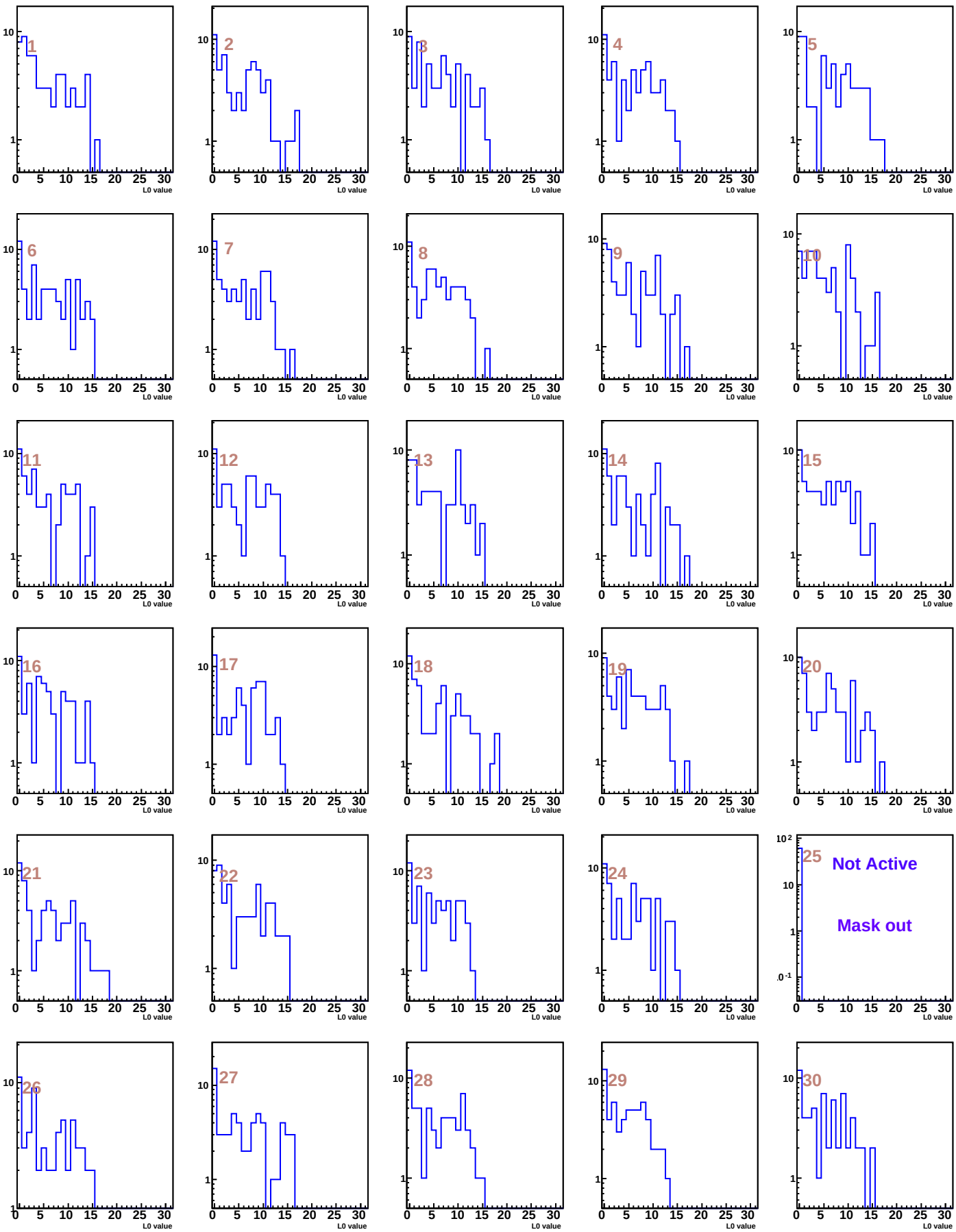
79
80
81
82
83
84
85
86
87
88
89
90
Counts
Chan #


91
92
93
94
95
96
97
98
99
100
101
102
Not Active
103
104
105
106
107
108
109
110
111
112
113
114
115
116
117
118
119
120
Counts
Chan #


TOF_L1_vs_ZDCadcsum
ZDC hardware adc Sum
TOF L1 Mult
TOF_L1_vs_ZDCadcsum
Entries 62
Mean x 638.3
Mean y 1260
RMS x 442
RMS y 460.4


TOF_L1_vs_sumL0
Sum L0 of hits
TOF L1 Mult
TOF_L1_vs_sumL0
Entries 62
Mean x 638.3
Mean y 697.4
RMS x 442
RMS y 494.3


1
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6
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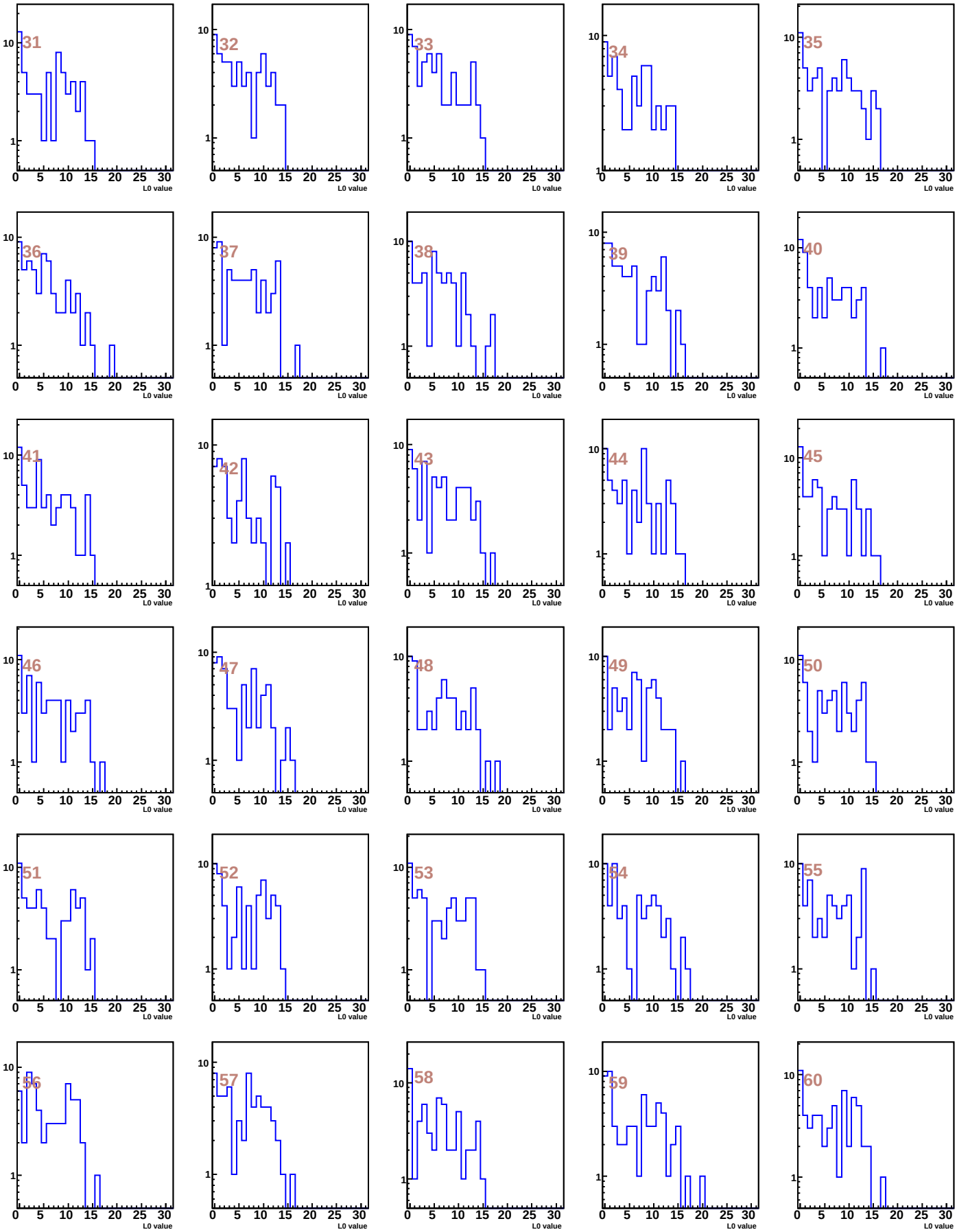
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23
24
25
Not Active
Mask out
26
27
28
29
30
L0 value


31
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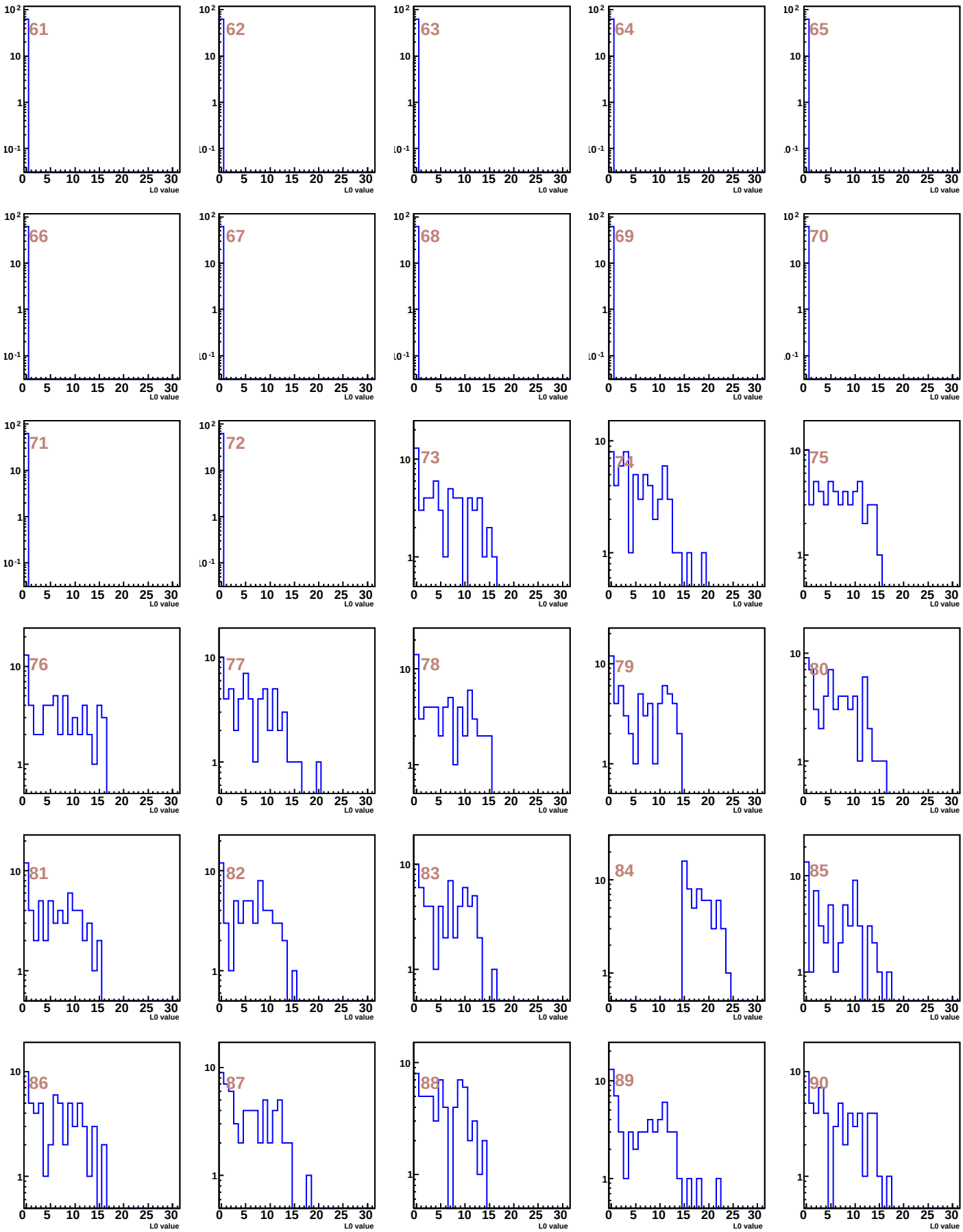
44
45
46
47
48
49
50
51
52
53
54
55
56
57
58
59
60
L0 value


61
62
63
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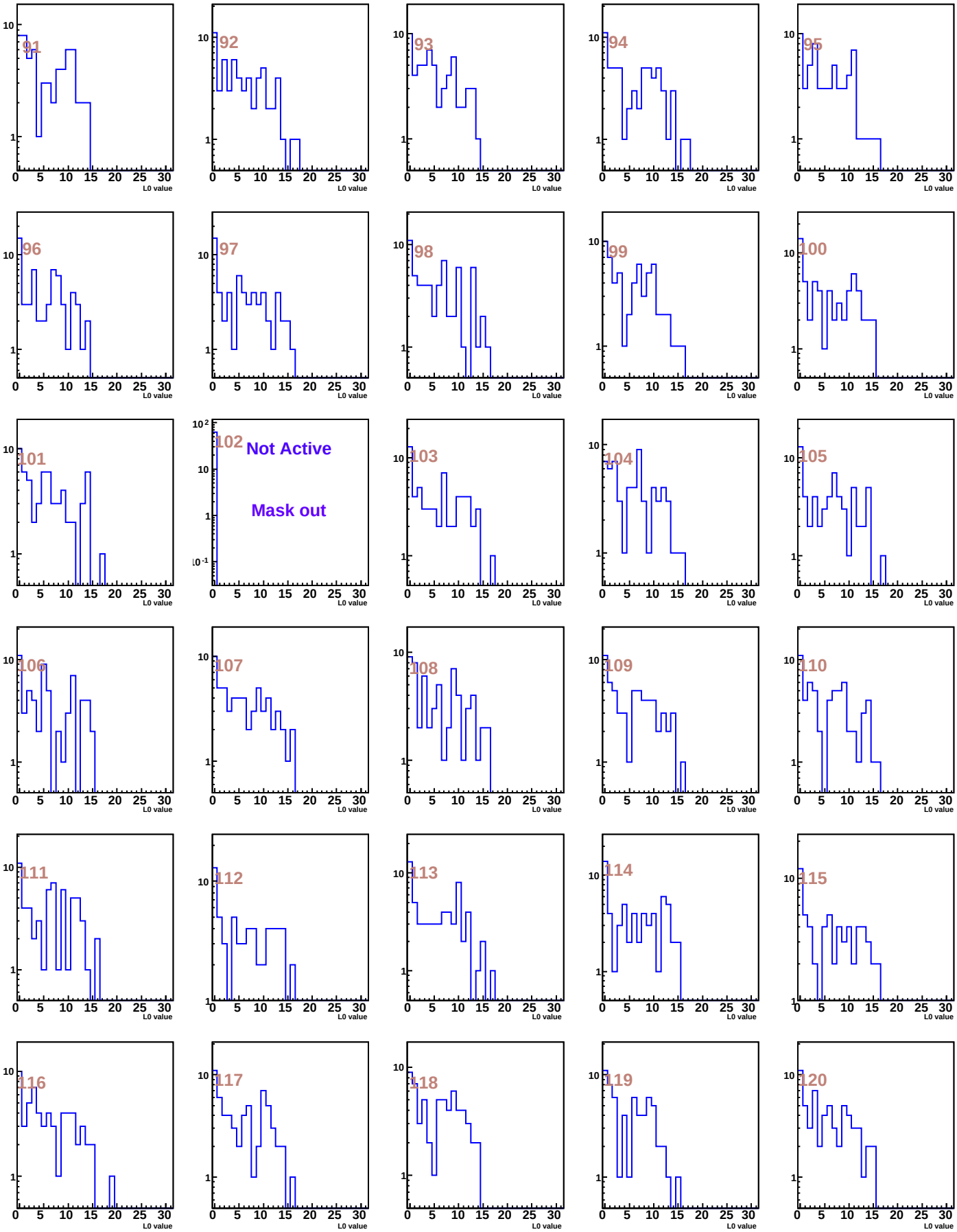
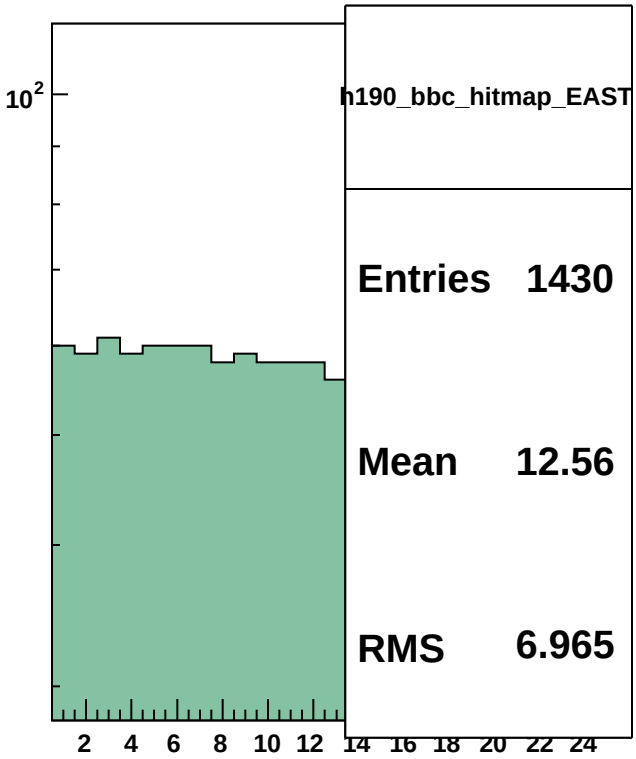
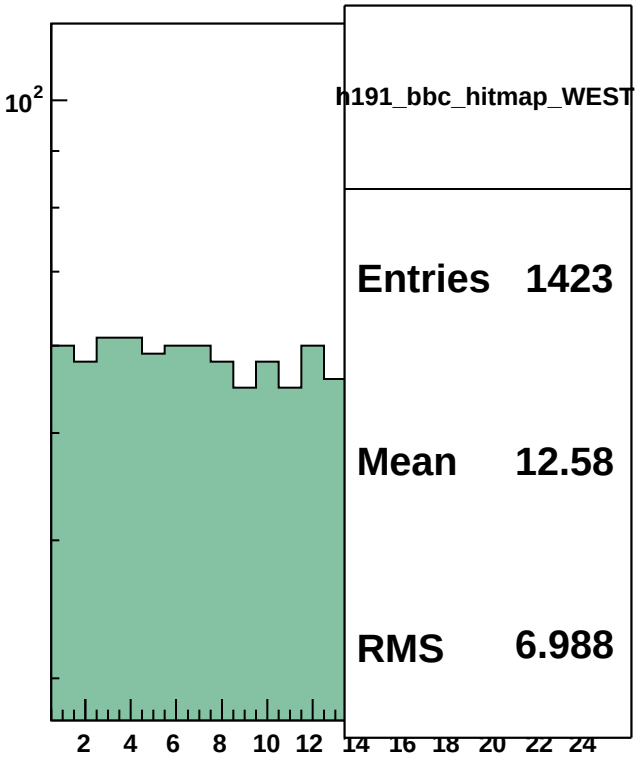
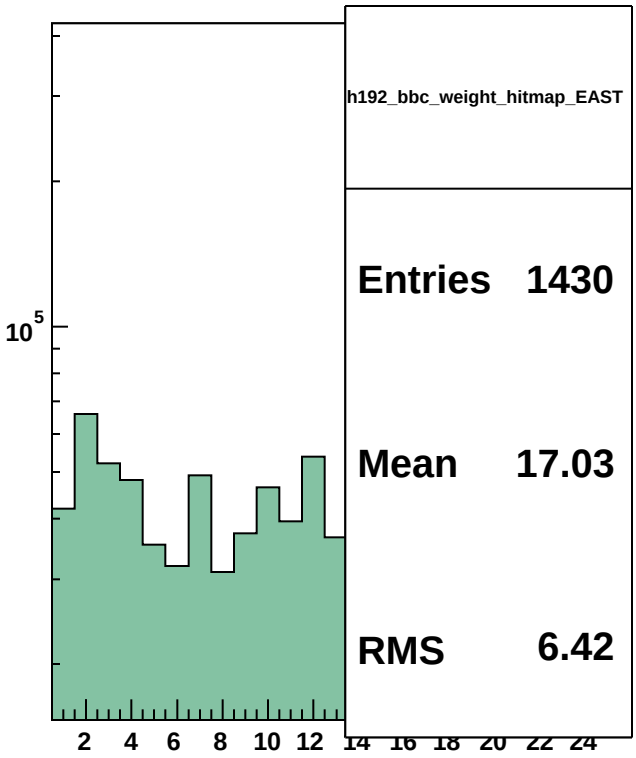
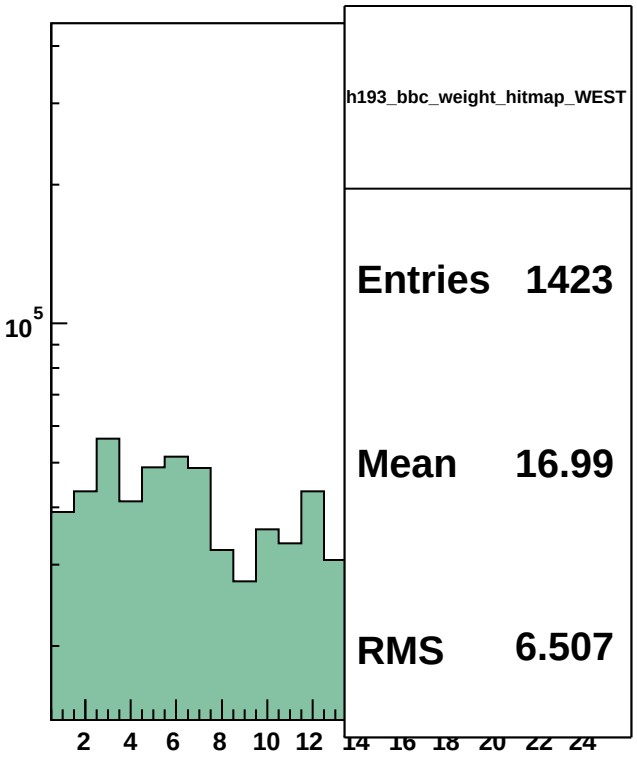
71
72
73
74
75
76
77
78
79
80
81
82
83
84
85
86
87
88
89
90
L0 value

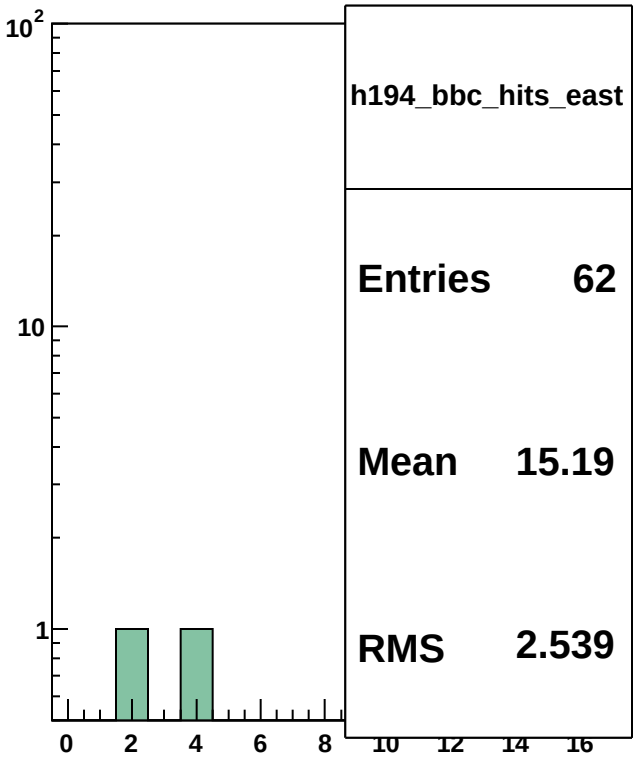

91
92
93
94
95
96
97
98
99
100
101
102
Not Active
Mask out
103
104
105
106
107
108
109
110
111
112
113
114
115
116
117
118
119
120
L0 value

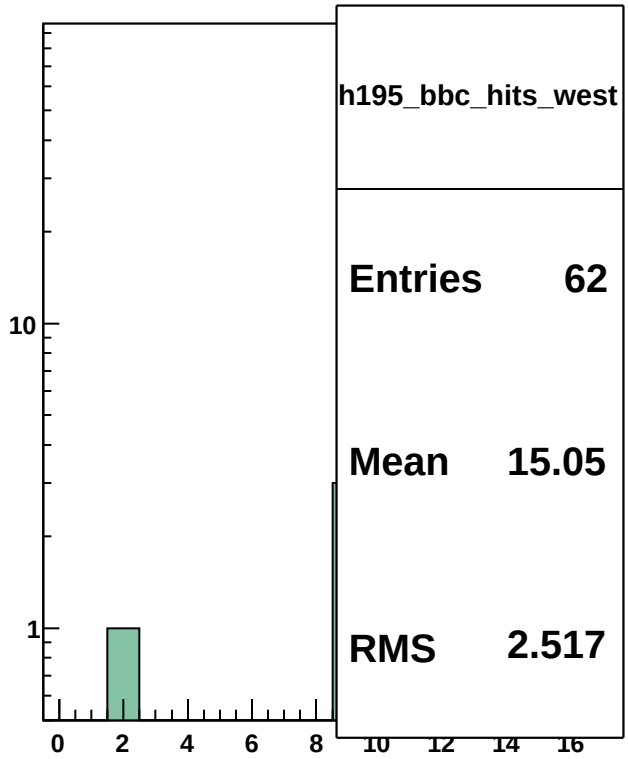

h190_bbc_hitmap_EAST
Entries 1430
Mean 12.56
RMS 6.965
10^2
2 4 6 8 10 12 14 16 18 20 22 24

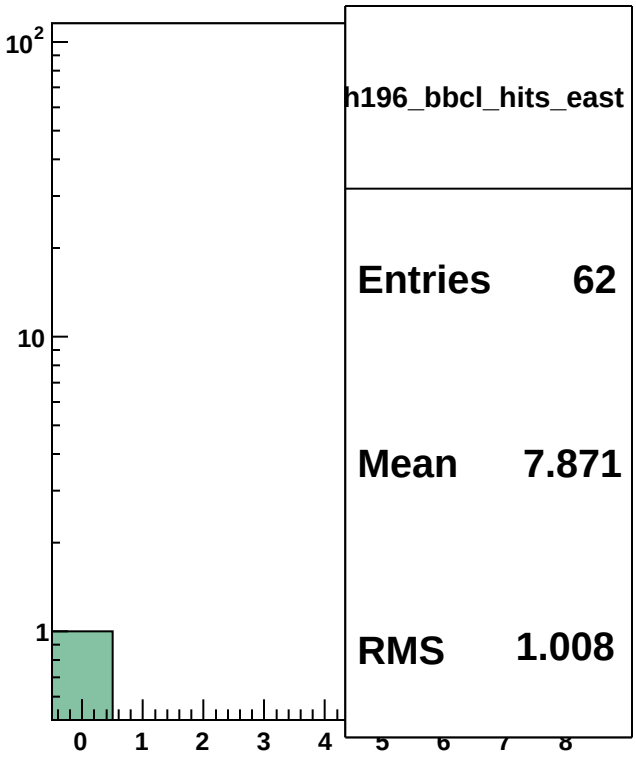

h191_bbc_hitmap_WEST
Entries 1423
Mean 12.58
RMS 6.988
10^2
2 4 6 8 10 12 14 16 18 20 22 24

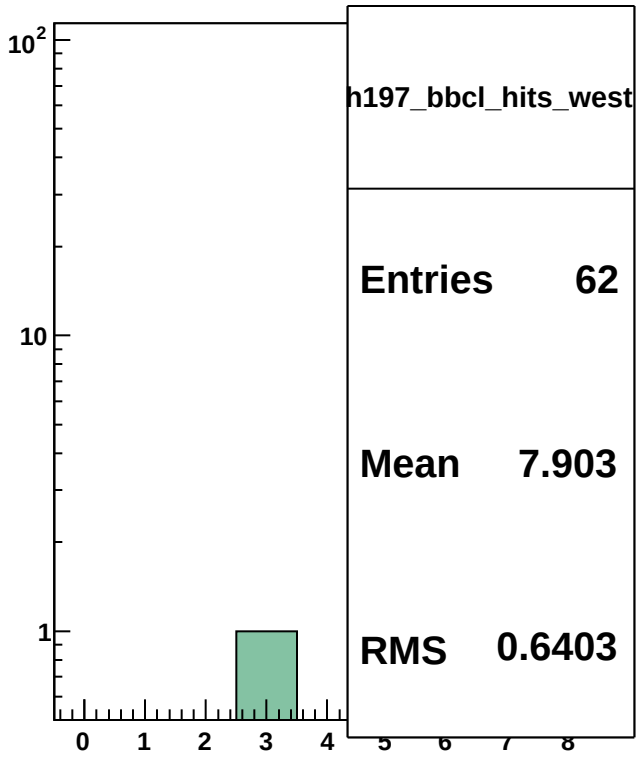

h192_bbc_weight_hitmap_EAST
Entries 1430
Mean 17.03
RMS 6.42
10^5
2 4 6 8 10 12 14 16 18 20 22 24

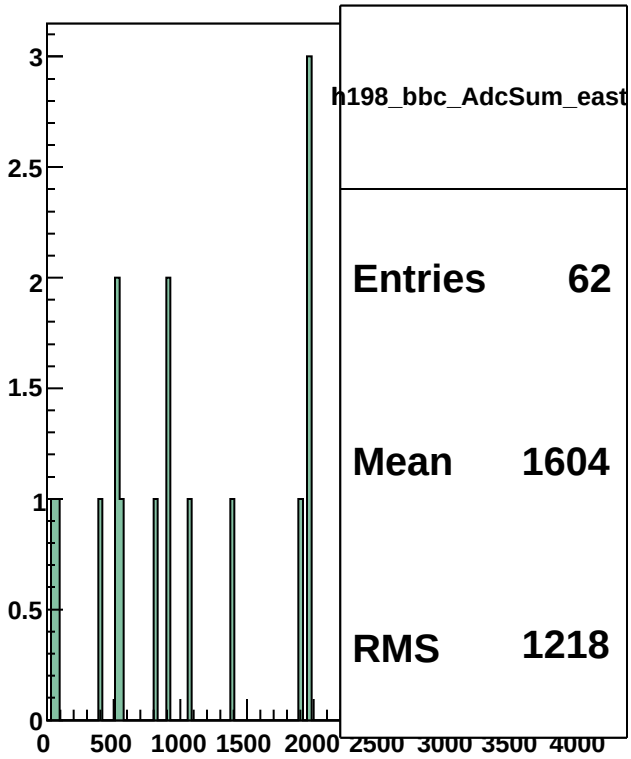

h193_bbc_weight_hitmap_WEST
Entries 1423
Mean 16.99
RMS 6.507
10^5
2 4 6 8 10 12 14 16 18 20 22 24

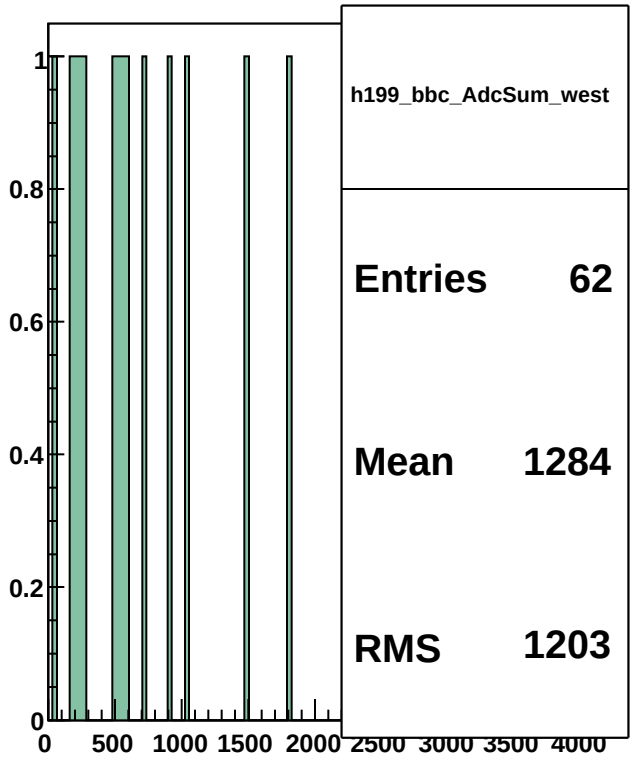

h194_bbc_hits_east
Entries 62
Mean 15.19
RMS 2.539

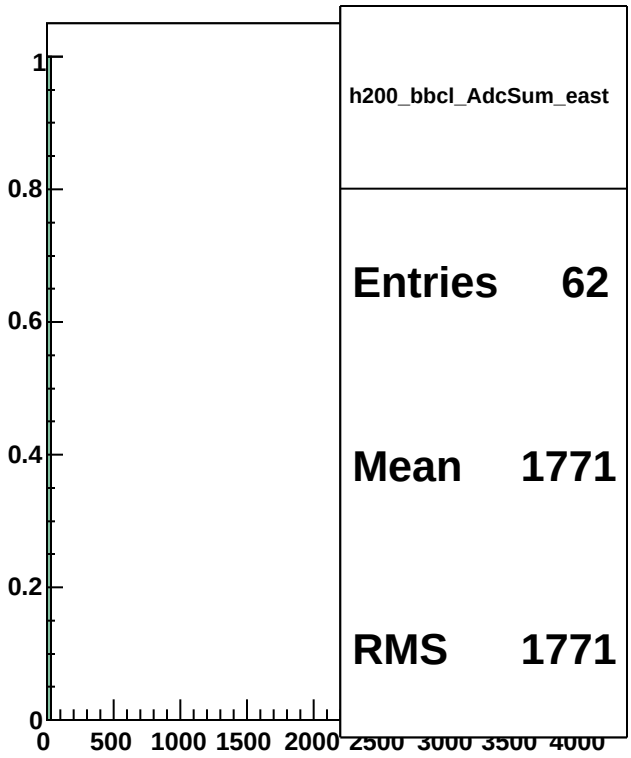

h195_bbc_hits_west
Entries 62
Mean 15.05
RMS 2.517

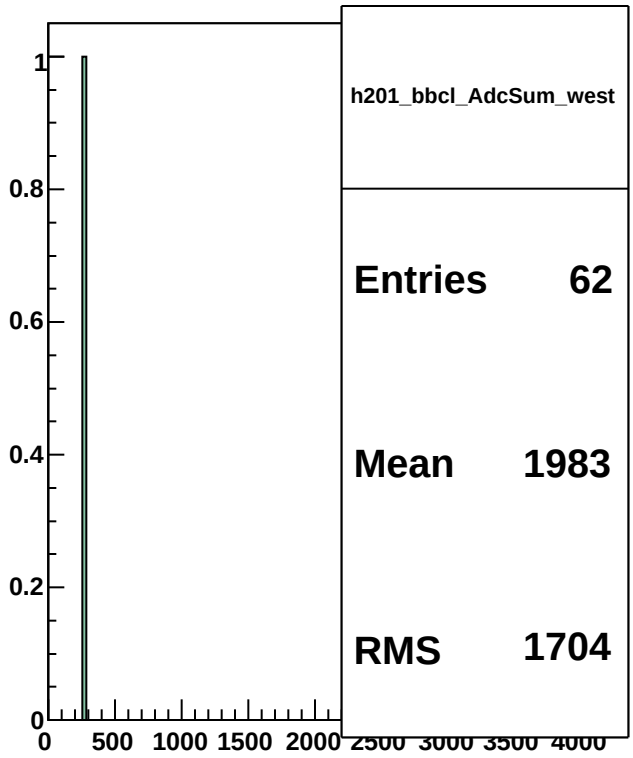

h196_bbcl_hits_east
Entries 62
Mean 7.871
RMS 1.008

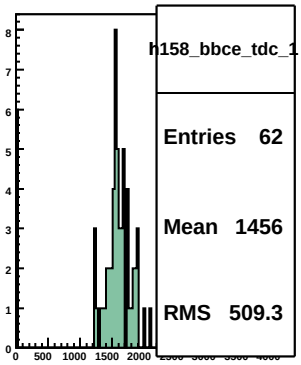

h197_bbcl_hits_west
Entries 62
Mean 7.903
RMS 0.6403

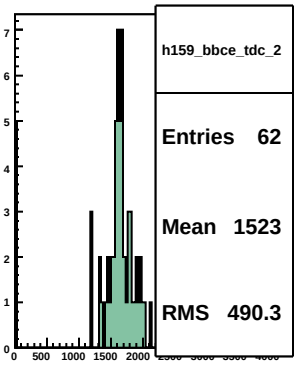

h198_bbc_AdcSum_east
Entries 62
Mean 1604
RMS 1218

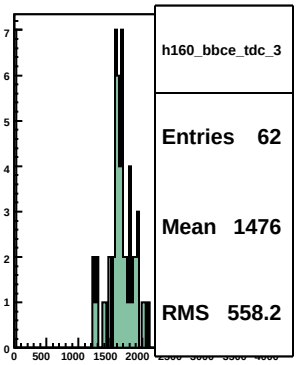

h199_bbc_AdcSum_west
Entries 62
Mean 1284
RMS 1203

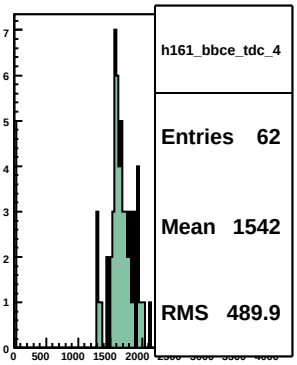

h200_bbcl_AdcSum_east
Entries 62
Mean 1771
RMS 1771

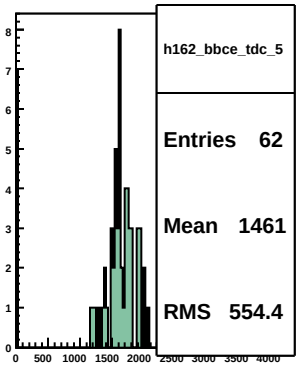

h201_bbcl_AdcSum_west
Entries 62
Mean 1983
RMS 1704

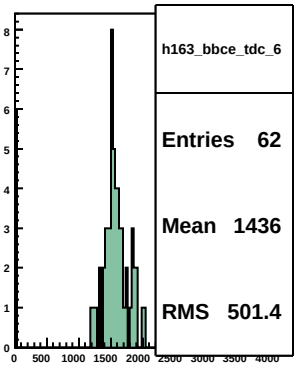

h158_bbce_tdc_1
Entries 62
Mean 1456
RMS 509.3

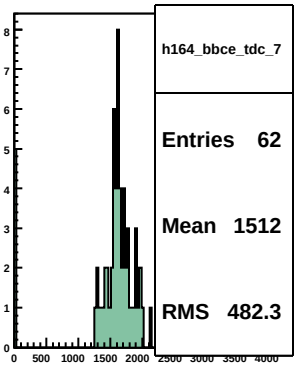

h159_bbce_tdc_2
Entries 62
Mean 1523
RMS 490.3

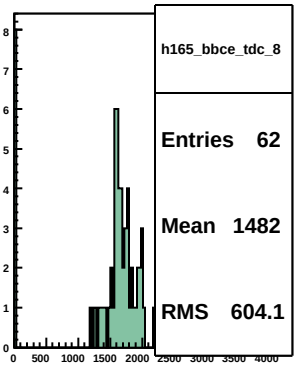

h160_bbce_tdc_3
Entries 62
Mean 1476
RMS 558.2

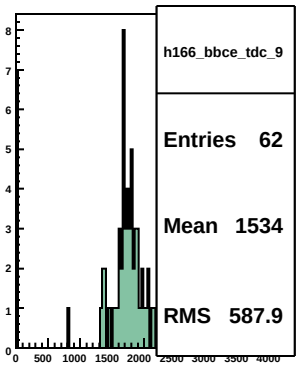

h161_bbce_tdc_4
Entries 62
Mean 1542
RMS 489.9

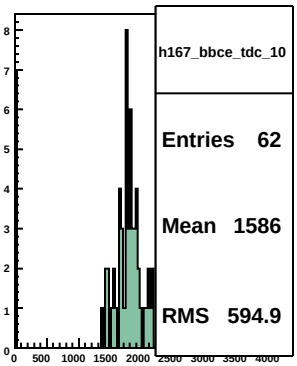

h162_bbce_tdc_5
Entries 62
Mean 1461
RMS 554.4

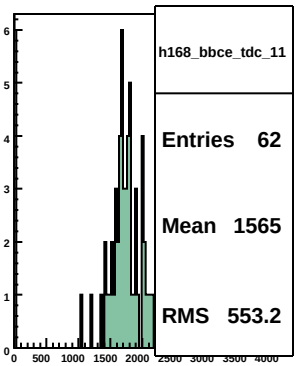

h163_bbce_tdc_6
Entries 62
Mean 1436
RMS 501.4

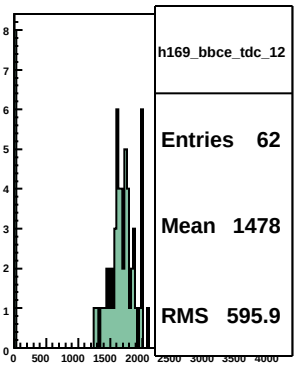

h164_bbce_tdc_7
Entries 62
Mean 1512
RMS 482.3

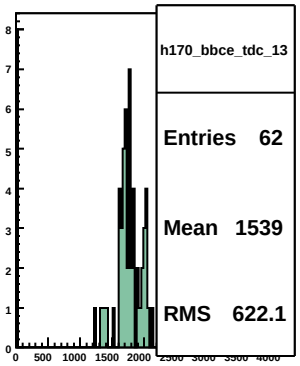

h165_bbce_tdc_8
Entries 62
Mean 1482
RMS 604.1

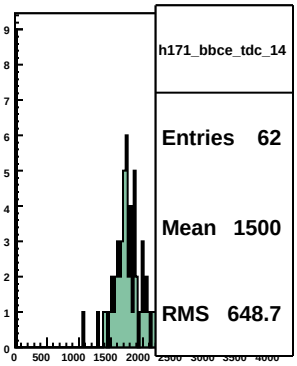

h166_bbce_tdc_9
Entries 62
Mean 1534
RMS 587.9

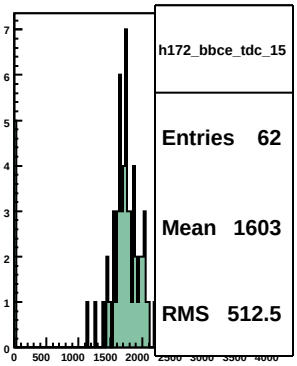

h167_bbce_tdc_10
Entries 62
Mean 1586
RMS 594.9

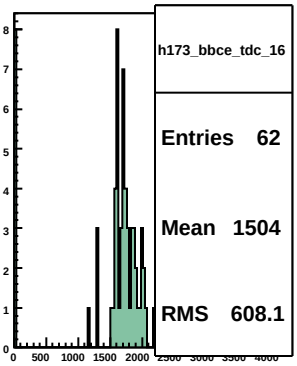

h168_bbce_tdc_11
Entries 62
Mean 1565
RMS 553.2

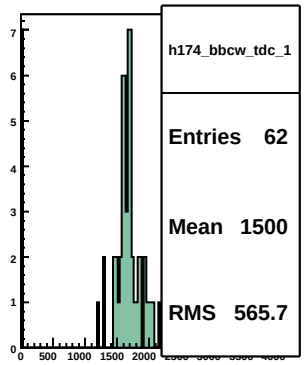

h169_bbce_tdc_12
Entries 62
Mean 1478
RMS 595.9

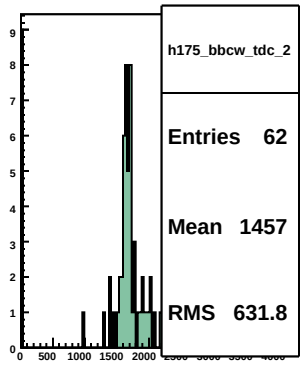

h170_bbce_tdc_13
Entries 62
Mean 1539
RMS 622.1

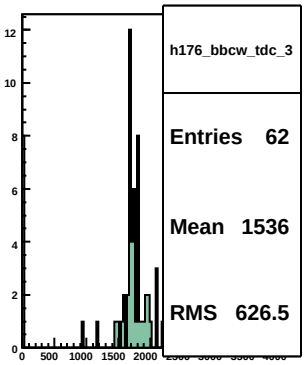

h171_bbce_tdc_14
Entries 62
Mean 1500
RMS 648.7

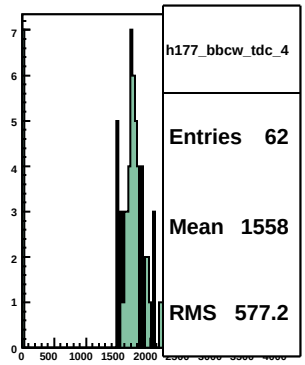

h172_bbce_tdc_15
Entries 62
Mean 1603
RMS 512.5

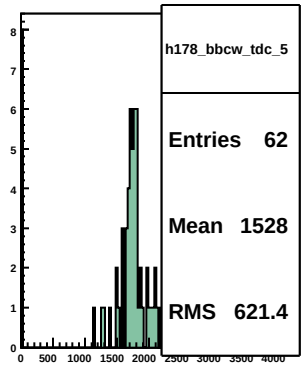

h173_bbce_tdc_16
Entries 62
Mean 1504
RMS 608.1

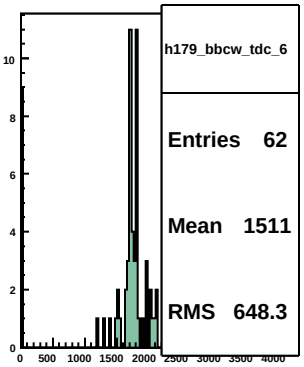

h174_bbcw_tdc_1
Entries 62
Mean 1500
RMS 565.7

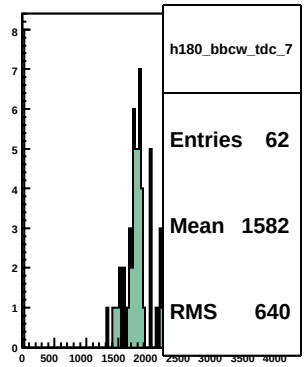

h175_bbcw_tdc_2
Entries 62
Mean 1457
RMS 631.8

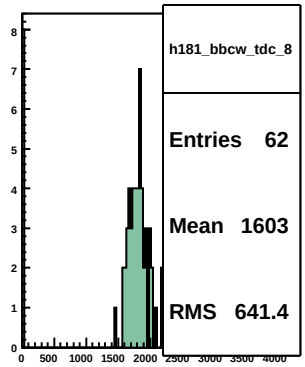

h176_bbcw_tdc_3
Entries 62
Mean 1536
RMS 626.5

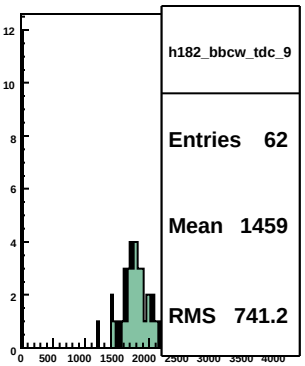

h177_bbcw_tdc_4
Entries 62
Mean 1558
RMS 577.2

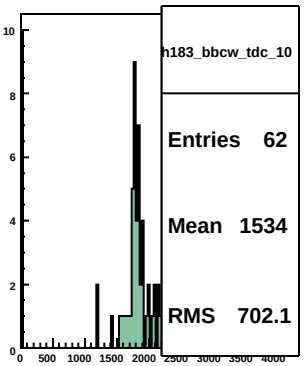

h178_bbcw_tdc_5
Entries 62
Mean 1528
RMS 621.4

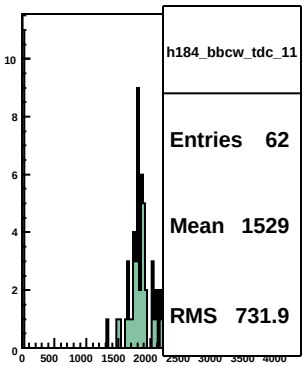

h179_bbcw_tdc_6
Entries 62
Mean 1511
RMS 648.3

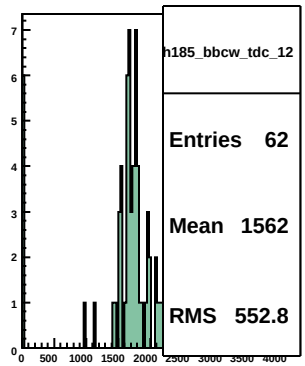

h180_bbcw_tdc_7
Entries 62
Mean 1582
RMS 640

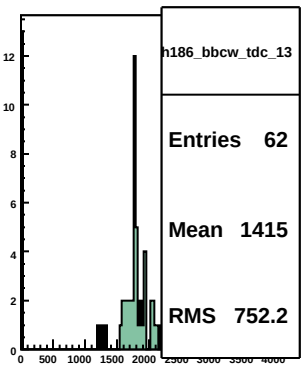

h181_bbcw_tdc_8
Entries 62
Mean 1603
RMS 641.4

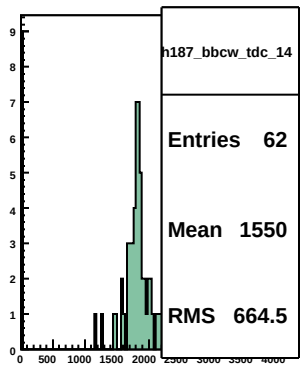

h182_bbcw_tdc_9
Entries 62
Mean 1459
RMS 741.2

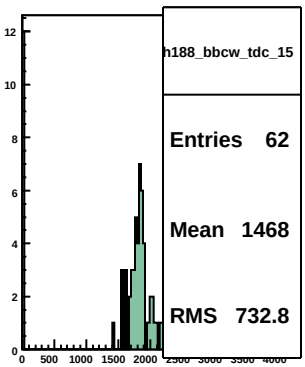

h183_bbcw_tdc_10
Entries 62
Mean 1534
RMS 702.1

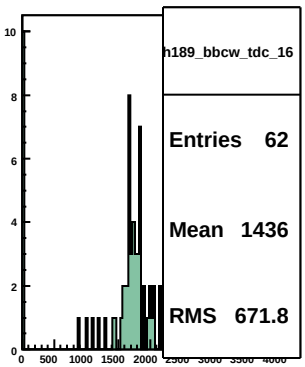

h184_bbcw_tdc_11
Entries 62
Mean 1529
RMS 731.9

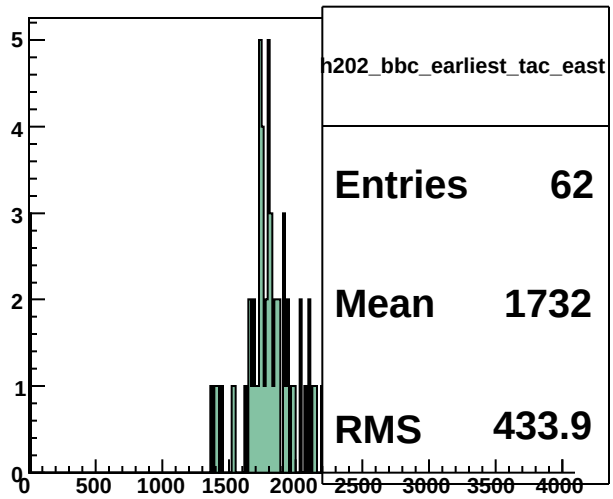

h185_bbcw_tdc_12
Entries 62
Mean 1562
RMS 552.8

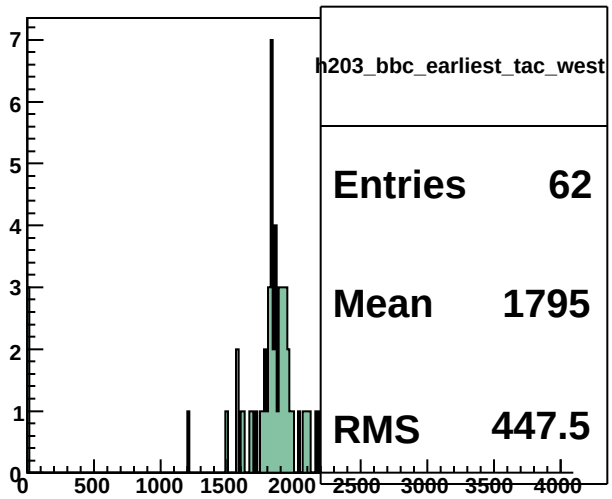

h186_bbcw_tdc_13
Entries 62
Mean 1415
RMS 752.2

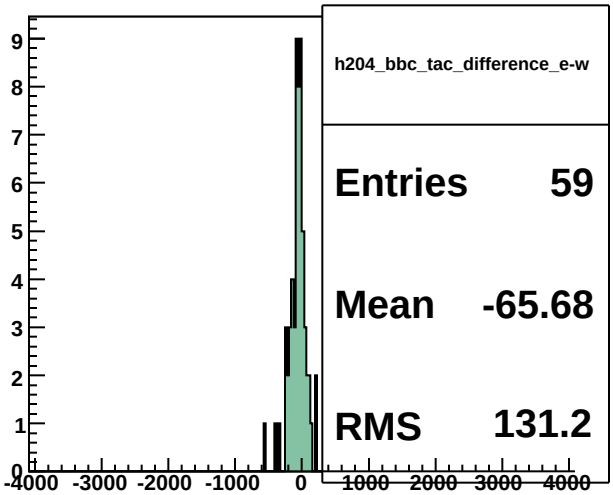

h187_bbcw_tdc_14
Entries 62
Mean 1550
RMS 664.5

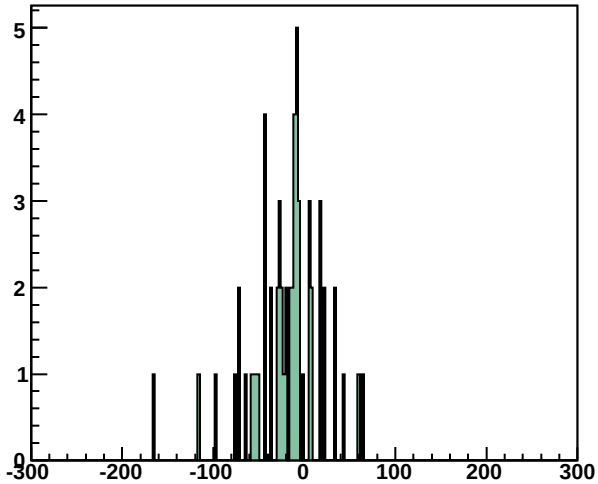

h188_bbcw_tdc_15
Entries 62
Mean 1468
RMS 732.8

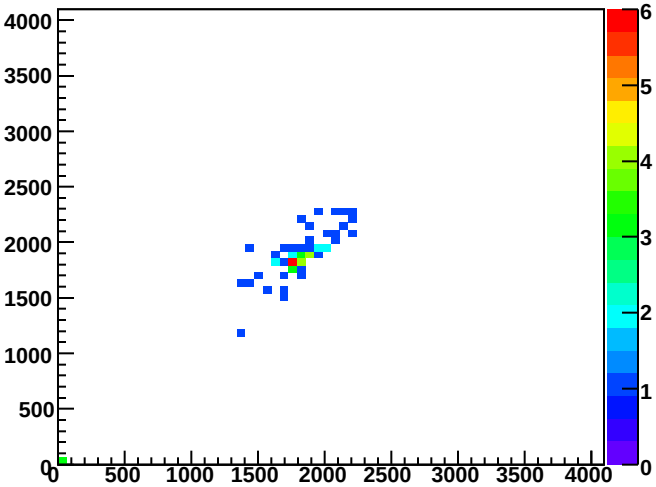

h189_bbcw_tdc_16
Entries 62
Mean 1436
RMS 671.8

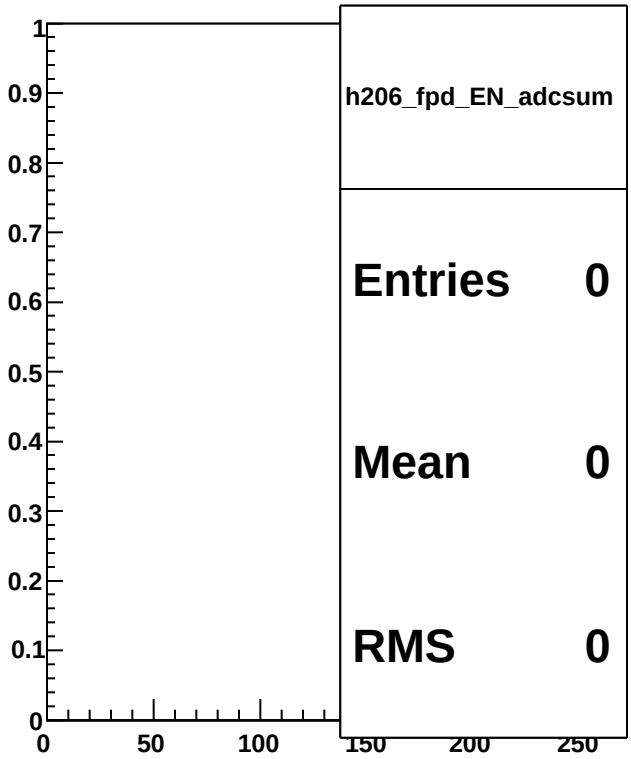

h202_bbc_earliest_tac_east
Entries 62
Mean 1732
RMS 433.9

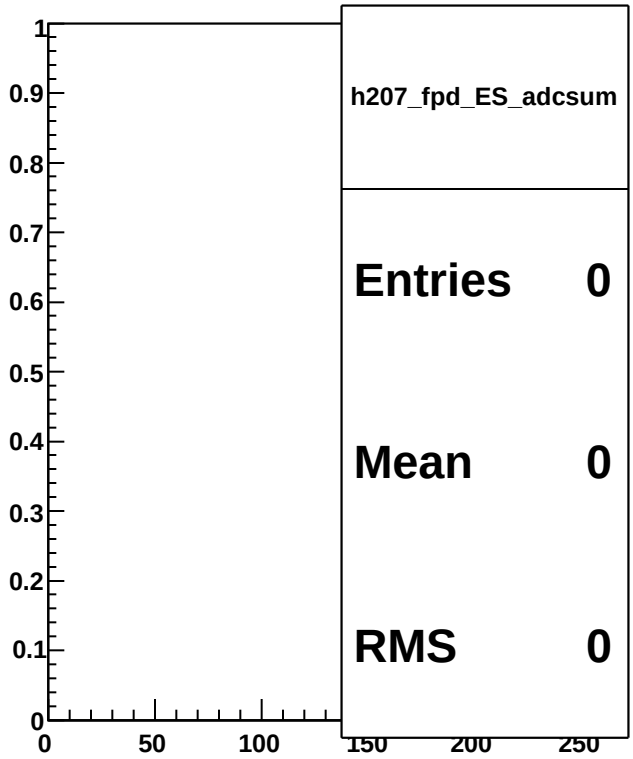

h203_bbc_earliest_tac_west
Entries 62
Mean 1795
RMS 447.5

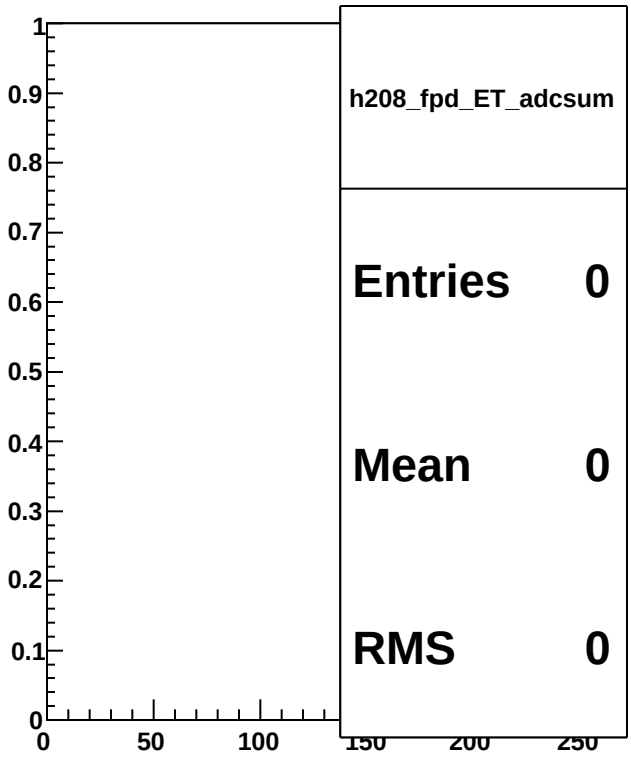

h204_bbc_tac_difference_e-w
Entries 59
Mean -65.68
RMS 131.2

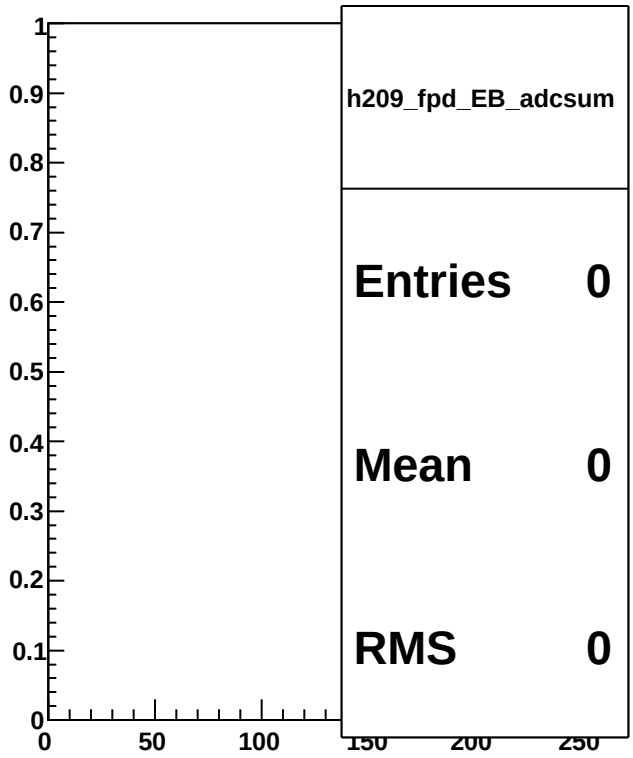


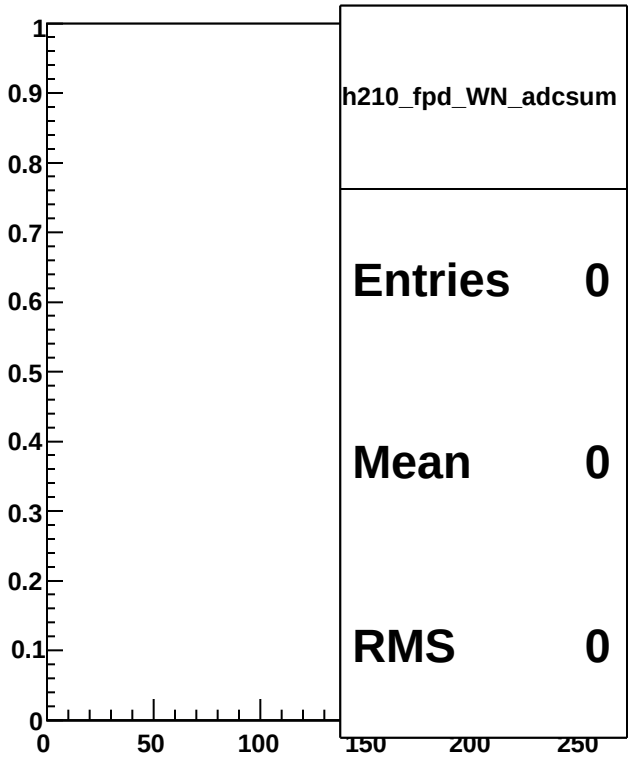


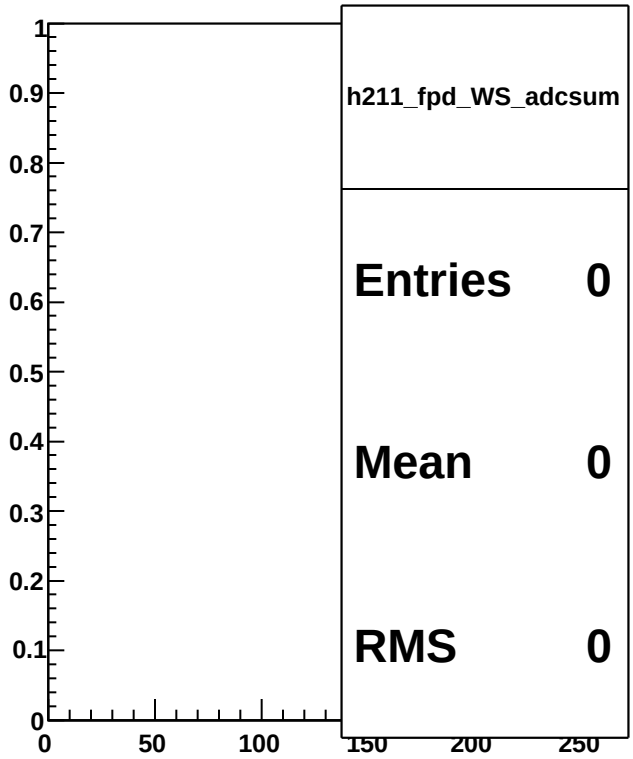

h206_fpd_EN_adcsum
Entries 0
Mean 0
RMS 0

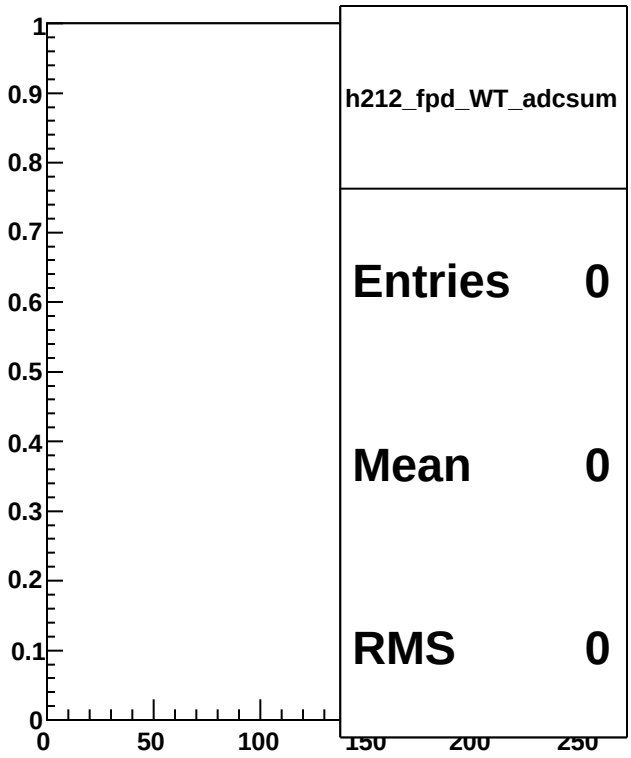

h207_fpd_ES_adcsum
Entries 0
Mean 0
RMS 0

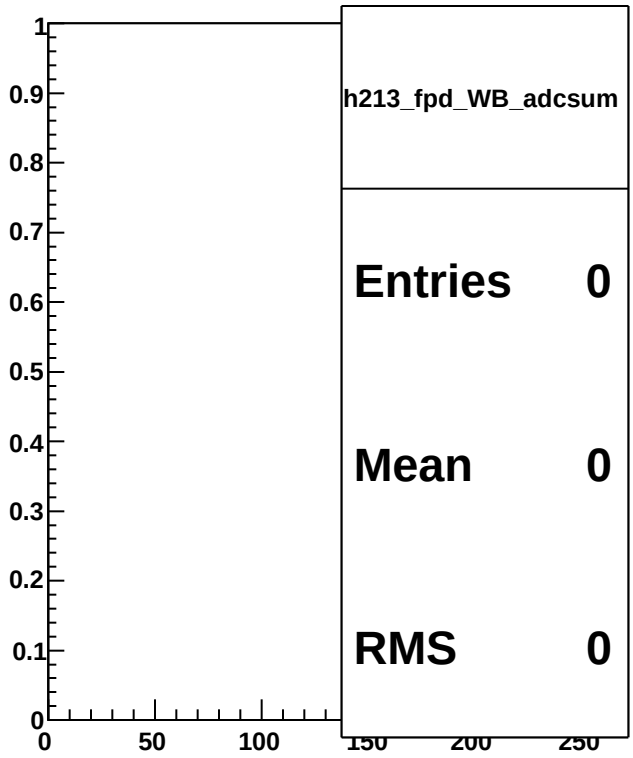

h208_fpd_ET_adcsum
Entries 0
Mean 0
RMS 0

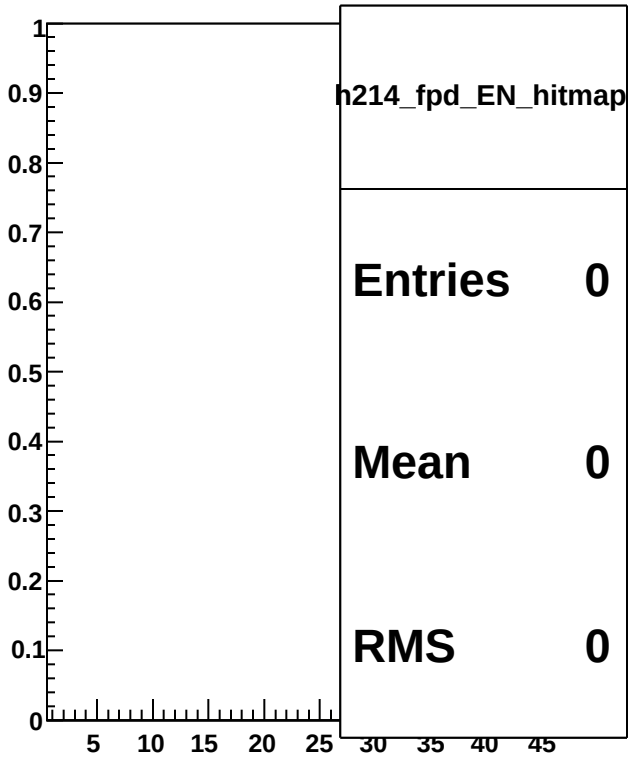

h209_fpd_EB_adcsum
Entries 0
Mean 0
RMS 0

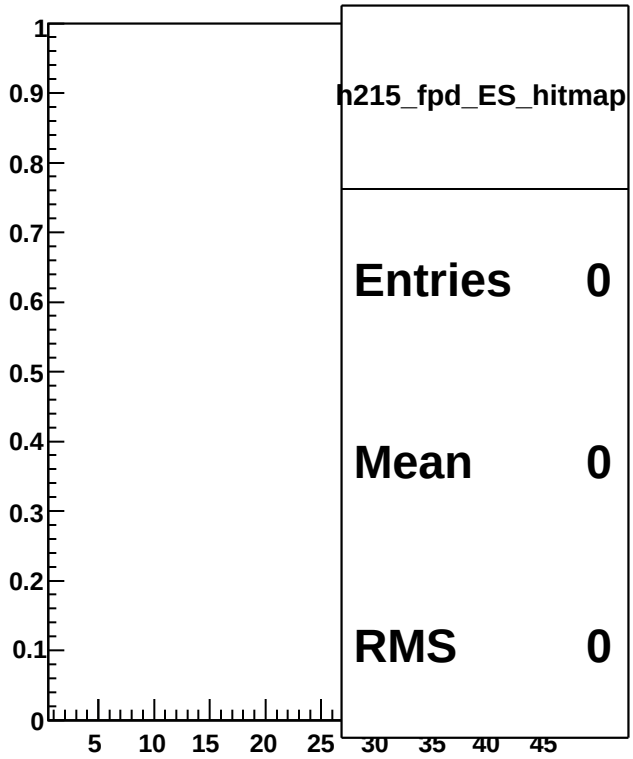

h210_fpd_WN_adcsum
Entries 0
Mean 0
RMS 0

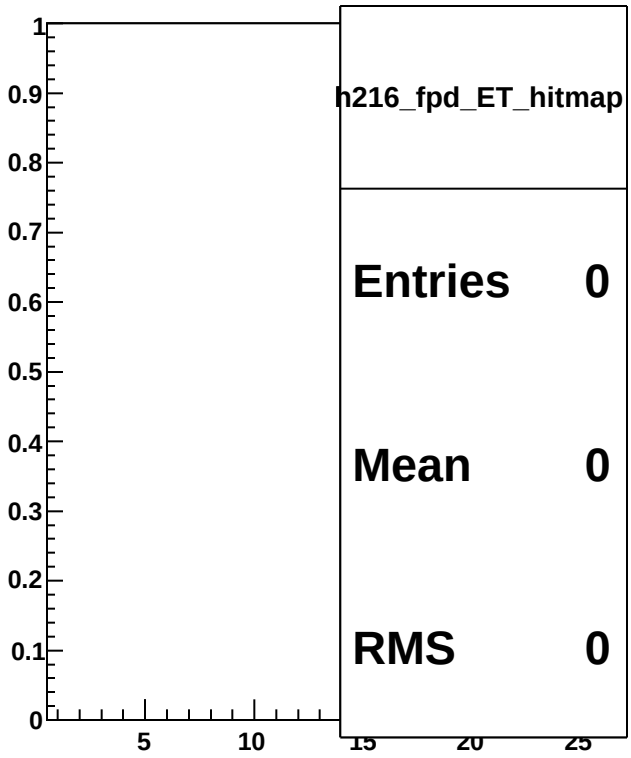

h211_fpd_WS_adcsum
Entries 0
Mean 0
RMS 0

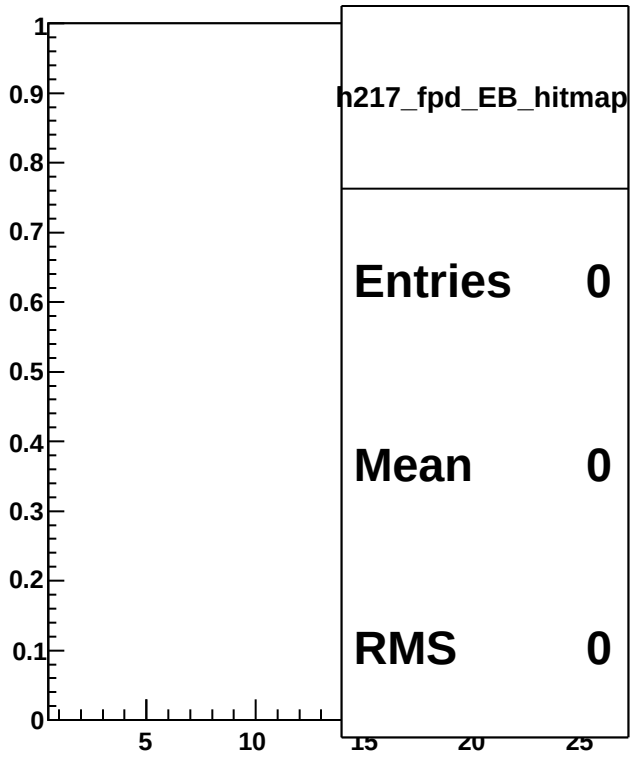

h212_fpd_WT_adcsum
Entries 0
Mean 0
RMS 0

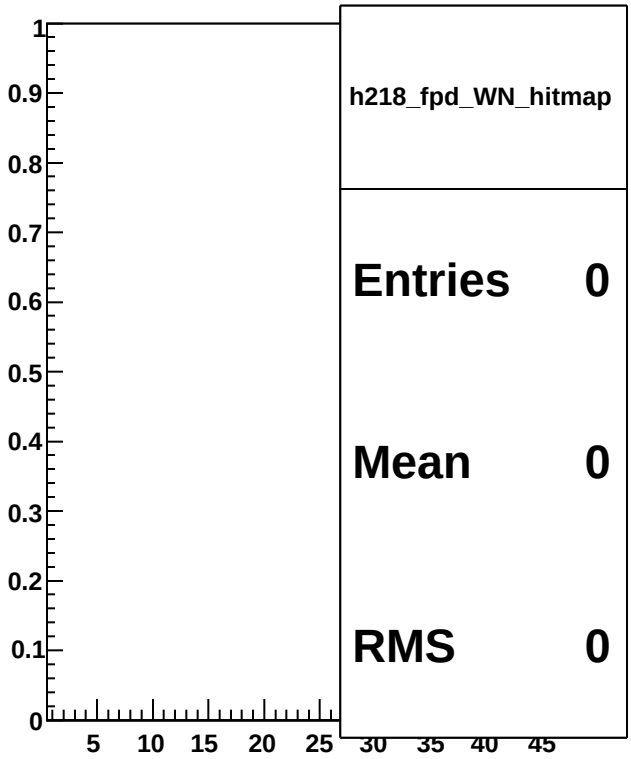

h213_fpd_WB_adcsum
Entries 0
Mean 0
RMS 0

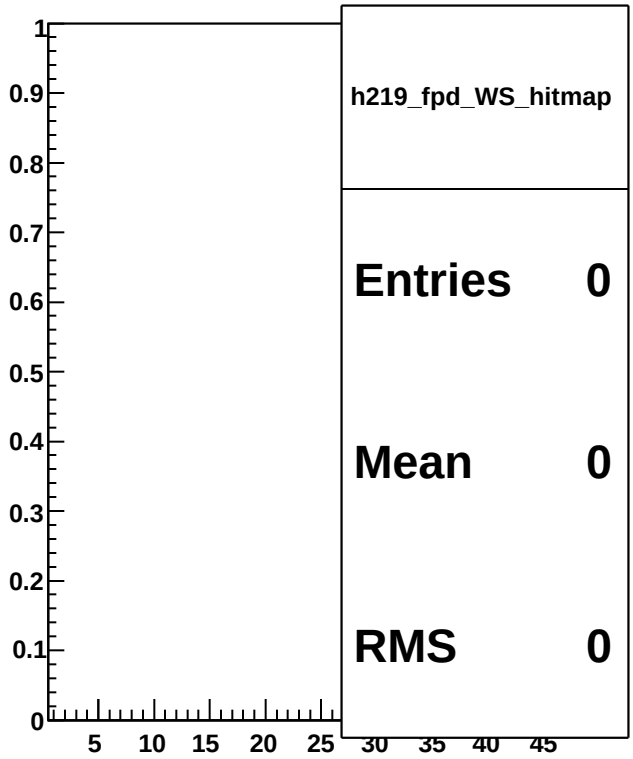

h214_fpd_EN_hitmap
Entries 0
Mean 0
RMS 0

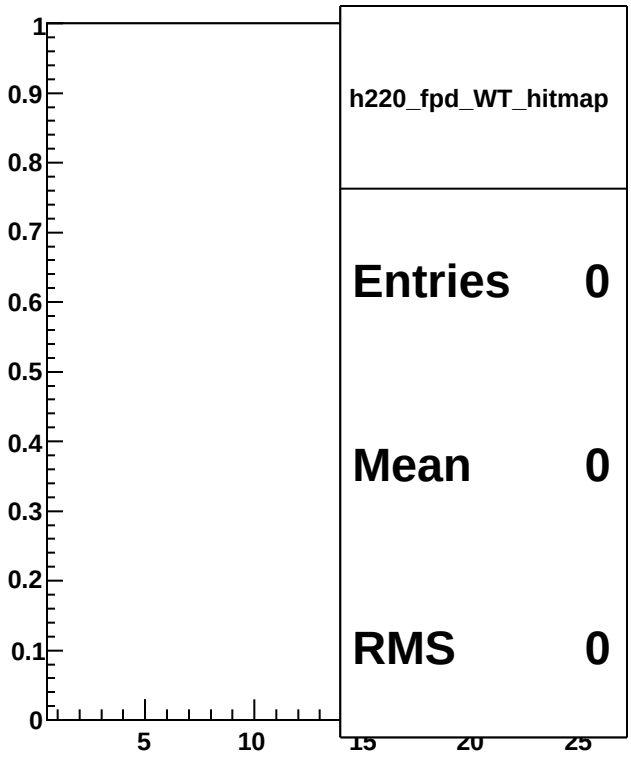

h215_fpd_ES_hitmap
Entries 0
Mean 0
RMS 0

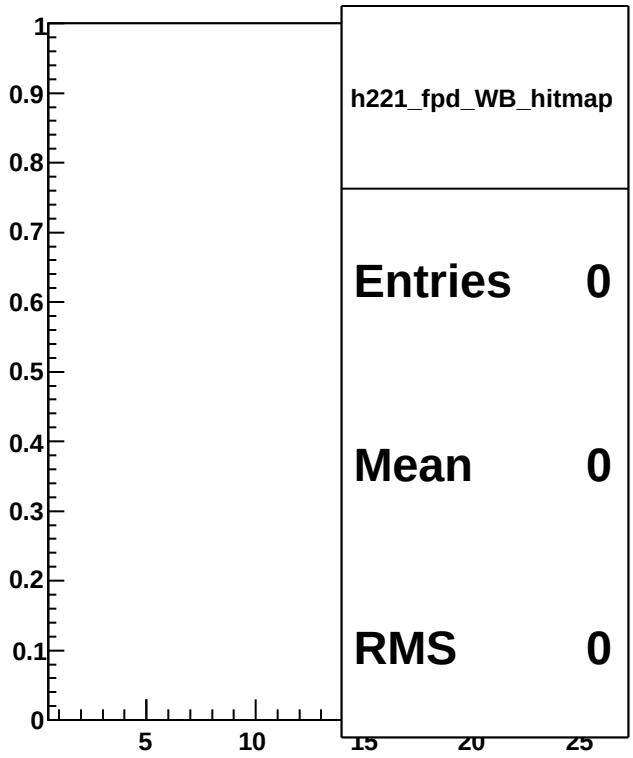

h216_fpd_ET_hitmap
Entries 0
Mean 0
RMS 0

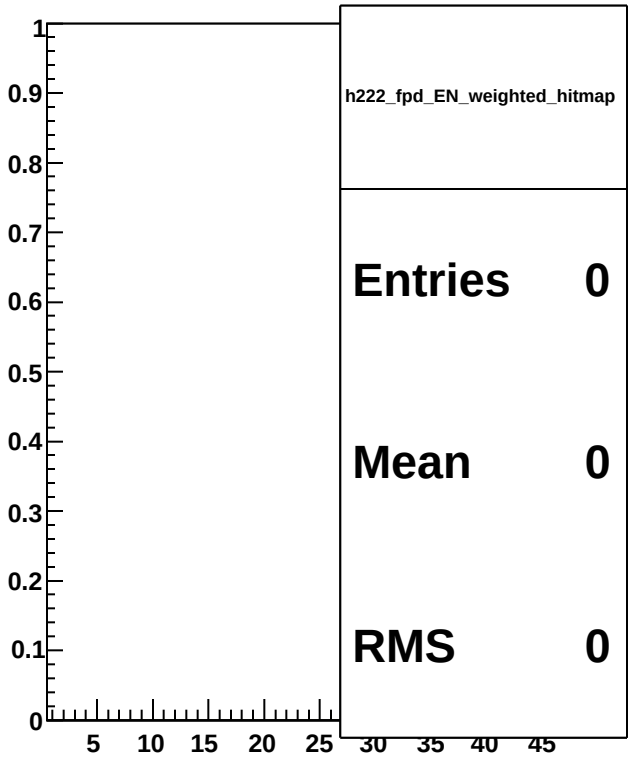

h217_fpd_EB_hitmap
Entries 0
Mean 0
RMS 0

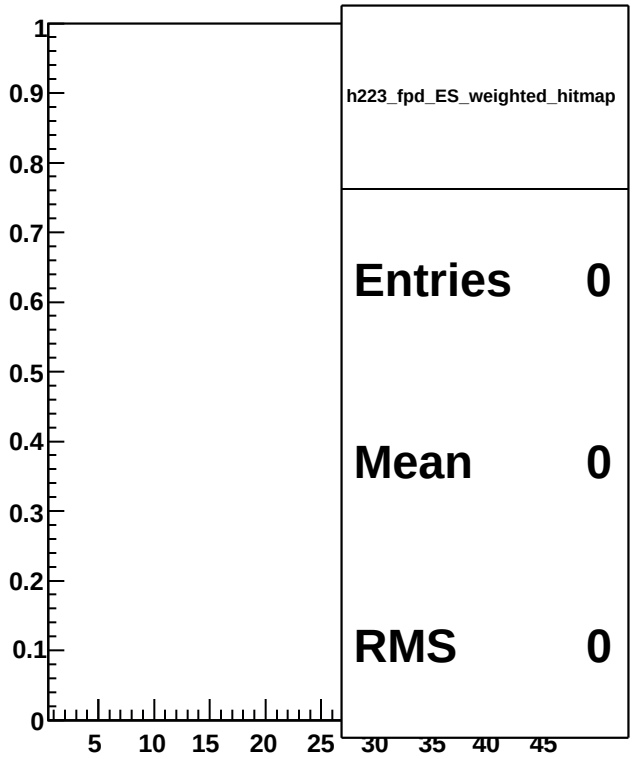

h218_fpd_WN_hitmap
Entries 0
Mean 0
RMS 0

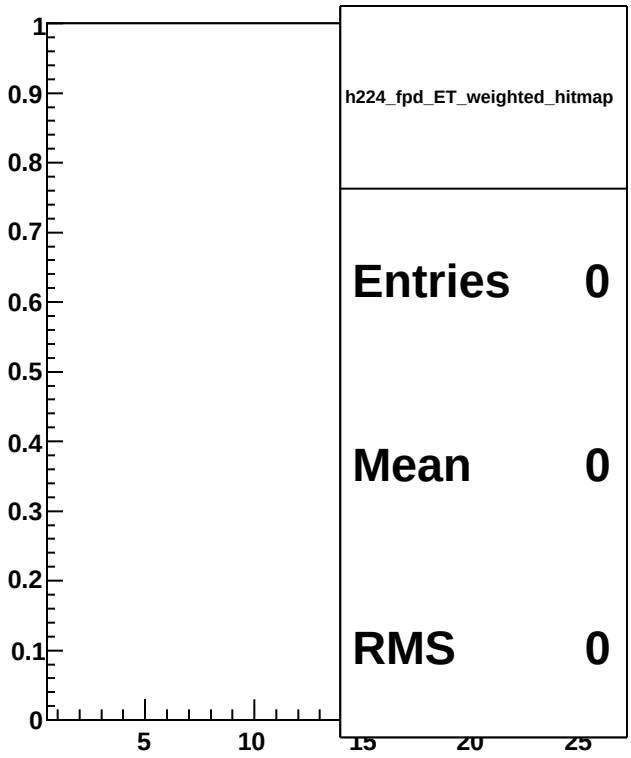

h219_fpd_WS_hitmap
Entries 0
Mean 0
RMS 0

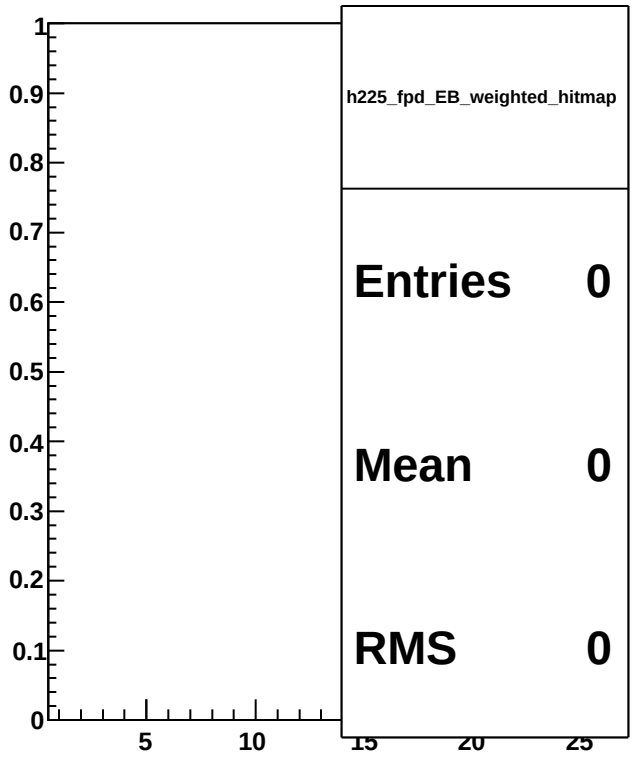

h220_fpd_WT_hitmap
Entries 0
Mean 0
RMS 0

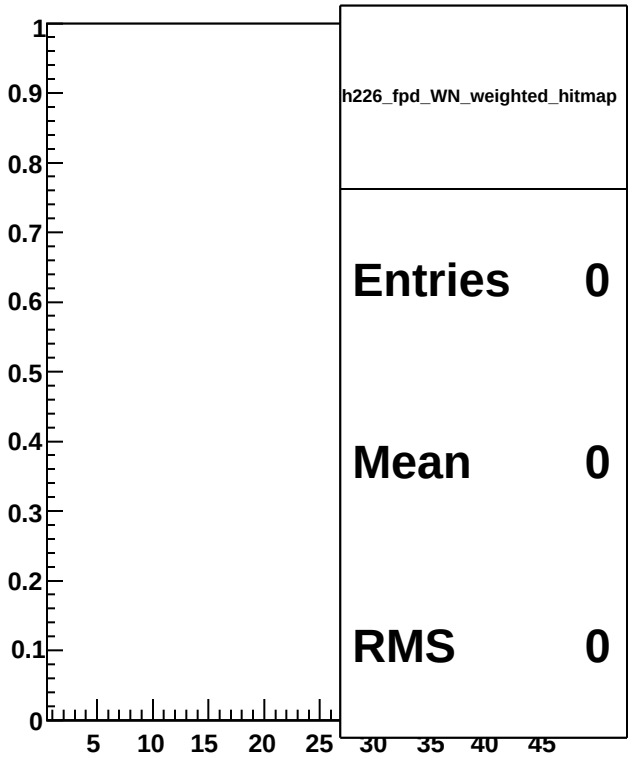

h221_fpd_WB_hitmap
Entries 0
Mean 0
RMS 0

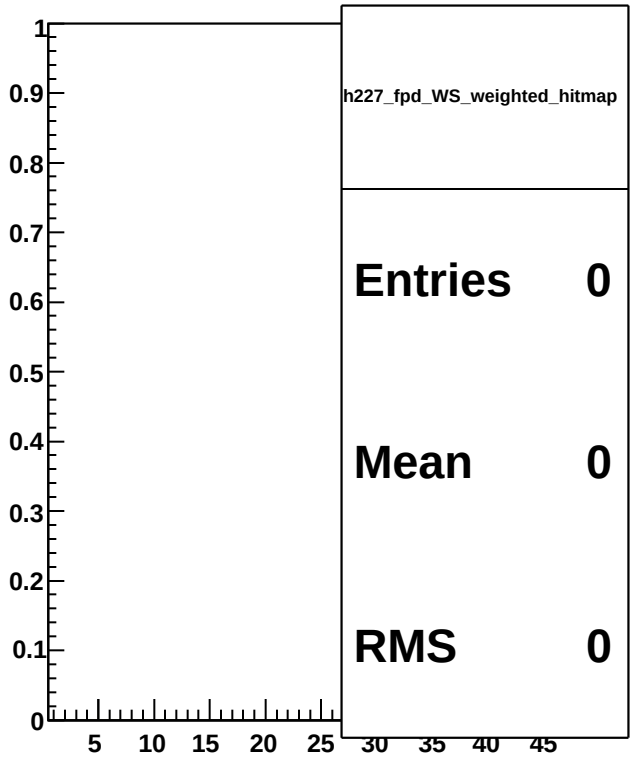

h222_fpd_EN_weighted_hitmap
Entries 0
Mean 0
RMS 0

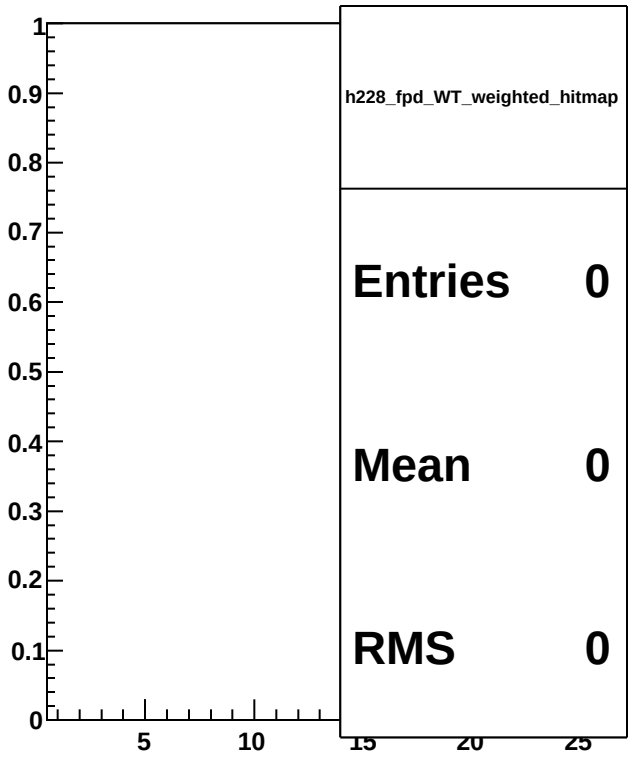

h223_fpd_ES_weighted_hitmap
Entries 0
Mean 0
RMS 0

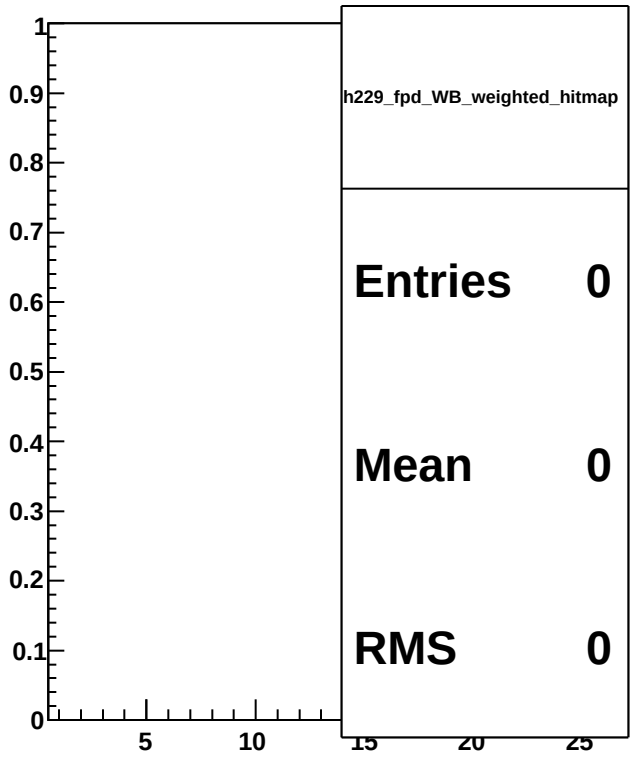

h224_fpd_ET_weighted_hitmap
Entries 0
Mean 0
RMS 0

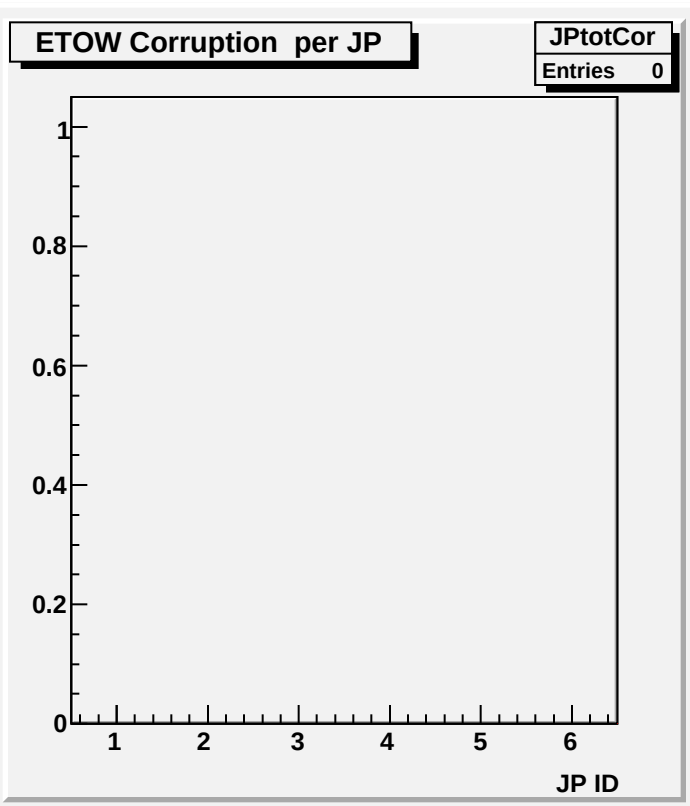

h225_fpd_EB_weighted_hitmap
Entries 0
Mean 0
RMS 0

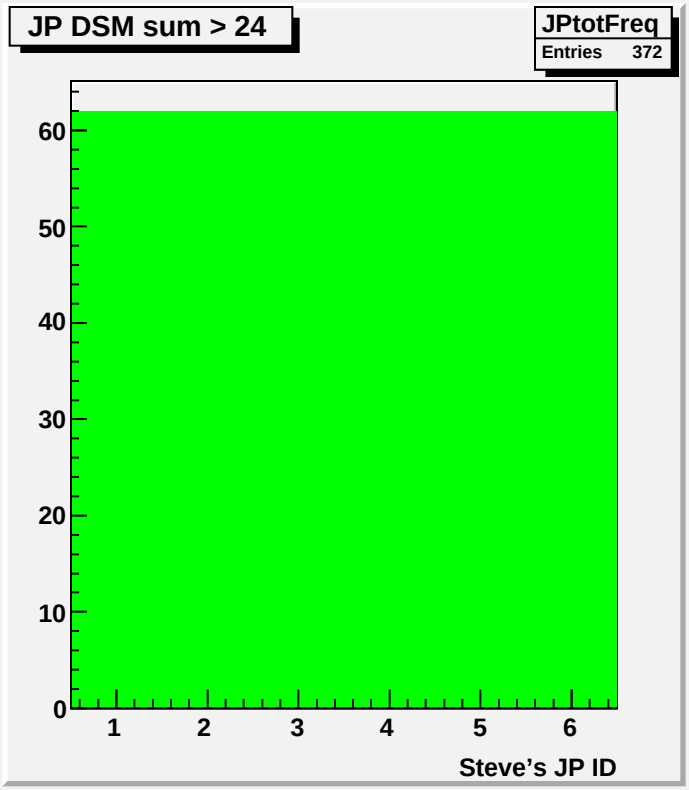

h226_fpd_WN_weighted_hitmap
Entries 0
Mean 0
RMS 0

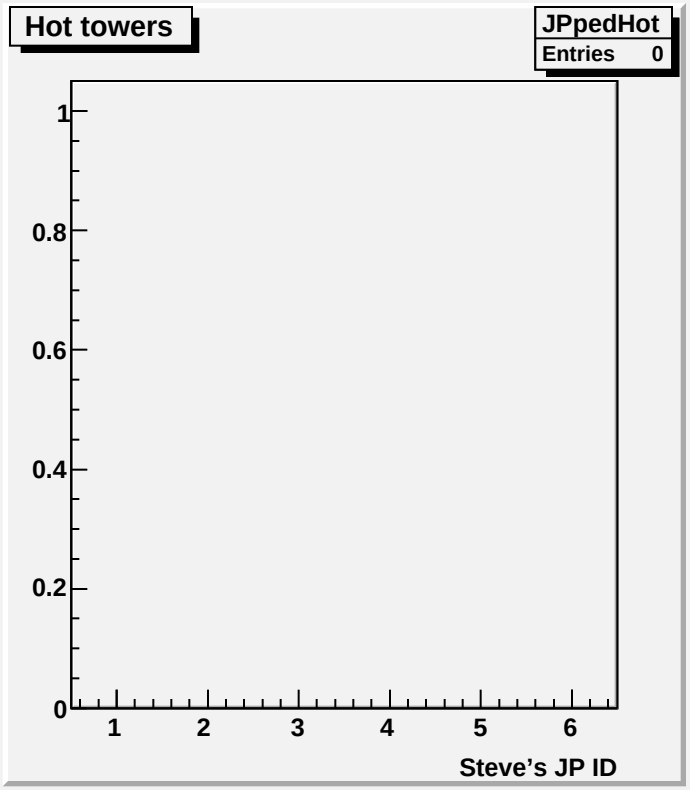

h227_fpd_WS_weighted_hitmap
Entries 0
Mean 0
RMS 0


h228_fpd_WT_weighted_hitmap
Entries 0
Mean 0
RMS 0


h229_fpd_WB_weighted_hitmap
Entries 0
Mean 0
RMS 0







ETOW masked (cr-ch-name): 1-97-12TC05, 4-117-07TC05, 4-18-06TA03, 2-98-02TC06, 6-67-11TB08,

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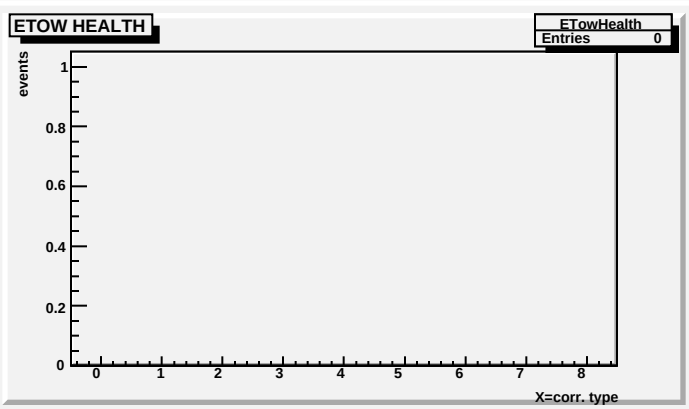
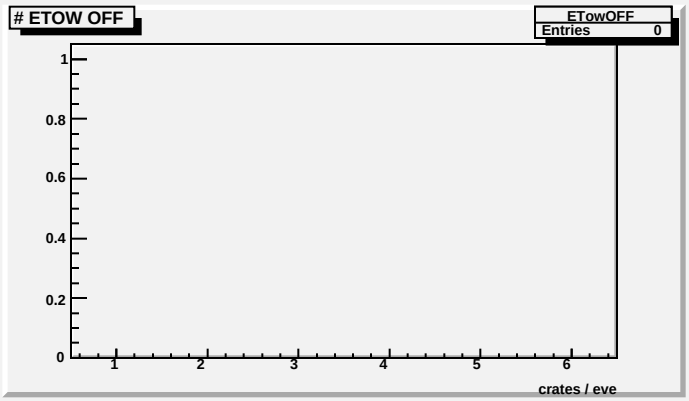
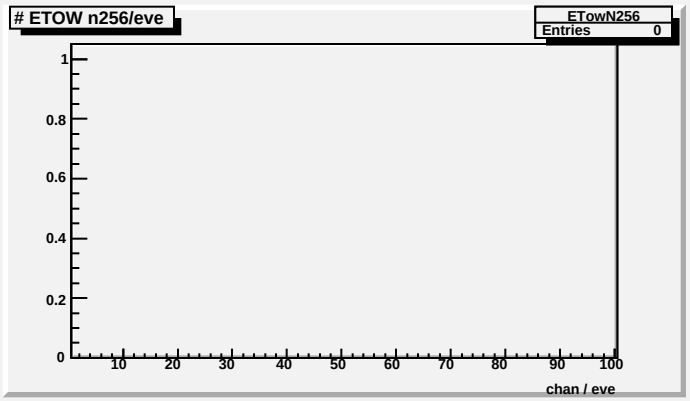
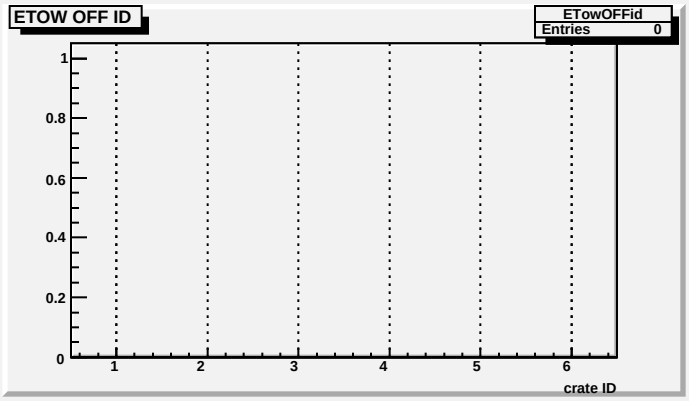

ETOW HEALTH
ETowHealth
Entries 0
events
X=corr. type

ETOW head corr
ETowHeadCorr
Entries 0
crates / eve


ETOW OFF
ETowOFF
Entries 0
crates / eve


ETOW n256/eve
ETowN256
Entries 0
chan / eve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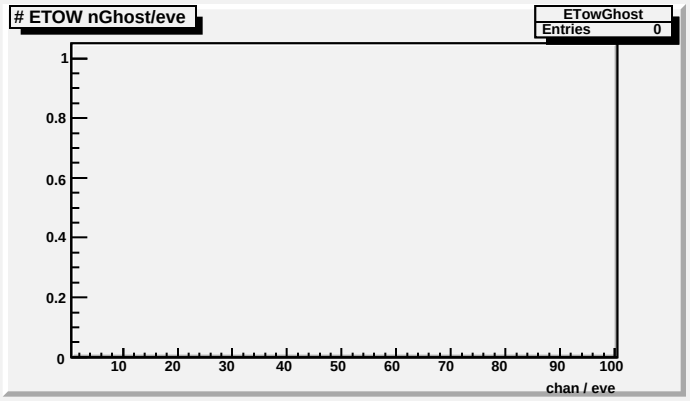
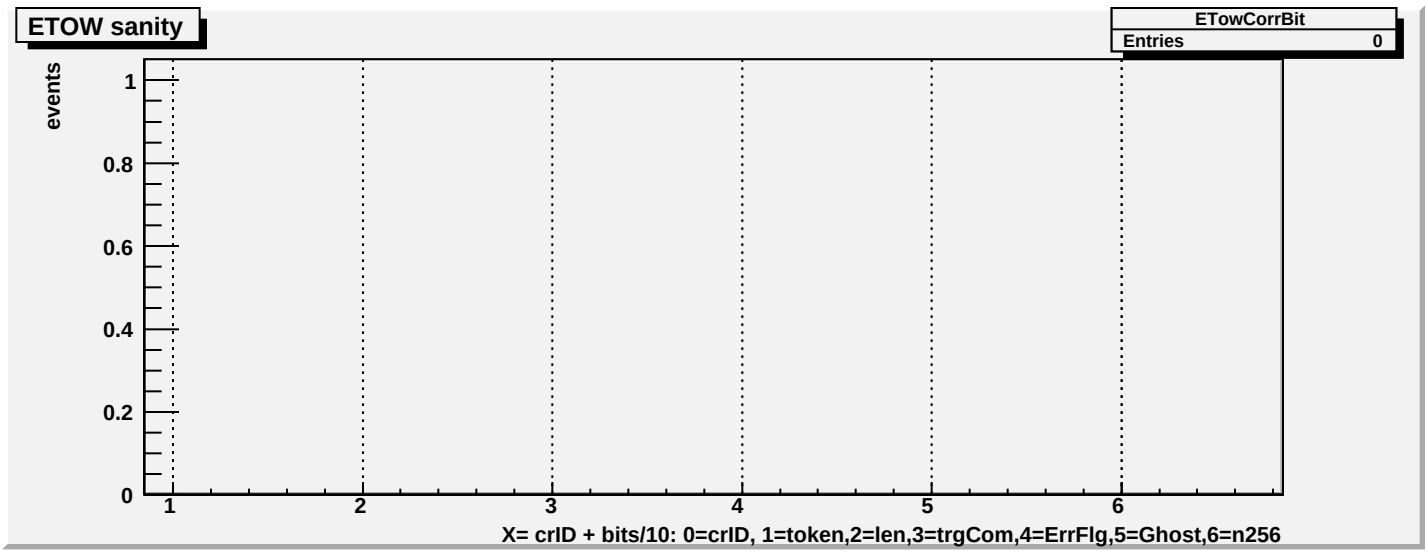
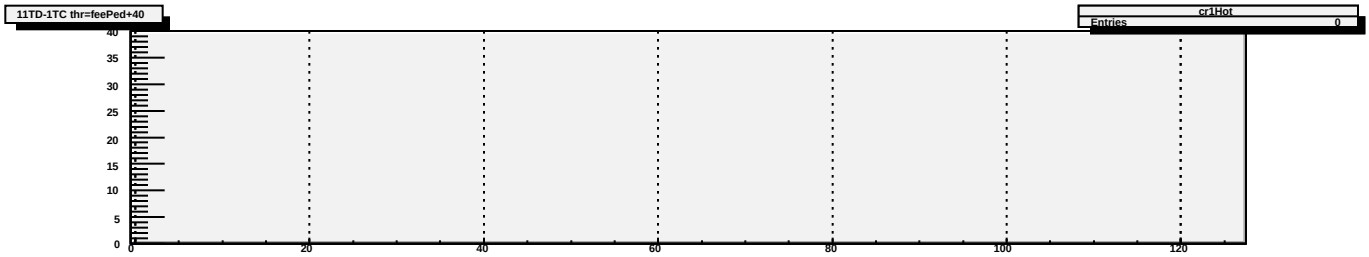
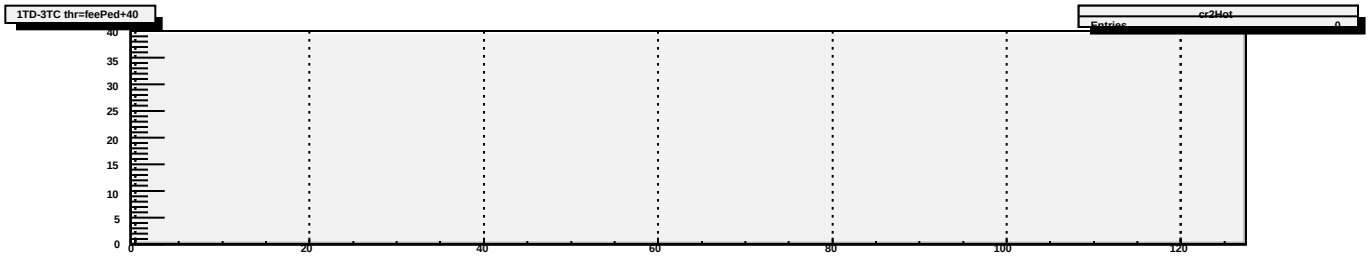
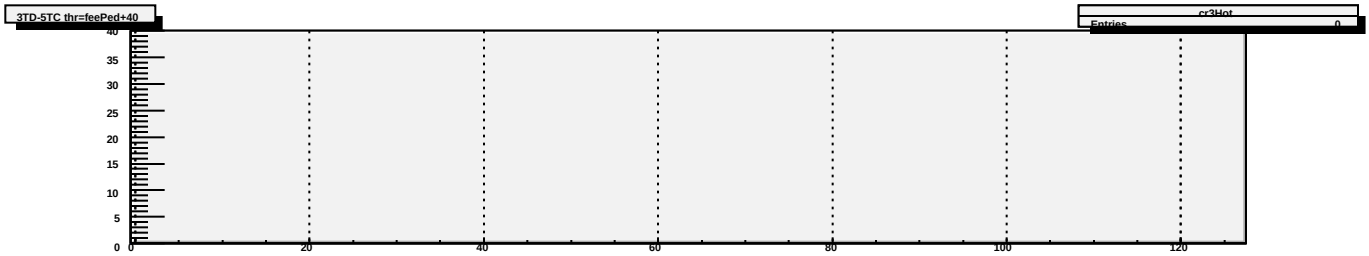
ETowOFFid
Entries 0
crate ID


ETOW nGhost/eve
ETowGhost
Entries 0
chan / eve


ETOW sanity
ETowCorrBit
Entries 0
events
X= crID + bits/10: 0=crID, 1=token,2=len,3=trgCom,4=ErrFlg,5=Ghost,6=n256


11TD-1TC thr=feePed+40
cr1Hot
Entries 0


1TD-3TC thr=feePed+40
cr2Hot
Entries 0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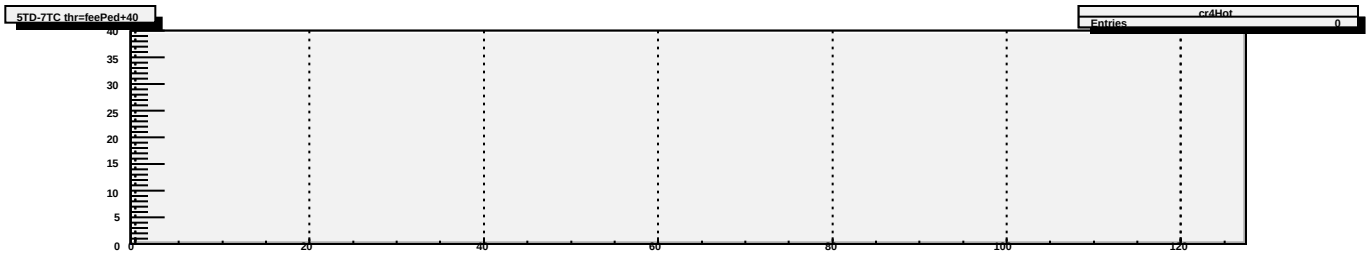
cr3Hot
Entries 0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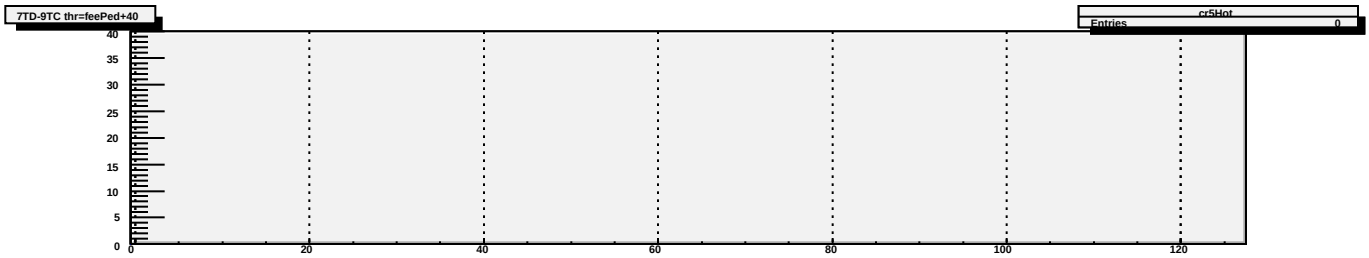
cr4Hot
Entries 0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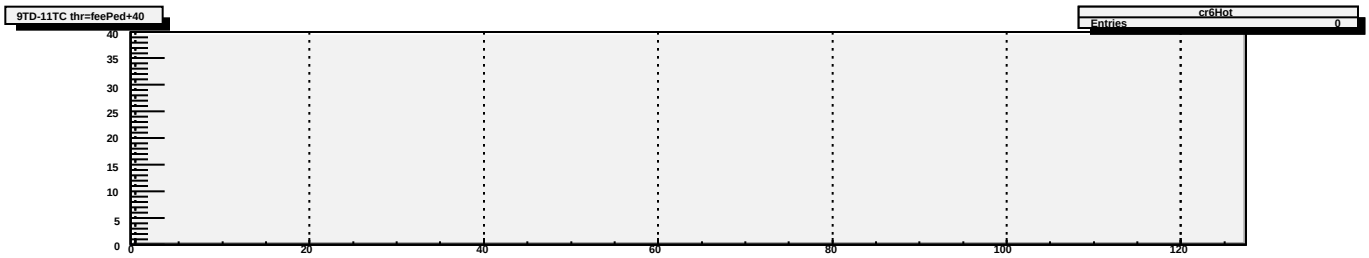
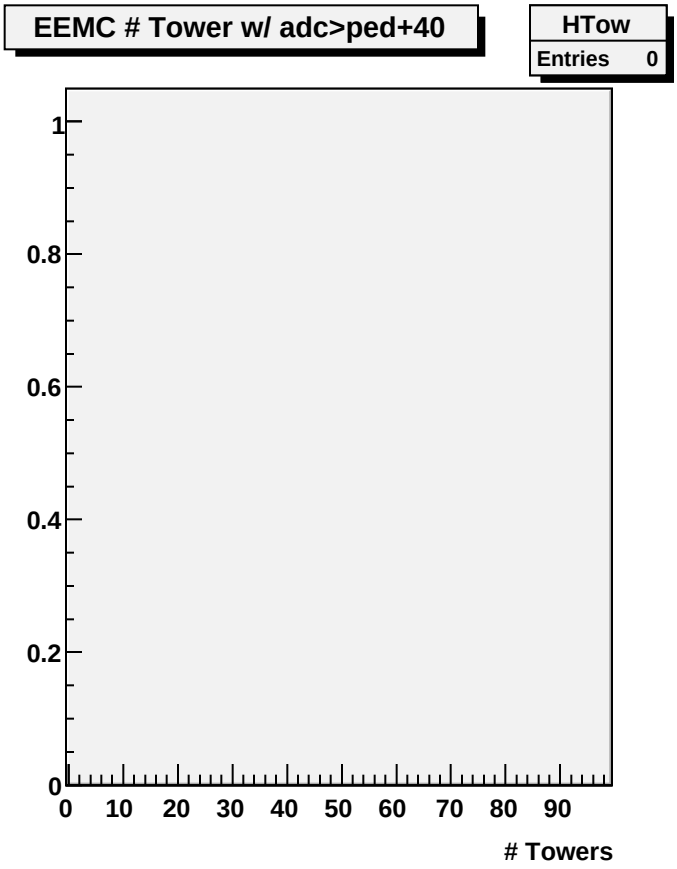
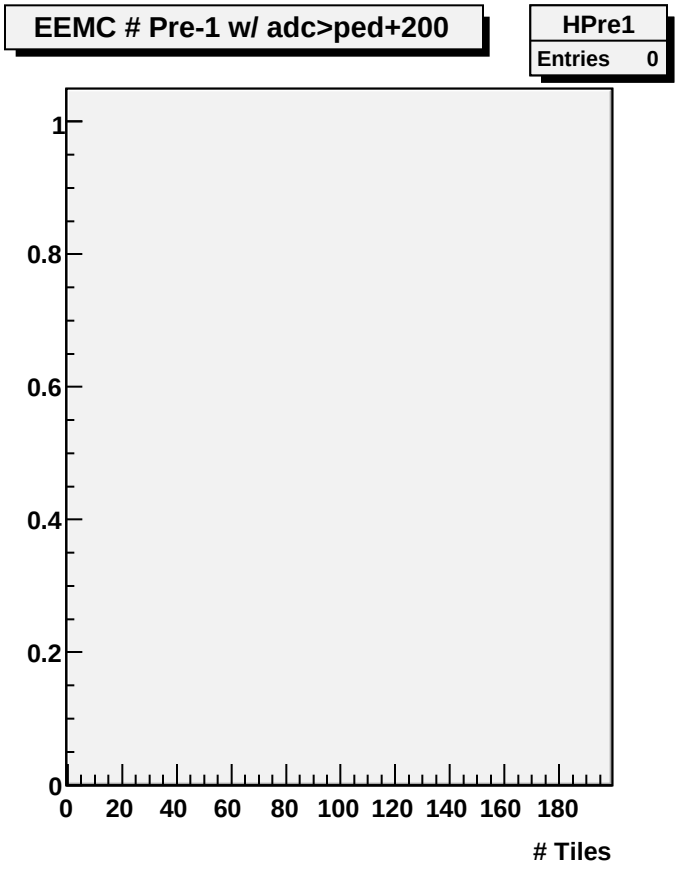
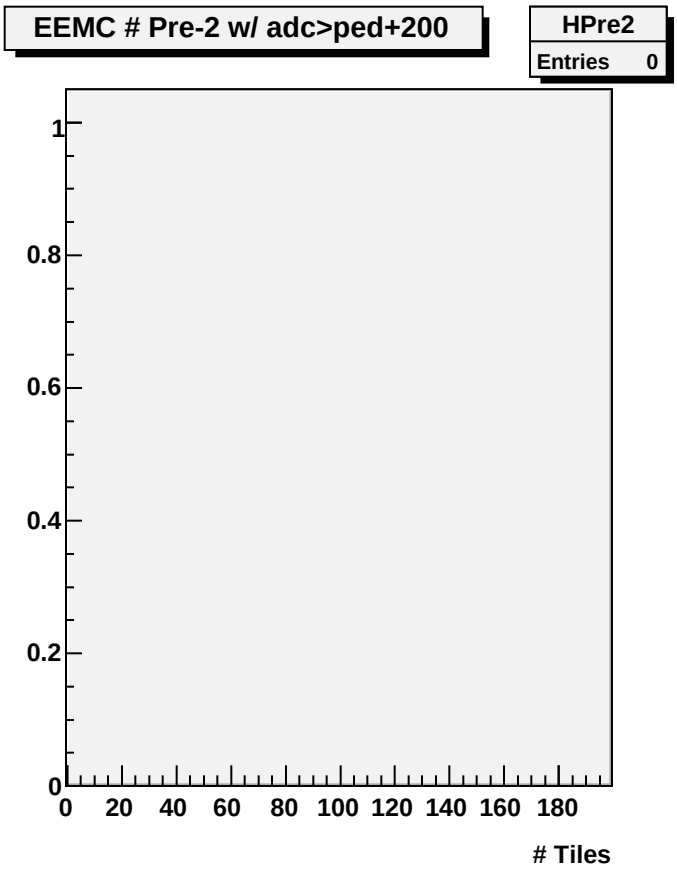
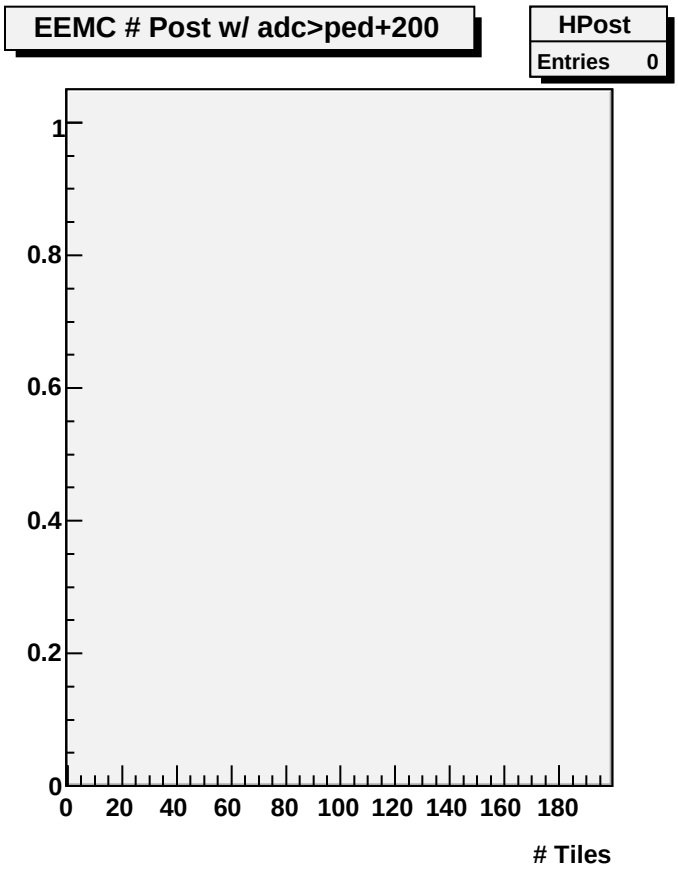
cr5Hot
Entries 0

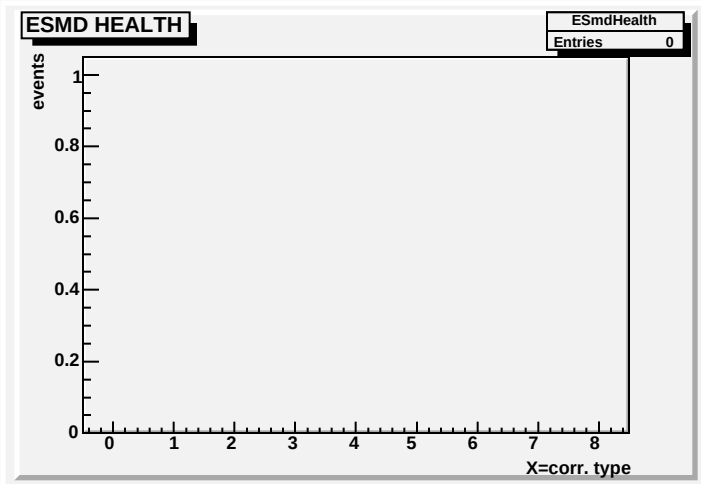

9TD-11TC thr=feePed+40
cr6Hot
Entries 0


EEMC # Tower w/ adc>ped+40
HTow
Entries 0
1
0.8
0.6
0.4
0.2
0
0 10 20 30 40 50 60 70 80 90
Towers


EEMC # Pre-1 w/ adc>ped+200
HPre1
Entries 0
1
0.8
0.6
0.4
0.2
0
0 20 40 60 80 100 120 140 160 180
Tiles


EEMC # Pre-2 w/ adc>ped+200
HPre2
Entries 0
1
0.8
0.6
0.4
0.2
0
0 20 40 60 80 100 120 140 160 180
Tiles


EEMC # Post w/ adc>ped+200
HPost
Entries 0
1
0.8
0.6
0.4
0.2
0
0 20 40 60 80 100 120 140 160 180
Tiles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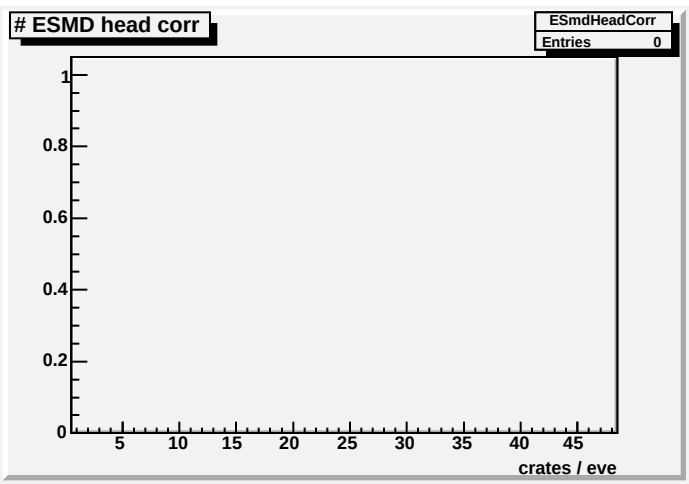
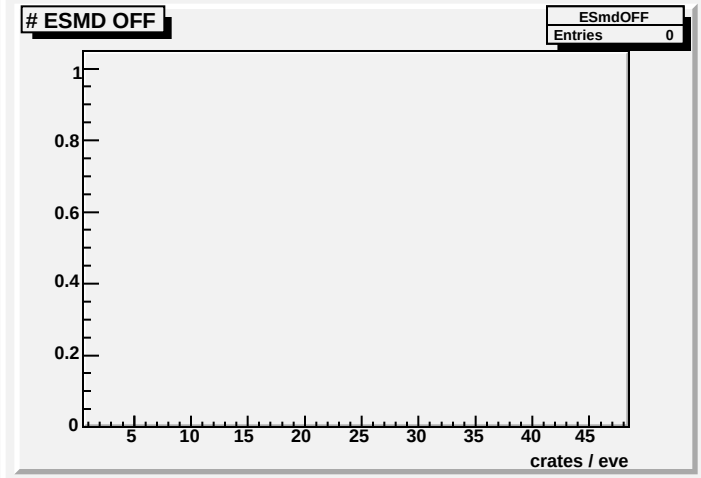
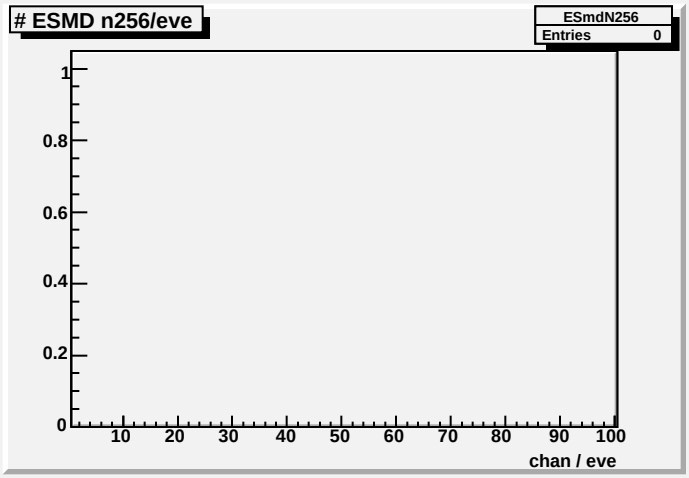
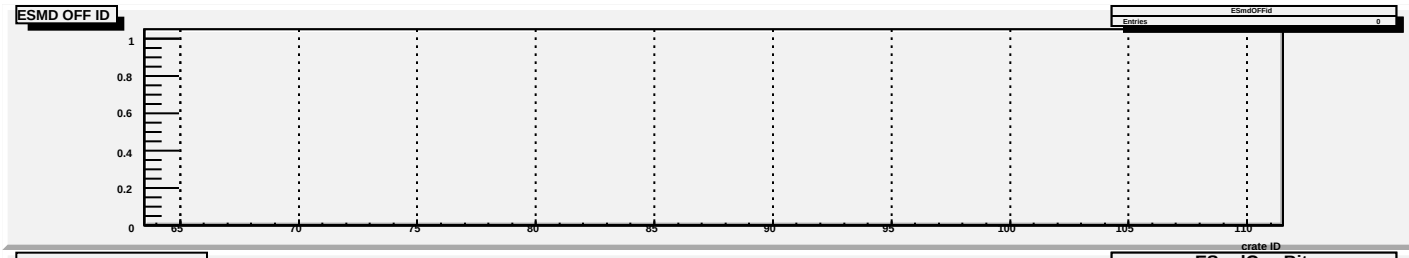
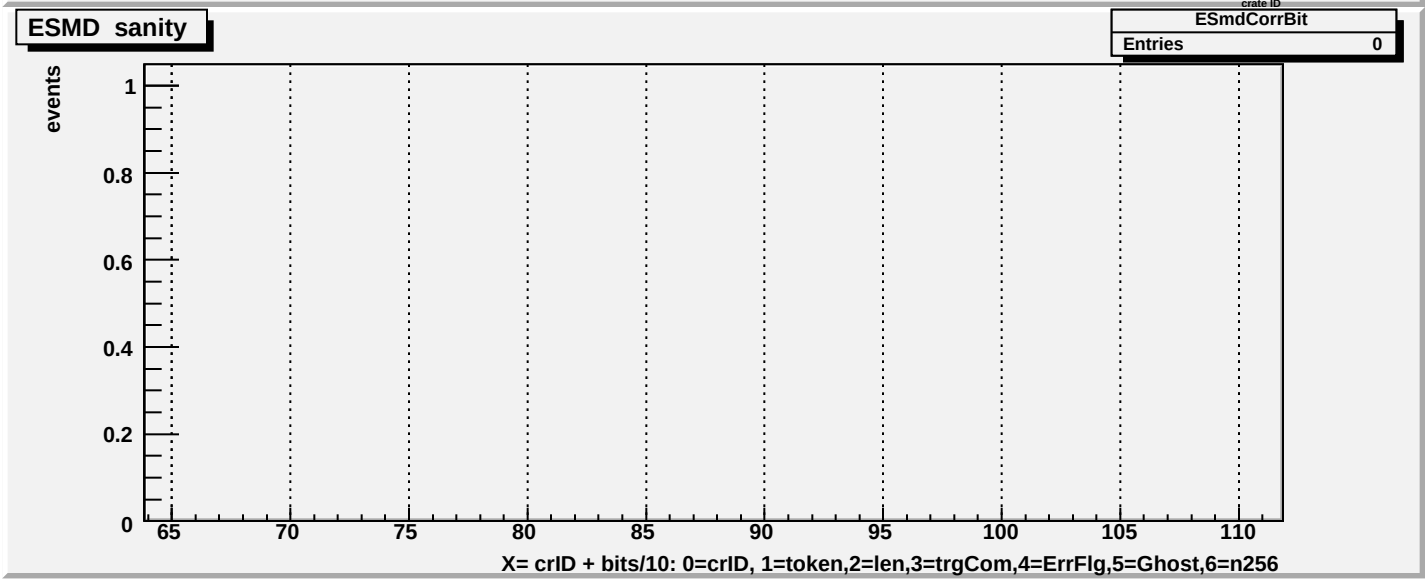
events
X=corr. type

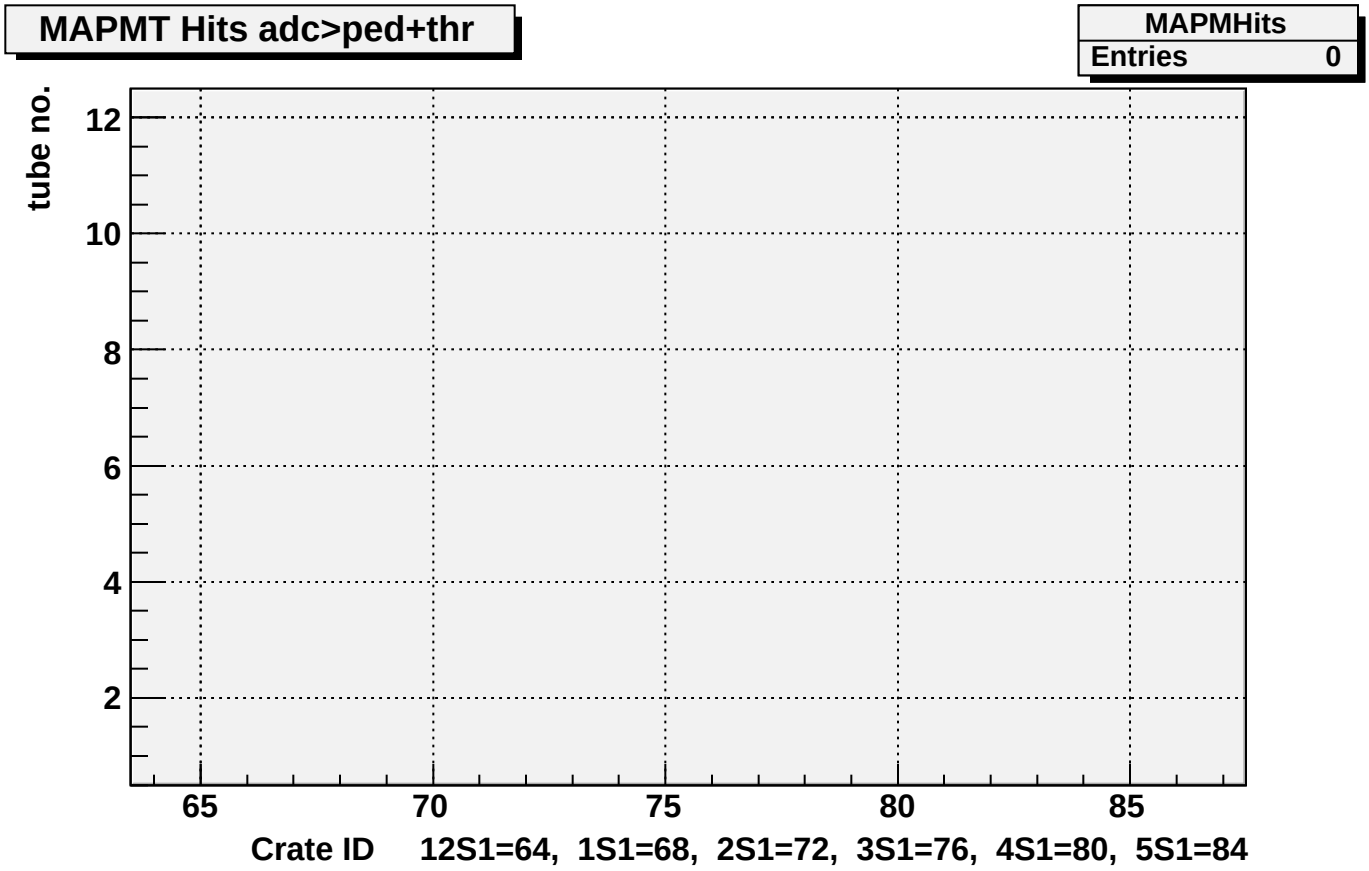

ESMD head corr
ESmdHeadCorr
Entries 0
crates / eve

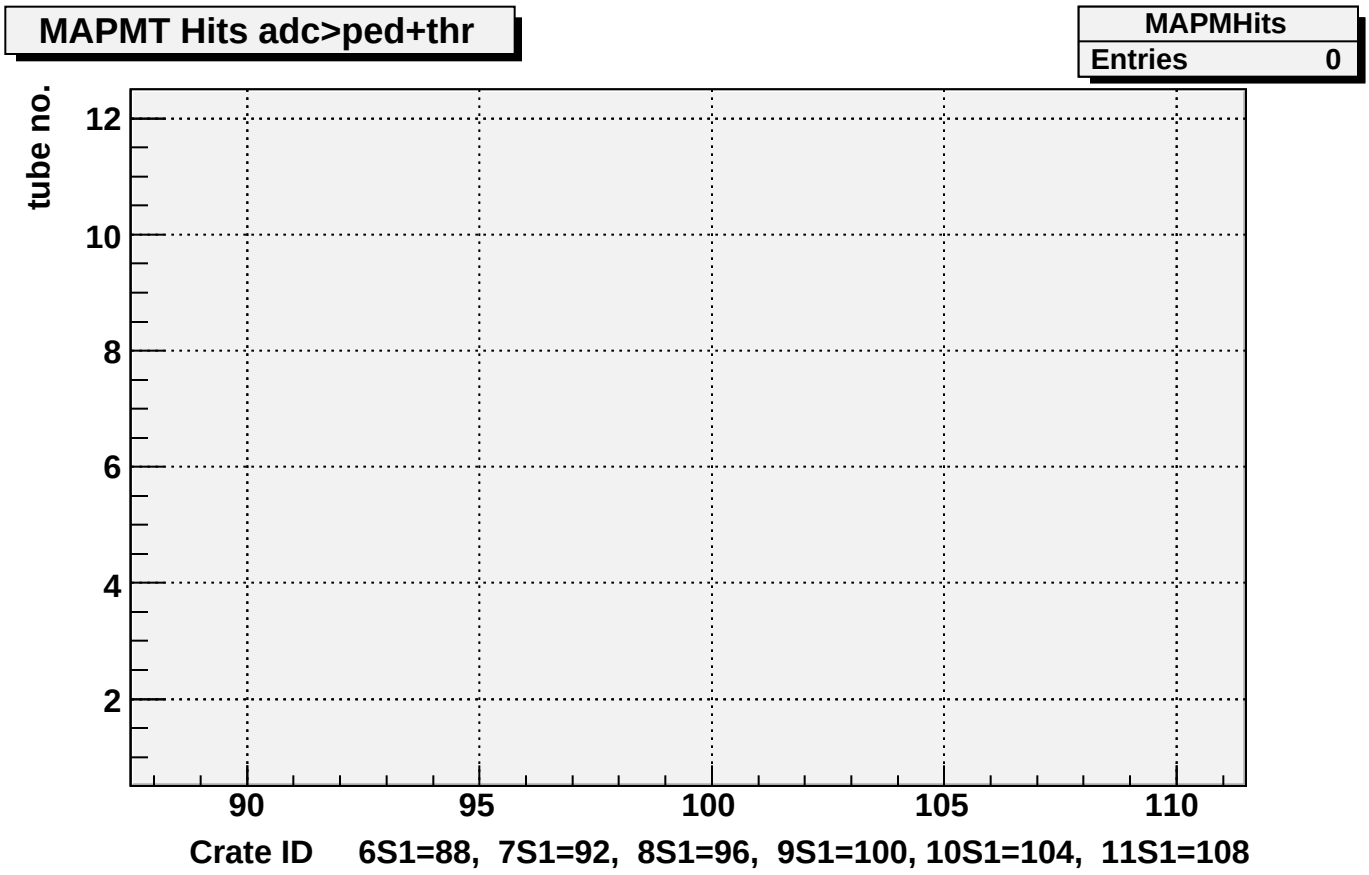

ESMD OFF
ESmdOFF
Entries 0
crates / eve

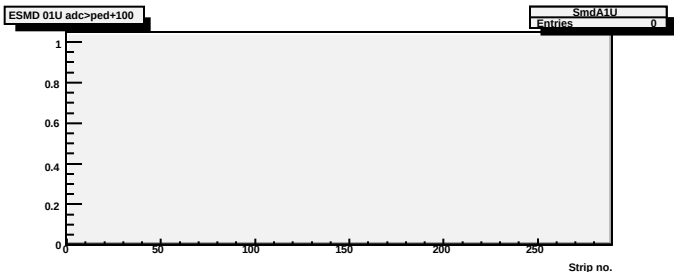

ESMD n256/eve
ESmdN256
Entries 0
chan / eve

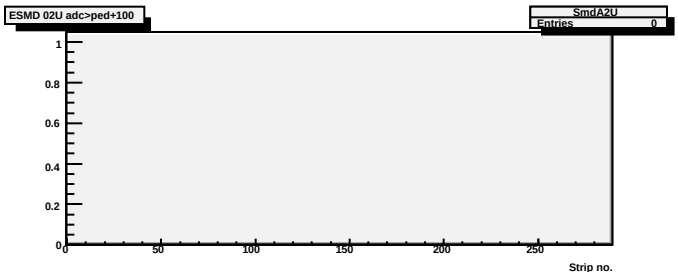

ESMD OFF ID
ESmdOFFid
Entries 0
crate ID


ESMD sanity
ESmdCorrBit
Entries 0
events
X= crID + bits/10: 0=crID, 1=token,2=len,3=trgCom,4=ErrFlg,5=Ghost,6=n256


MAPMT Hits adc>ped+thr
MAPMHits
Entries 0
tube no.
12
10
8
6
4
2
65
70
75
80
85
Crate ID 12S1=64, 1S1=68, 2S1=72, 3S1=76, 4S1=80, 5S1=84


MAPMT Hits adc>ped+thr
MAPMHits
Entries 0
tube no.
12
10
8
6
4
2
90
95
100
105
110
Crate ID 6S1=88, 7S1=92, 8S1=96, 9S1=100, 10S1=104, 11S1=108


ESMD 01U adc>ped+100
SmdA1U
Entries 0
Strip no.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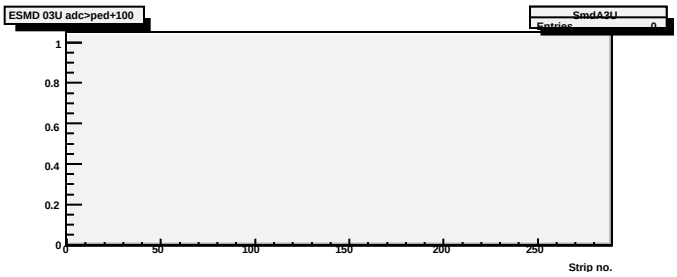
SmdA2U
Entries 0
Strip no.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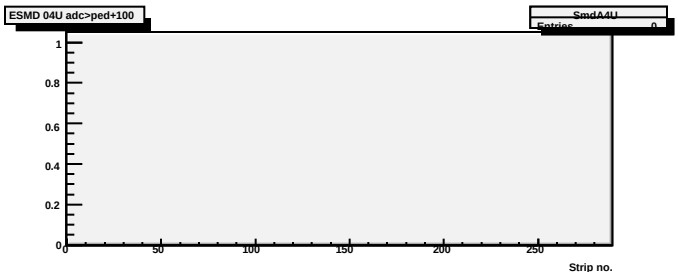
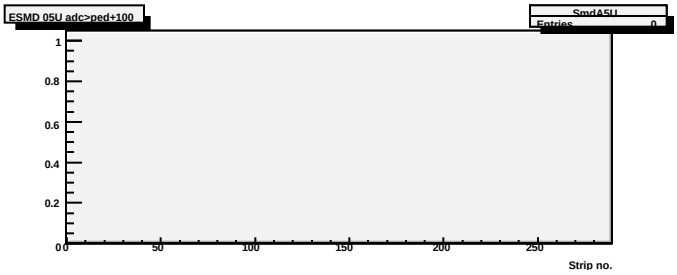
SmdA3U
Entries 0
Strip no.


ESMD 04U adc>ped+100
SmdA4U
Entries 0
Strip no.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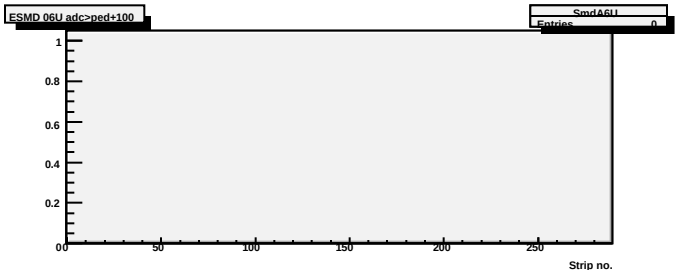
SmdA5U
Entries 0
Strip no.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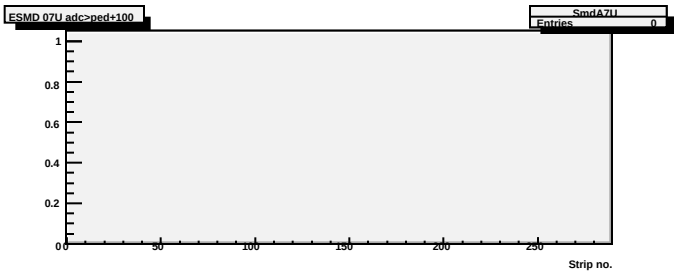
SmdA6U
Entries 0
Strip no.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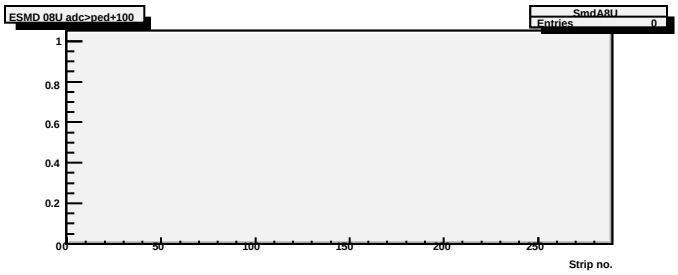
Entries 0
Strip no.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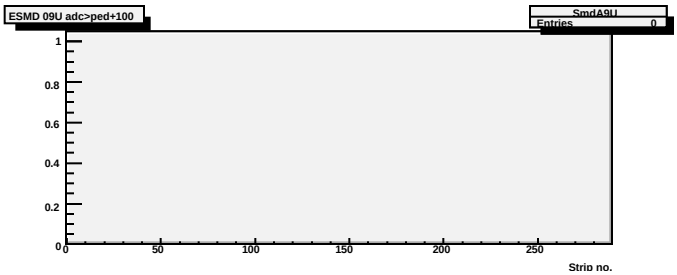
Entries 0
Strip no.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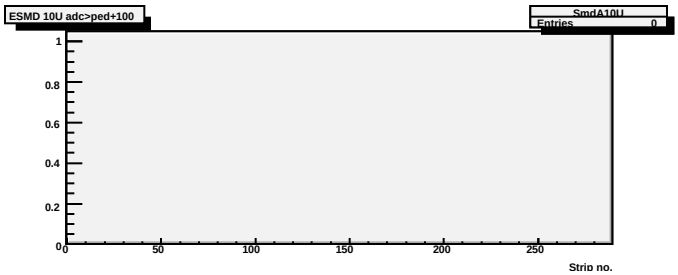
SmdA9U
Entries 0
Strip no.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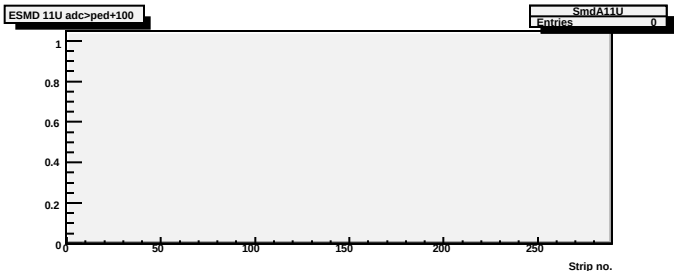
SmdA10U
Entries 0
Strip no.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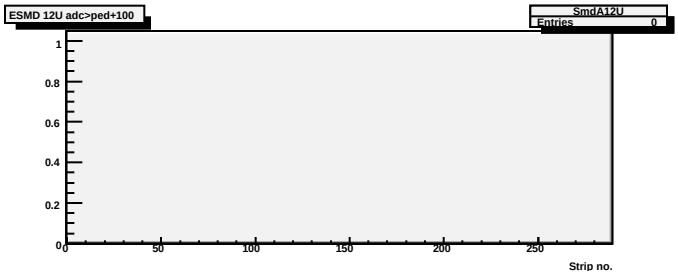
SmdA11U
Entries 0
Strip no.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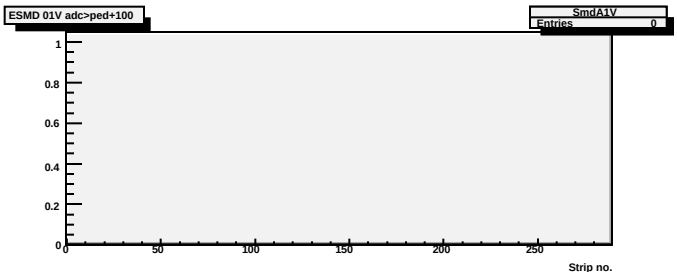
Strip no.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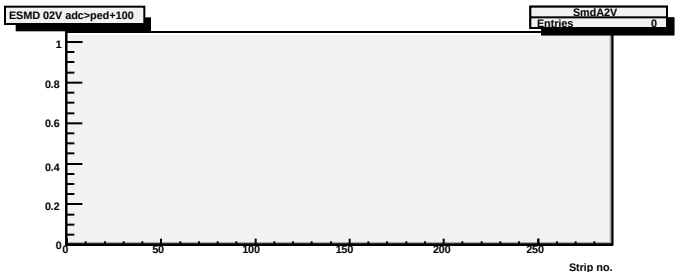
Entries 0
Strip no.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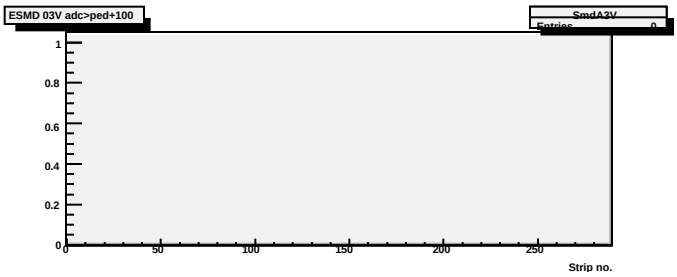
SmdA2V
Entries 0
Strip no.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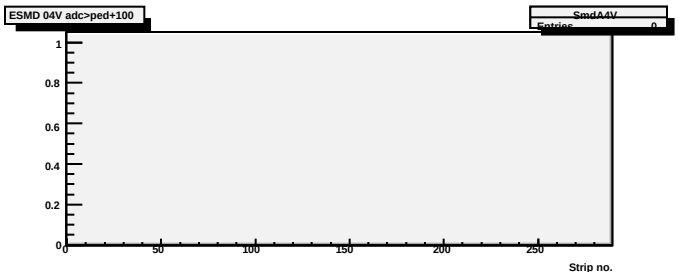
Entries 0
Strip no.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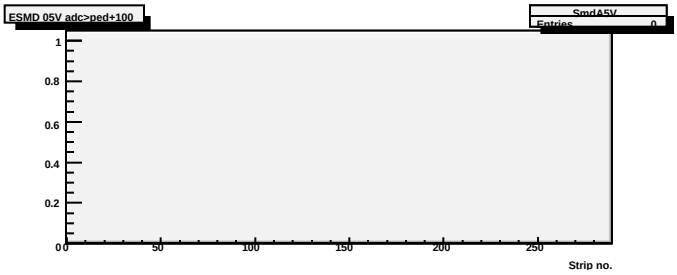
SmdA4V
Entries 0
Strip no.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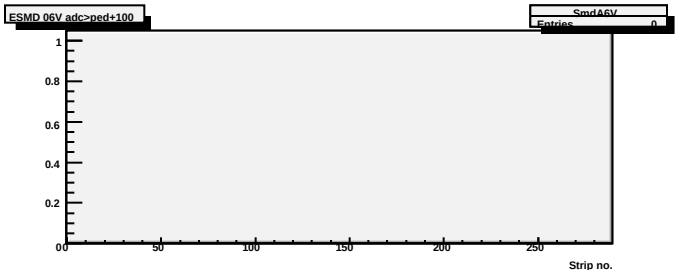
SmdA5V
Entries 0
Strip no.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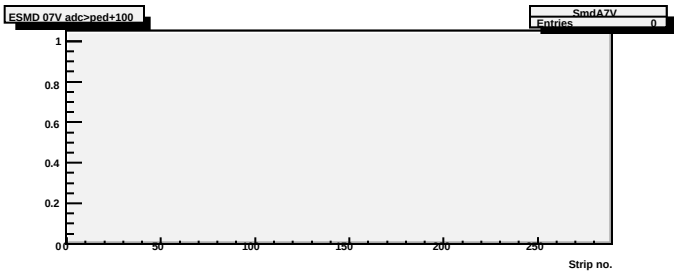
SmdA6V
Entries 0
Strip no.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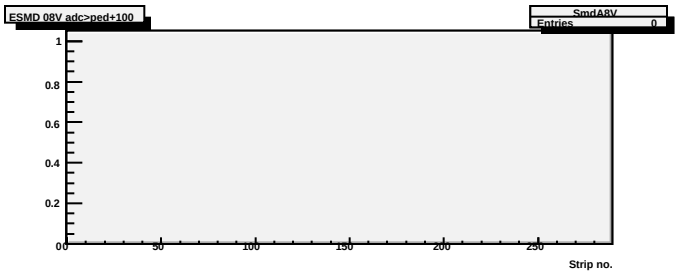
Entries 0
Strip no.


ESMD 08V adc>ped+100
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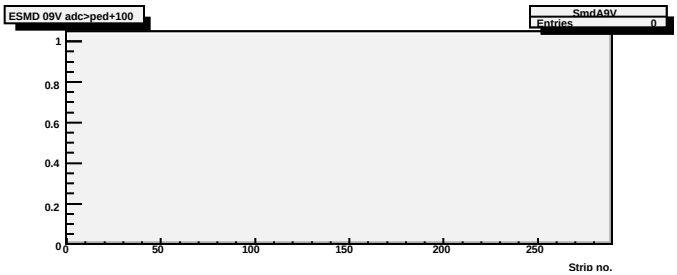
Entries 0
Strip no.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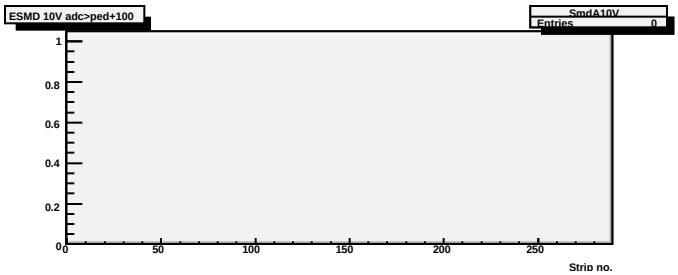
SmdA9V
Entries 0
Strip no.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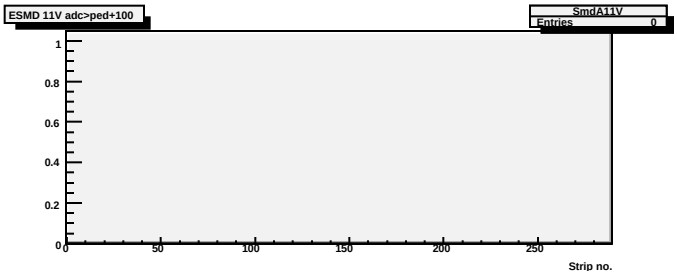
SmdA10V
Entries 0
Strip no.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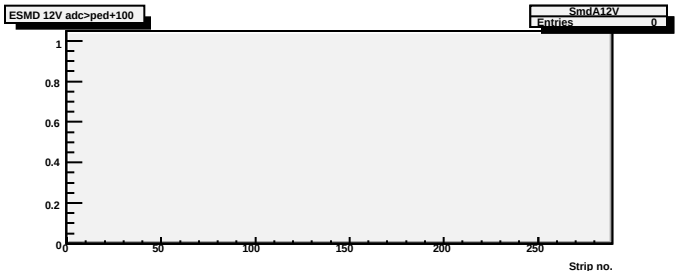
SmdA11V
Entries 0
Strip no.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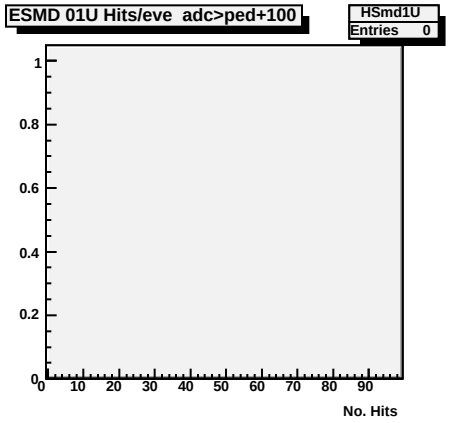
Strip no.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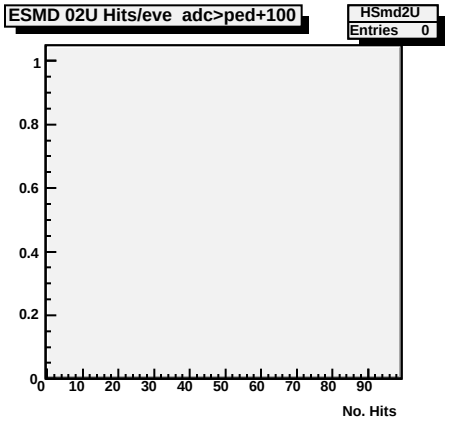
HSmd1U
Entries 0
No. Hits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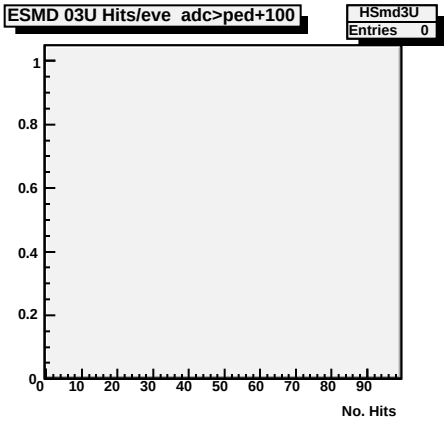
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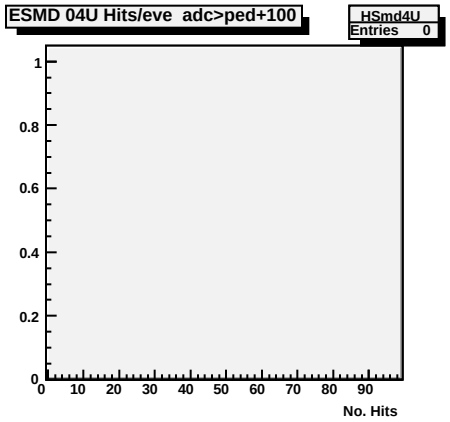
Entries 0
No. Hits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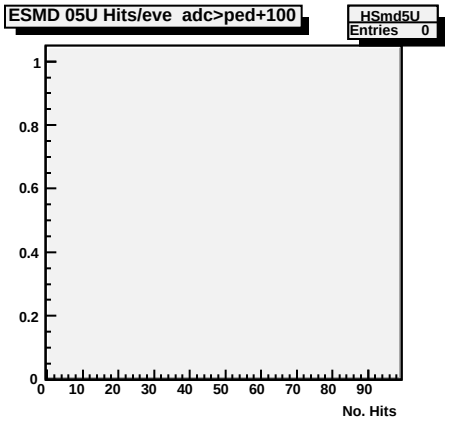
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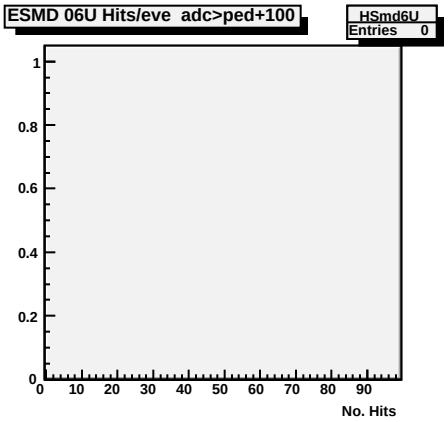
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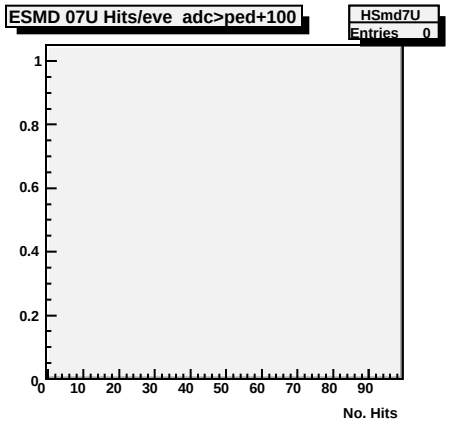
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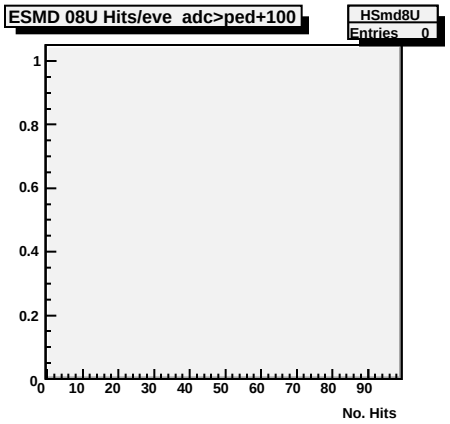
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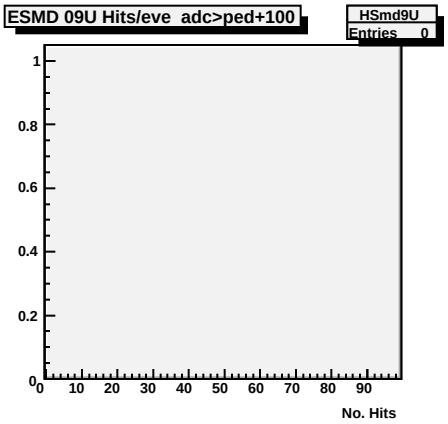
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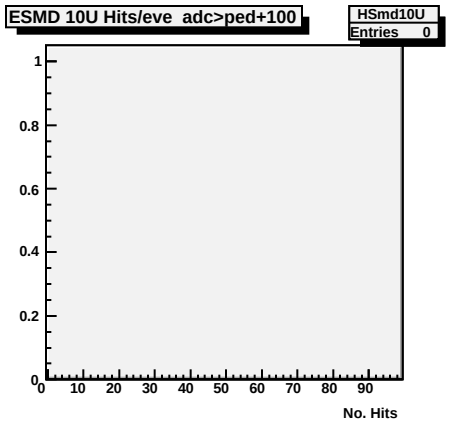
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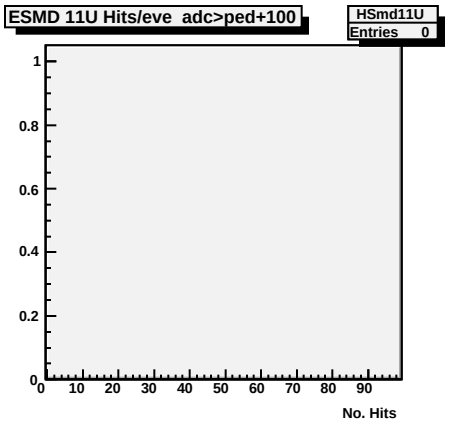
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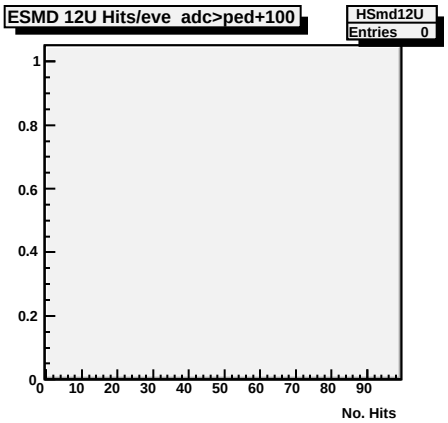
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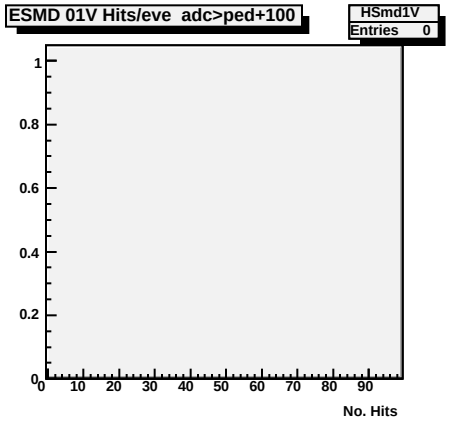
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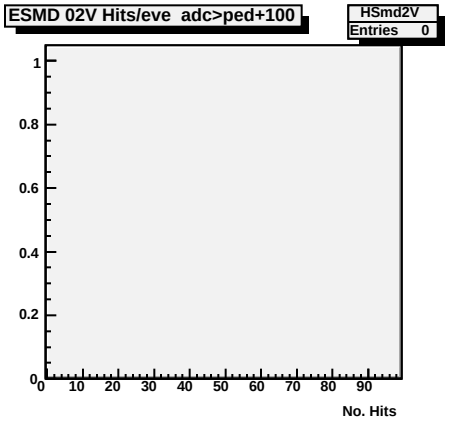
ESMD 02V Hits/eve adc>ped+100
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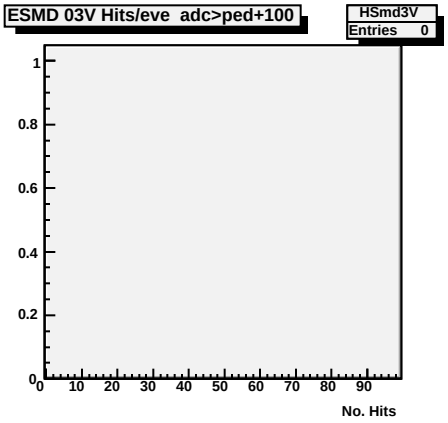
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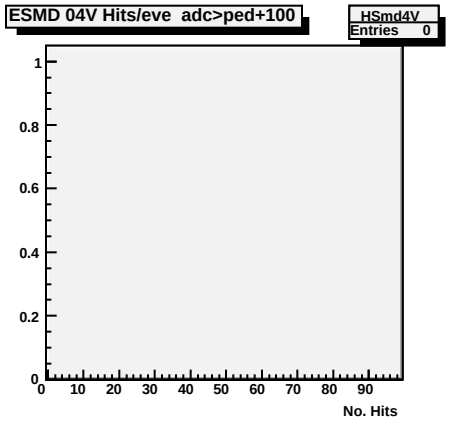
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HSmd4V
Entries 0
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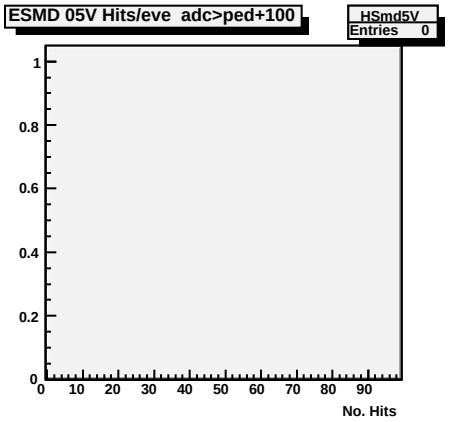
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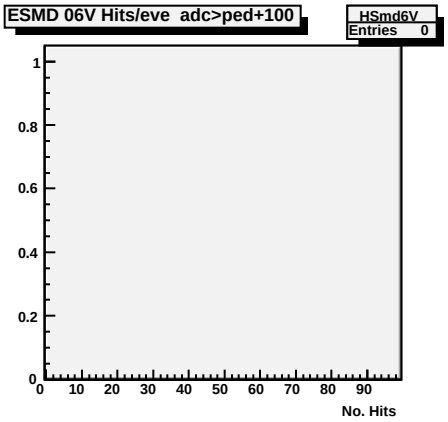
No. Hits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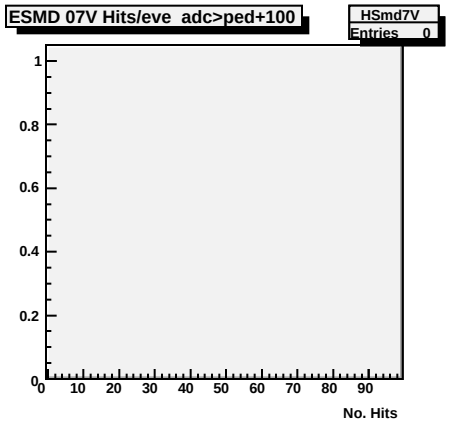
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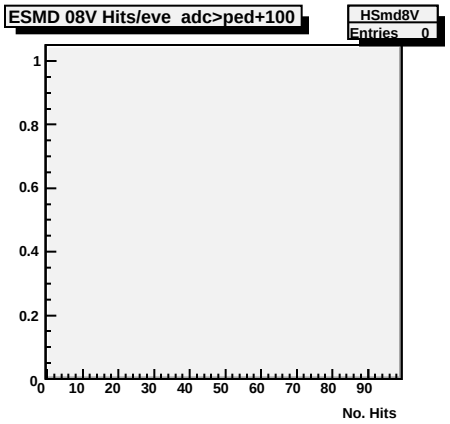
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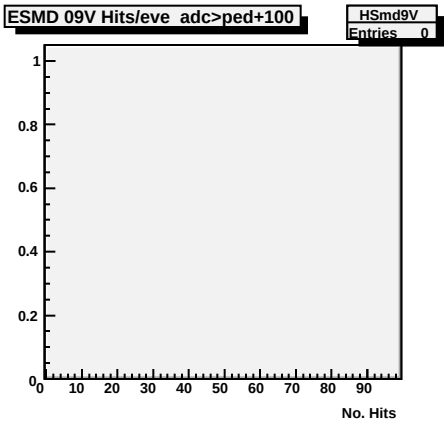
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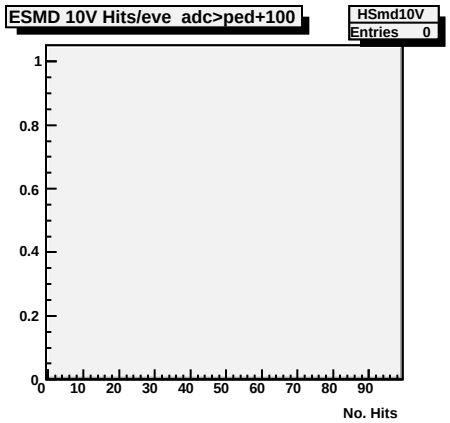
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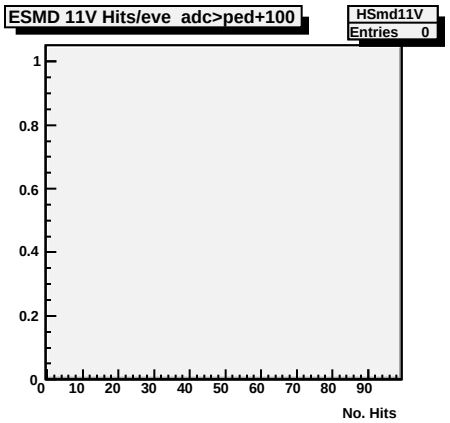
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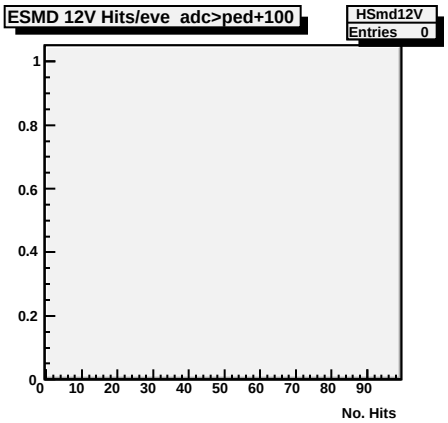
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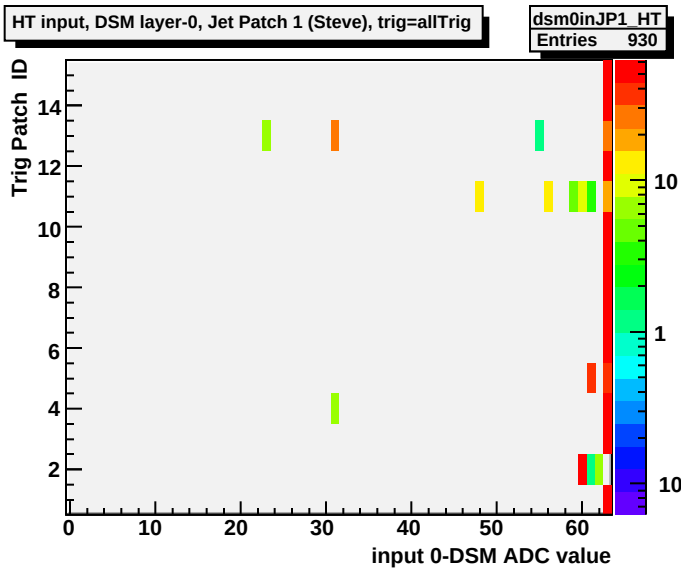
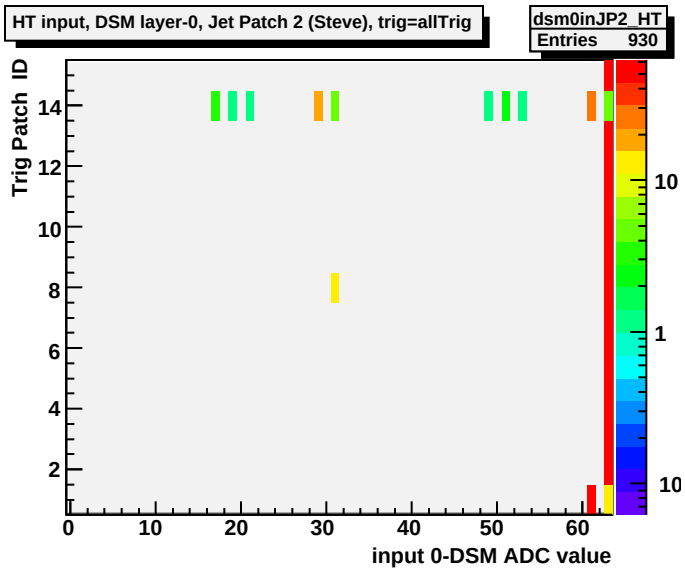

HT input, DSM layer-0, Jet Patch 1 (Steve), trig=allTrig
dsm0inJP1_HT
Entries 930
Trig Patch ID
input 0-DSM ADC value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dsm0inJP2_HT
Entries 930
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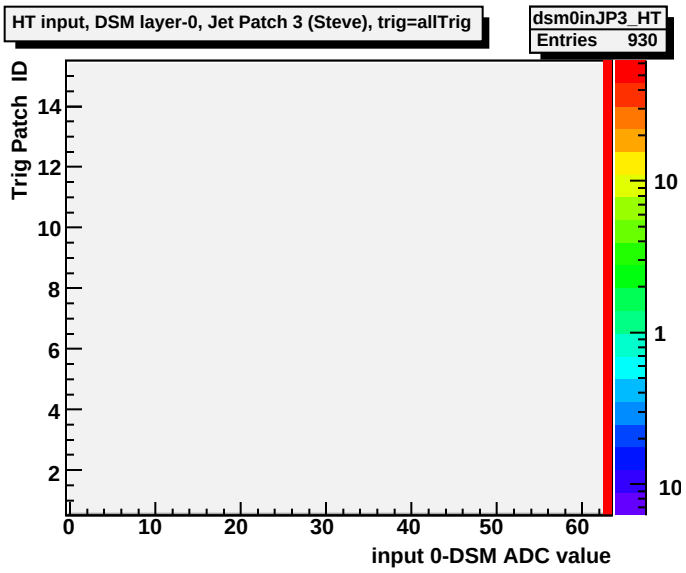
input 0-DSM ADC value


HT input, DSM layer-0, Jet Patch 3 (Steve), trig=allTrig
dsm0inJP3_HT
Entries 930
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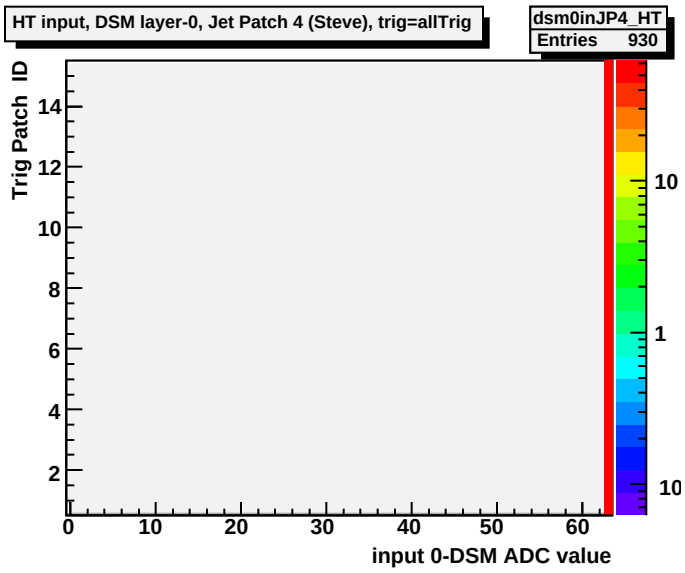
input 0-DSM ADC value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Entries 930
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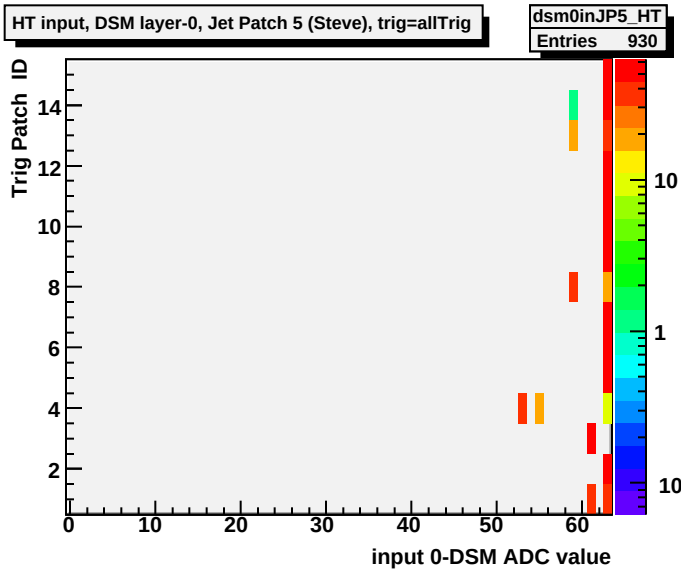
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Entries 930
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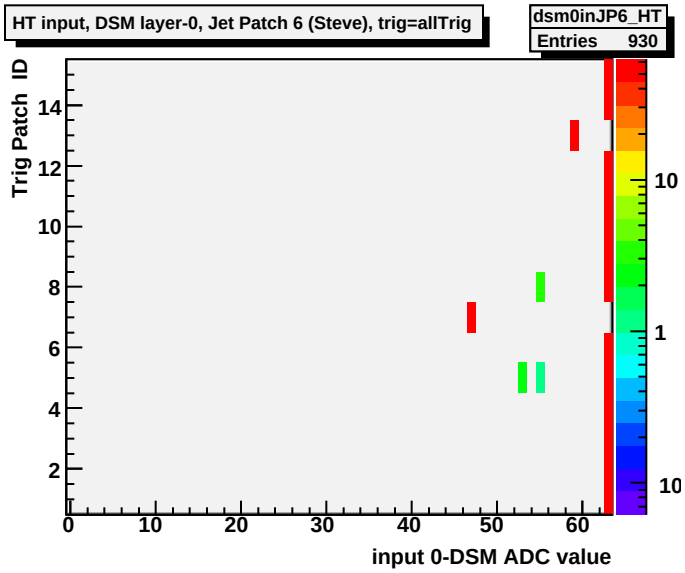
HT input, DSM layer-0, Jet Patch 6 (Steve), trig=allTrig
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Entries 930
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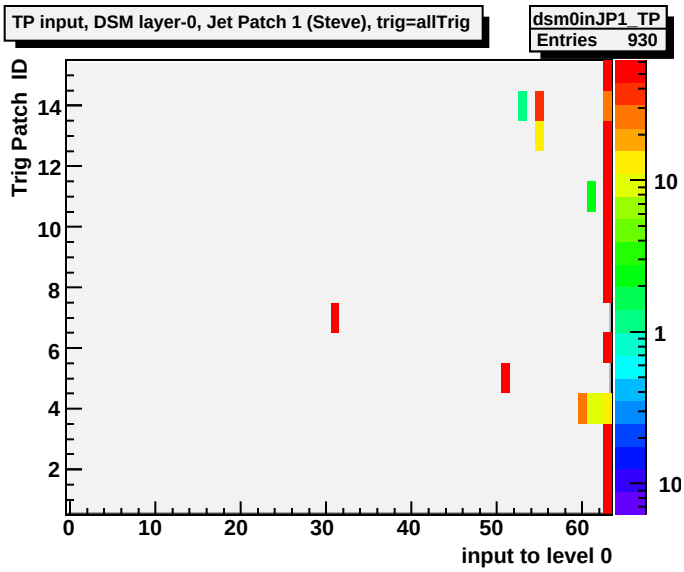
input 0-DSM ADC value


TP input, DSM layer-0, Jet Patch 1 (Steve), trig=allTrig
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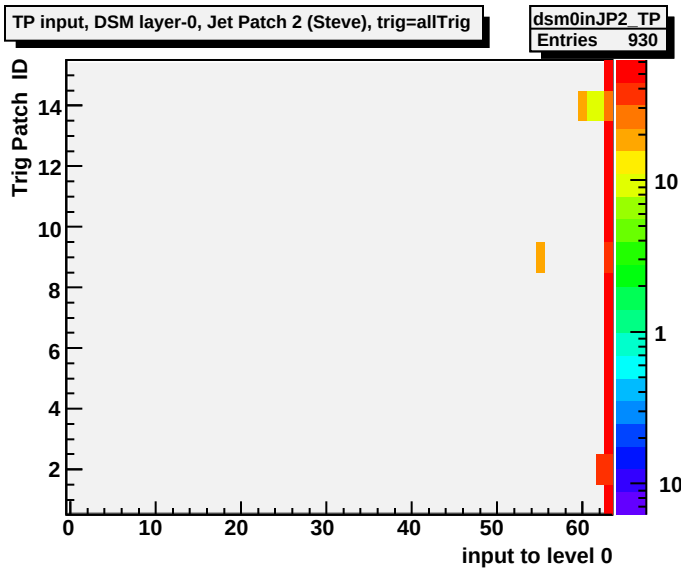
input to level 0


TP input, DSM layer-0, Jet Patch 2 (Steve), trig=allTrig
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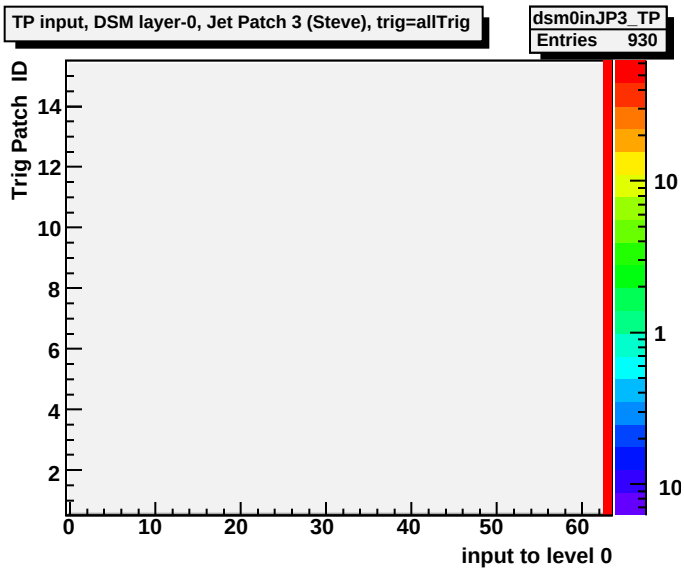
Entries 930
Trig Patch ID
input to level 0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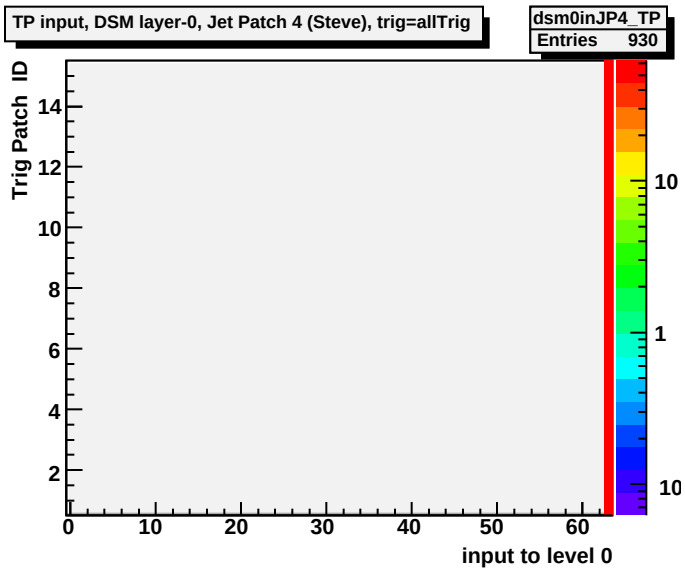
Entries 930
Trig Patch ID
input to level 0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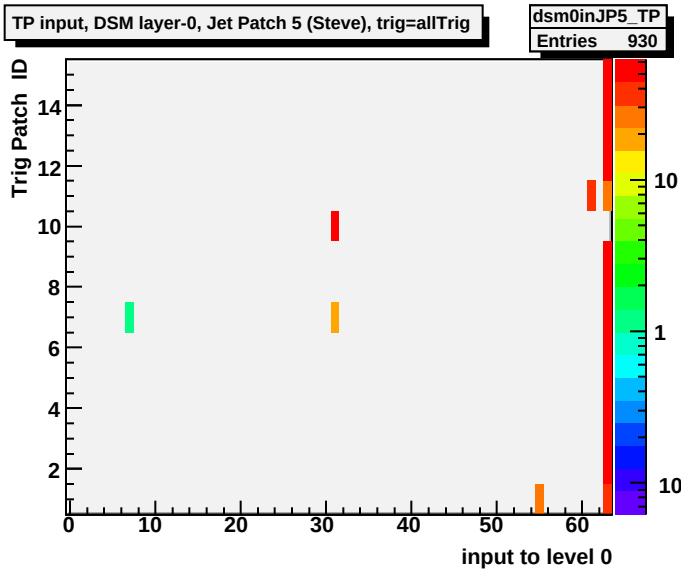
TP input, DSM layer-0, Jet Patch 5 (Steve), trig=allTrig
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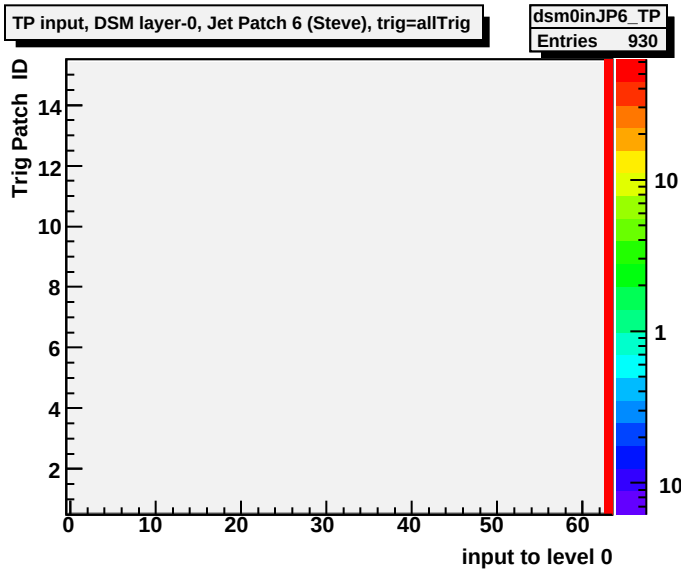
Entries 930
Trig Patch ID
input to level 0


TP input, DSM layer-0, Jet Patch 6 (Steve), trig=allTrig
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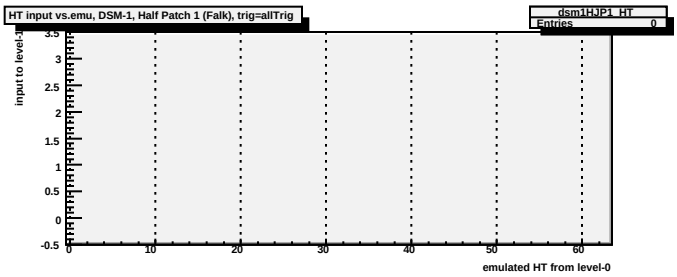
Entries 930
Trig Patch ID
input to level 0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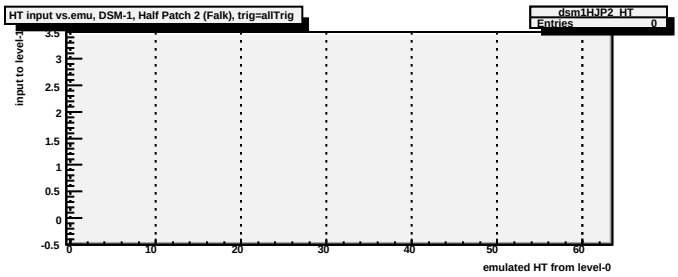
dsm1HJP1_HT
Entries 0
input to level-1
emulated HT from level-0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Entries 0
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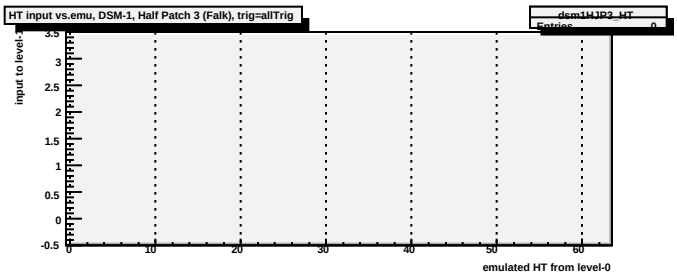
emulated HT from level-0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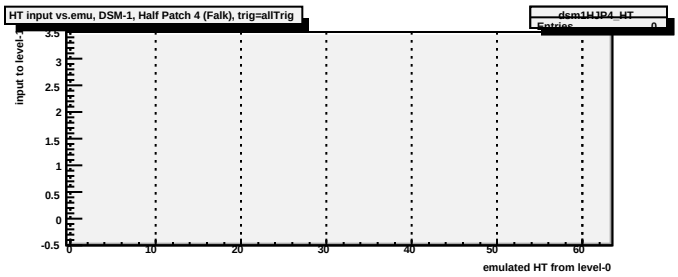
emulated HT from level-0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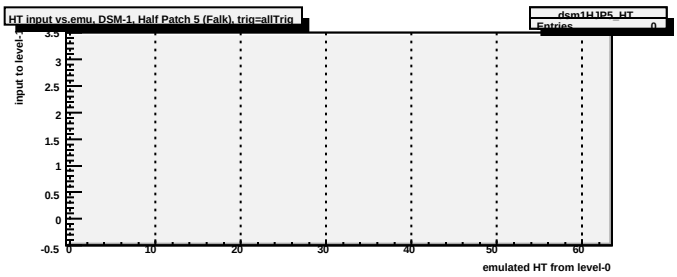
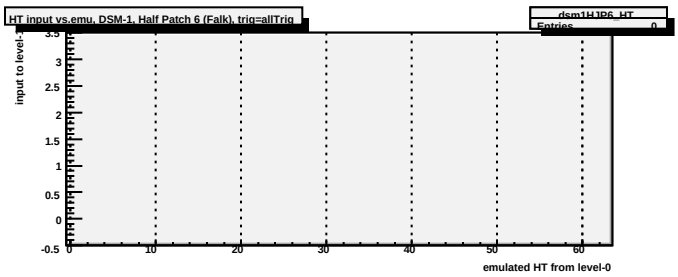
Entries 0
input to level-1
emulated HT from level-0


HT input vs.emu, DSM-1, Half Patch 5 (Falk), trig=allTrig
dsm1HJP5_HT
Entries 0
input to level-1
emulated HT from level-0


HT input vs.emu, DSM-1, Half Patch 6 (Falk), trig=allTrig
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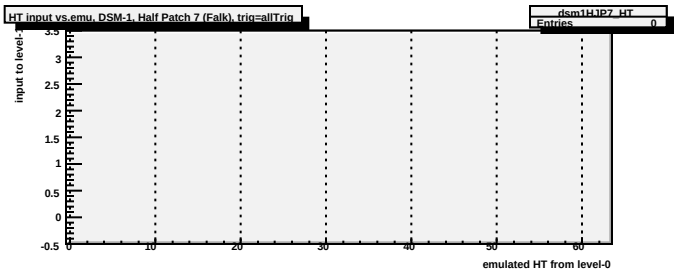
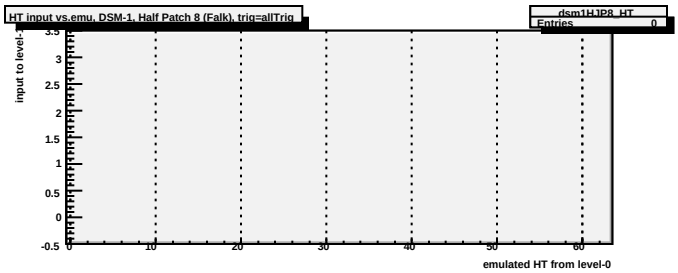
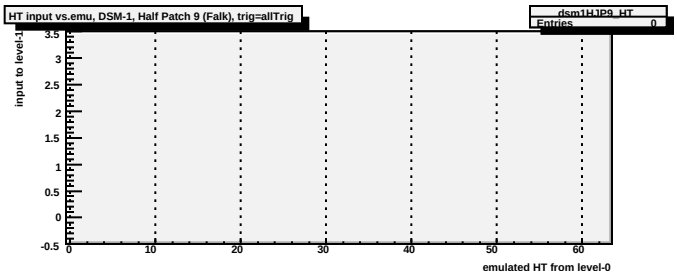
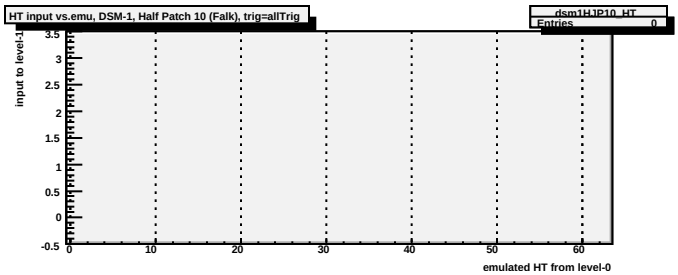
Entries 0
input to level-1
emulated HT from level-0


HT input vs.emu, DSM-1, Half Patch 7 (Falk), trig=allTrig
dsm1HJP7_HT
Entries 0
input to level-1
emulated HT from level-0


HT input vs.emu, DSM-1, Half Patch 8 (Falk), trig=allTrig
dsm1HJP8_HT
Entries 0
input to level-1
emulated HT from level-0


HT input vs.emu, DSM-1, Half Patch 9 (Falk), trig=allTrig
dsm1HJP9_HT
Entries 0
input to level-1
emulated HT from level-0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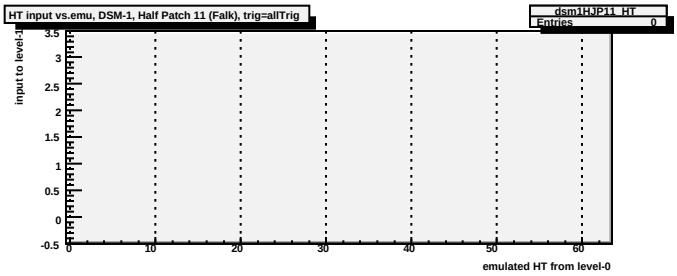
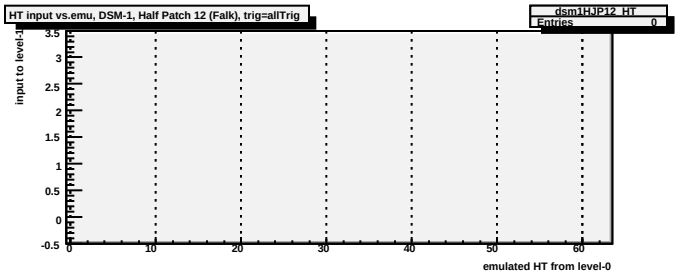
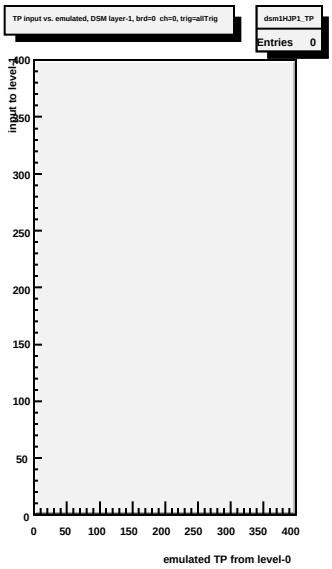
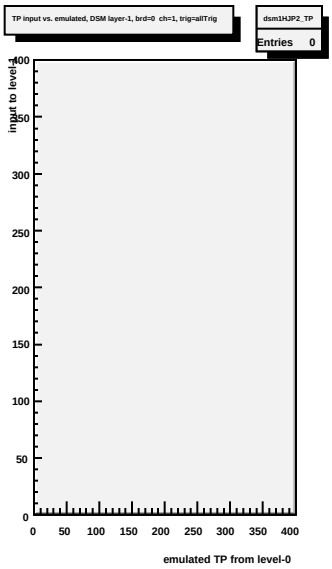
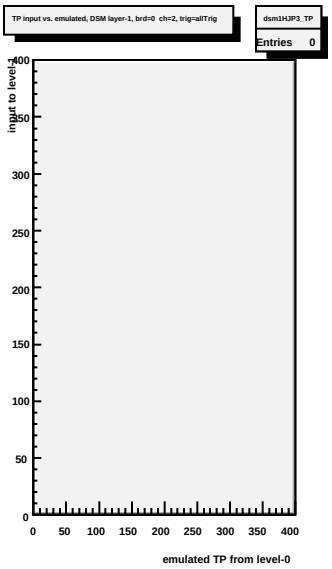
dsm1HJP10_HT
Entries 0
input to level-1
emulated HT from level-0

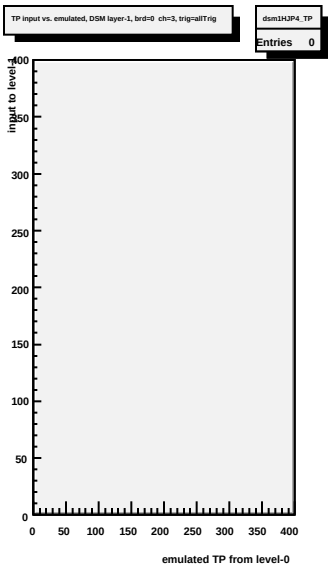

HT input vs.emu, DSM-1, Half Patch 11 (Falk), trig=allTrig
dsm1HJP11_HT
Entries 0
input to level-1
emulated HT from level-0

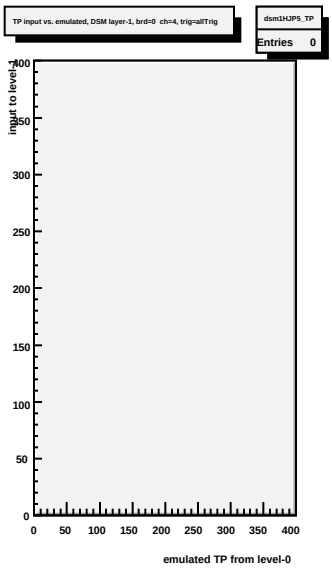

HT input vs.emu, DSM-1, Half Patch 12 (Falk), trig=allTrig
dsm1HJP12_HT
Entries 0
input to level-1
emulated HT from level-0

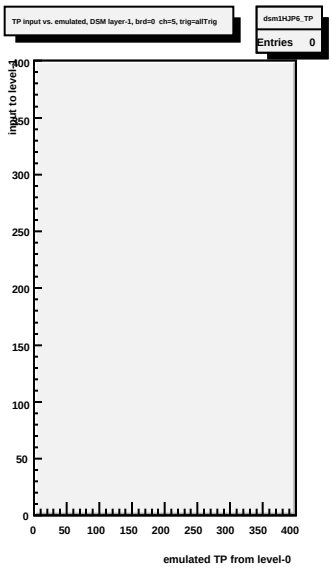

TP input vs. emulated, DSM layer-1, brd=0 ch=0, trig=allTrig
dsm1HJP1_TP
Entries 0
input to level-1
emulated TP from level-0

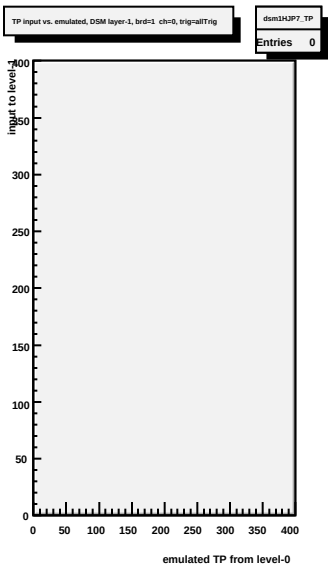

TP input vs. emulated, DSM layer-1, brd=0 ch=1, trig=allTrig
dsm1HJP2_TP
Entries 0
input to level-1
emulated TP from level-0

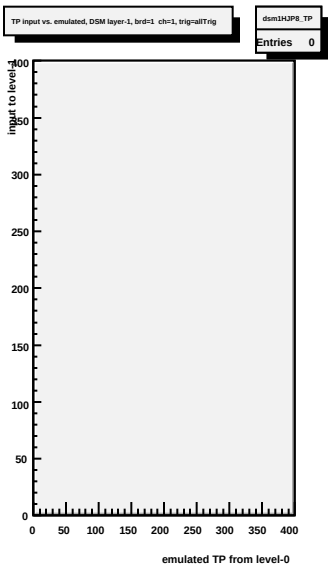

TP input vs. emulated, DSM layer-1, brd=0 ch=2, trig=allTrig
dsm1HJP3_TP
Entries 0
input to level-1
emulated TP from level-0

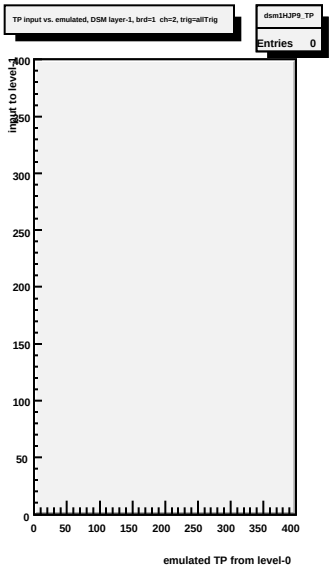

TP input vs. emulated, DSM layer-1, brd=0 ch=3, trig=allTrig
dsm1HJP4_TP
Entries 0
input to level-1
emulated TP from level-0

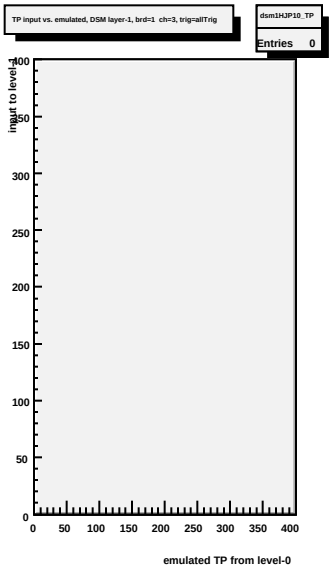

TP input vs. emulated, DSM layer-1, brd=0 ch=4, trig=allTrig
dsm1HJP5_TP
Entries 0
input to level-1
emulated TP from level-0

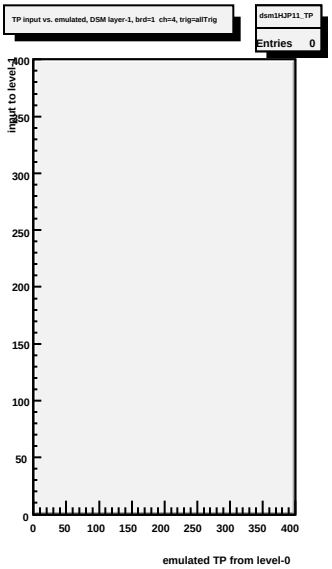

TP input vs. emulated, DSM layer-1, brd=0 ch=5, trig=allTrig
dsm1HJP6_TP
Entries 0
input to level-1
emulated TP from level-0

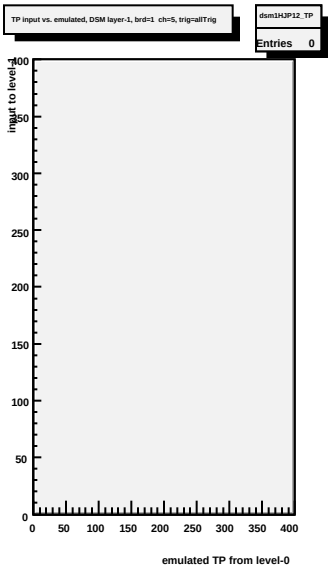

TP input vs. emulated, DSM layer-1, brd=1 ch=0, trig=allTrig
dsm1HJP7_TP
Entries 0
input to level-1
emulated TP from level-0

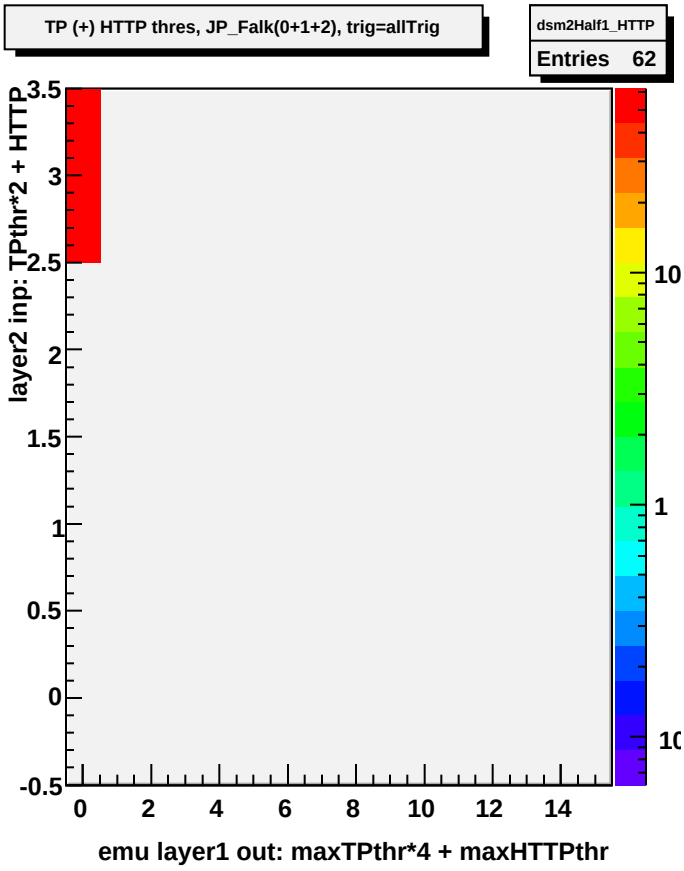

TP input vs. emulated, DSM layer-1, brd=1 ch=1, trig=allTrig
dsm1HJP8_TP
Entries 0
input to level-1
emulated TP from level-0

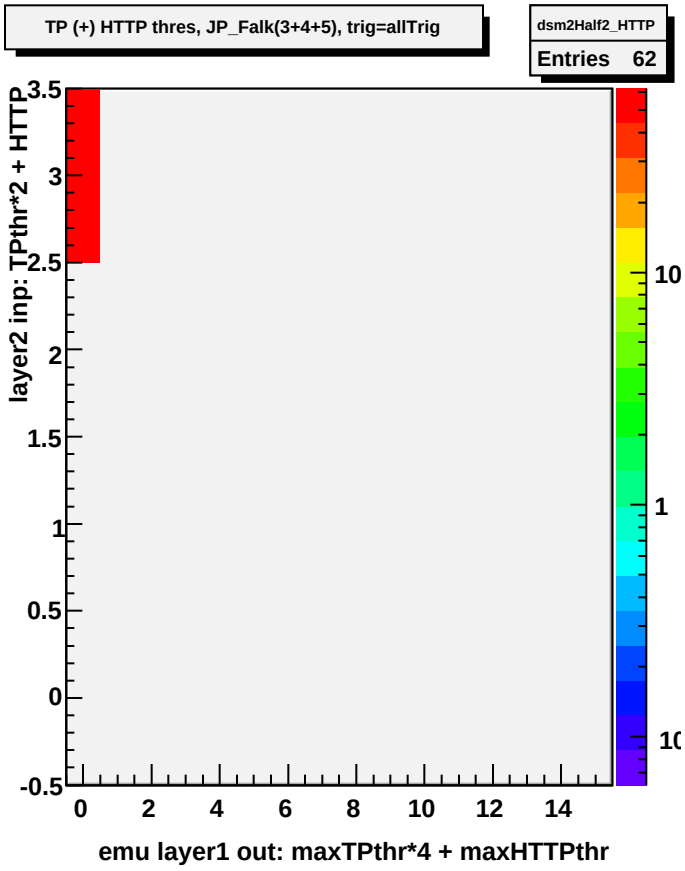

TP input vs. emulated, DSM layer-1, brd=1 ch=2, trig=allTrig
dsm1HJP9_TP
Entries 0
input to level-1
emulated TP from level-0

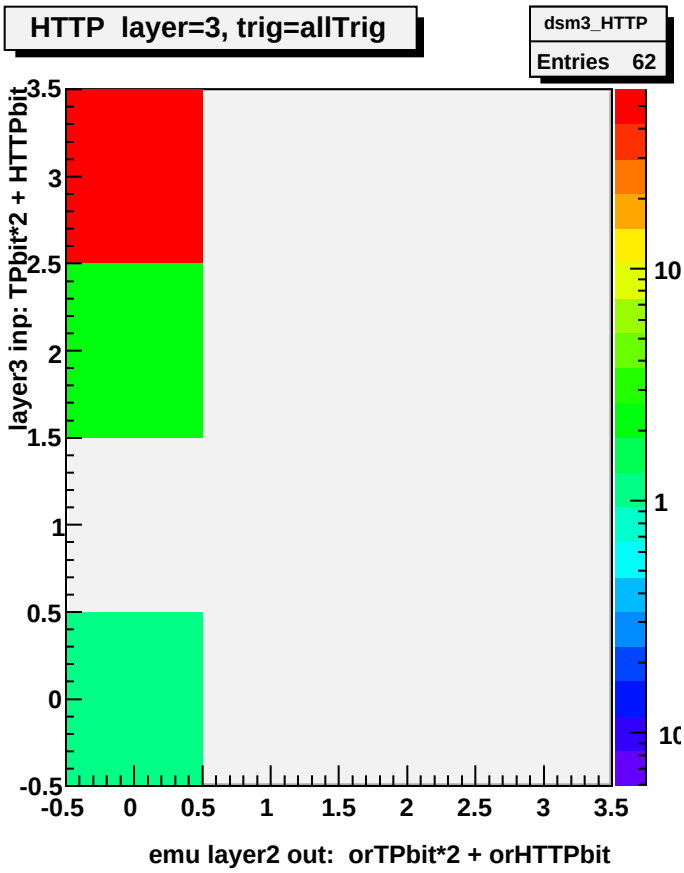

TP input vs. emulated, DSM layer-1, brd=1 ch=3, trig=allTrig
dsm1HJP10_TP
Entries 0
input to level-1
emulated TP from level-0

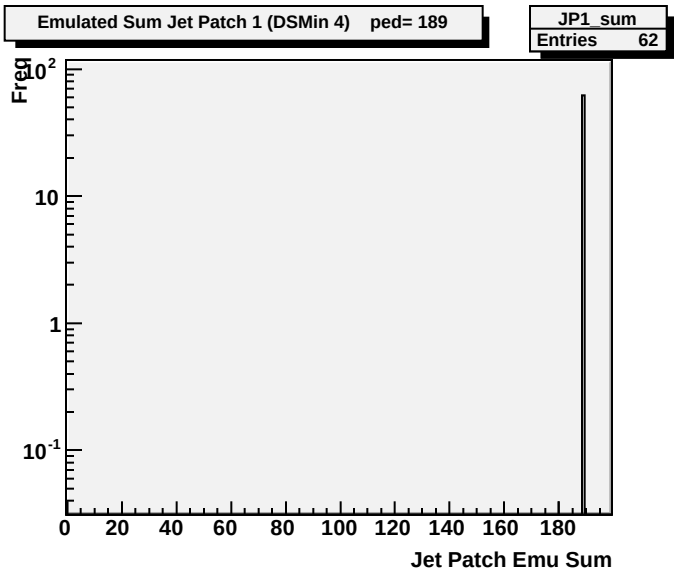

TP input vs. emulated, DSM layer-1, brd=1 ch=4, trig=allTrig
dsm1HJP11_TP
Entries 0
input to level-1
emulated TP from level-0

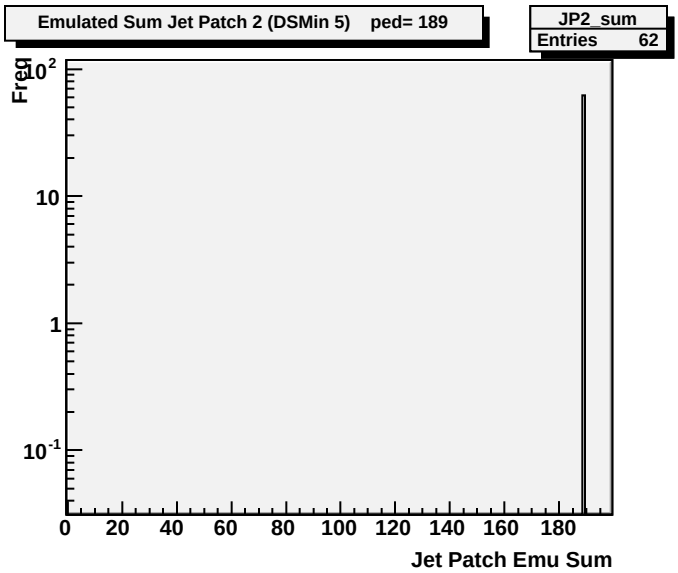

TP input vs. emulated, DSM layer-1, brd=1 ch=5, trig=allTrig
dsm1HJP12_TP
Entries 0
input to level-1
emulated TP from level-0

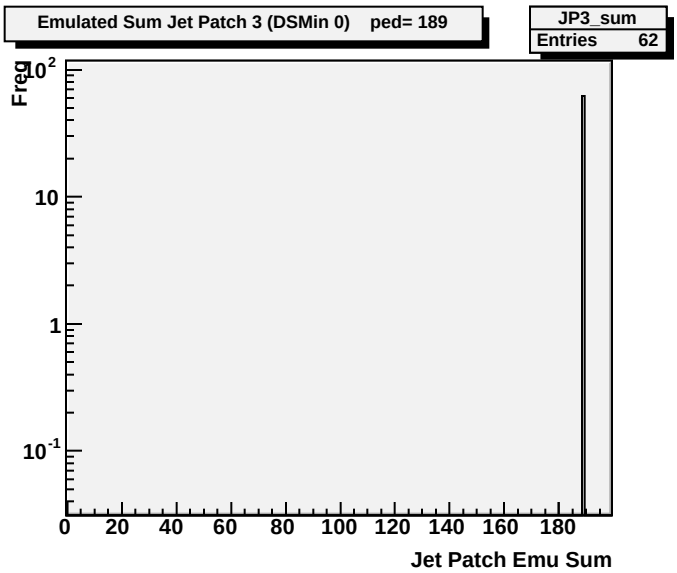

TP (+) HTTP thres, JP_Falk(0+1+2), trig=allTrig
dsm2Half1_HTTP
Entries 62
layer2 inp: TPthr*2 + HTTP
emu layer1 out: maxTPthr*4 + maxHTTPthr

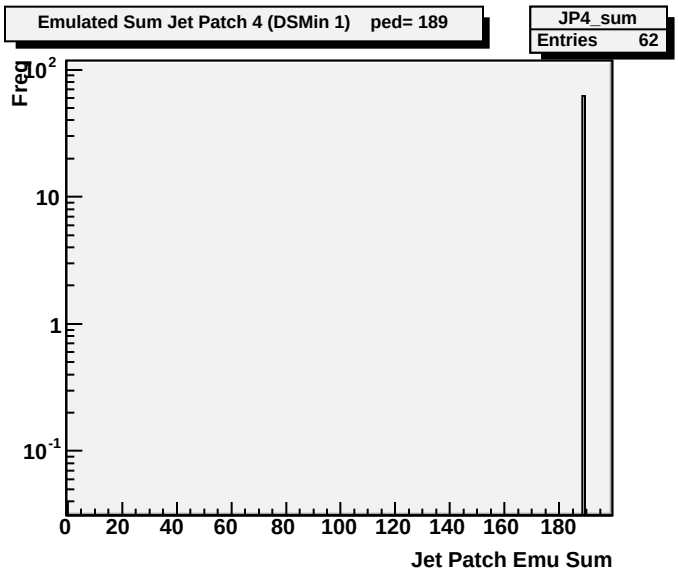

TP (+) HTTP thres, JP_Falk(3+4+5), trig=allTrig
dsm2Half2_HTTP
Entries 62
layer2 inp: TPthr*2 + HTTP
emu layer1 out: maxTPthr*4 + maxHTTPthr

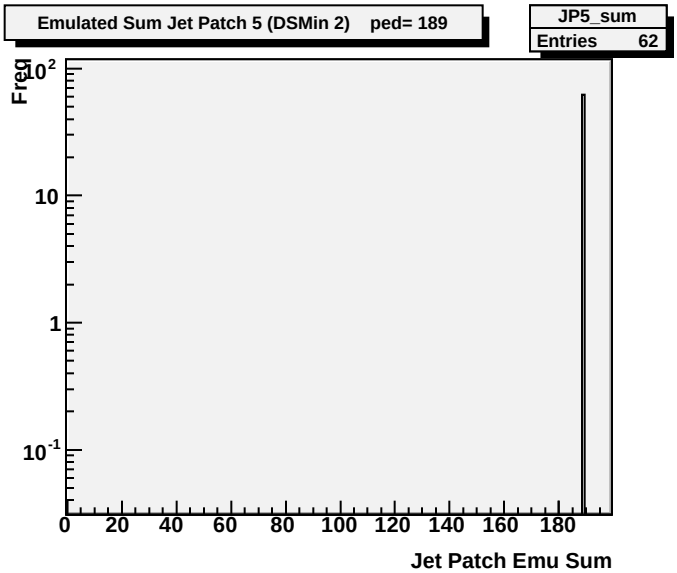

HTTP layer=3, trig=allTrig
dsm3_HTTP
Entries 62
layer3 inp: TPbit*2 + HTTPbit
emu layer2 out: orTPbit*2 + orHTTPbit

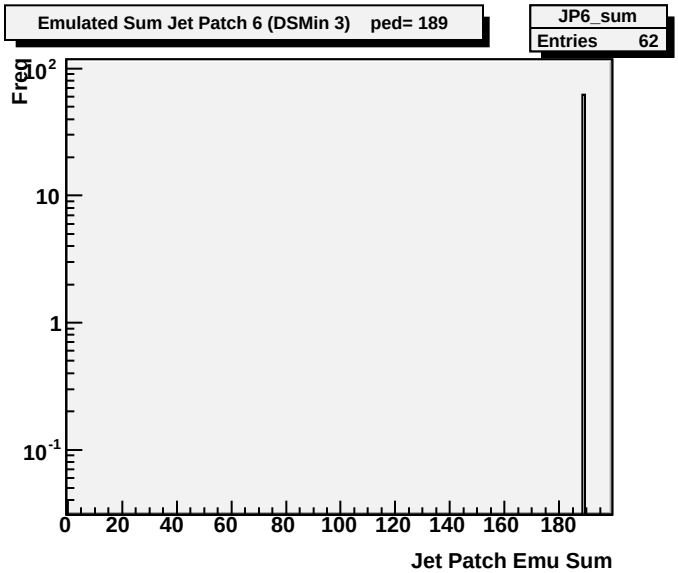

Emulated Sum Jet Patch 1 (DSMin 4) ped= 189
JP1_sum
Entries 62
Freq
Jet Patch Emu Sum

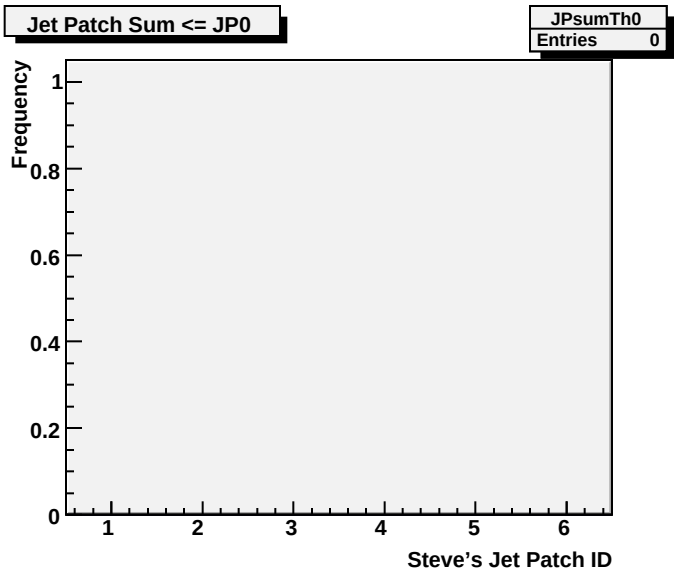

Emulated Sum Jet Patch 2 (DSMin 5) ped= 189
JP2_sum
Entries 62
Freq
Jet Patch Emu Sum

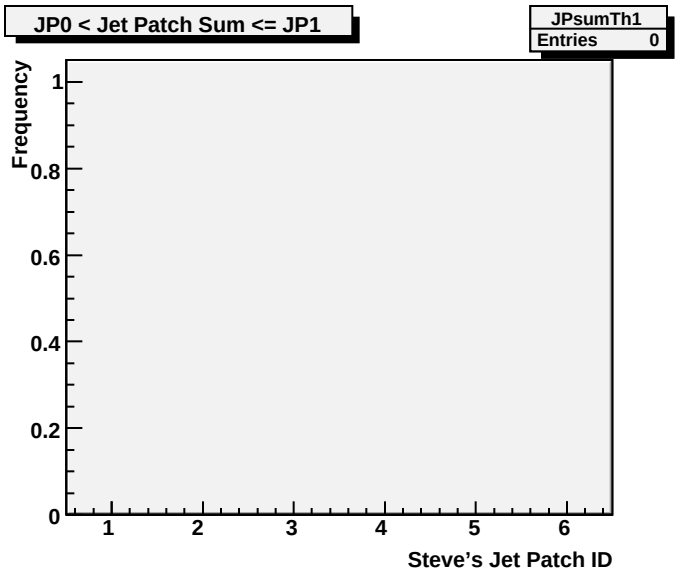

Emulated Sum Jet Patch 3 (DSMin 0) ped= 189
JP3_sum
Entries 62
Freq
Jet Patch Emu Sum

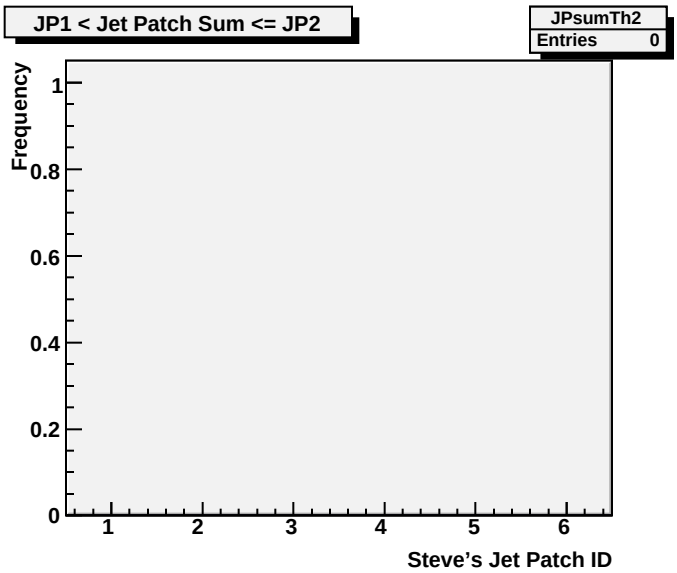

Emulated Sum Jet Patch 4 (DSMin 1) ped= 189
JP4_sum
Entries 62
Freq
Jet Patch Emu Sum

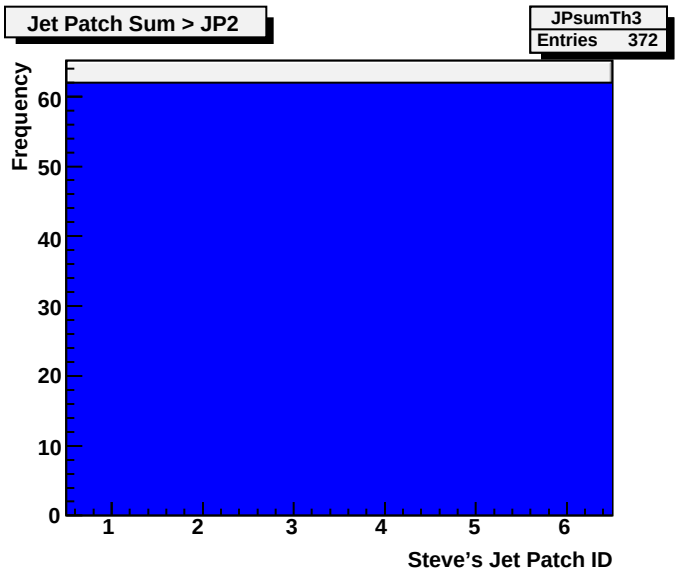

Emulated Sum Jet Patch 5 (DSMin 2) ped= 189
JP5_sum
Entries 62
Freq
Jet Patch Emu Sum

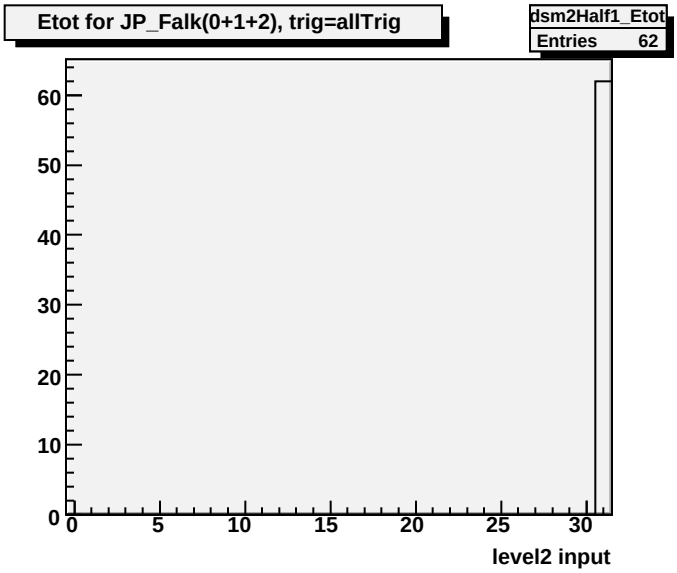

Emulated Sum Jet Patch 6 (DSMin 3) ped= 189
JP6_sum
Entries 62
Freq
Jet Patch Emu Sum

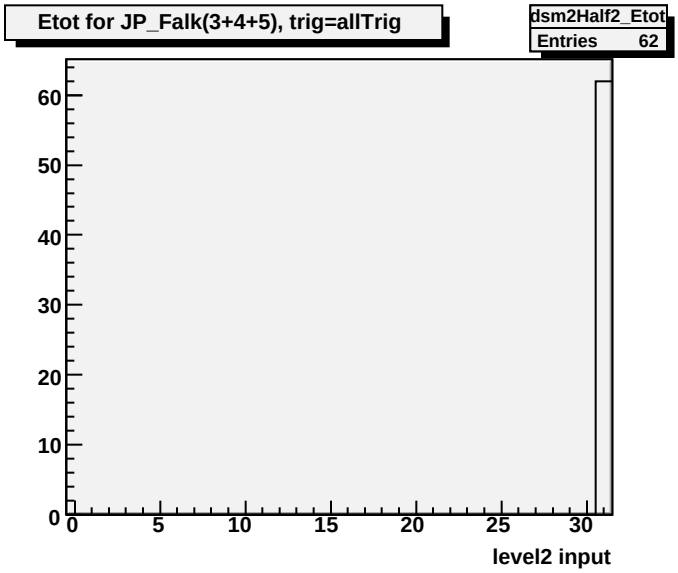

Jet Patch Sum <= JP0
JPsumTh0
Entries 0
Frequency
Steve's Jet Patch ID

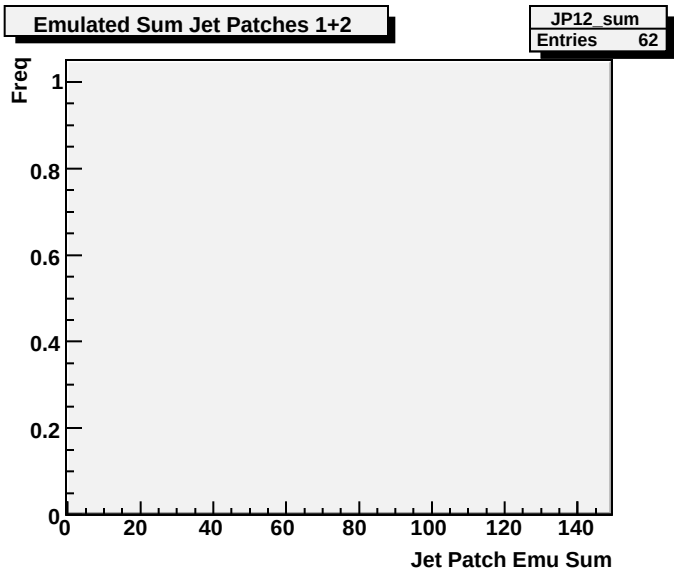

JP0 < Jet Patch Sum <= JP1
JPsumTh1
Entries 0
Frequency
Steve's Jet Patch ID

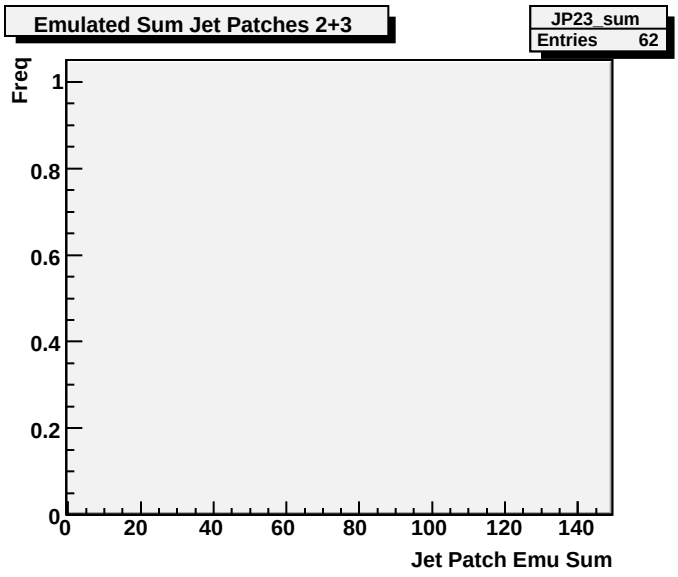

JP1 < Jet Patch Sum <= JP2
JPsumTh2
Entries 0
Frequency
Steve's Jet Patch ID

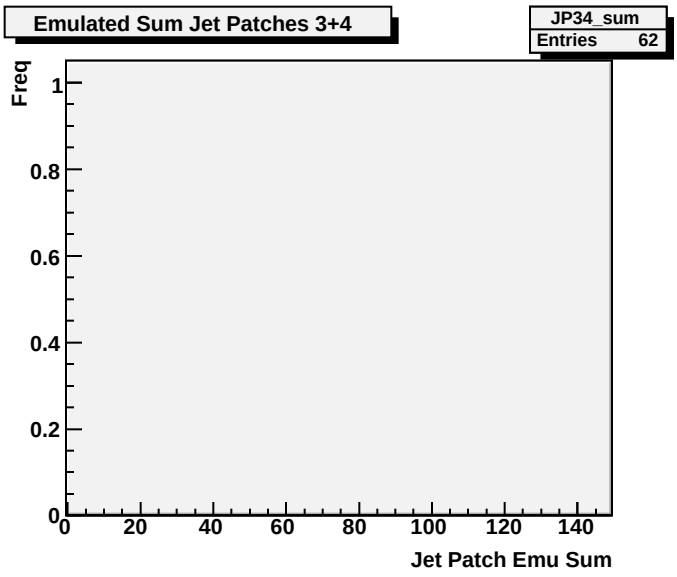

Jet Patch Sum > JP2
JPsumTh3
Entries 372
Frequency
Steve's Jet Patch ID

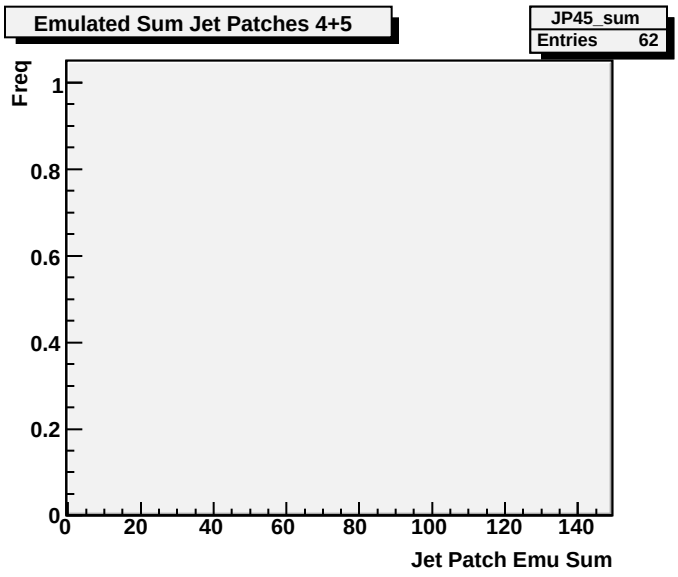

Etot for JP_Falk(0+1+2), trig=allTrig
dsm2Half1_Etot
Entries 62
level2 input

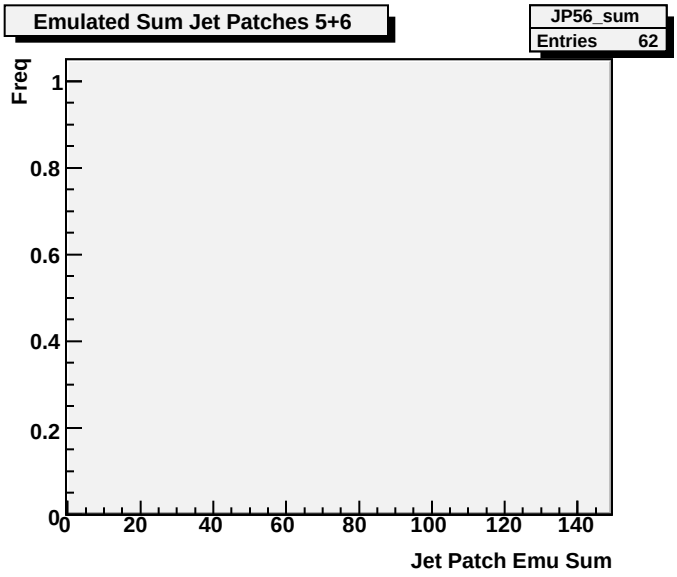

Etot for JP_Falk(3+4+5), trig=allTrig
dsm2Half2_Etot
Entries 62
level2 input

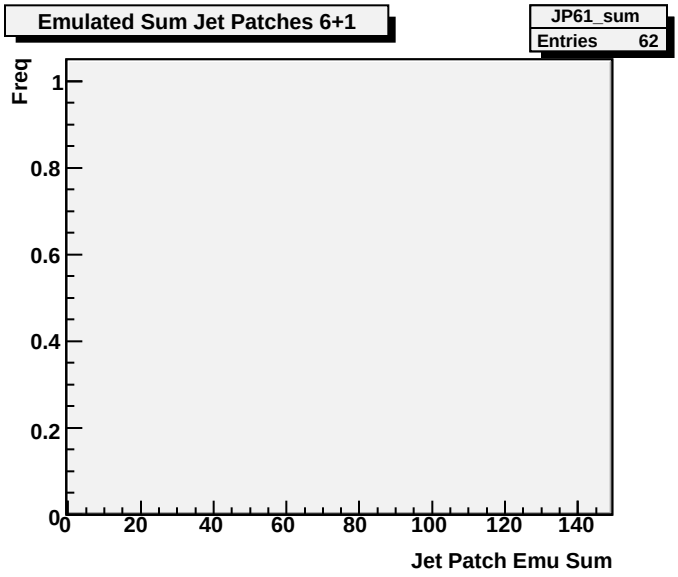

Emulated Sum Jet Patches 1+2
JP12_sum
Entries 62
Freq
Jet Patch Emu Sum

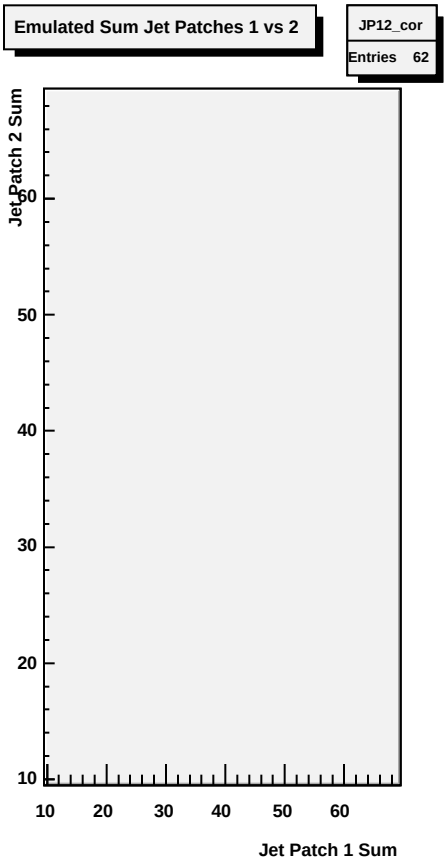

Emulated Sum Jet Patches 2+3
JP23_sum
Entries 62
Freq
Jet Patch Emu Sum

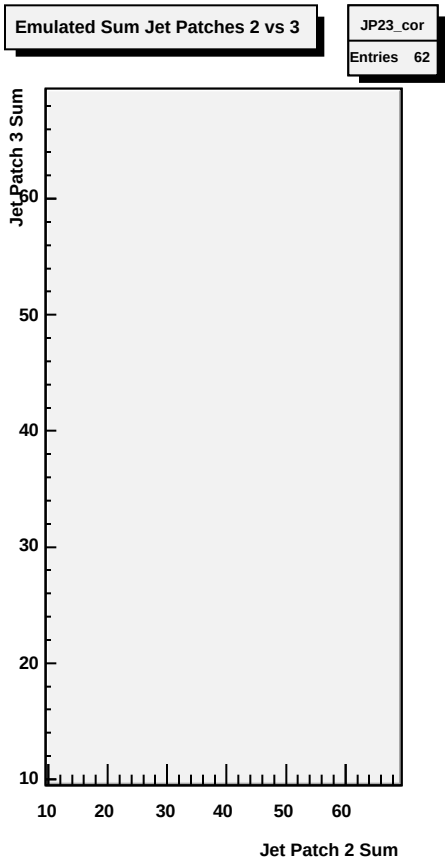

Emulated Sum Jet Patches 3+4
JP34_sum
Entries 62
Freq
Jet Patch Emu Sum

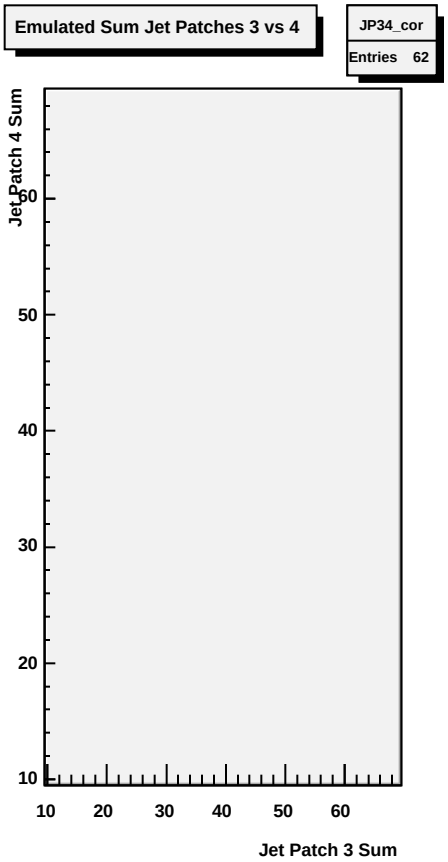

Emulated Sum Jet Patches 4+5
JP45_sum
Entries 62
Freq
Jet Patch Emu Sum

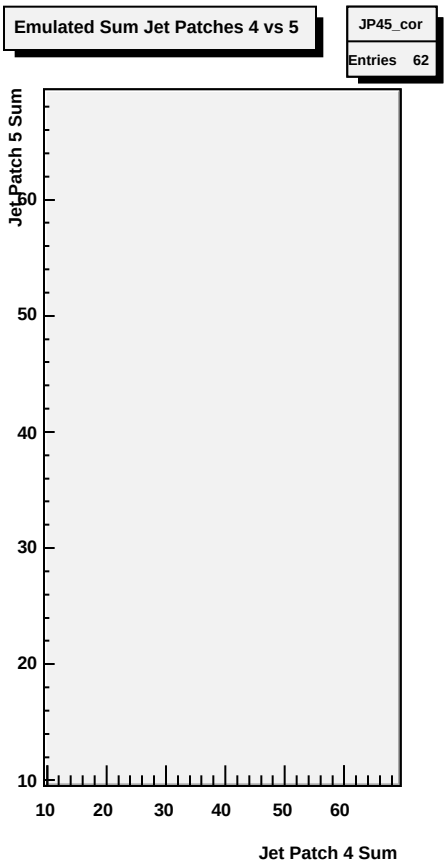

Emulated Sum Jet Patches 5+6
JP56_sum
Entries 62
Freq
Jet Patch Emu Sum

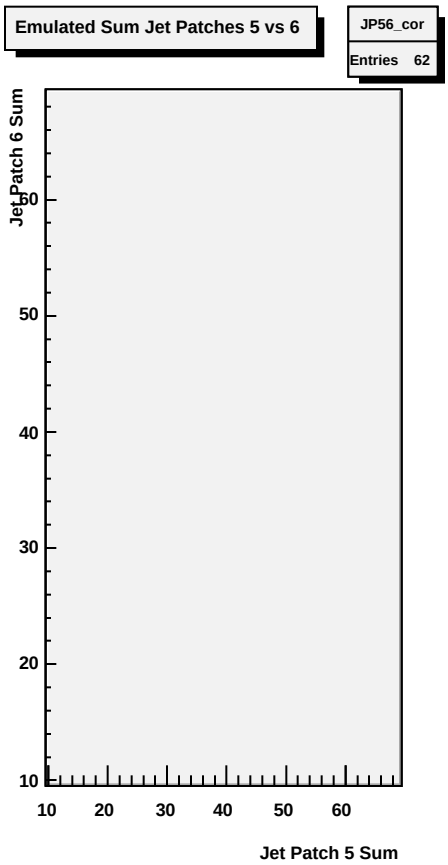

Emulated Sum Jet Patches 6+1
JP61_sum
Entries 62
Freq
Jet Patch Emu Sum

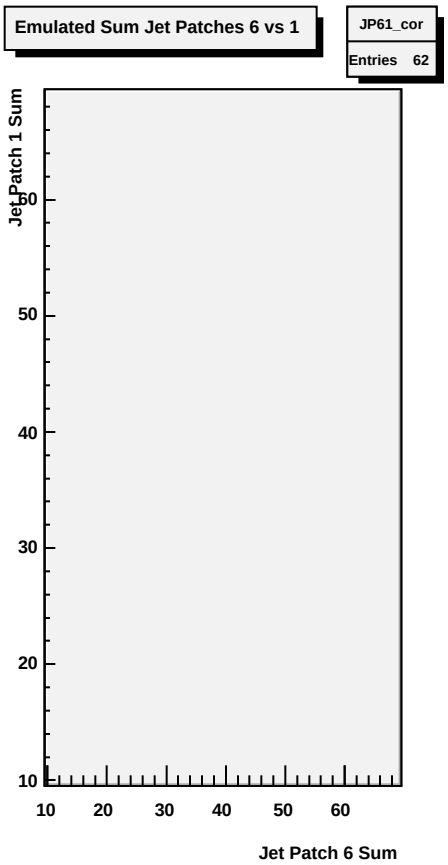

Emulated Sum Jet Patches 1 vs 2
JP12_cor
Entries 62
Jet Patch 2 Sum
Jet Patch 1 Sum

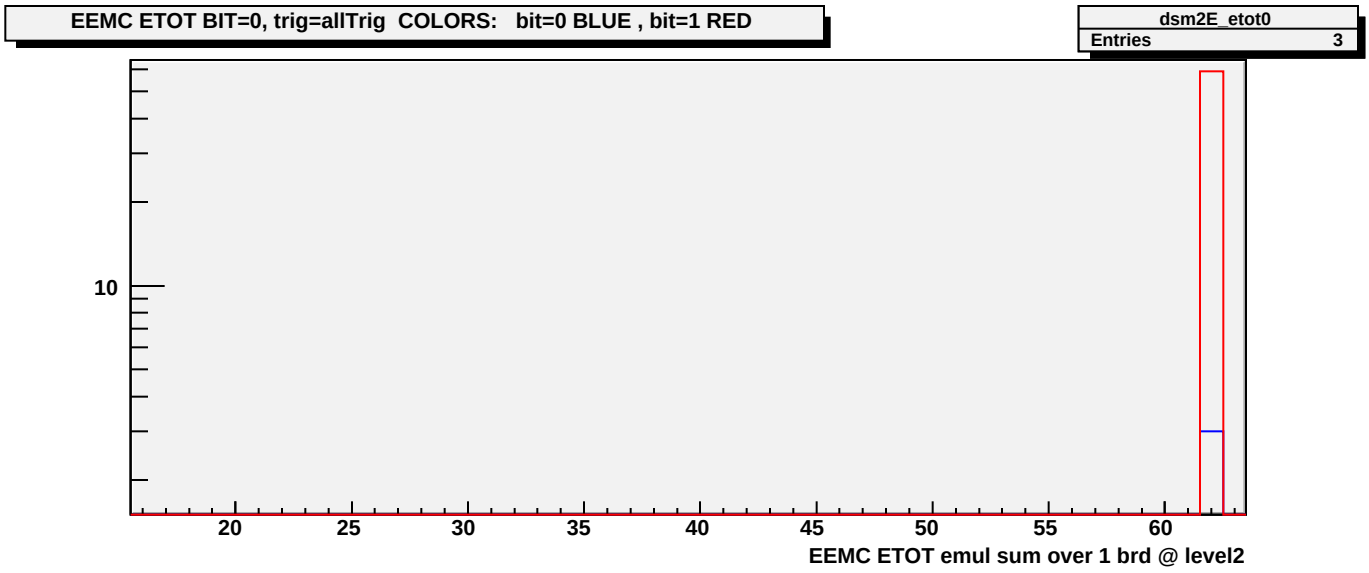

Emulated Sum Jet Patches 2 vs 3
JP23_cor
Entries 62
Jet Patch 3 Sum
Jet Patch 2 Sum

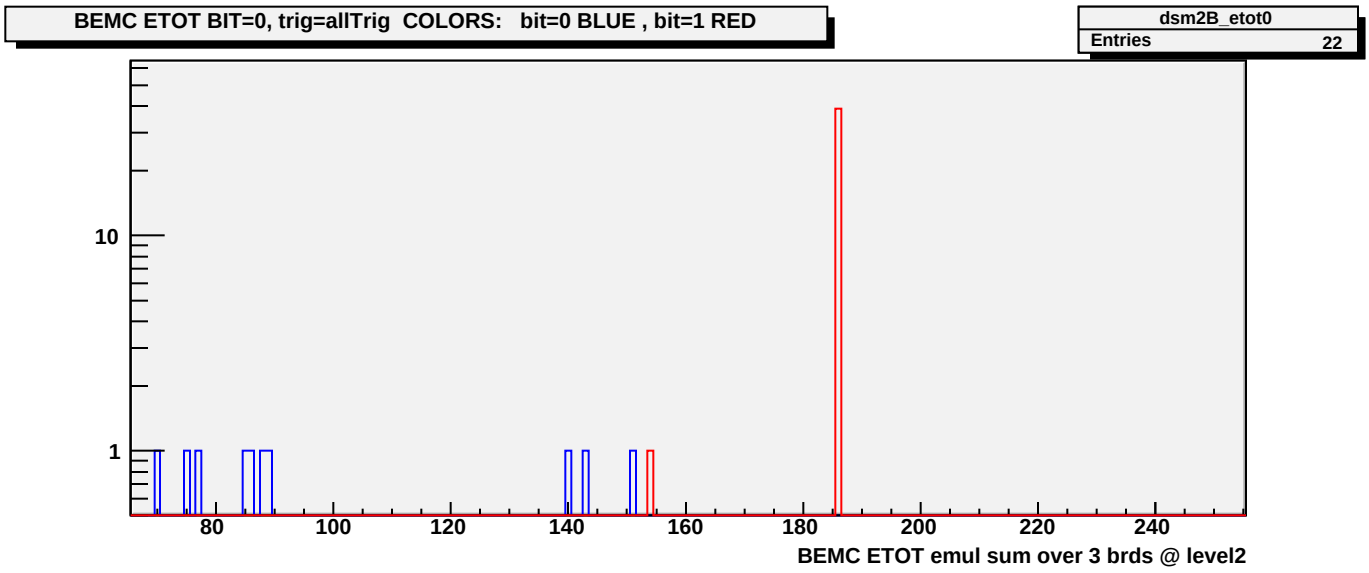

Emulated Sum Jet Patches 3 vs 4
JP34_cor
Entries 62
Jet Patch 4 Sum
Jet Patch 3 Sum

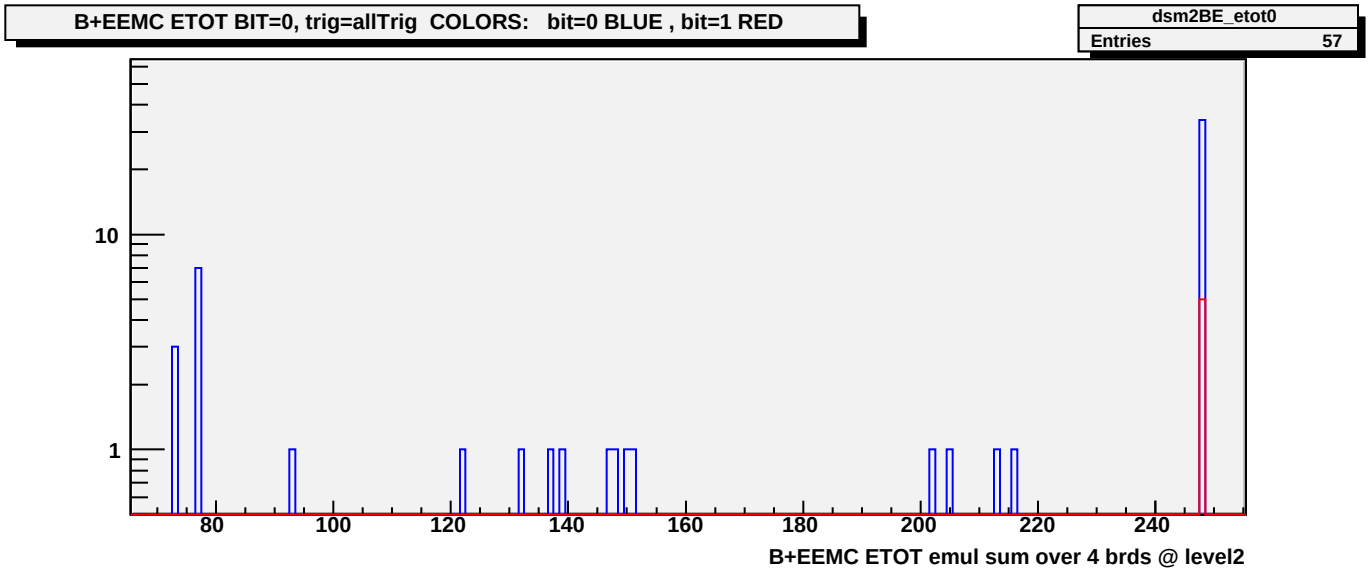

Emulated Sum Jet Patches 4 vs 5
JP45_cor
Entries 62
Jet Patch 5 Sum
Jet Patch 4 Sum

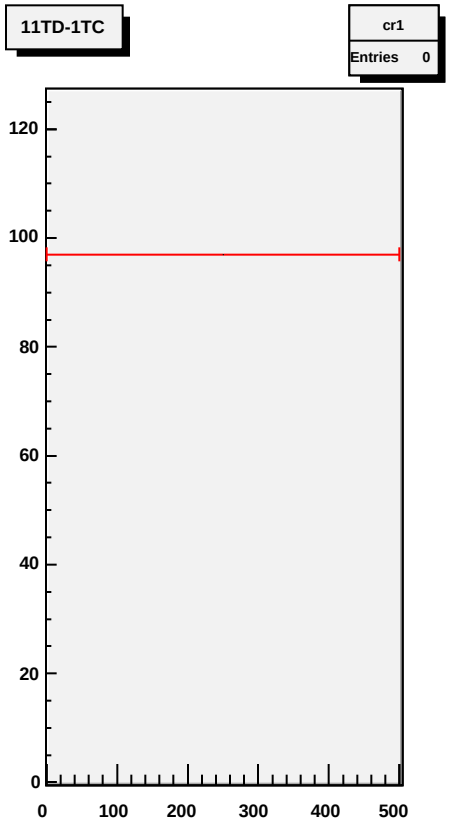

Emulated Sum Jet Patches 5 vs 6
JP56_cor
Entries 62
Jet Patch 6 Sum
Jet Patch 5 Sum

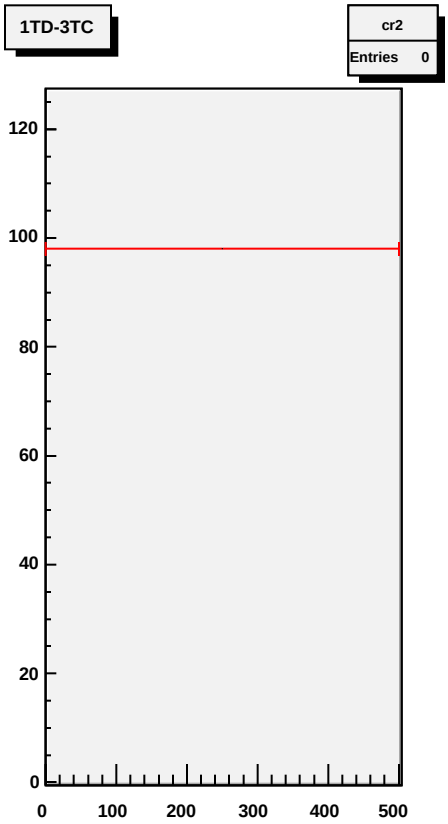

Emulated Sum Jet Patches 6 vs 1
JP61_cor
Entries 62
Jet Patch 1 Sum
Jet Patch 6 Sum

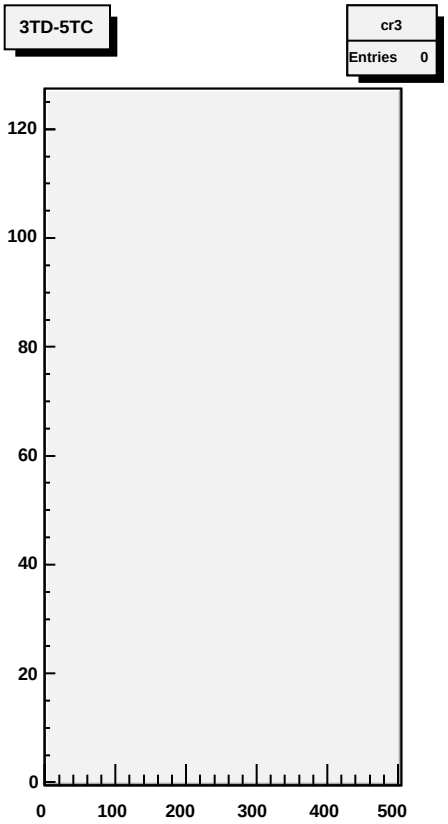

EEMC ETOT BIT=0, trig=allTrig COLORS: bit=0 BLUE , bit=1 RED
dsm2E_etot0
Entries 3
10
20
25
30
35
40
45
50
55
60
EEMC ETOT emul sum over 1 brd @ level2

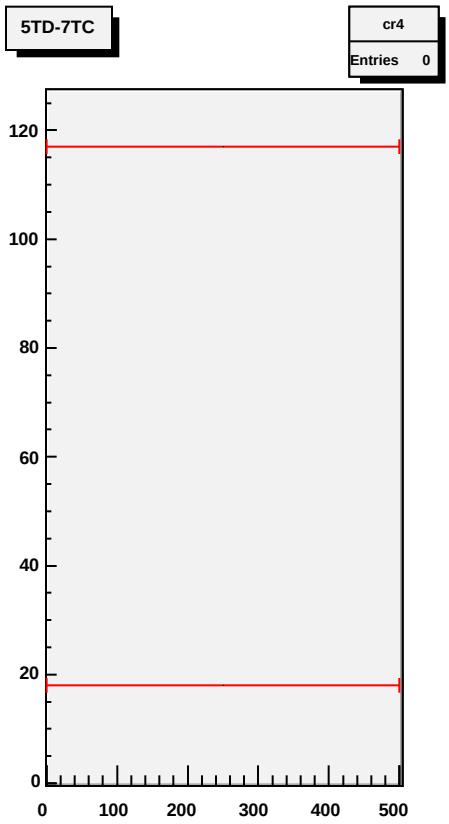

BEMC ETOT BIT=0, trig=allTrig COLORS: bit=0 BLUE , bit=1 RED
dsm2B_etot0
Entries 22
10
1
80
100
120
140
160
180
200
220
240
BEMC ETOT emul sum over 3 brds @ level2

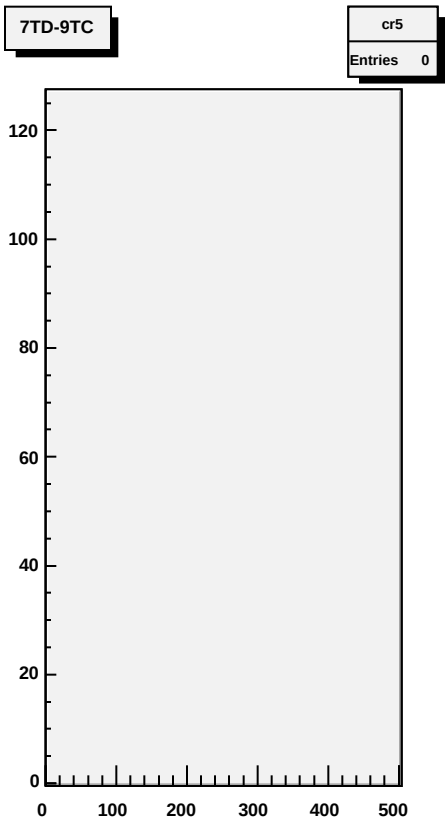

B+EEMC ETOT BIT=0, trig=allTrig COLORS: bit=0 BLUE , bit=1 RED
dsm2BE_etot0
Entries 57
10
1
80
100
120
140
160
180
200
220
240
B+EEMC ETOT emul sum over 4 brds @ level2

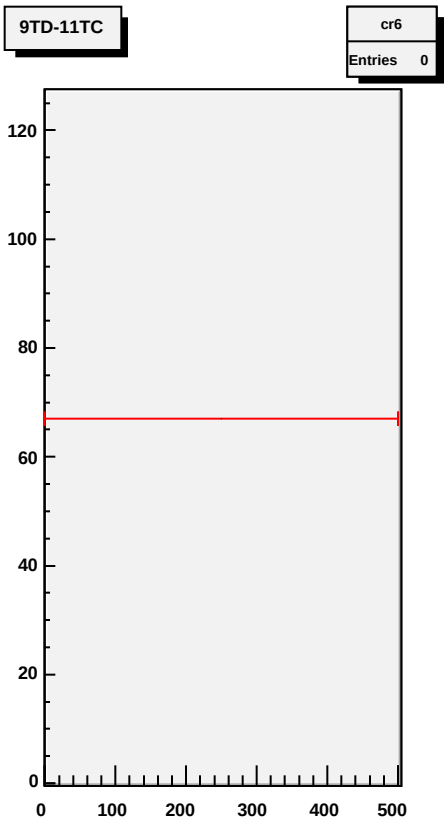

11TD-1TC
cr1
Entries 0
120
100
80
60
40
20
0
0
100
200
300
400
500


1TD-3TC
cr2
Entries 0
120
100
80
60
40
20
0
0
100
200
300
400
500

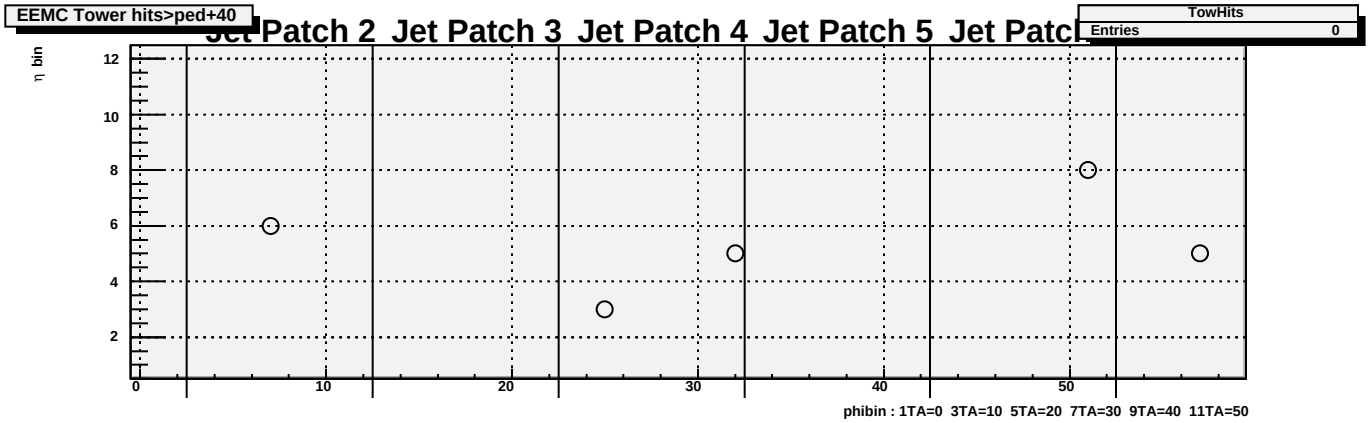

3TD-5TC
cr3
Entries 0
120
100
80
60
40
20
0
0
100
200
300
400
500


5TD-7TC
cr4
Entries 0
120
100
80
60
40
20
0
0
100
200
300
400
500

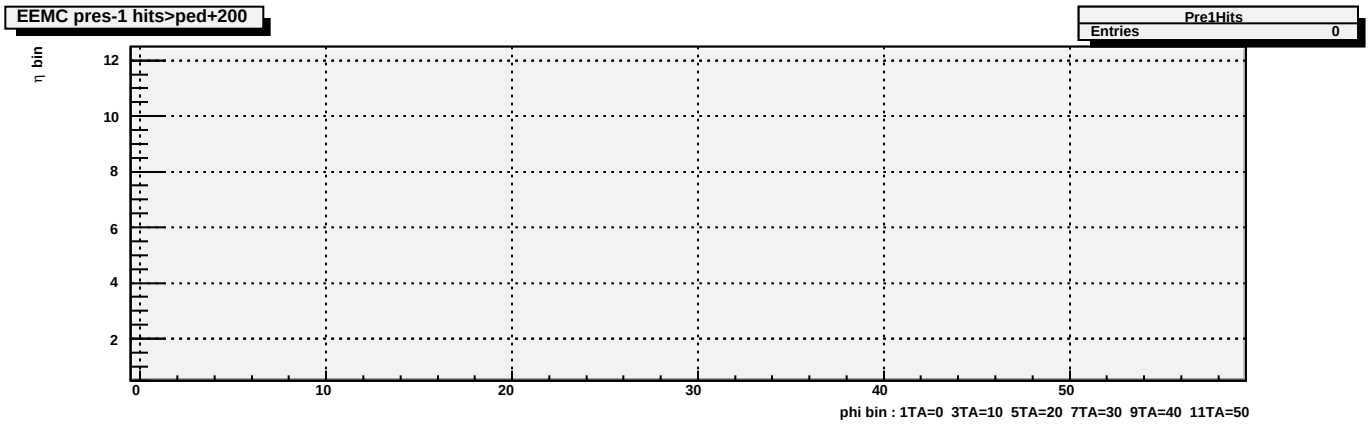

7TD-9TC
cr5
Entries 0
120
100
80
60
40
20
0
0
100
200
300
400
500


9TD-11TC
cr6
Entries 0
120
100
80
60
40
20
0
0
100
200
300
400
500

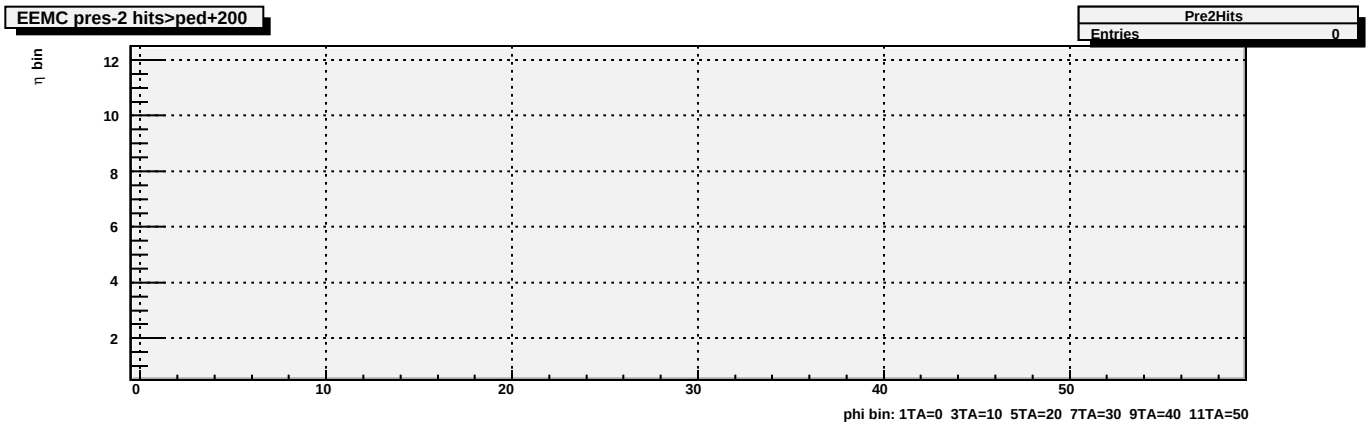
EEMC Tower hits>ped+40



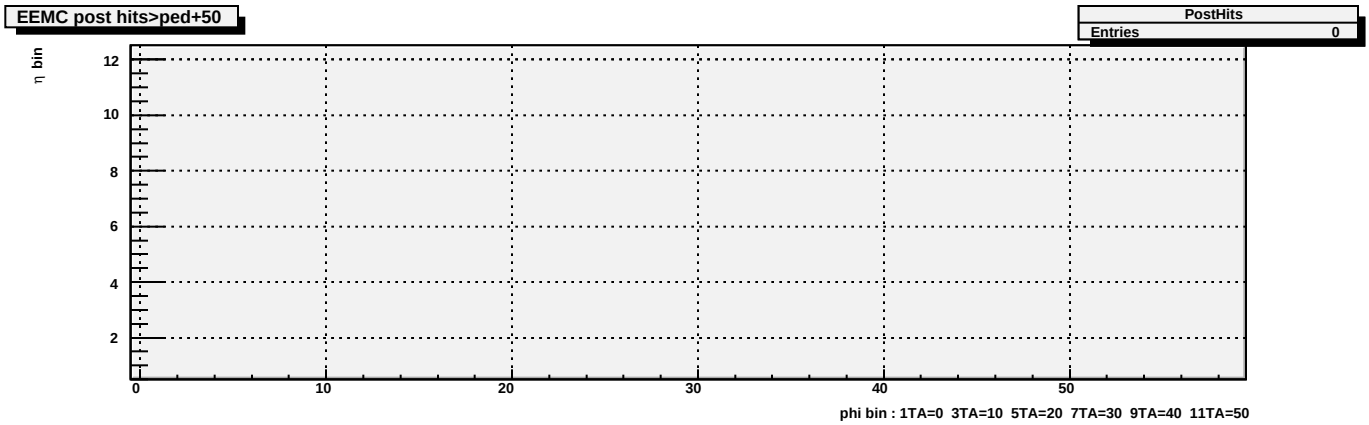
EEMC pres-1 hits>ped+200

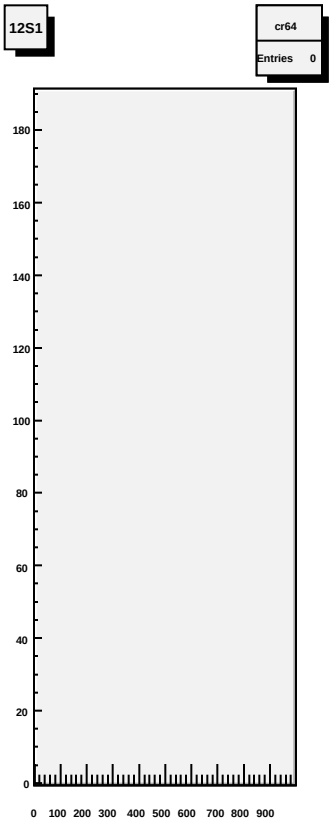


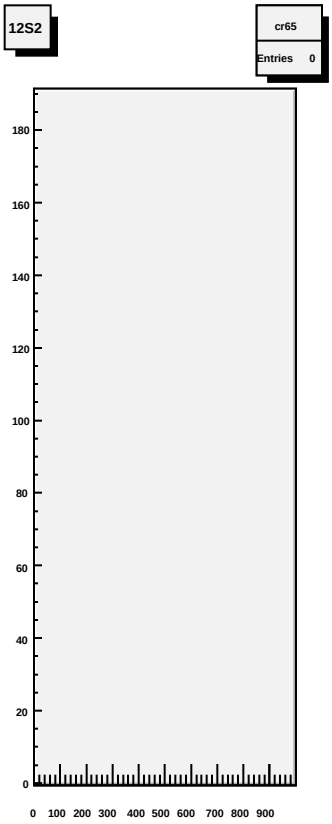
EEMC pres-2 hits>ped+200

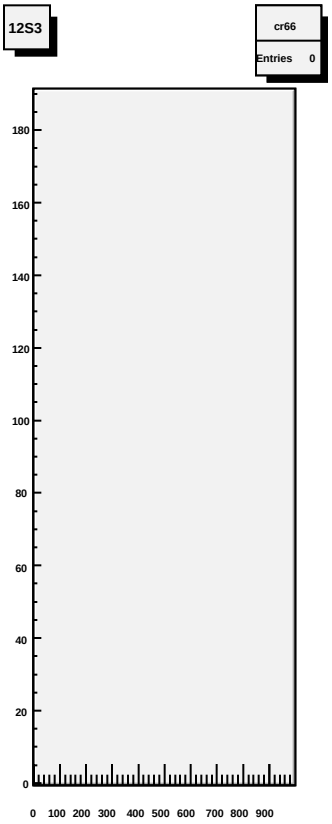


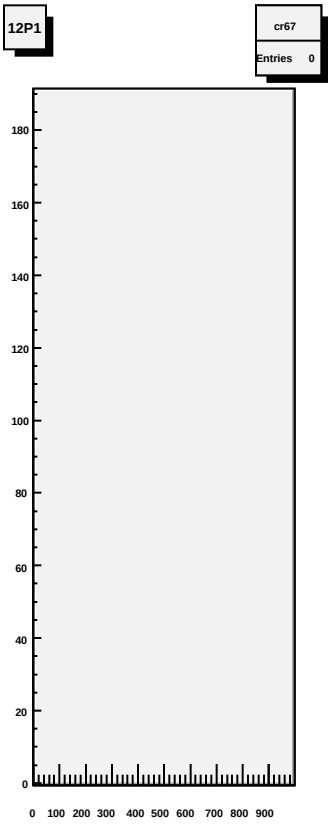
EEMC post hits>ped+50

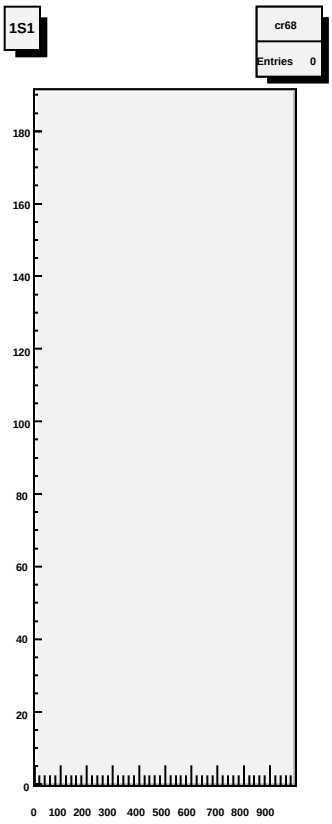


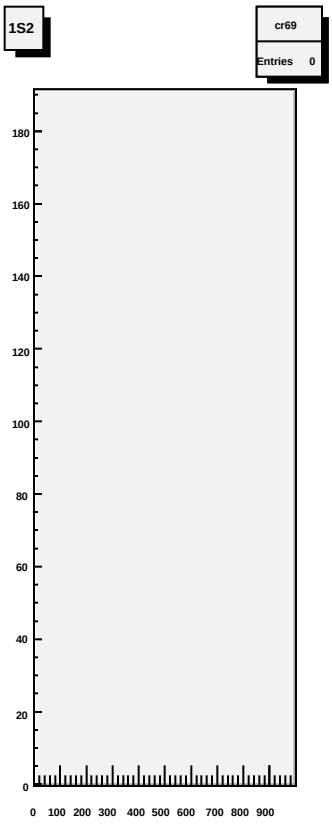

12S1
cr64
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

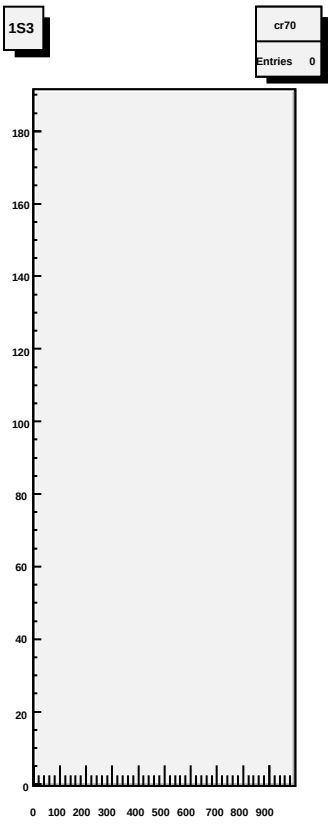

12S2
cr65
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

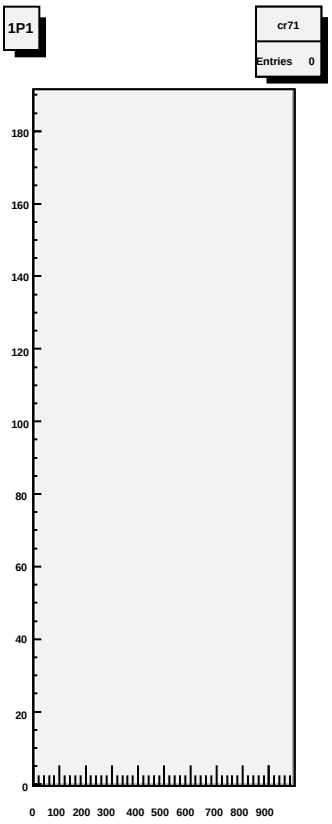

12S3
cr66
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

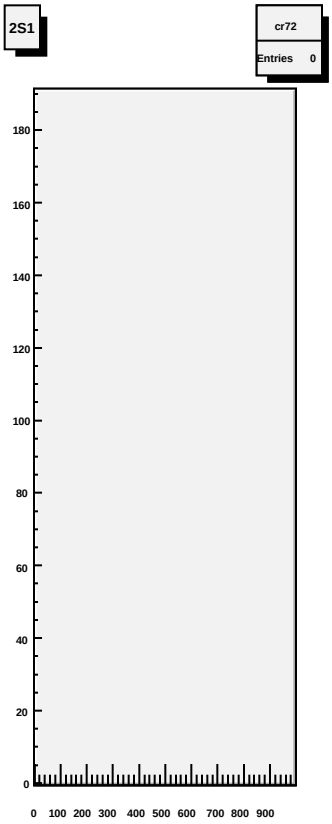

12P1
cr67
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

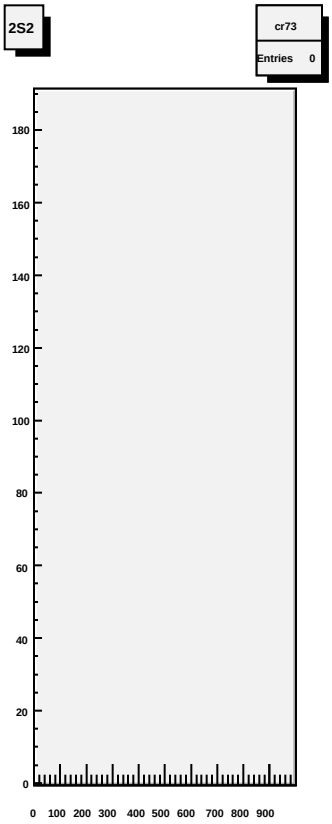

1S1
cr68
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

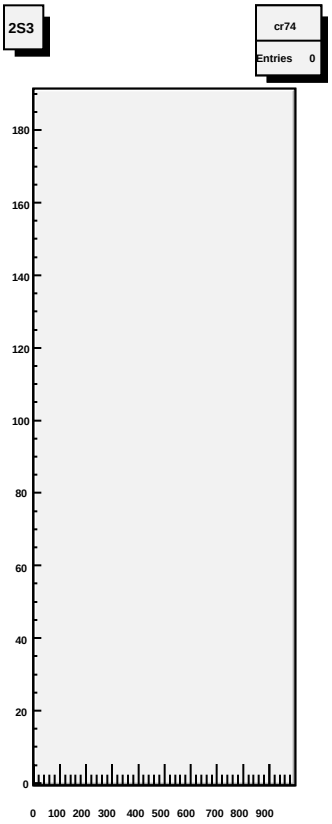

1S2
cr69
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

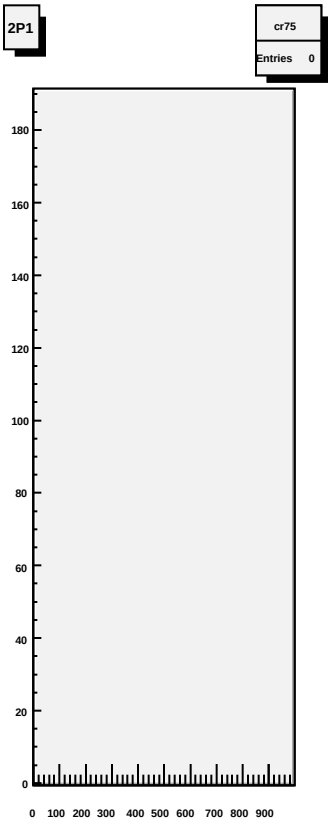

1S3
cr70
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

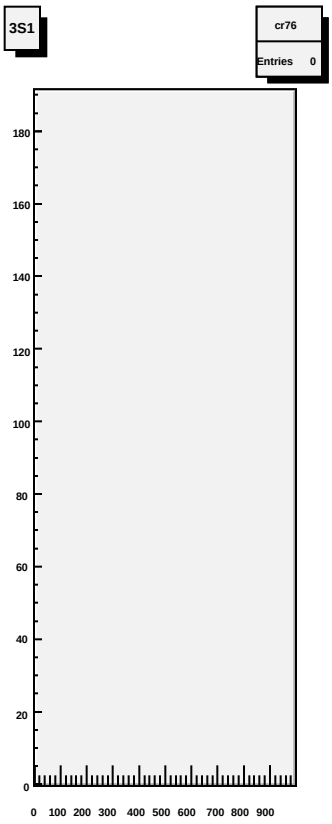

1P1
cr71
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

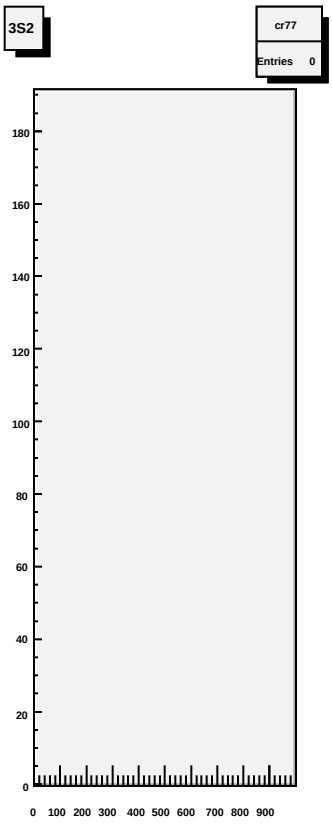

2S1
cr72
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

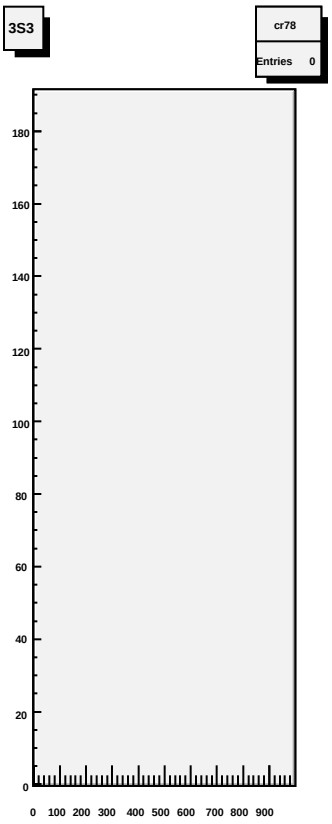

2S2
cr73
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

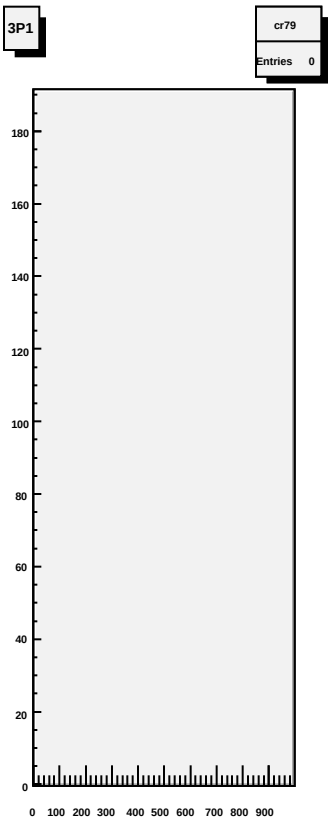

2S3
cr74
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

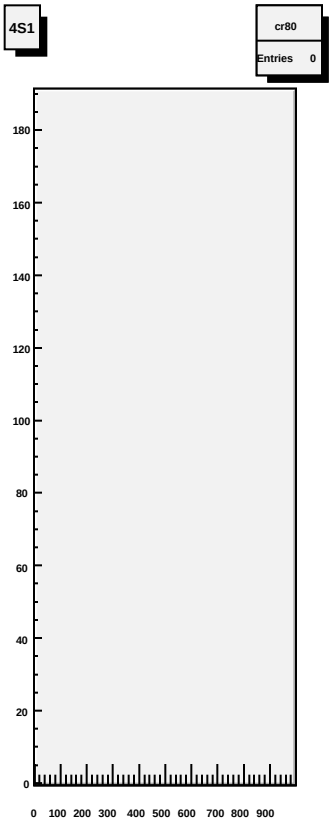

2P1
cr75
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

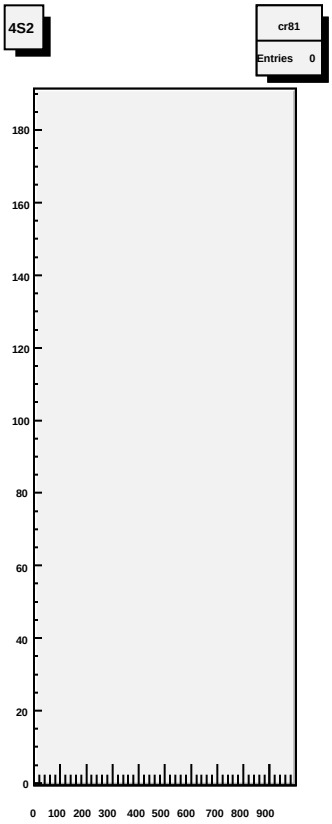

3S1
cr76
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

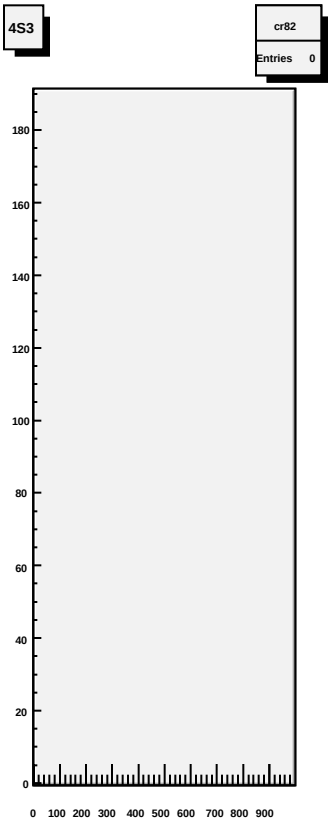

3S2
cr77
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

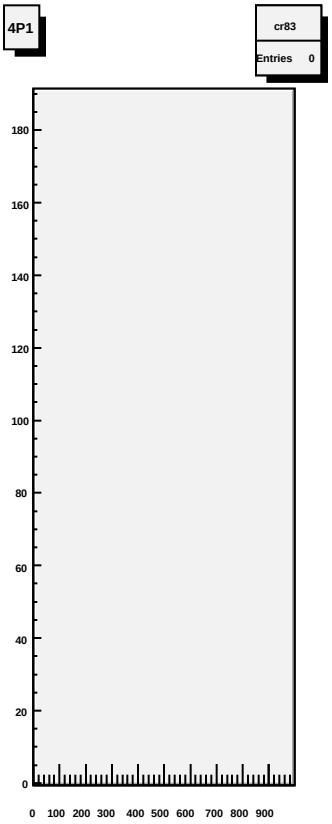

3S3
cr78
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

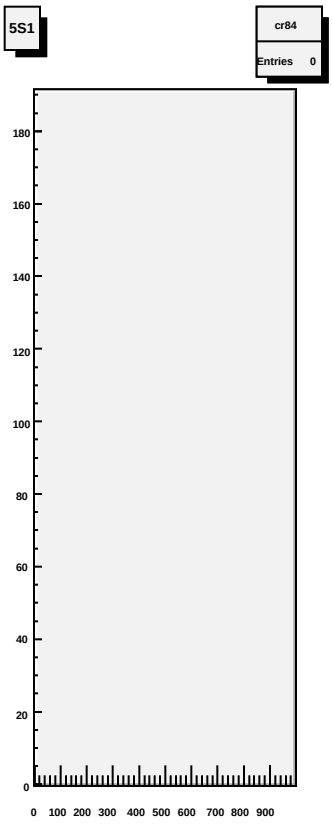

3P1
cr79
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

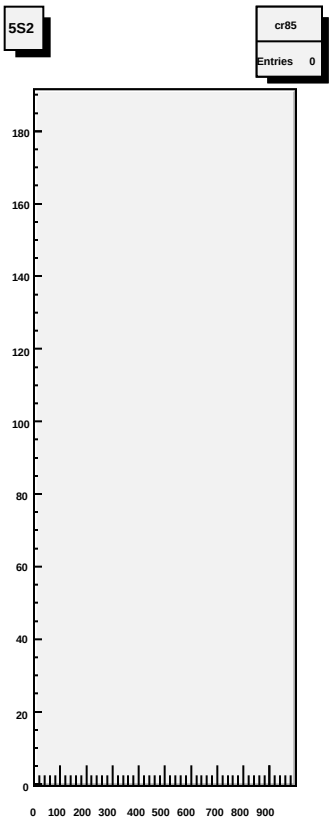

4S1
cr80
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

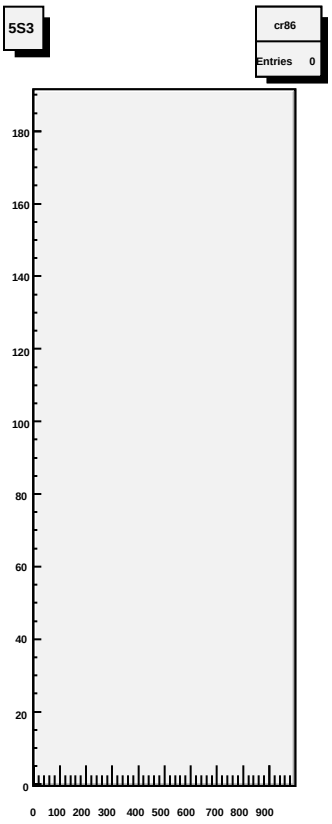

4S2
cr81
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

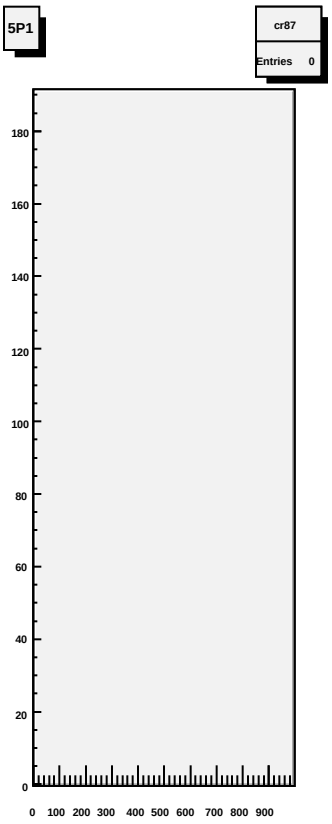

4S3
cr82
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

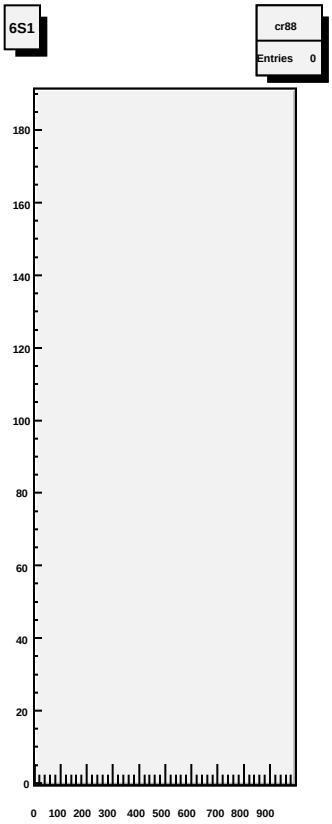

4P1
cr83
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

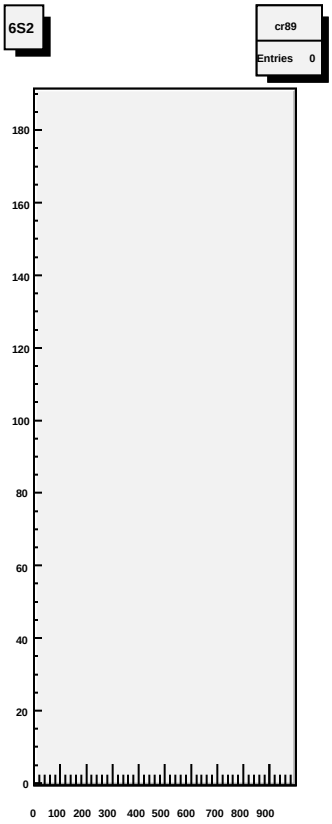

5S1
cr84
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

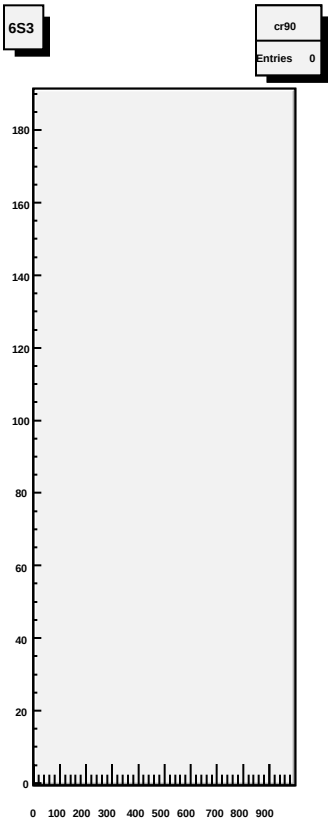

5S2
cr85
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

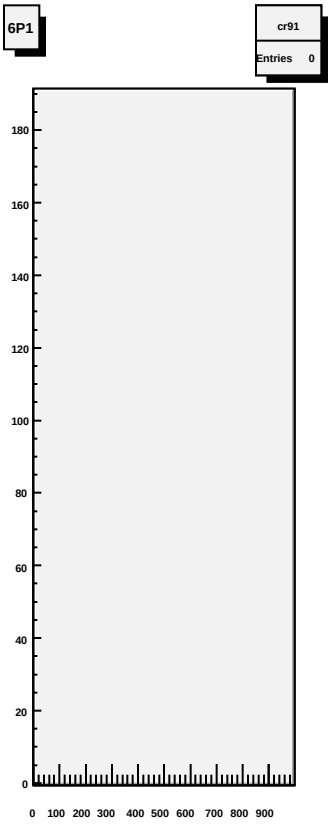

5S3
cr86
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

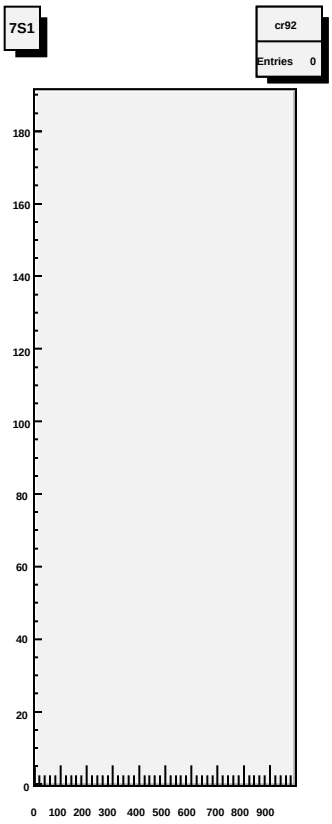

5P1
cr87
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

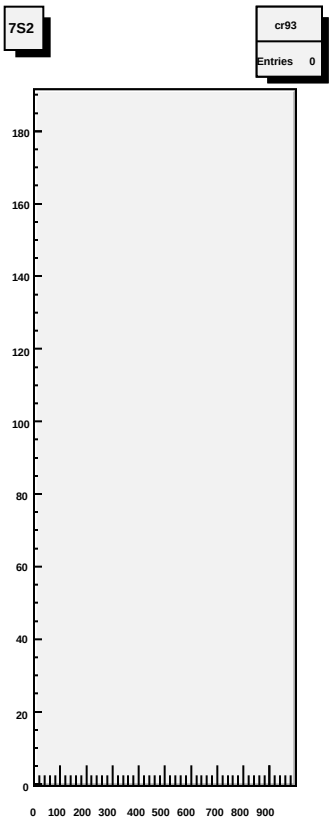

6S1
cr88
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

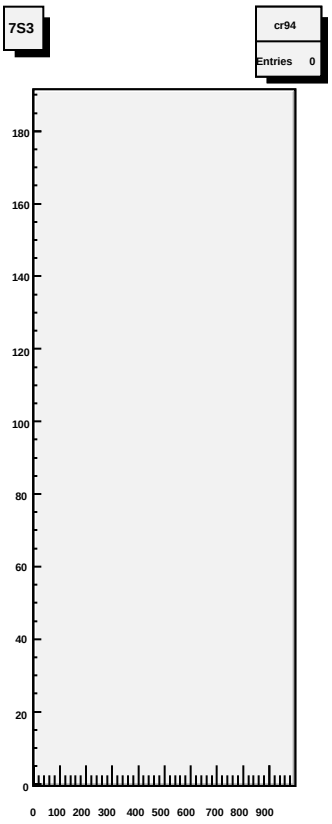

6S2
cr89
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

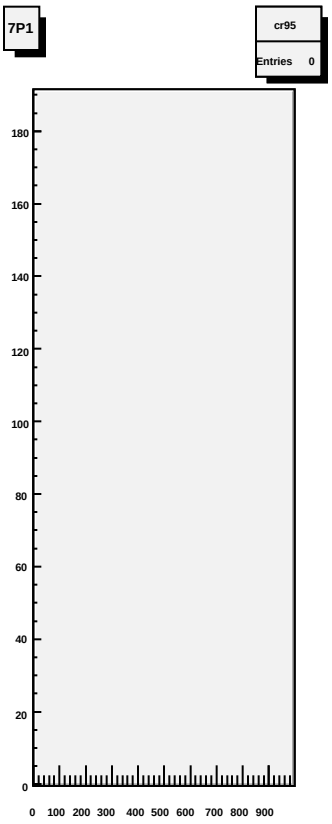

6S3
cr90
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

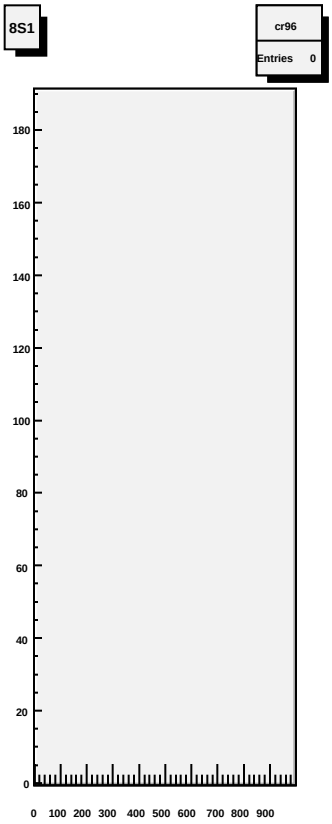

6P1
cr91
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

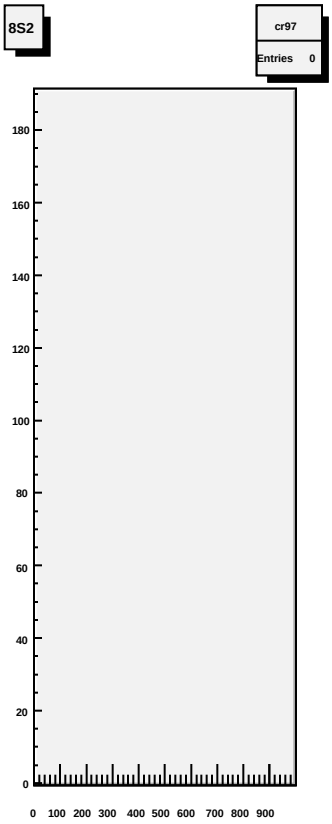

7S1
cr92
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

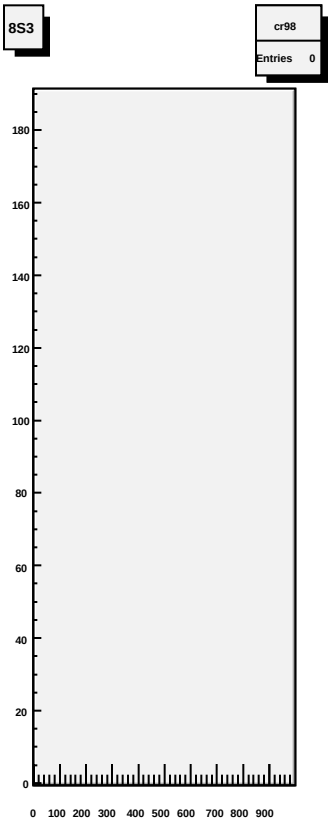

7S2
cr93
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

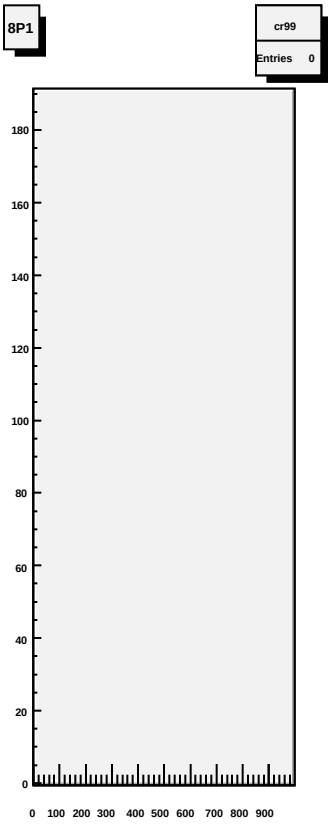

7S3
cr94
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

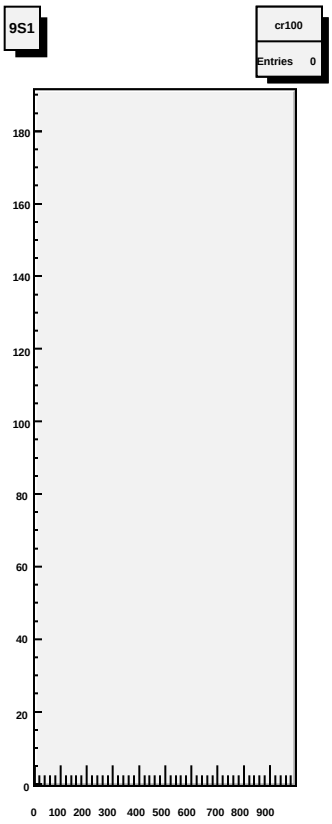

7P1
cr95
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

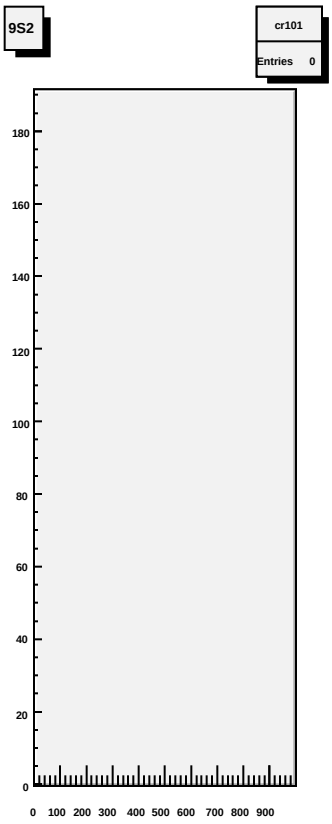

8S1
cr96
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

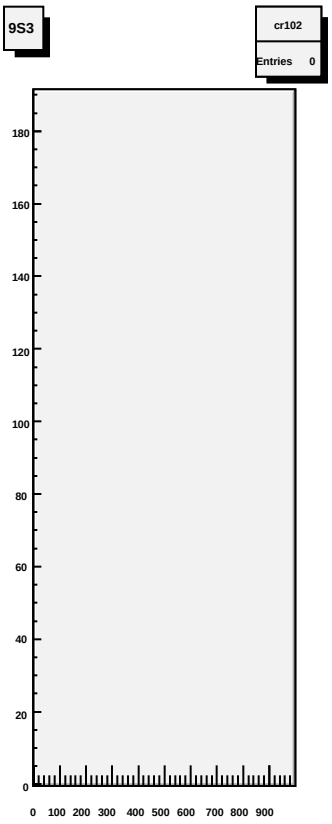

8S2
cr97
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

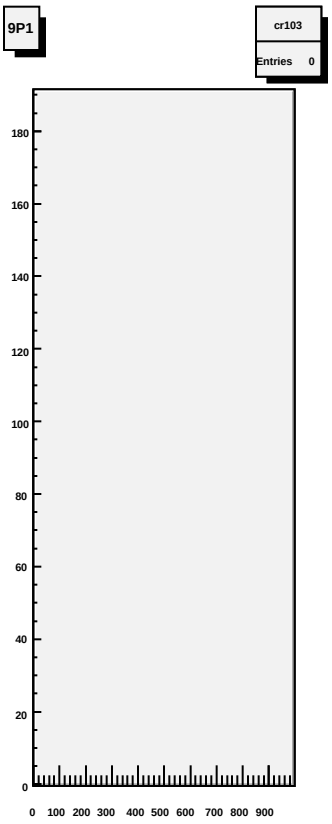

8S3
cr98
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

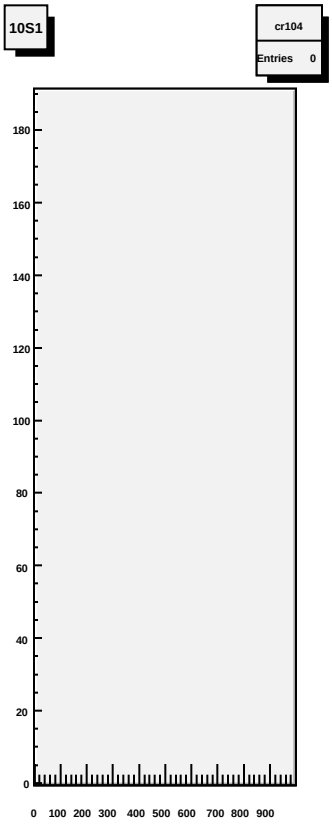

8P1
cr99
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

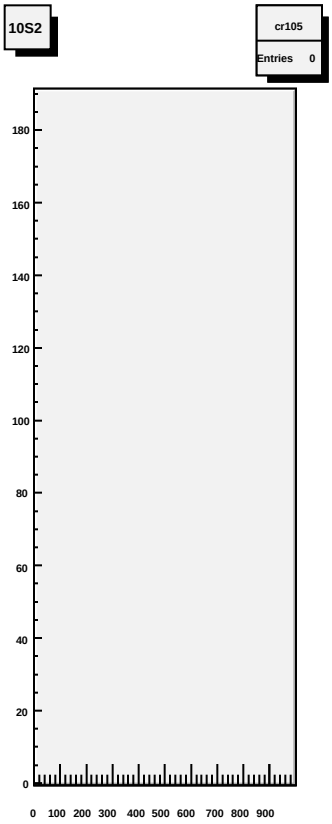

9S1
cr100
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900

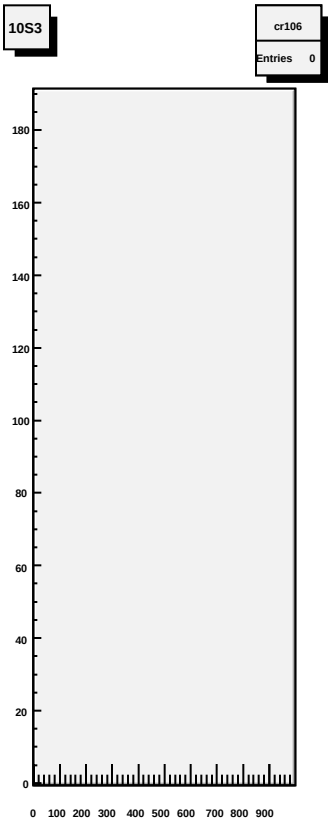

9S2
cr101
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900


9S3
cr102
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900


9P1
cr103
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900


10S1
cr104
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900


10S2
cr105
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900


10S3
cr106
Entries 0
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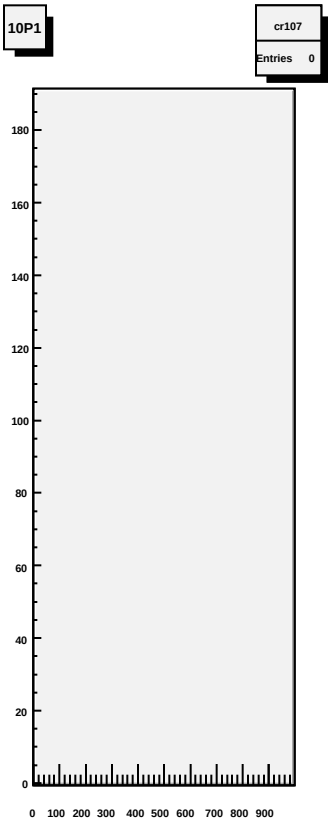
0 100 200 300 400 500 600 700 800 900


10P1
cr107
Entries 0
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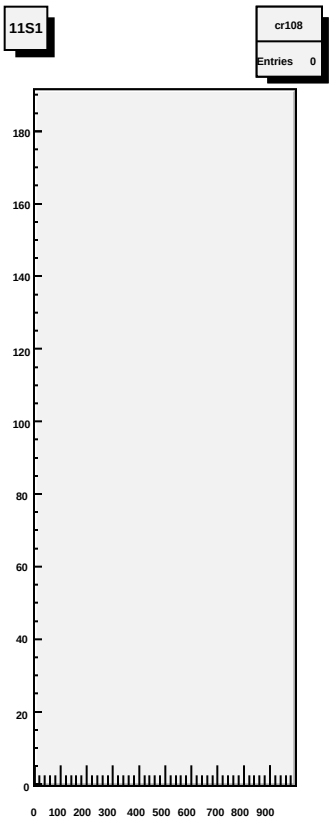
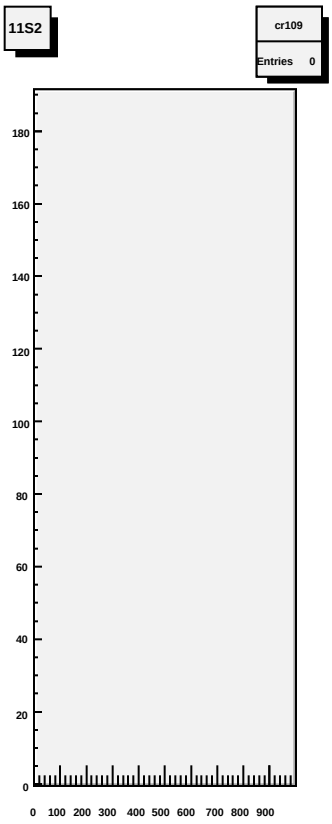
0 100 200 300 400 500 600 700 800 900


11S1
cr108
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900


11S2
cr109
Entries 0
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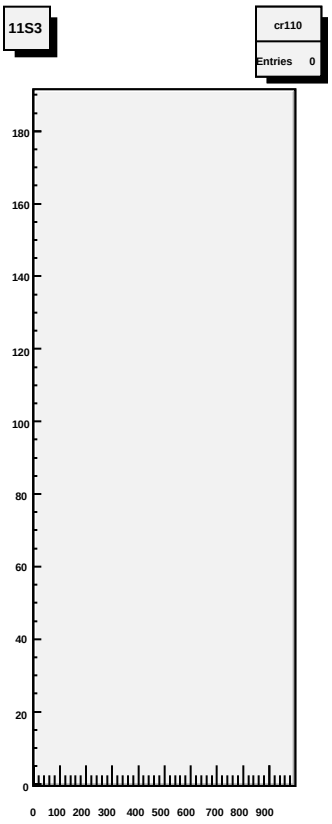
11S3
cr110
Entries 0
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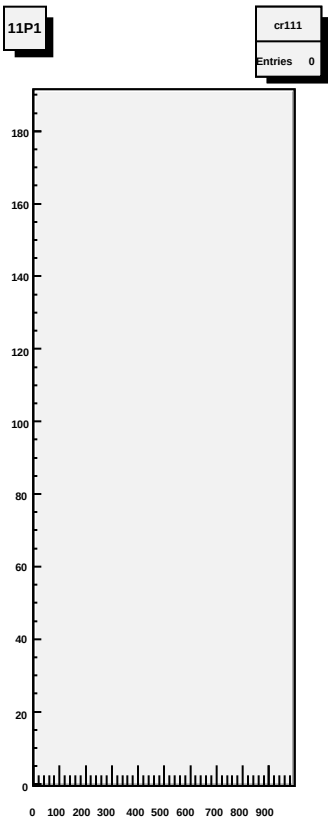
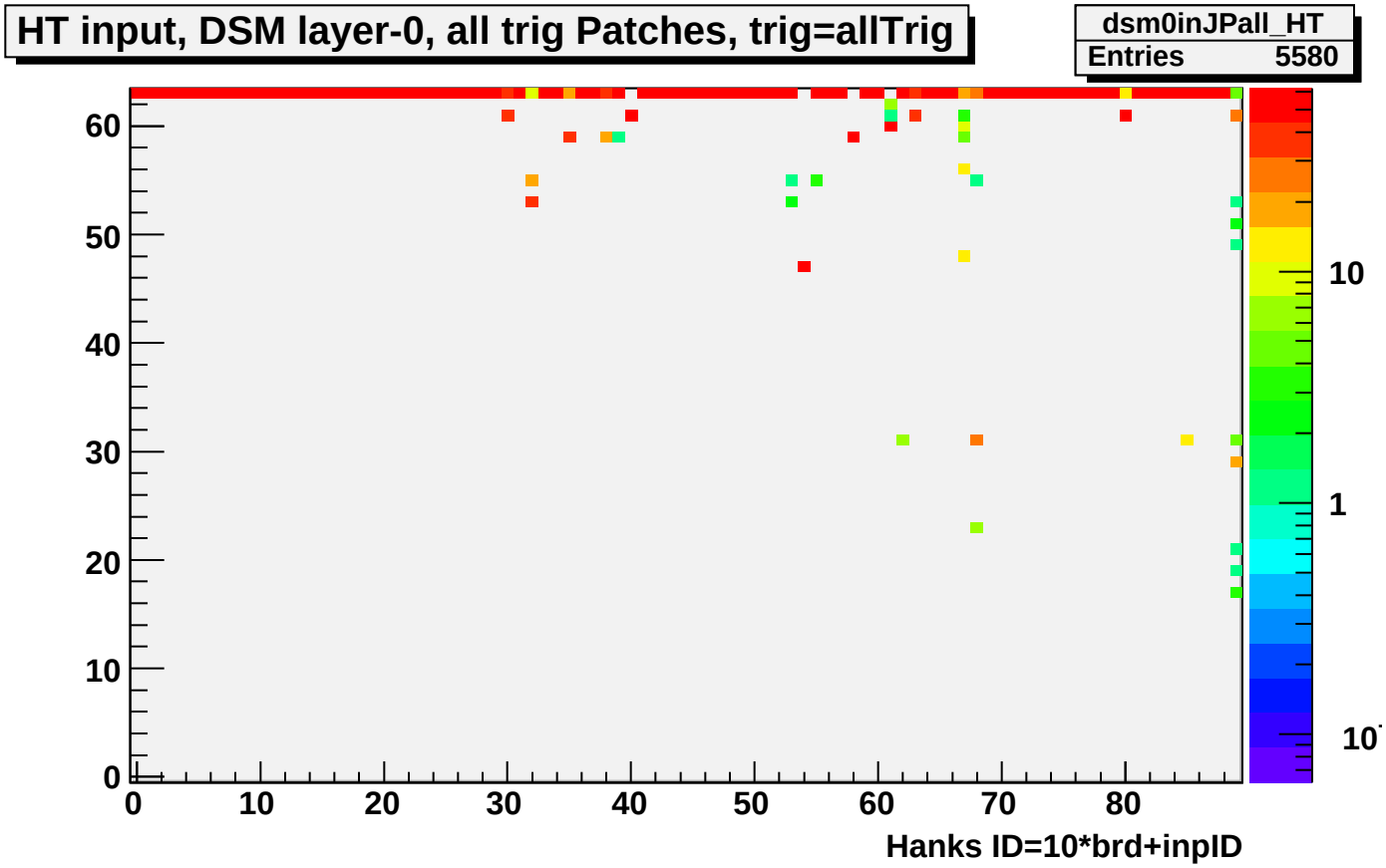
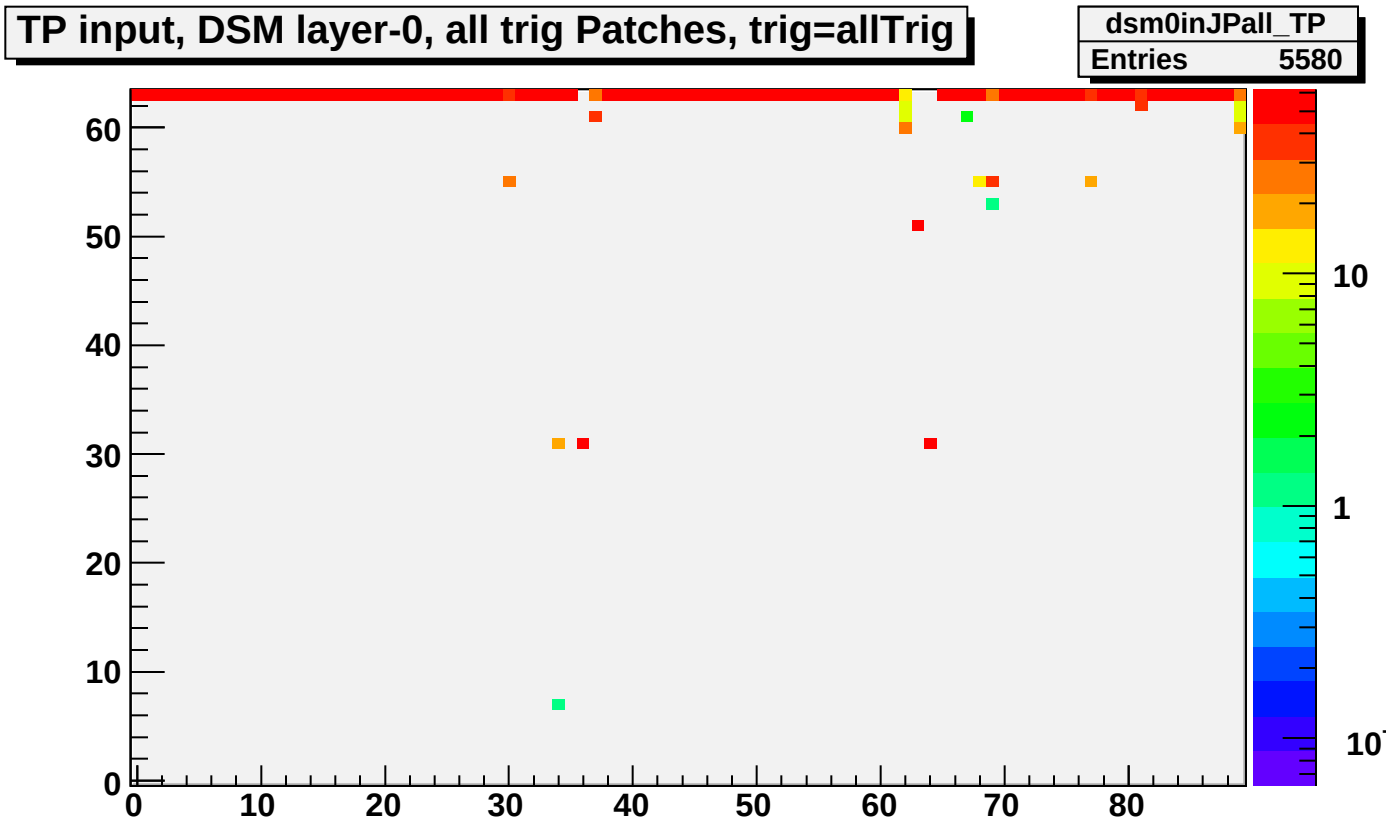
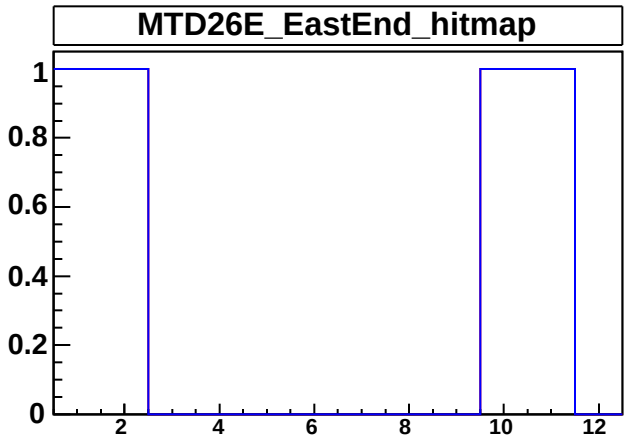
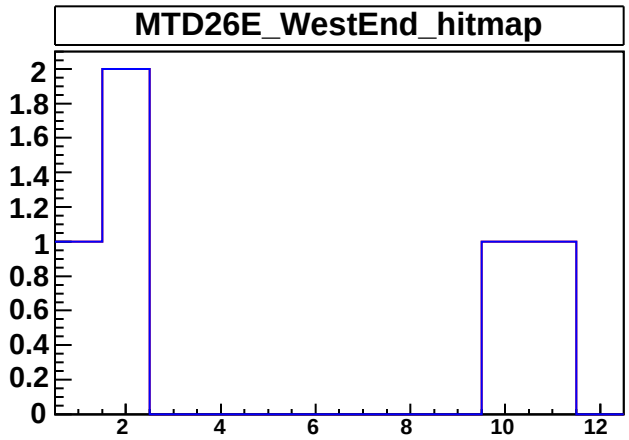
0 100 200 300 400 500 600 700 800 900


11P1
cr111
Entries 0
0 20 40 60 80 100 120 140 160 180
0 100 200 300 400 500 600 700 800 900


HT input, DSM layer-0, all trig Patches, trig=allTrig
dsm0inJPall_HT
Entries 5580
Hanks ID=10*brd+inpID


TP input, DSM layer-0, all trig Patches, trig=allTrig
dsm0inJPall_TP
Entries 5580


MTD26E_EastEnd_hitmap
0
0.2
0.4
0.6
0.8
1
2
4
6
8
10
12


MTD26E_WestEnd_hitmap
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
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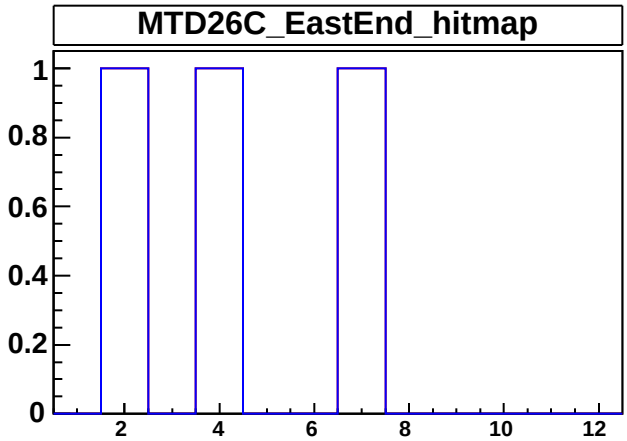
2
4
6
8
10
12


MTD26C_EastEnd_hitmap
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0.8
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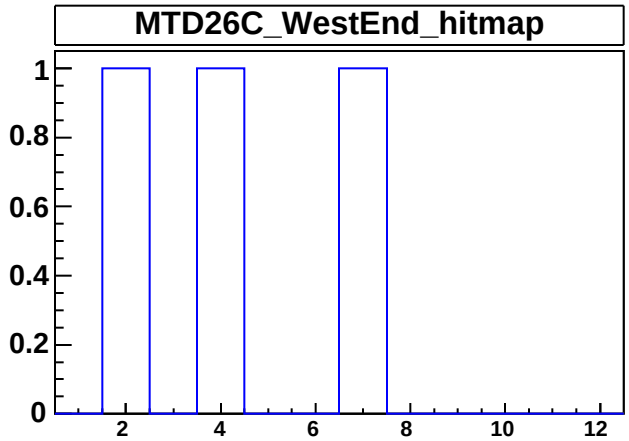
MTD26C_WestEnd_hitmap
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0.6
0.8
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2
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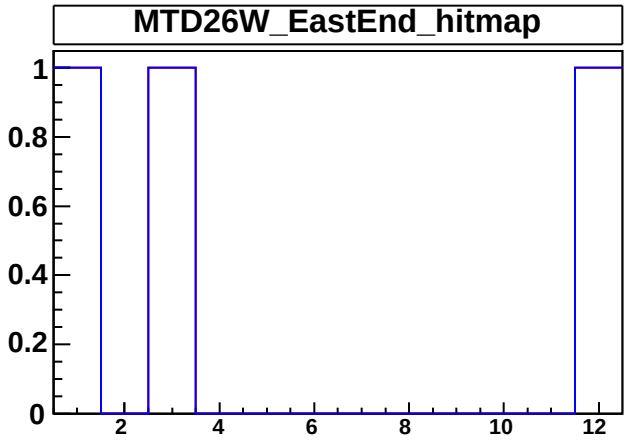
6
8
10
12


MTD26W_EastEnd_hitmap
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0.4
0.6
0.8
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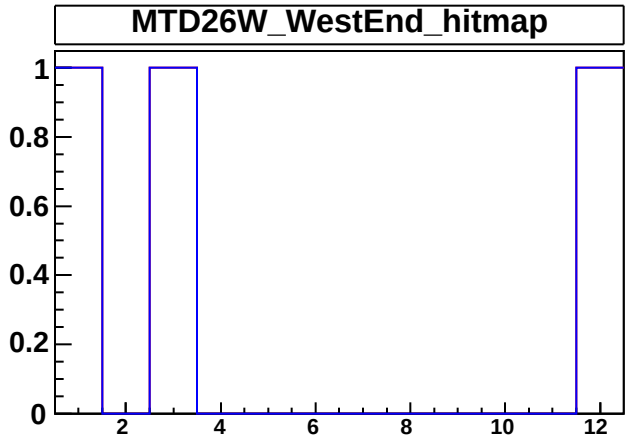
2
4
6
8
10
12


MTD26W_WestEnd_hitmap
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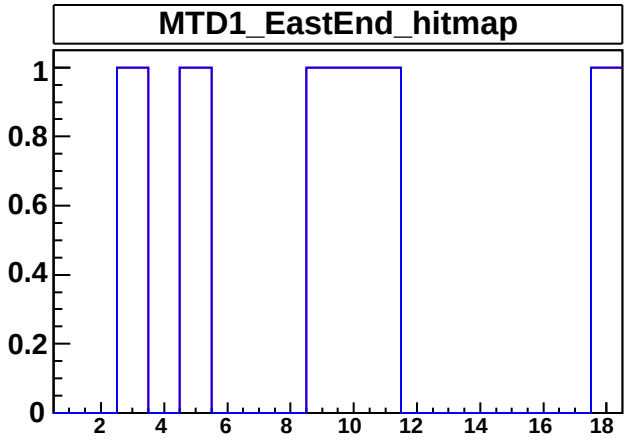
0.4
0.6
0.8
1
2
4
6
8
10
12


MTD1_EastEnd_hitmap
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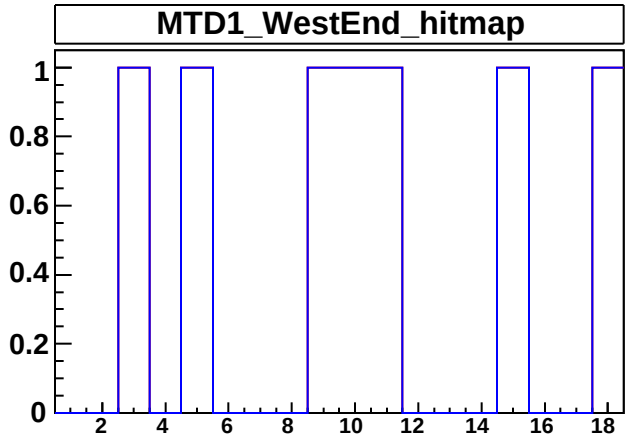
0.4
0.6
0.8
1
2
4
6
8
10
12
14
16
18


MTD1_WestEnd_hitmap
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0.8
1
2
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8
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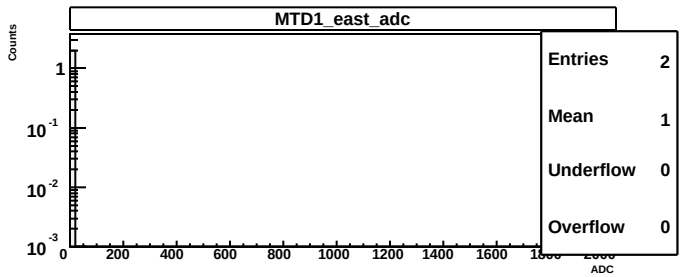
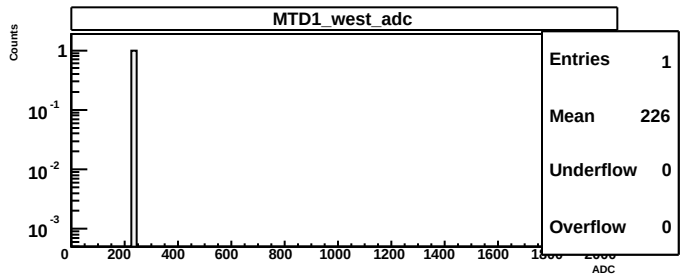
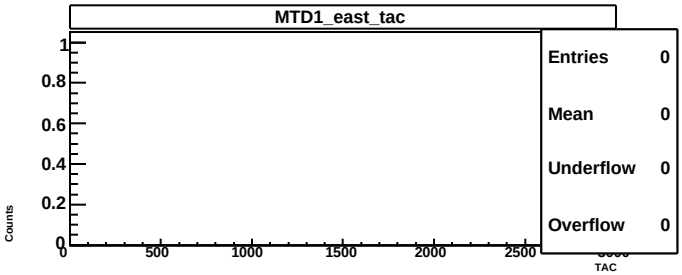
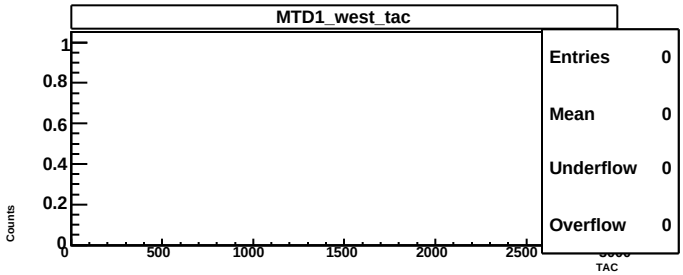
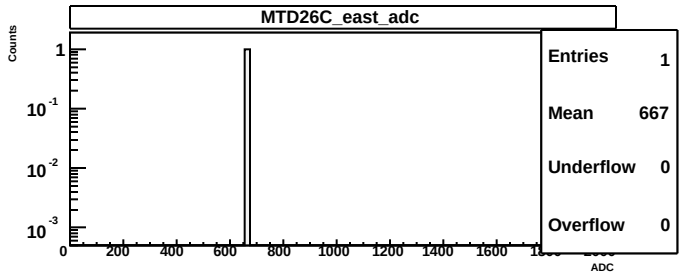
14
16
18

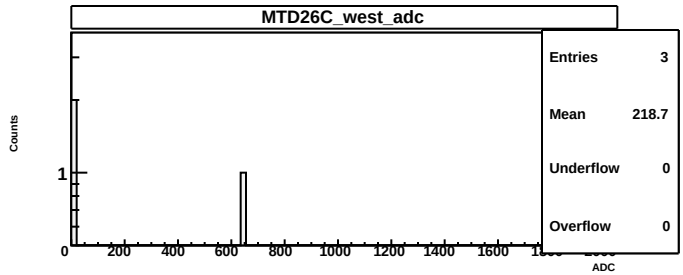

MTD1_east_adc
Entries 2
Mean 1
Underflow 0
Overflow 0
Counts
ADC

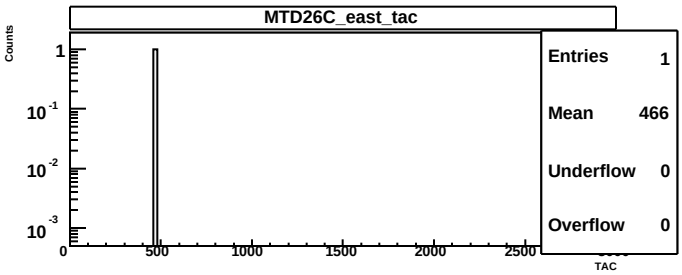

MTD1_west_adc
Entries 1
Mean 226
Underflow 0
Overflow 0
Counts
ADC

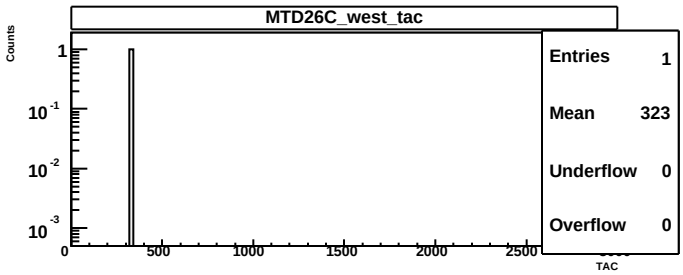

MTD1_east_tac
Entries 0
Mean 0
Underflow 0
Overflow 0
Counts
TAC

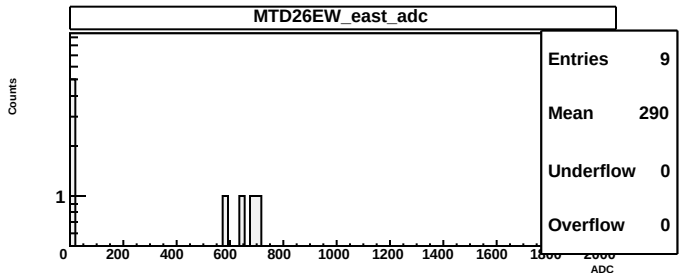

MTD1_west_tac
Entries 0
Mean 0
Underflow 0
Overflow 0
Counts
TAC

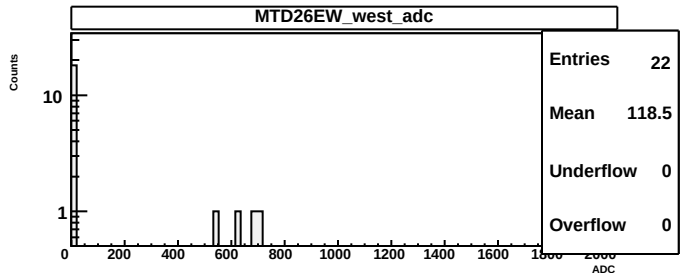

MTD26C_east_adc
Entries 1
Mean 667
Underflow 0
Overflow 0
Counts
ADC

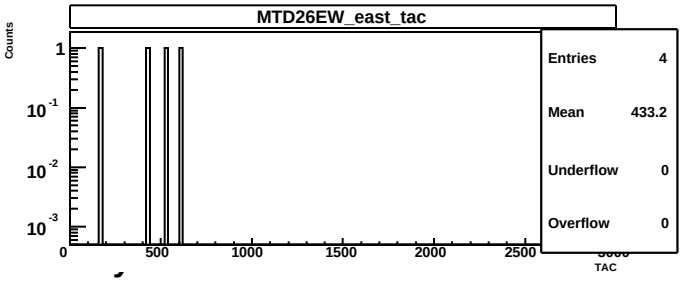

MTD26C_west_adc
Entries 3
Mean 218.7
Underflow 0
Overflow 0
Counts
ADC

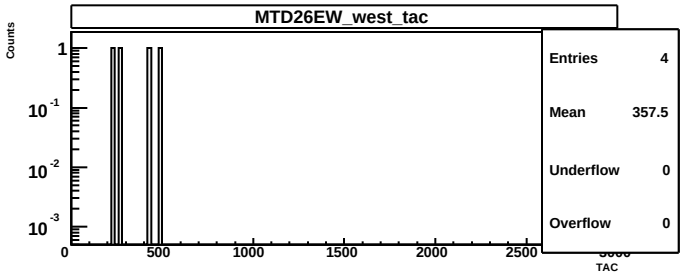

MTD26C_east_tac
Entries 1
Mean 466
Underflow 0
Overflow 0
Counts
TAC

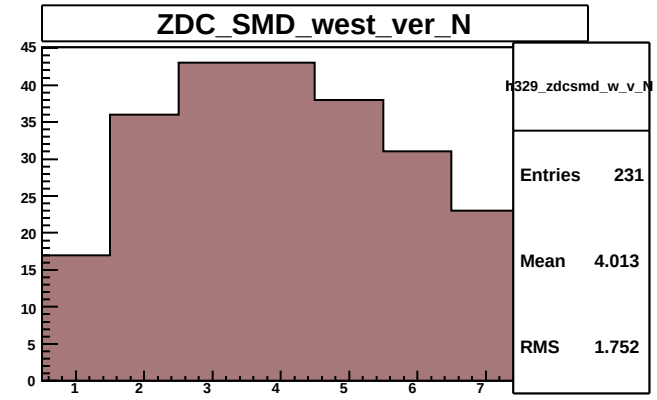

MTD26C_west_tac
Entries 1
Mean 323
Underflow 0
Overflow 0
Counts
TAC

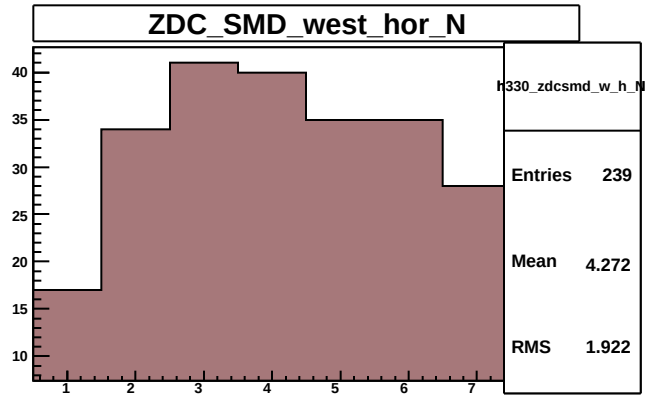

MTD26EW_east_adc
Entries 9
Mean 290
Underflow 0
Overflow 0
Counts
ADC

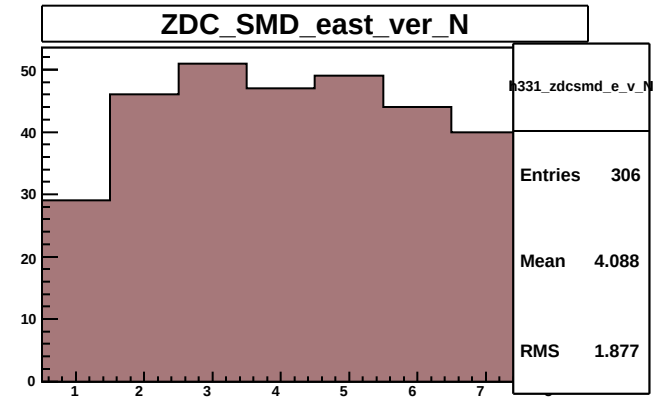

MTD26EW_west_adc
Entries 22
Mean 118.5
Underflow 0
Overflow 0
Counts
ADC

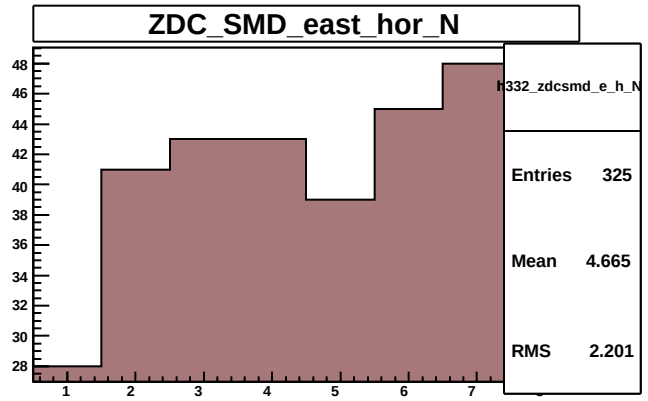

MTD26EW_east_tac
Entries 4
Mean 433.2
Underflow 0
Overflow 0
Counts
TAC

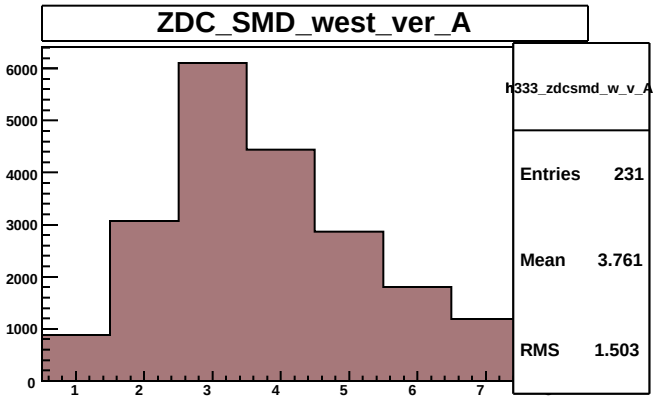

MTD26EW_west_tac
Entries 4
Mean 357.5
Underflow 0
Overflow 0
Counts
TAC

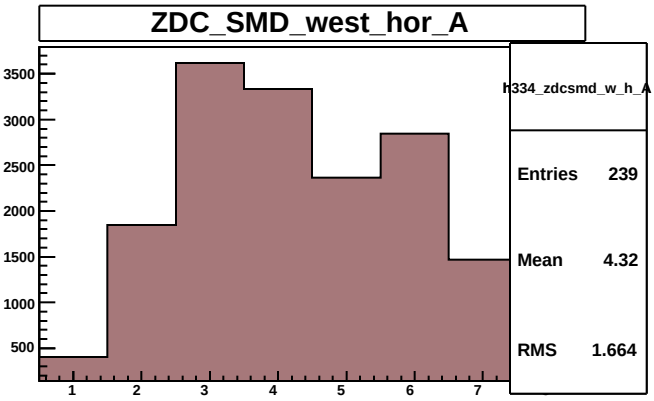

ZDC_SMD_west_ver_N
h329_zdcsmd_w_v_N
Entries 231
Mean 4.013
RMS 1.752

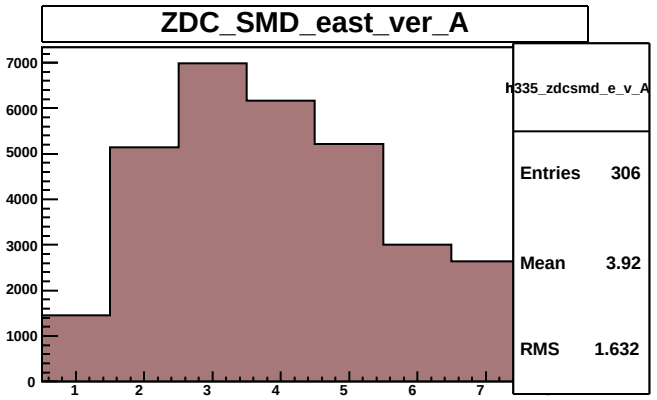

ZDC_SMD_west_hor_N
h330_zdcsmd_w_h_N
Entries 239
Mean 4.272
RMS 1.922

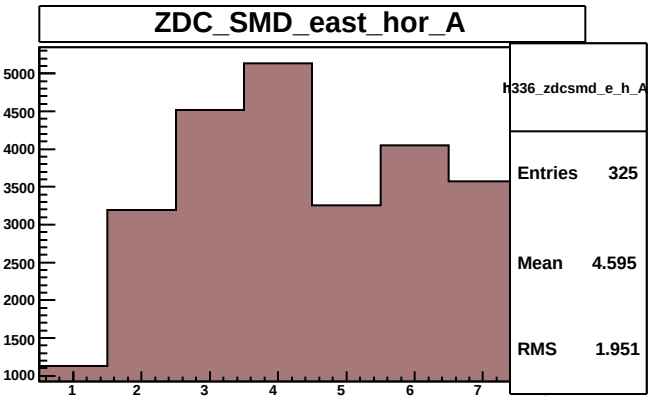

ZDC_SMD_east_ver_N
h331_zdcsmd_e_v_N
Entries 306
Mean 4.088
RMS 1.877

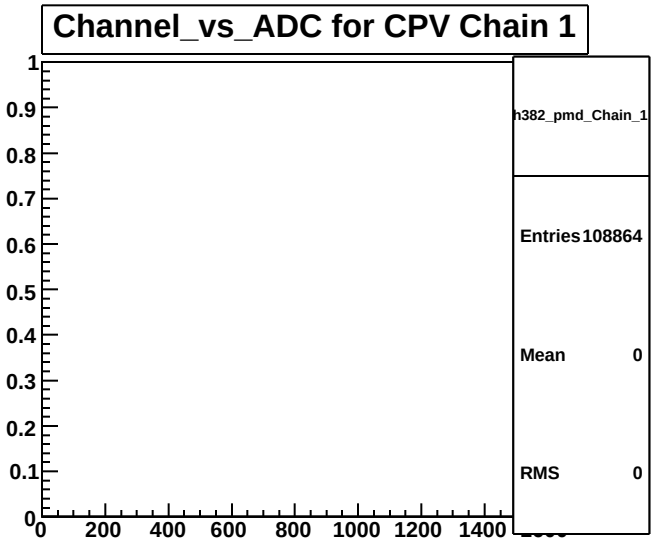

ZDC_SMD_east_hor_N
h332_zdcsmd_e_h_N
Entries 325
Mean 4.665
RMS 2.201

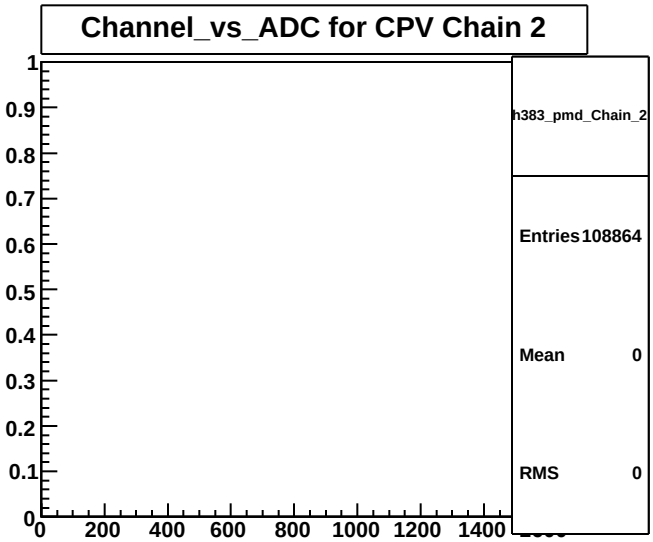

ZDC_SMD_west_ver_A
h333_zdcsmd_w_v_A
Entries 231
Mean 3.761
RMS 1.503

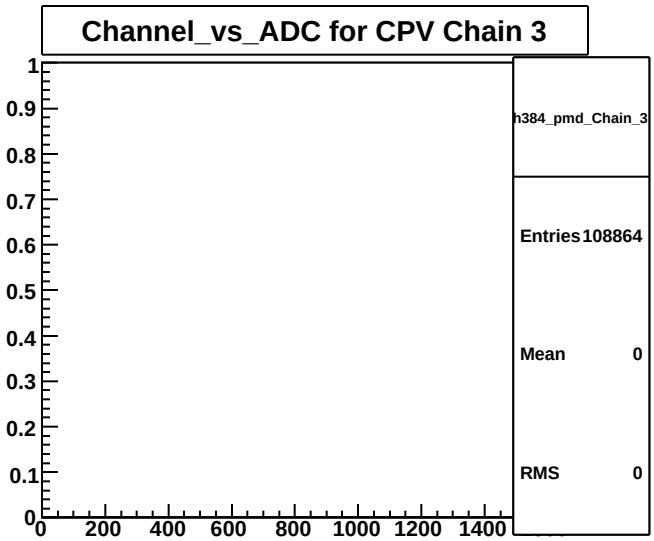

ZDC_SMD_west_hor_A
h334_zdcsmd_w_h_A
Entries 239
Mean 4.32
RMS 1.664

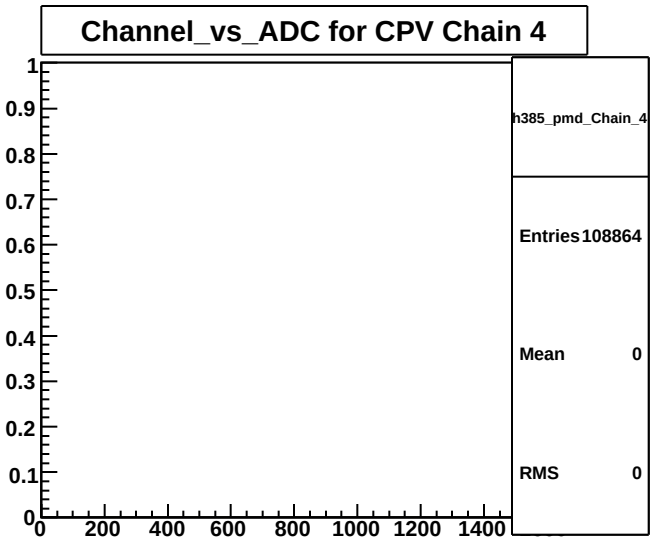

ZDC_SMD_east_ver_A
h335_zdcsmd_e_v_A
Entries 306
Mean 3.92
RMS 1.632

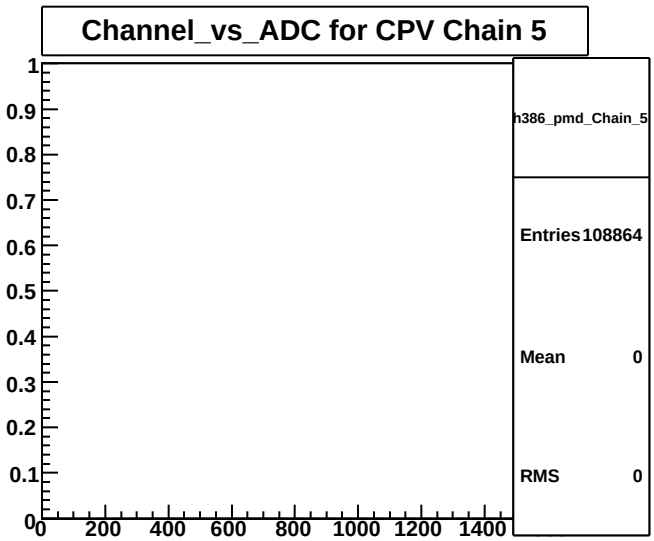

ZDC_SMD_east_hor_A
h336_zdcsmd_e_h_A
Entries 325
Mean 4.595
RMS 1.951

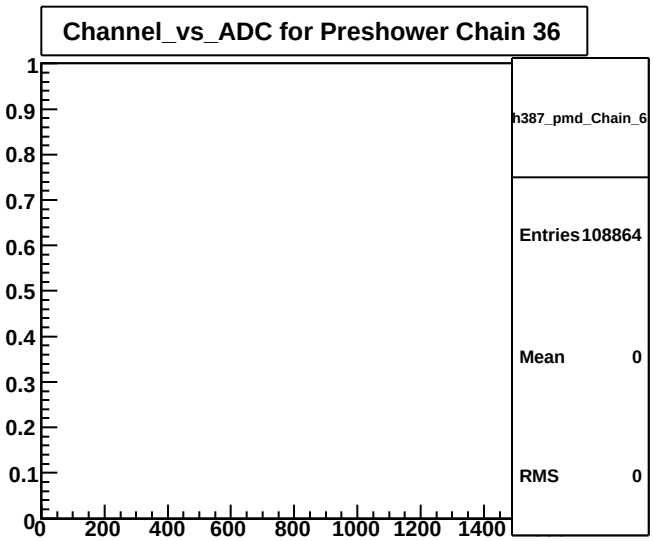

Channel_vs_ADC for CPV Chain 1
h382_pmd_Chain_1
Entries 108864
Mean 0
RMS 0

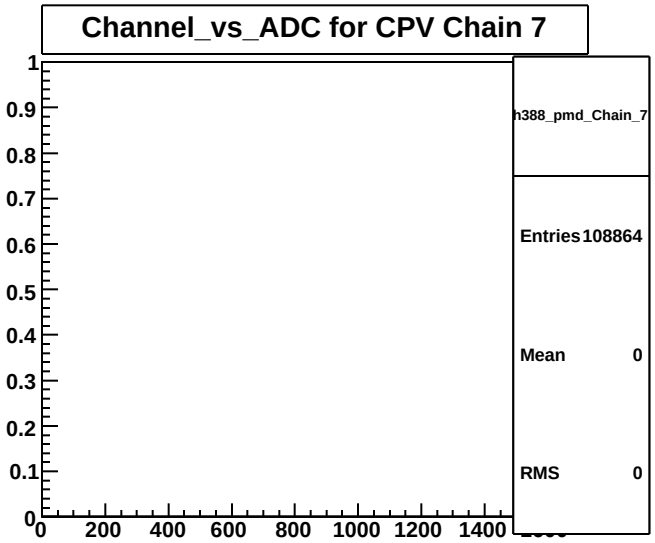

Channel_vs_ADC for CPV Chain 2
h383_pmd_Chain_2
Entries 108864
Mean 0
RMS 0

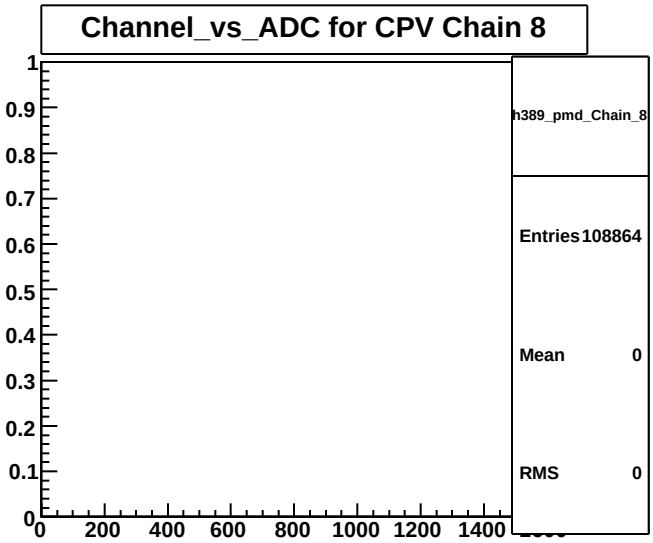

Channel_vs_ADC for CPV Chain 3
h384_pmd_Chain_3
Entries 108864
Mean 0
RMS 0

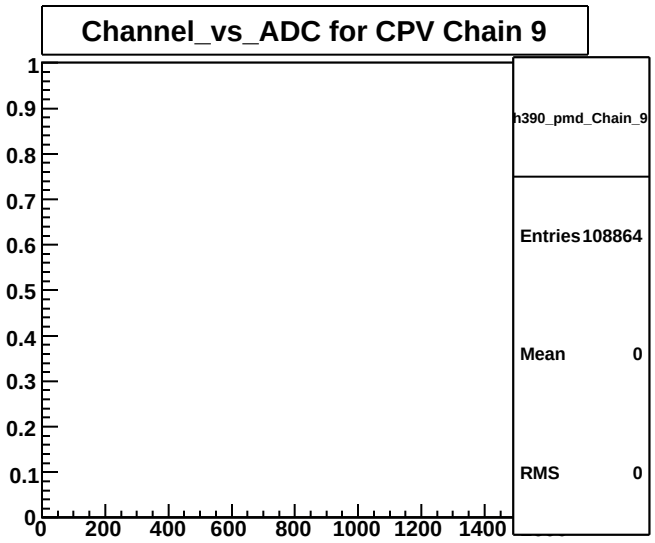

Channel_vs_ADC for CPV Chain 4
h385_pmd_Chain_4
Entries 108864
Mean 0
RMS 0

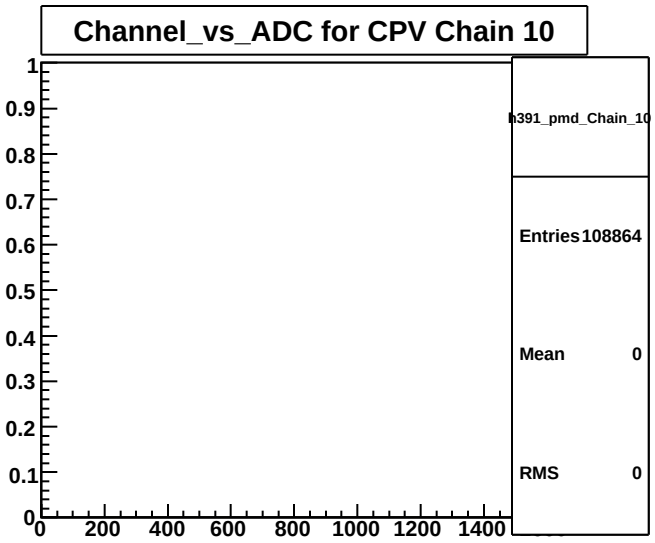

Channel_vs_ADC for CPV Chain 5
h386_pmd_Chain_5
Entries 108864
Mean 0
RMS 0

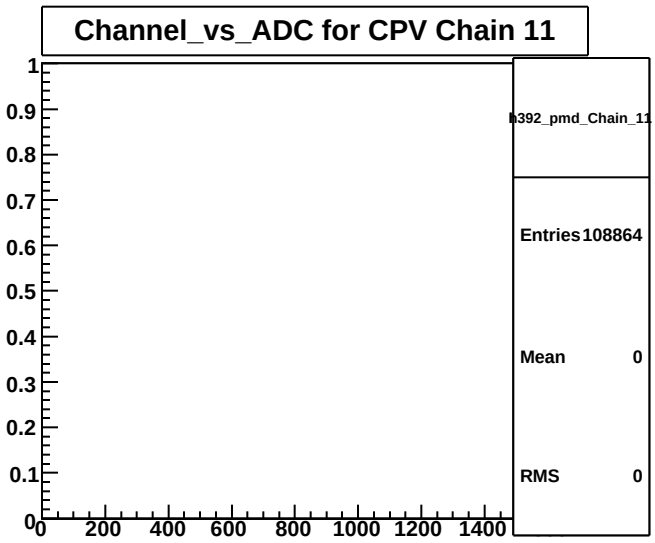

Channel_vs_ADC for Preshower Chain 36
h387_pmd_Chain_6
Entries 108864
Mean 0
RMS 0

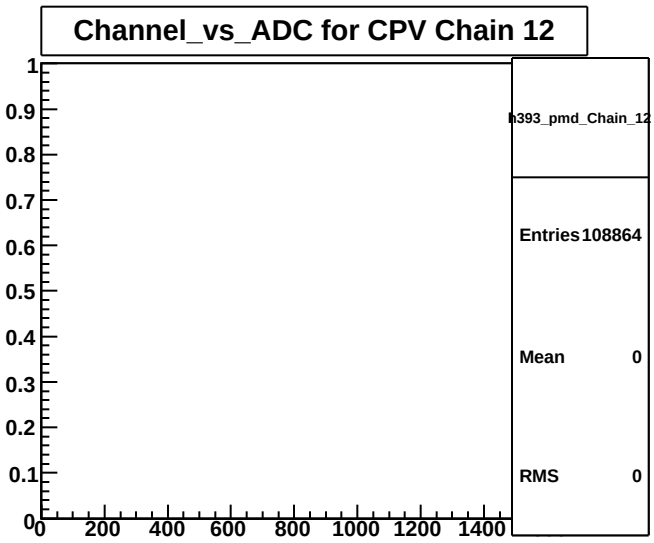

Channel_vs_ADC for CPV Chain 7
h388_pmd_Chain_7
Entries 108864
Mean 0
RMS 0

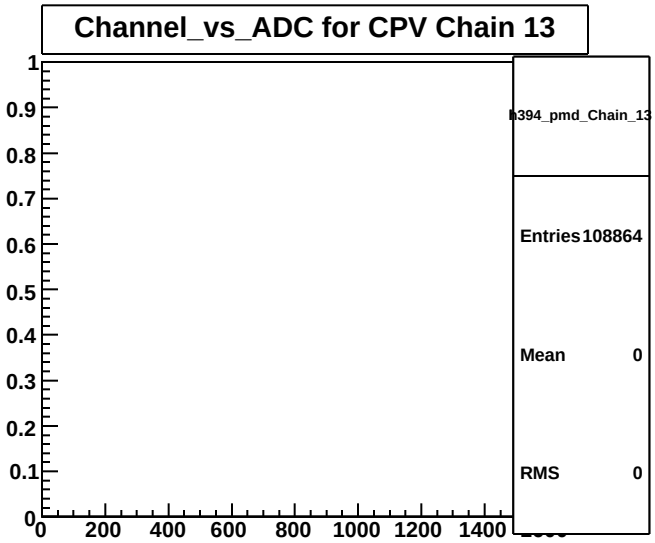

Channel_vs_ADC for CPV Chain 8
h389_pmd_Chain_8
Entries 108864
Mean 0
RMS 0

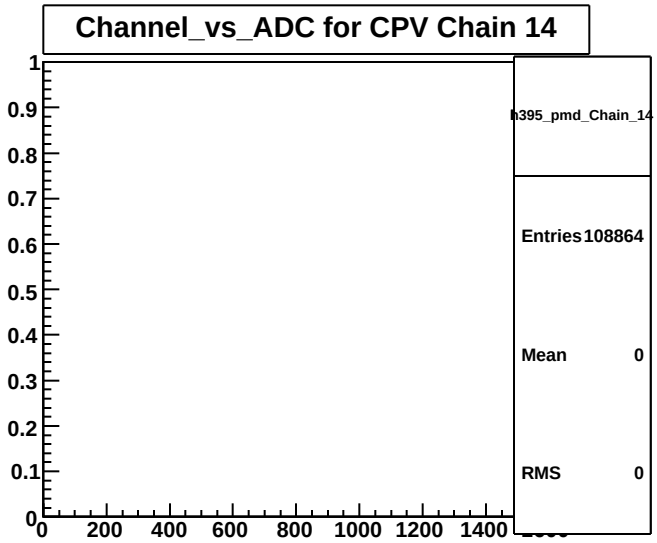

Channel_vs_ADC for CPV Chain 9
h390_pmd_Chain_9
Entries 108864
Mean 0
RMS 0

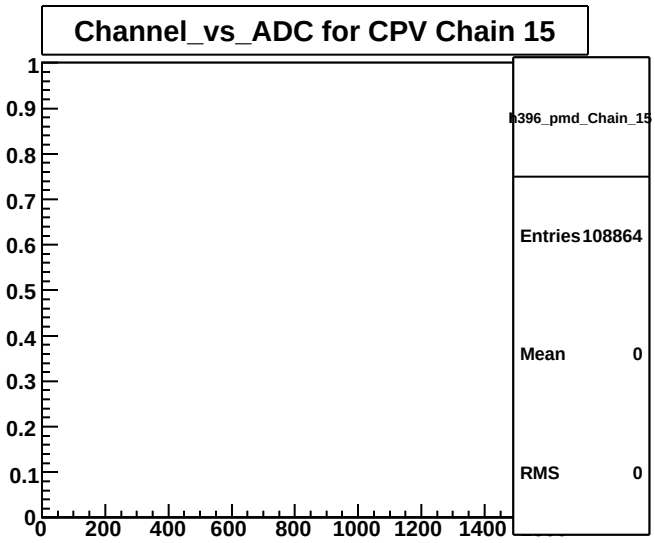

Channel_vs_ADC for CPV Chain 10
h391_pmd_Chain_10
Entries 108864
Mean 0
RMS 0

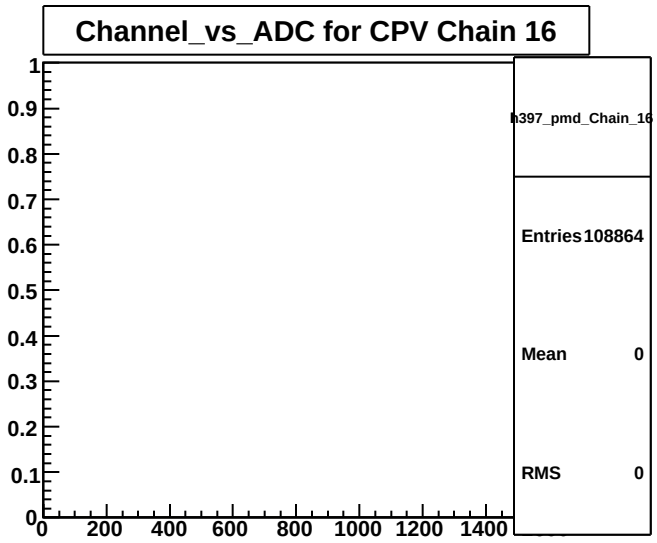

Channel_vs_ADC for CPV Chain 11
h392_pmd_Chain_11
Entries 108864
Mean 0
RMS 0

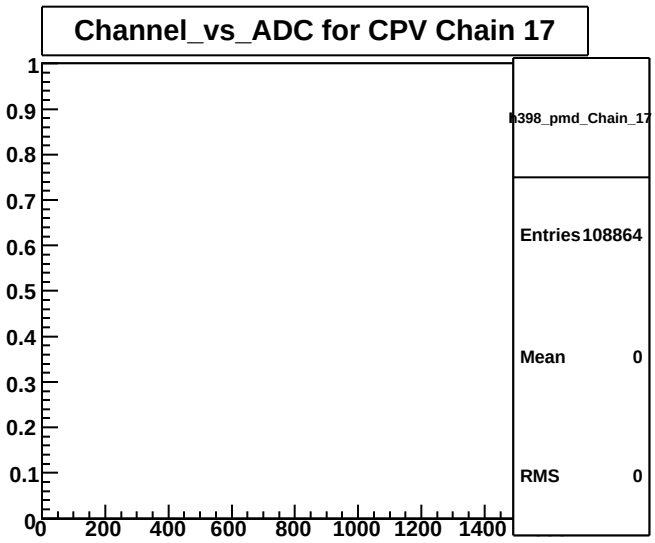

Channel_vs_ADC for CPV Chain 12
h393_pmd_Chain_12
Entries 108864
Mean 0
RMS 0

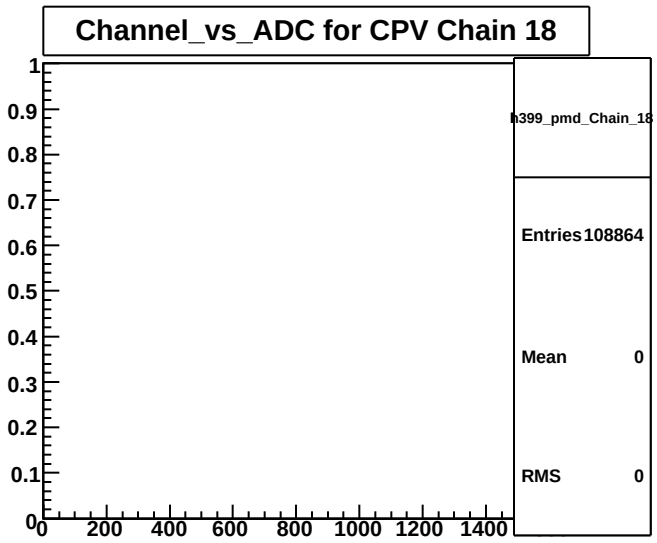

Channel_vs_ADC for CPV Chain 13
h394_pmd_Chain_13
Entries 108864
Mean 0
RMS 0

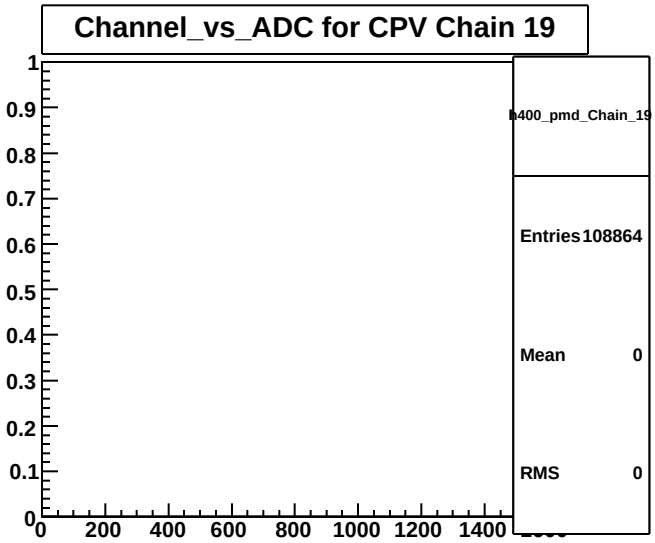

Channel_vs_ADC for CPV Chain 14
h395_pmd_Chain_14
Entries 108864
Mean 0
RMS 0

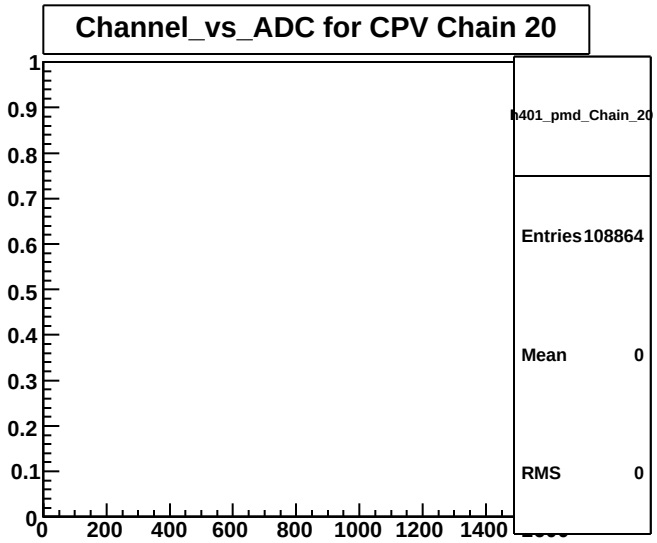

Channel_vs_ADC for CPV Chain 15
h396_pmd_Chain_15
Entries 108864
Mean 0
RMS 0

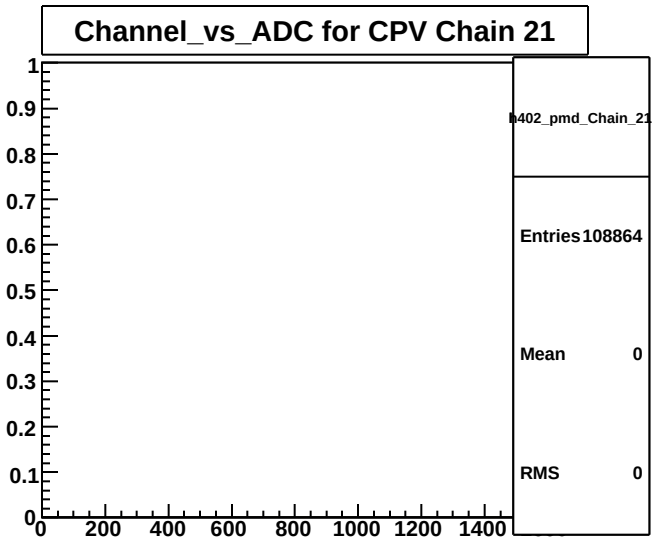

Channel_vs_ADC for CPV Chain 16
h397_pmd_Chain_16
Entries 108864
Mean 0
RMS 0

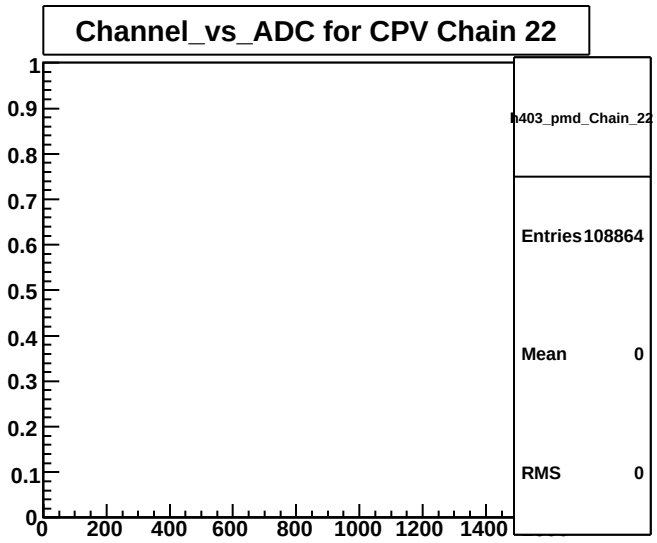

Channel_vs_ADC for CPV Chain 17
h398_pmd_Chain_17
Entries 108864
Mean 0
RMS 0

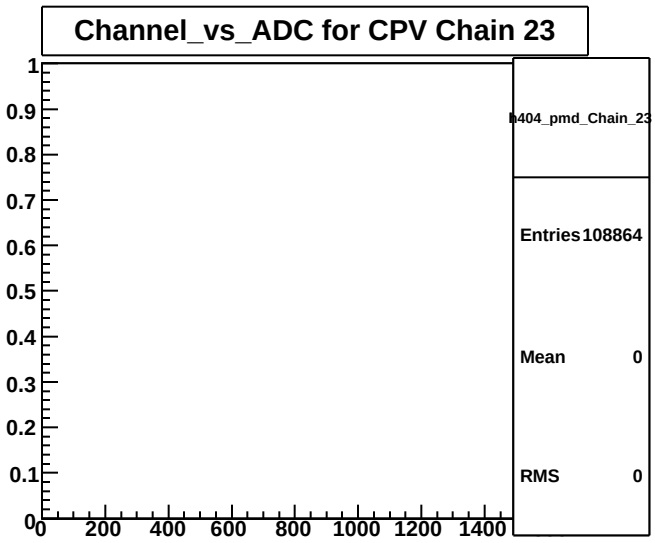

Channel_vs_ADC for CPV Chain 18
h399_pmd_Chain_18
Entries 108864
Mean 0
RMS 0

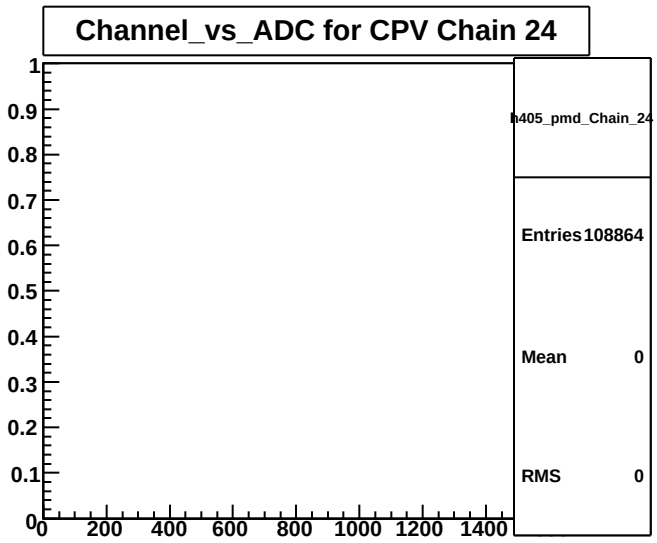

Channel_vs_ADC for CPV Chain 19
h400_pmd_Chain_19
Entries 108864
Mean 0
RMS 0

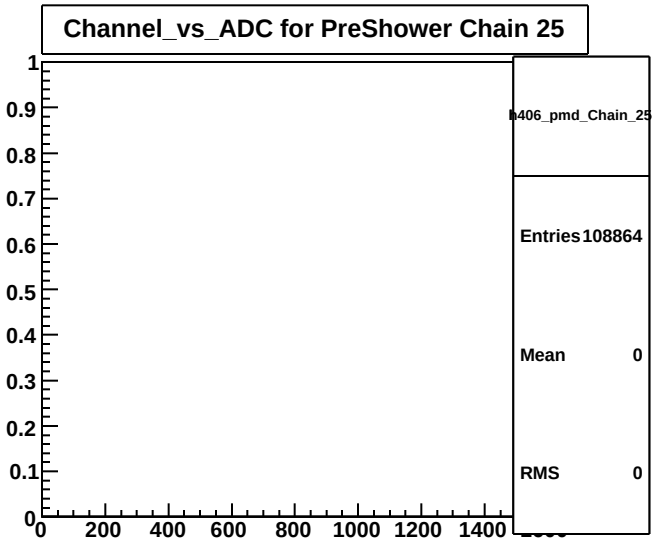

Channel_vs_ADC for CPV Chain 20
h401_pmd_Chain_20
Entries 108864
Mean 0
RMS 0

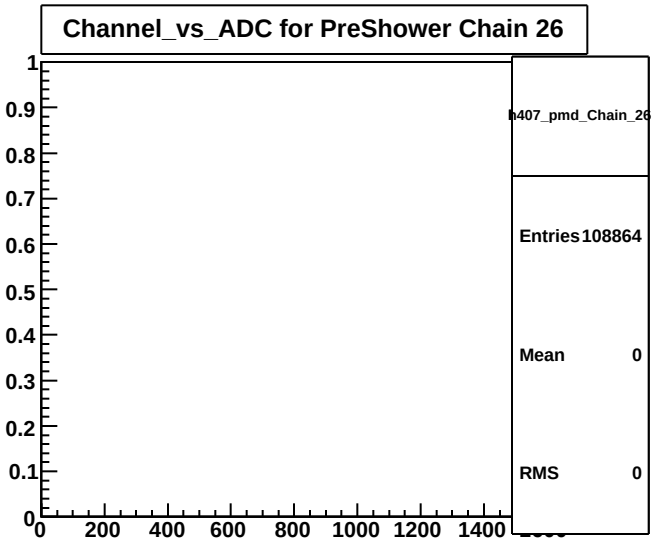

Channel_vs_ADC for CPV Chain 21
h402_pmd_Chain_21
Entries 108864
Mean 0
RMS 0

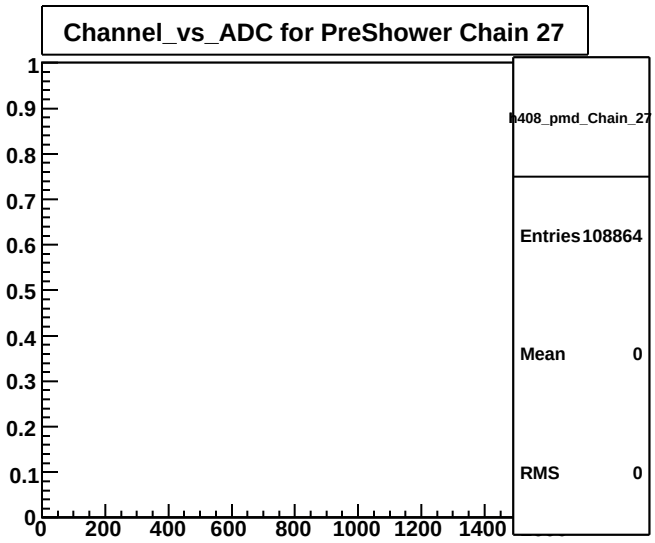

Channel_vs_ADC for CPV Chain 22
h403_pmd_Chain_22
Entries 108864
Mean 0
RMS 0

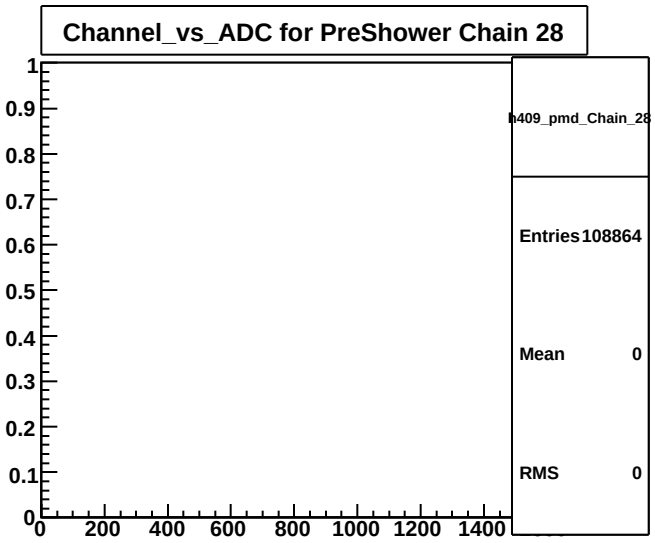

Channel_vs_ADC for CPV Chain 23
h404_pmd_Chain_23
Entries 108864
Mean 0
RMS 0

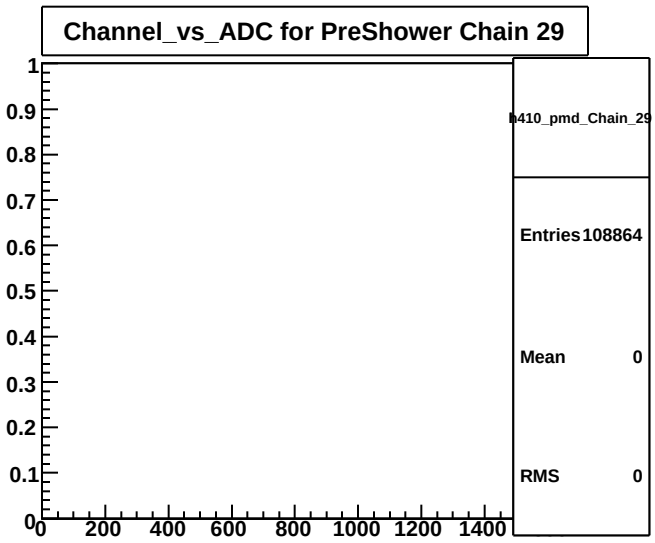

Channel_vs_ADC for CPV Chain 24
h405_pmd_Chain_24
Entries 108864
Mean 0
RMS 0

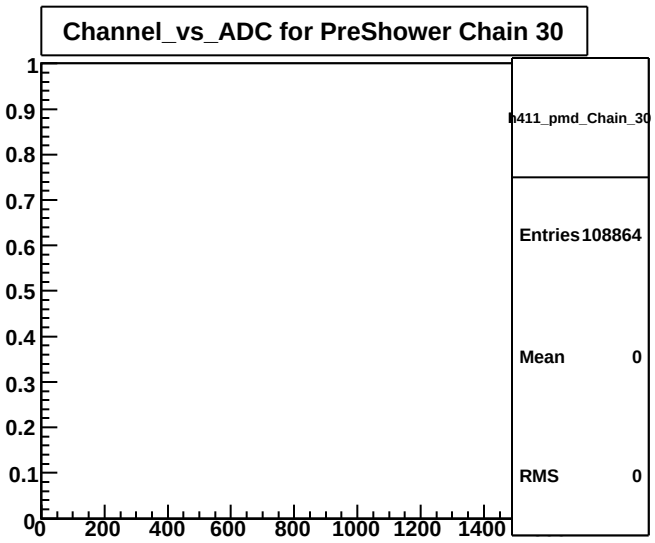

Channel_vs_ADC for PreShower Chain 25
h406_pmd_Chain_25
Entries 108864
Mean 0
RMS 0

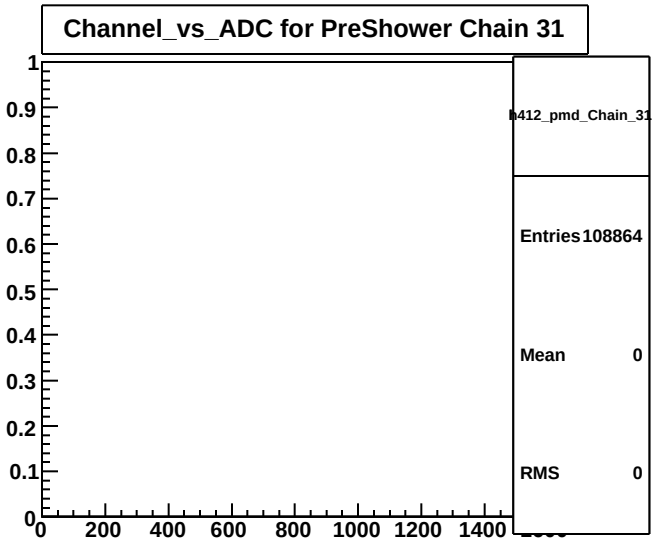

Channel_vs_ADC for PreShower Chain 26
h407_pmd_Chain_26
Entries 108864
Mean 0
RMS 0

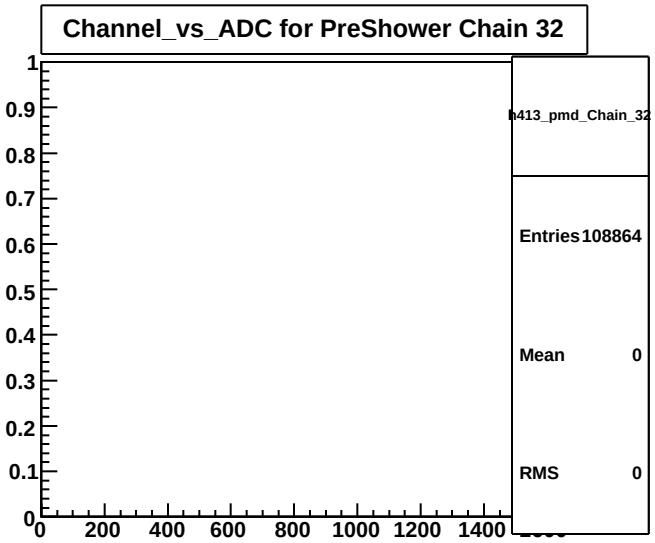

Channel_vs_ADC for PreShower Chain 27
h408_pmd_Chain_27
Entries 108864
Mean 0
RMS 0

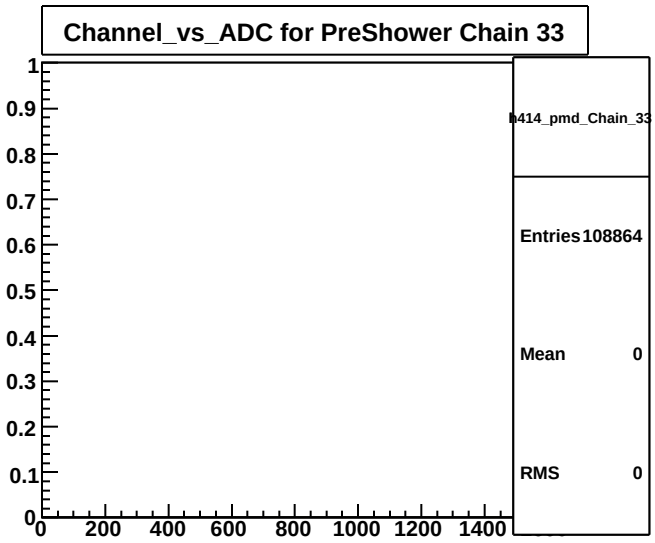

Channel_vs_ADC for PreShower Chain 28
h409_pmd_Chain_28
Entries 108864
Mean 0
RMS 0

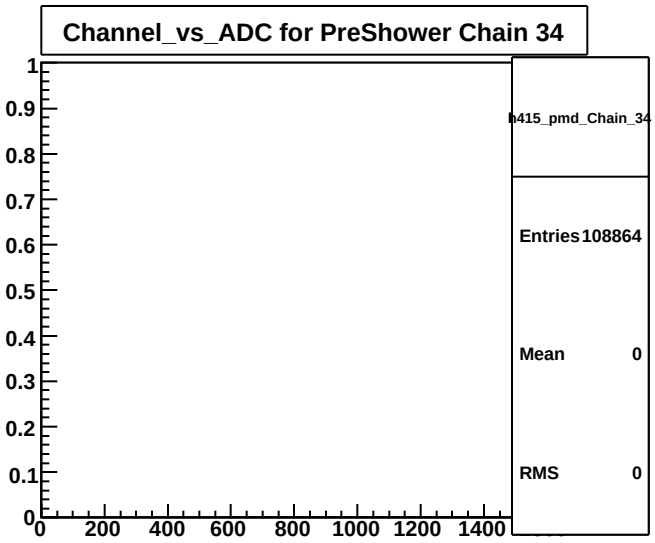

Channel_vs_ADC for PreShower Chain 29
h410_pmd_Chain_29
Entries 108864
Mean 0
RMS 0

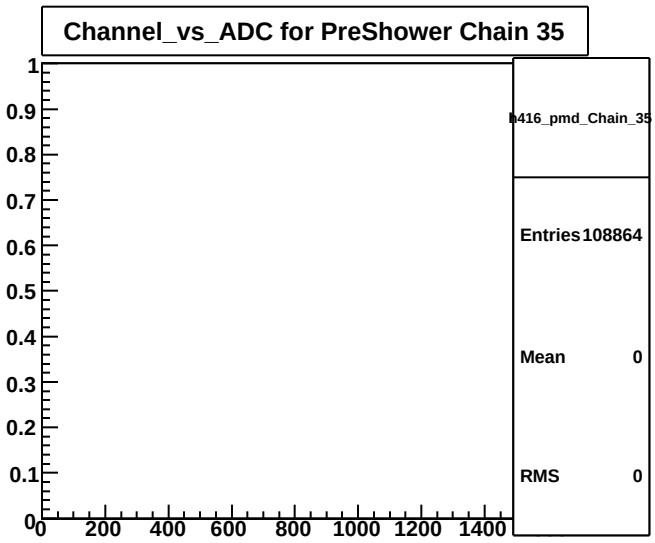

Channel_vs_ADC for PreShower Chain 30
h411_pmd_Chain_30
Entries 108864
Mean 0
RMS 0

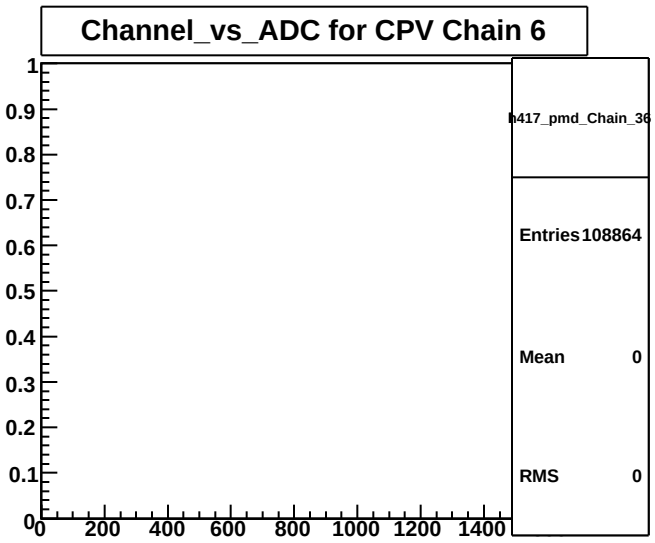

Channel_vs_ADC for PreShower Chain 31
h412_pmd_Chain_31
Entries 108864
Mean 0
RMS 0

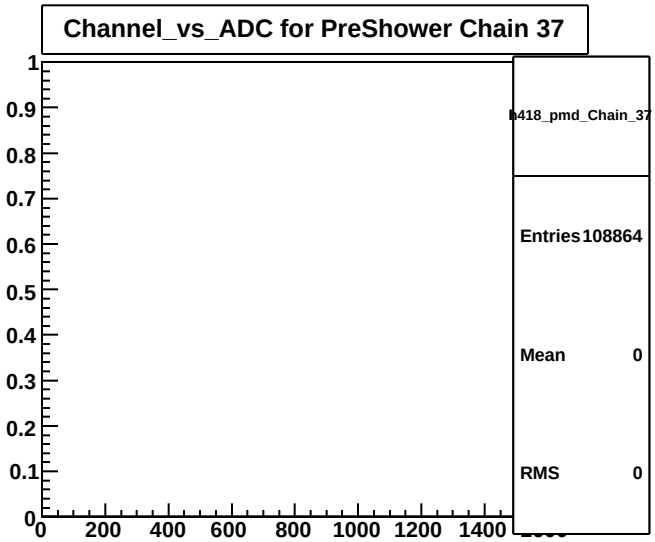

Channel_vs_ADC for PreShower Chain 32
h413_pmd_Chain_32
Entries 108864
Mean 0
RMS 0

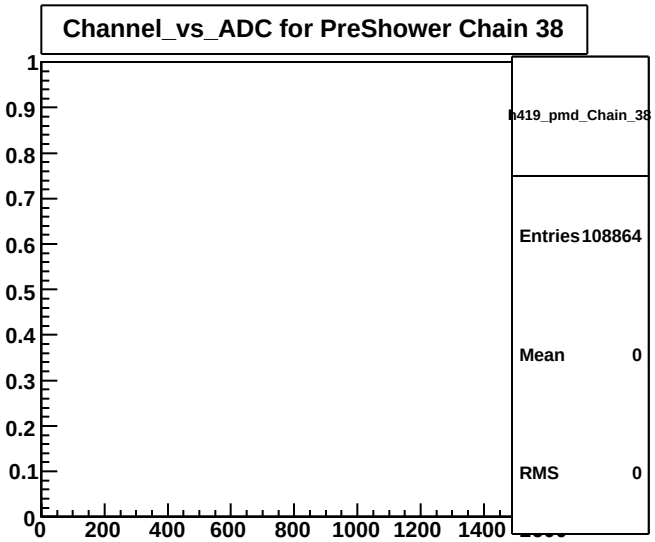

Channel_vs_ADC for PreShower Chain 33
h414_pmd_Chain_33
Entries 108864
Mean 0
RMS 0

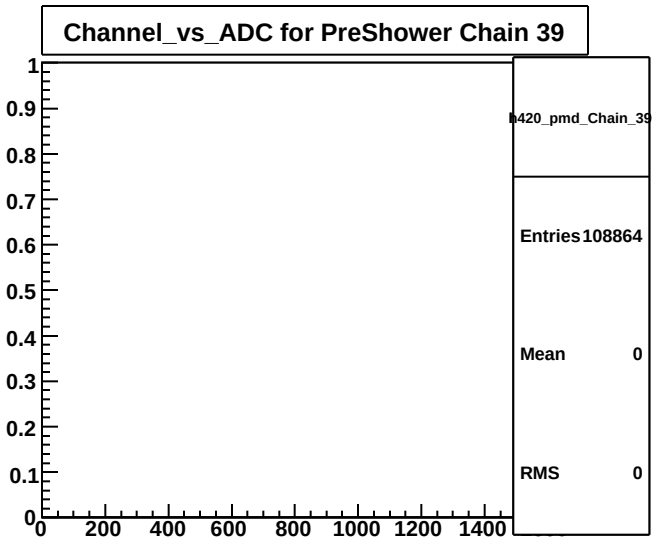

Channel_vs_ADC for PreShower Chain 34
h415_pmd_Chain_34
Entries 108864
Mean 0
RMS 0

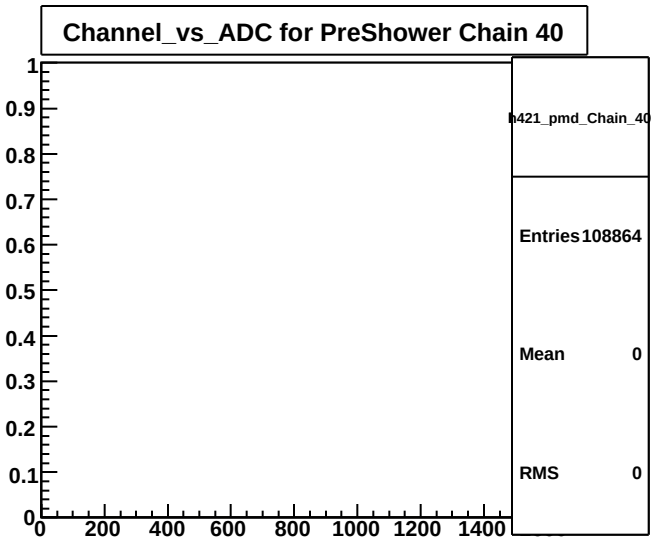

Channel_vs_ADC for PreShower Chain 35
h416_pmd_Chain_35
Entries 108864
Mean 0
RMS 0

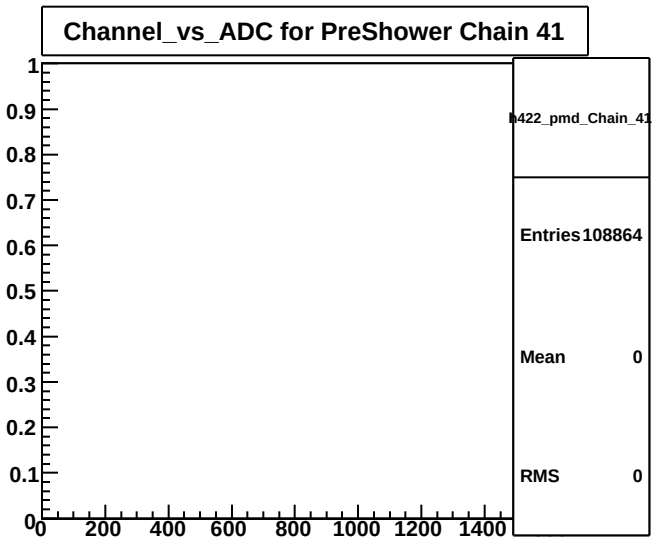

Channel_vs_ADC for CPV Chain 6
h417_pmd_Chain_36
Entries 108864
Mean 0
RMS 0

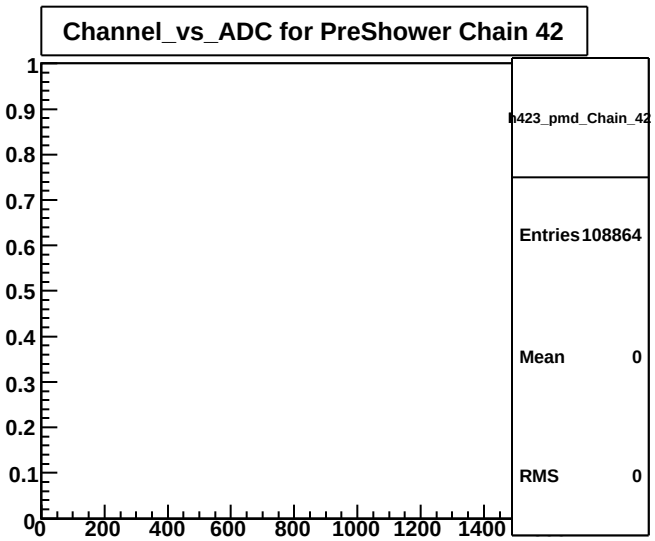

Channel_vs_ADC for PreShower Chain 37
h418_pmd_Chain_37
Entries 108864
Mean 0
RMS 0

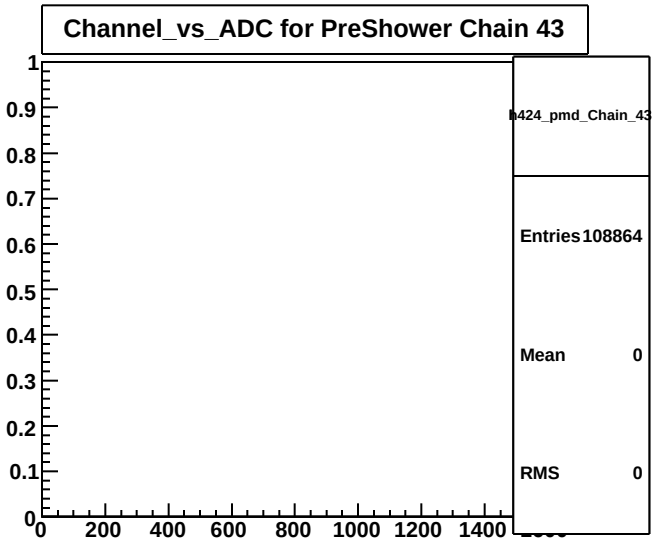

Channel_vs_ADC for PreShower Chain 38
h419_pmd_Chain_38
Entries 108864
Mean 0
RMS 0

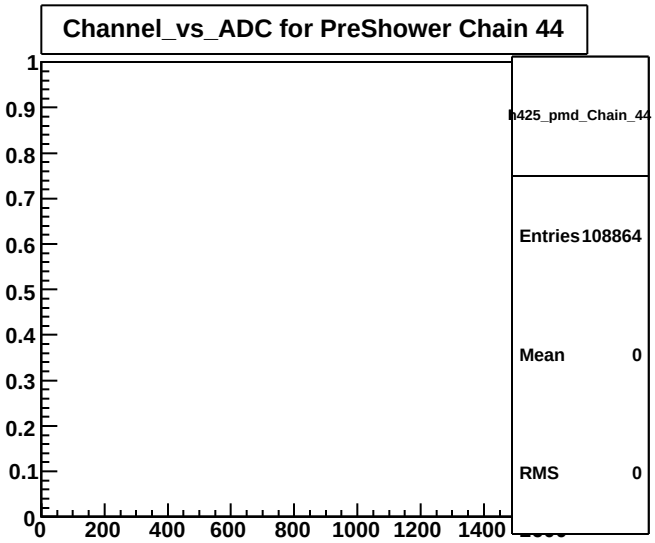

Channel_vs_ADC for PreShower Chain 39
h420_pmd_Chain_39
Entries 108864
Mean 0
RMS 0

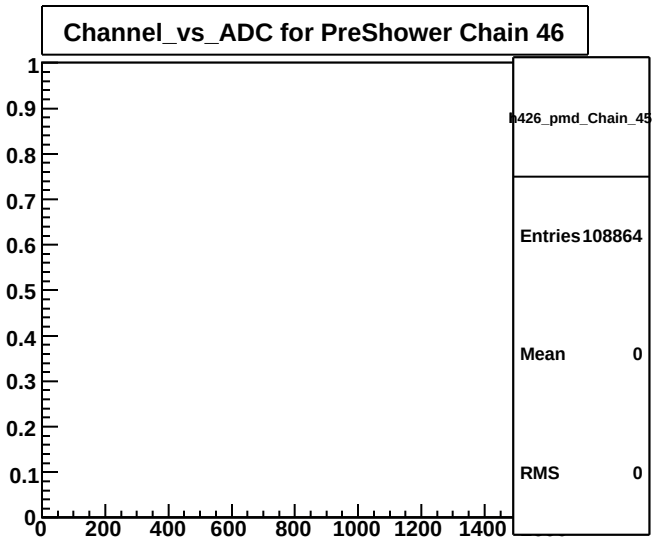

Channel_vs_ADC for PreShower Chain 40
h421_pmd_Chain_40
Entries 108864
Mean 0
RMS 0

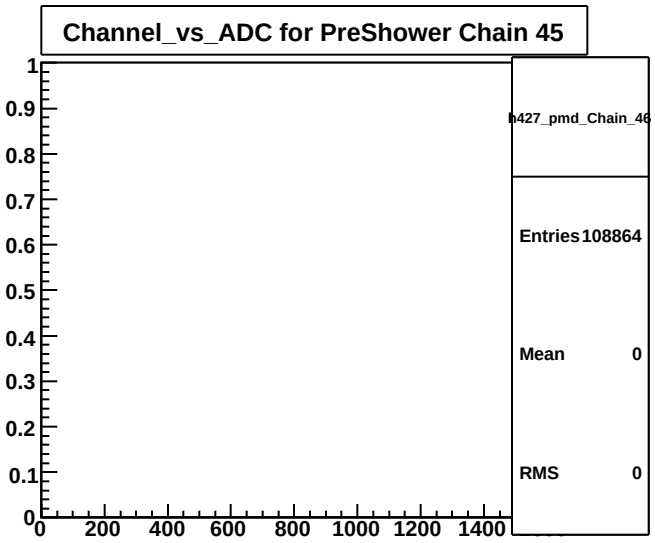

Channel_vs_ADC for PreShower Chain 41
h422_pmd_Chain_41
Entries 108864
Mean 0
RMS 0

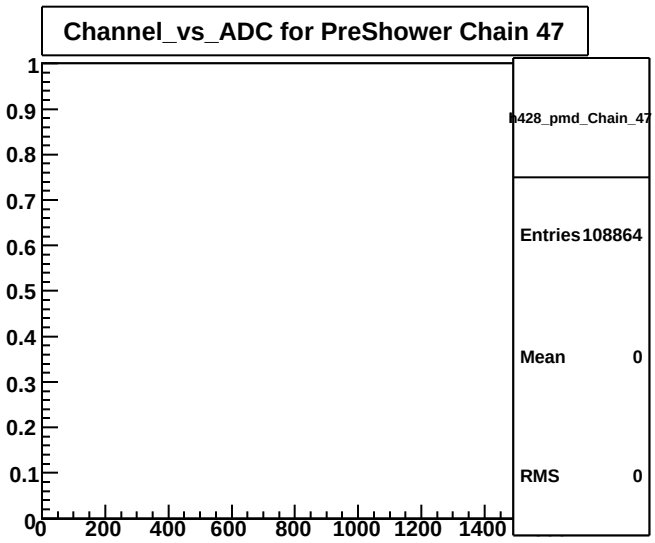

Channel_vs_ADC for PreShower Chain 42
h423_pmd_Chain_42
Entries 108864
Mean 0
RMS 0

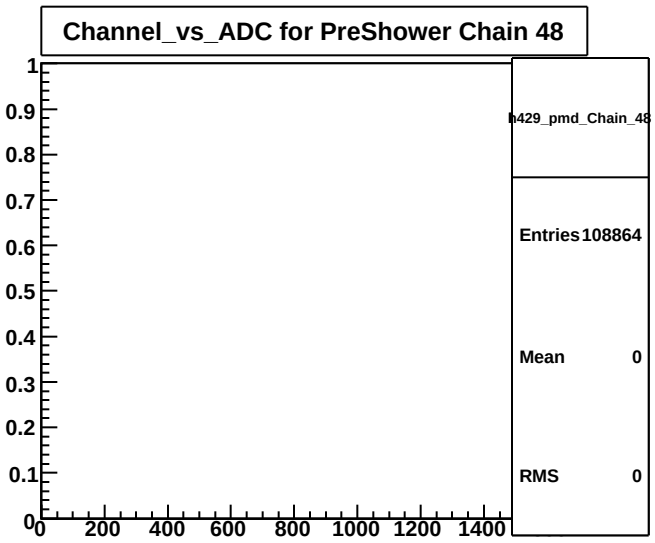

Channel_vs_ADC for PreShower Chain 43
h424_pmd_Chain_43
Entries 108864
Mean 0
RMS 0

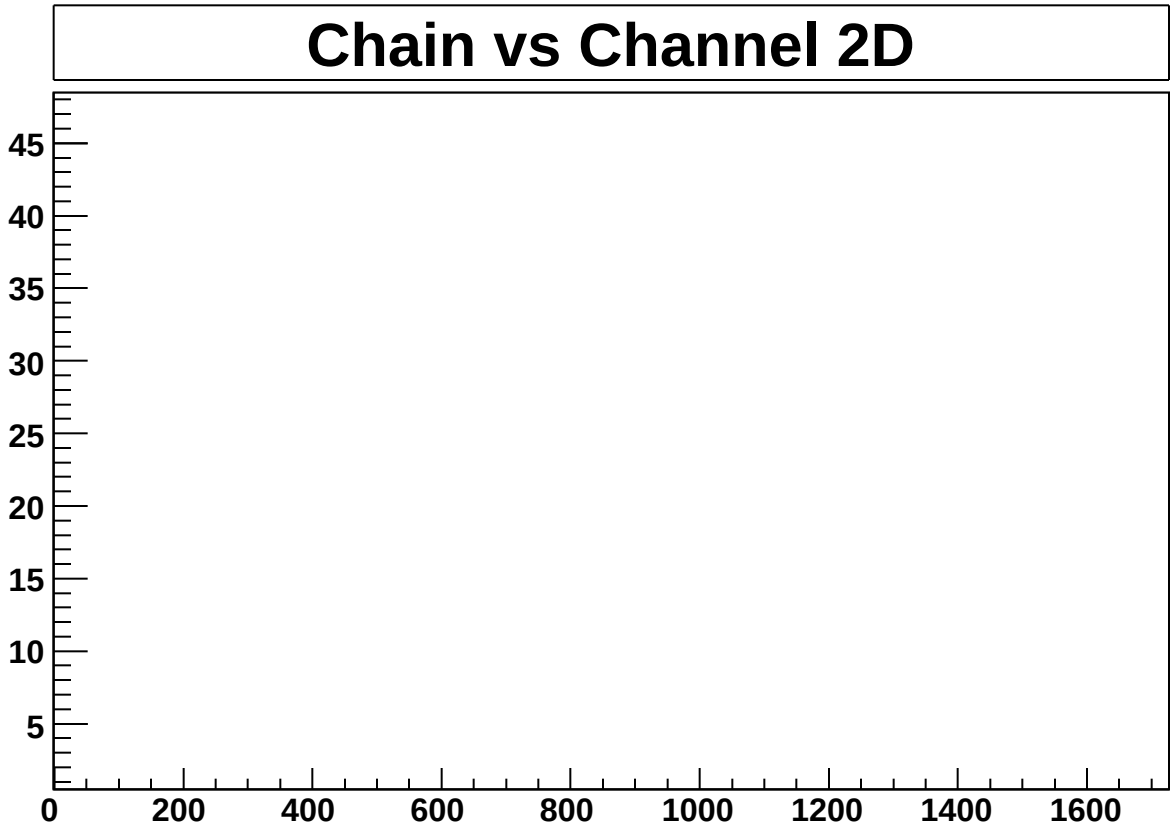

Channel_vs_ADC for PreShower Chain 44
h425_pmd_Chain_44
Entries 108864
Mean 0
RMS 0

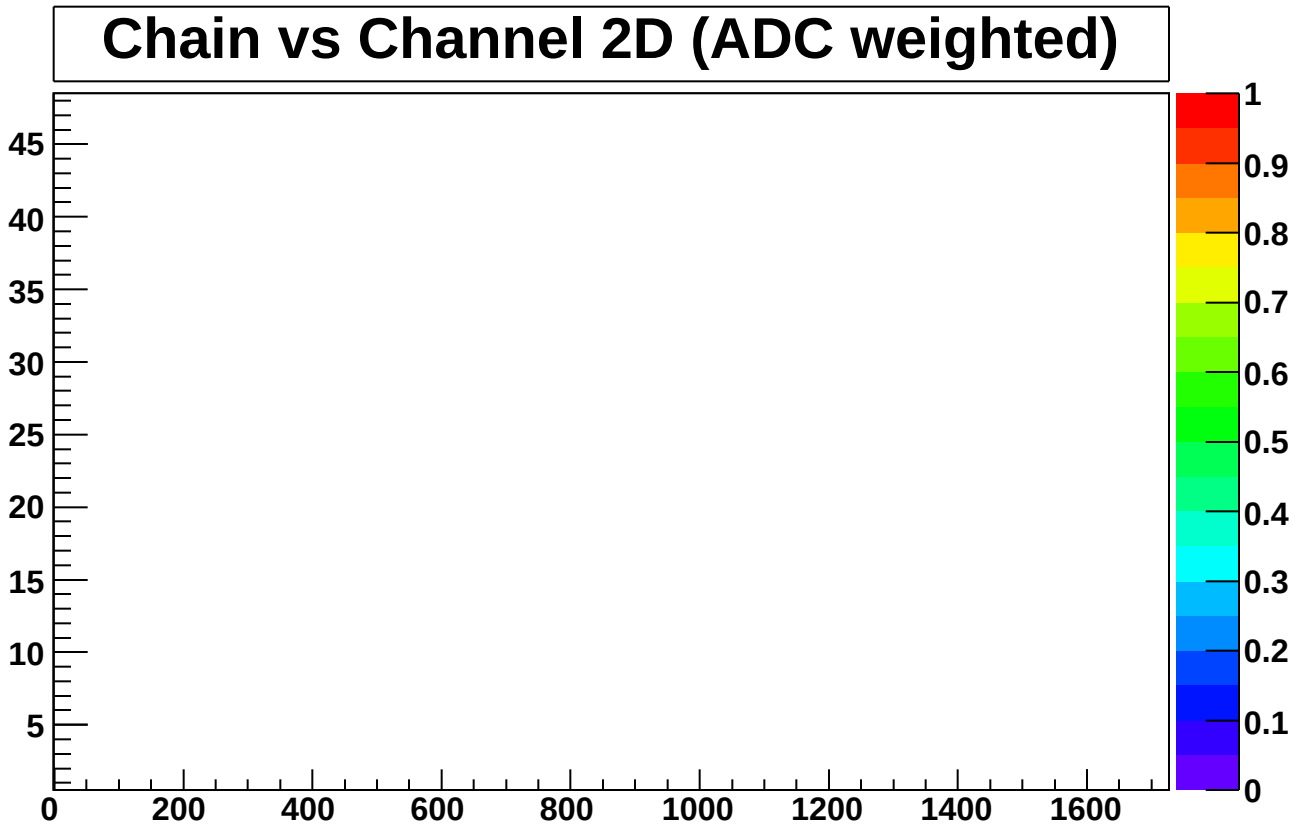

Channel_vs_ADC for PreShower Chain 46
h426_pmd_Chain_45
Entries 108864
Mean 0
RMS 0


Channel_vs_ADC for PreShower Chain 45
h427_pmd_Chain_46
Entries 108864
Mean 0
RMS 0

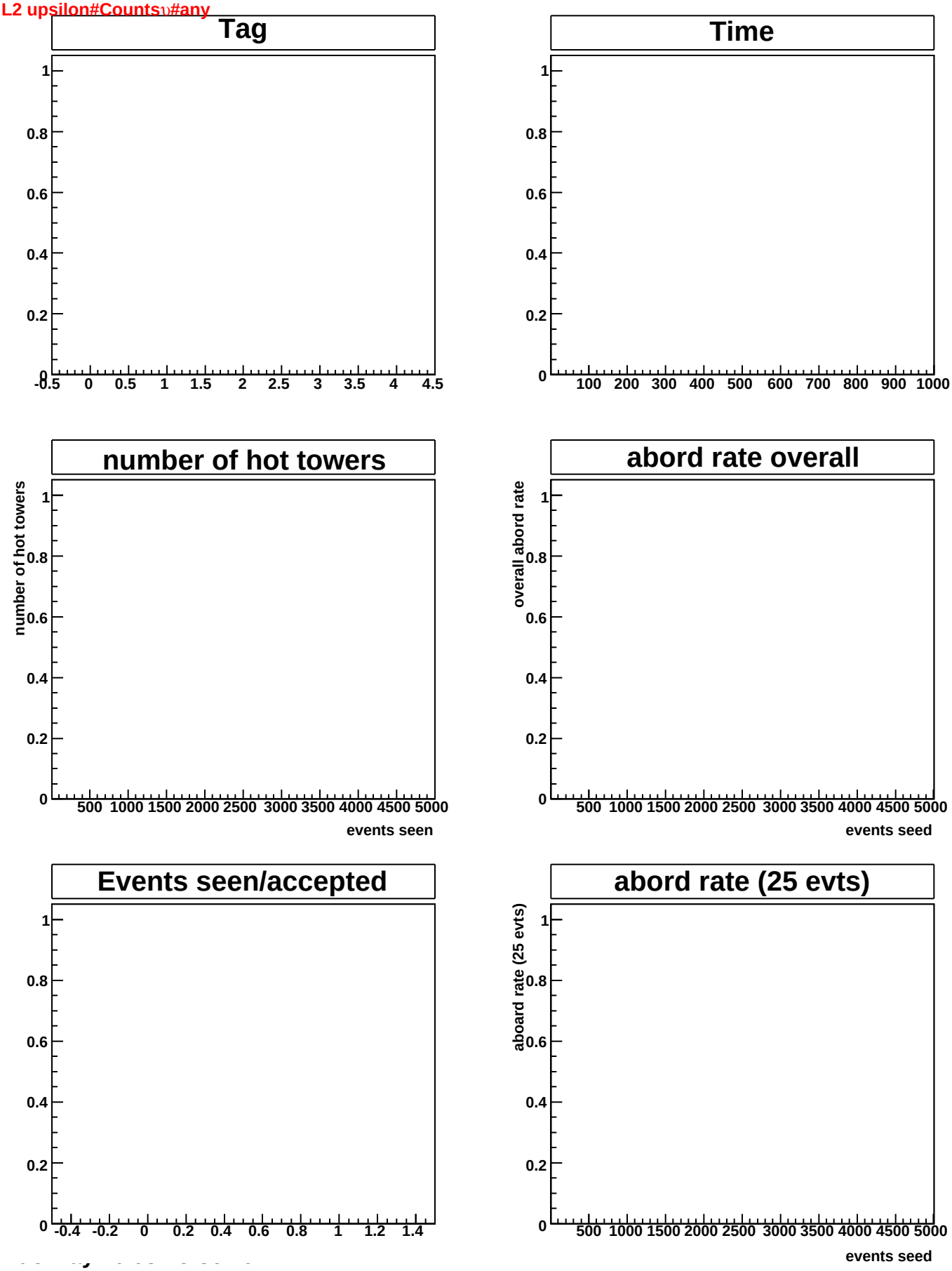

Channel_vs_ADC for PreShower Chain 47
h428_pmd_Chain_47
Entries 108864
Mean 0
RMS 0


Channel_vs_ADC for PreShower Chain 48
h429_pmd_Chain_48
Entries 108864
Mean 0
RMS 0

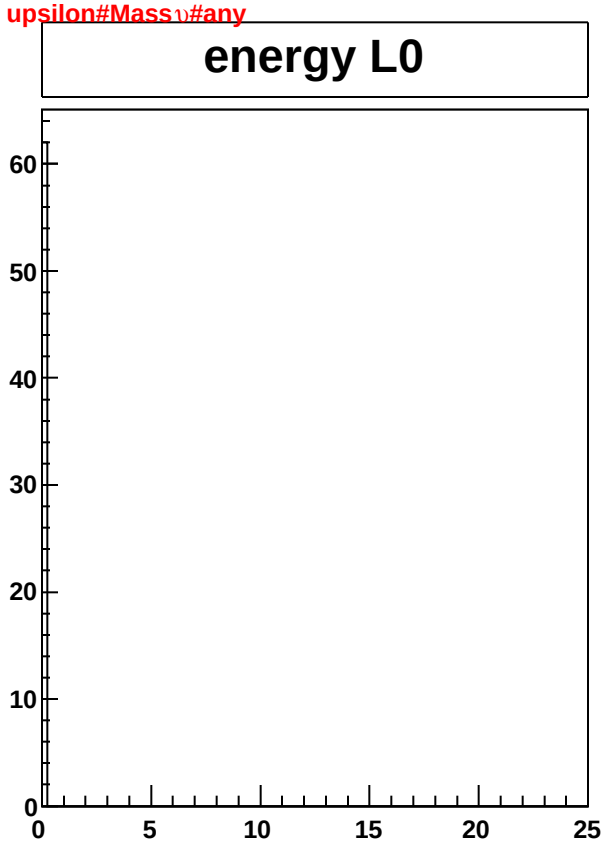

Chain vs Channel 2D
45
40
35
30
25
20
15
10
5
0
200
400
600
800
1000
1200
1400
1600

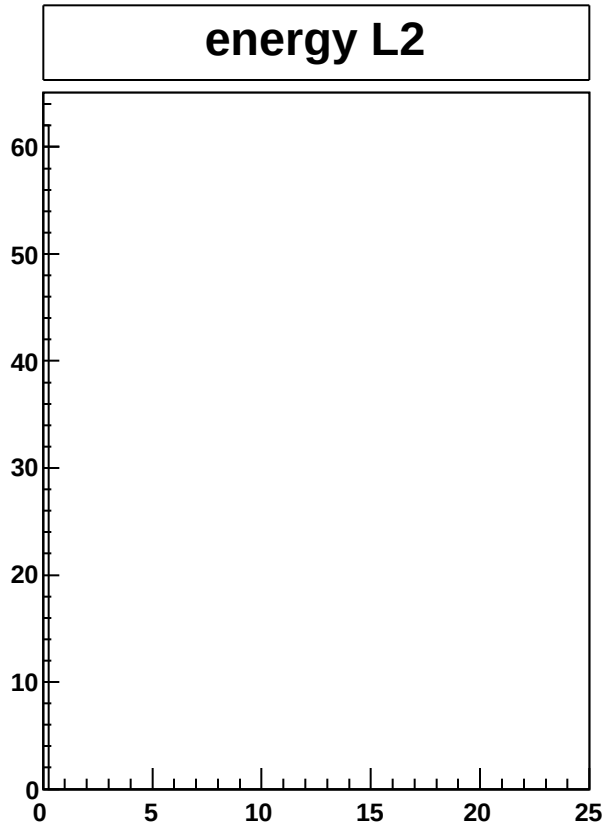

Chain vs Channel 2D (ADC weighted)
45
40
35
30
25
20
15
10
5
0
200
400
600
800
1000
1200
1400
1600
1
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0

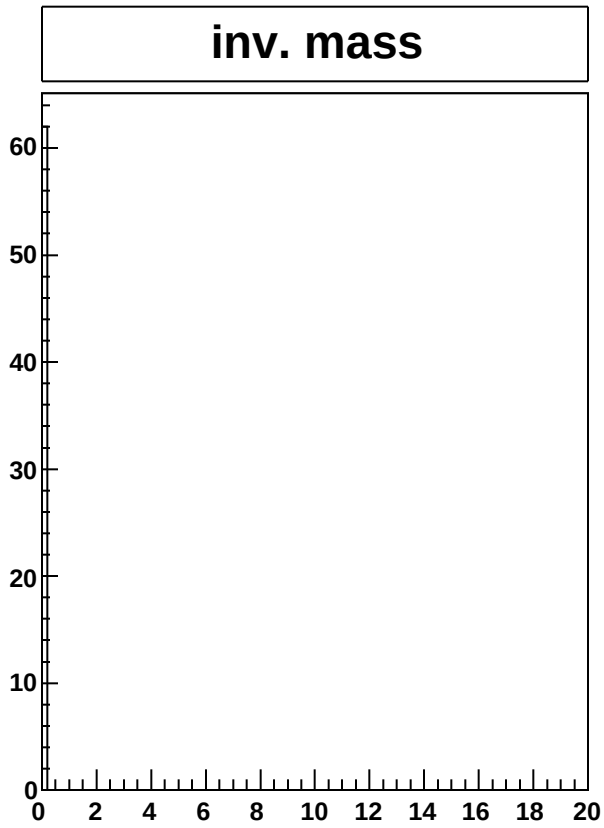
L2 upsilon#Countsυ#any

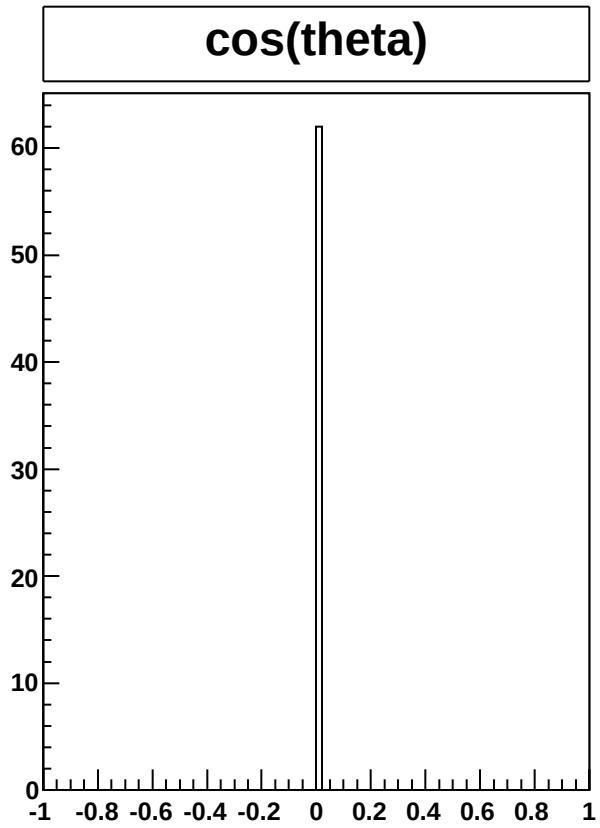


L2 upsilon#Massυ#any

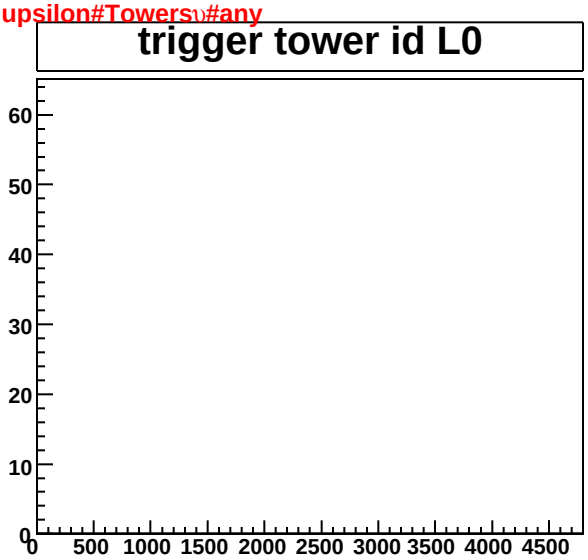


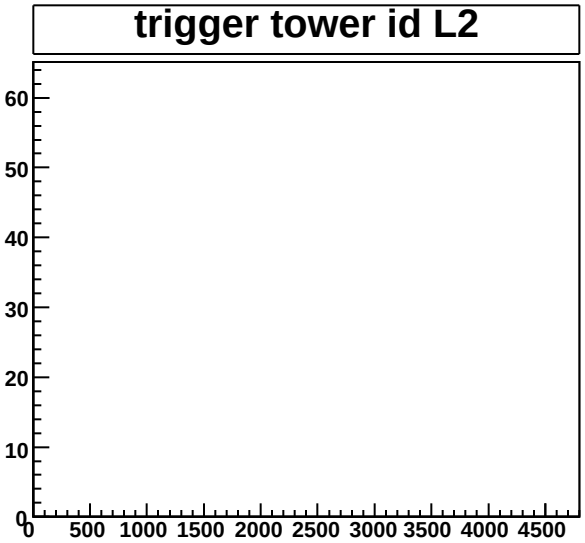


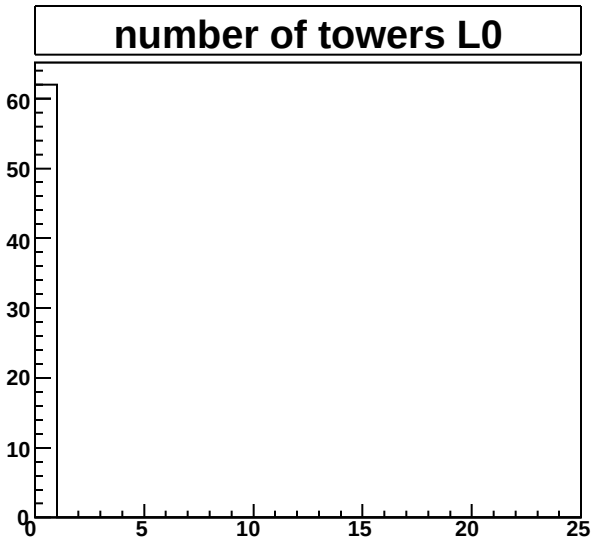


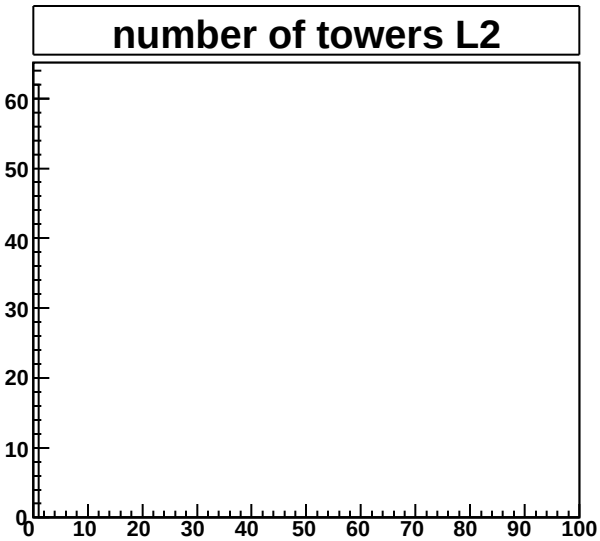


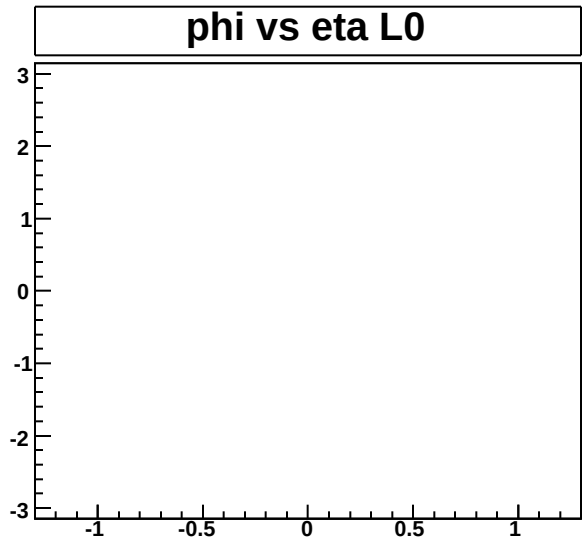
L2 upsilon#Towersυ#any

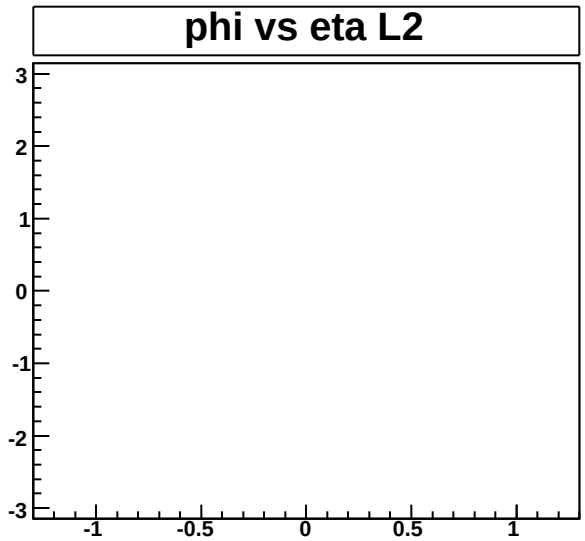






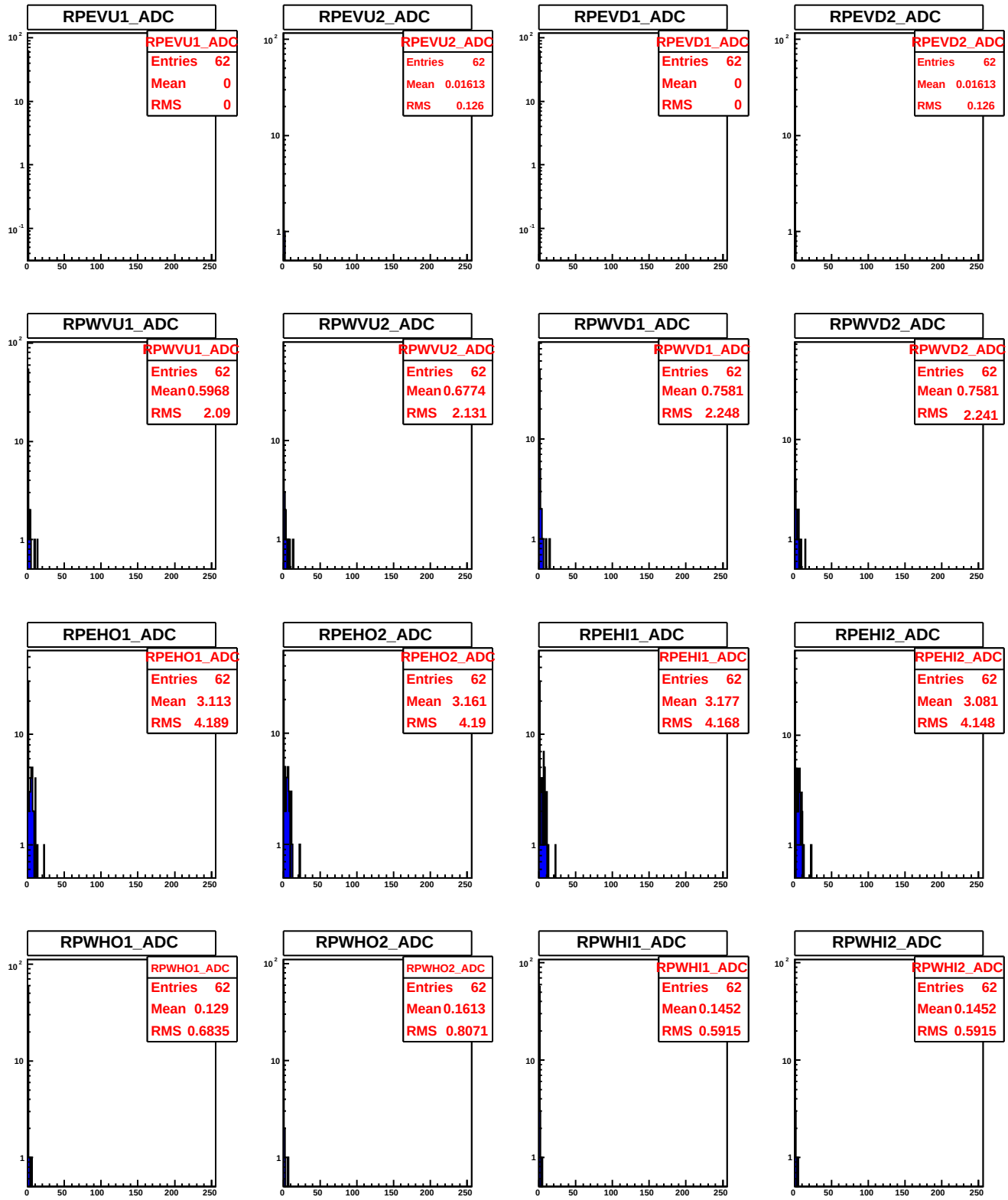






P2P#Trg1-16#any#trg

Run: #12130023



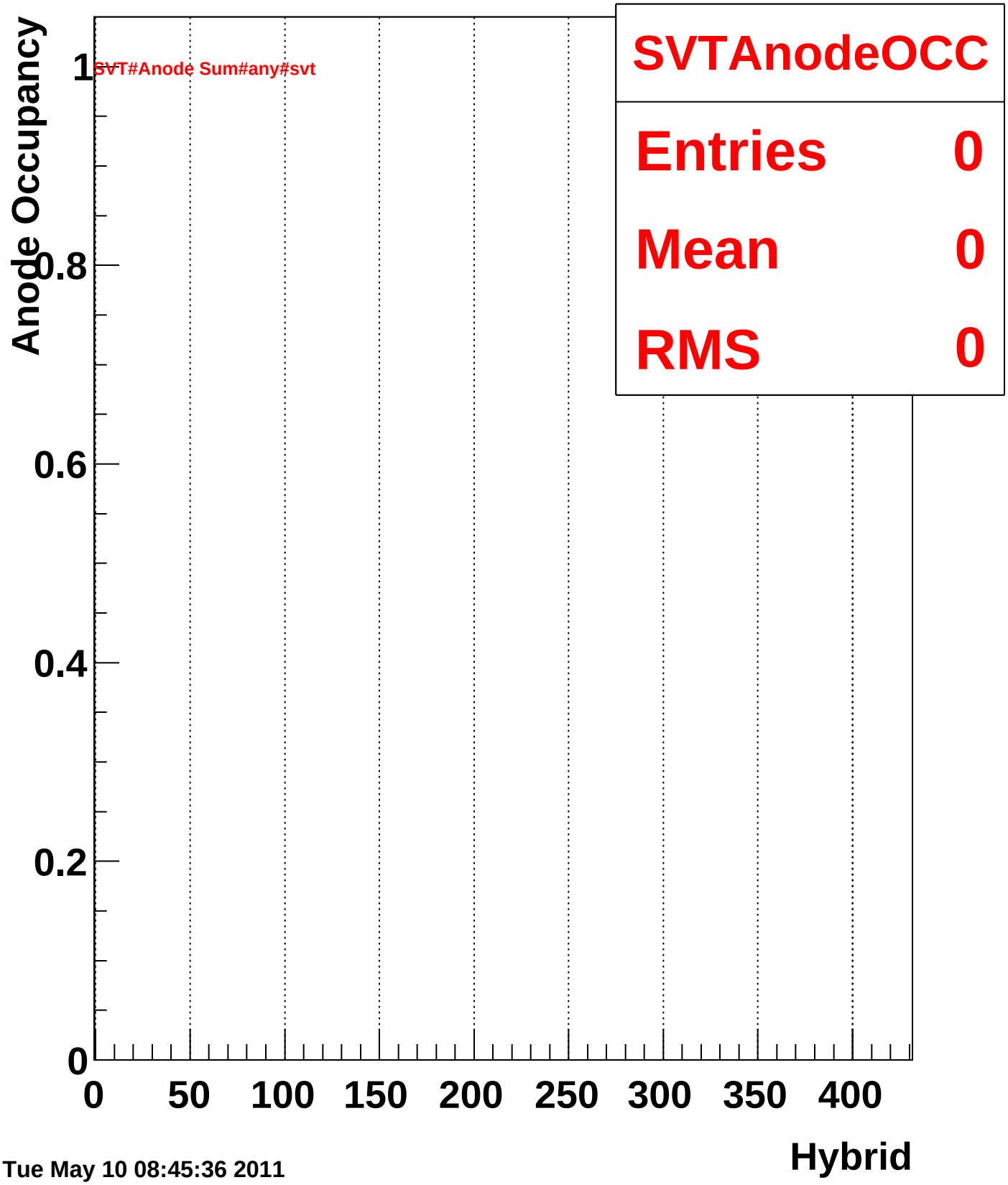
Tue May 10 08:45:36 2011

P2P#Trg17-32#any#trg

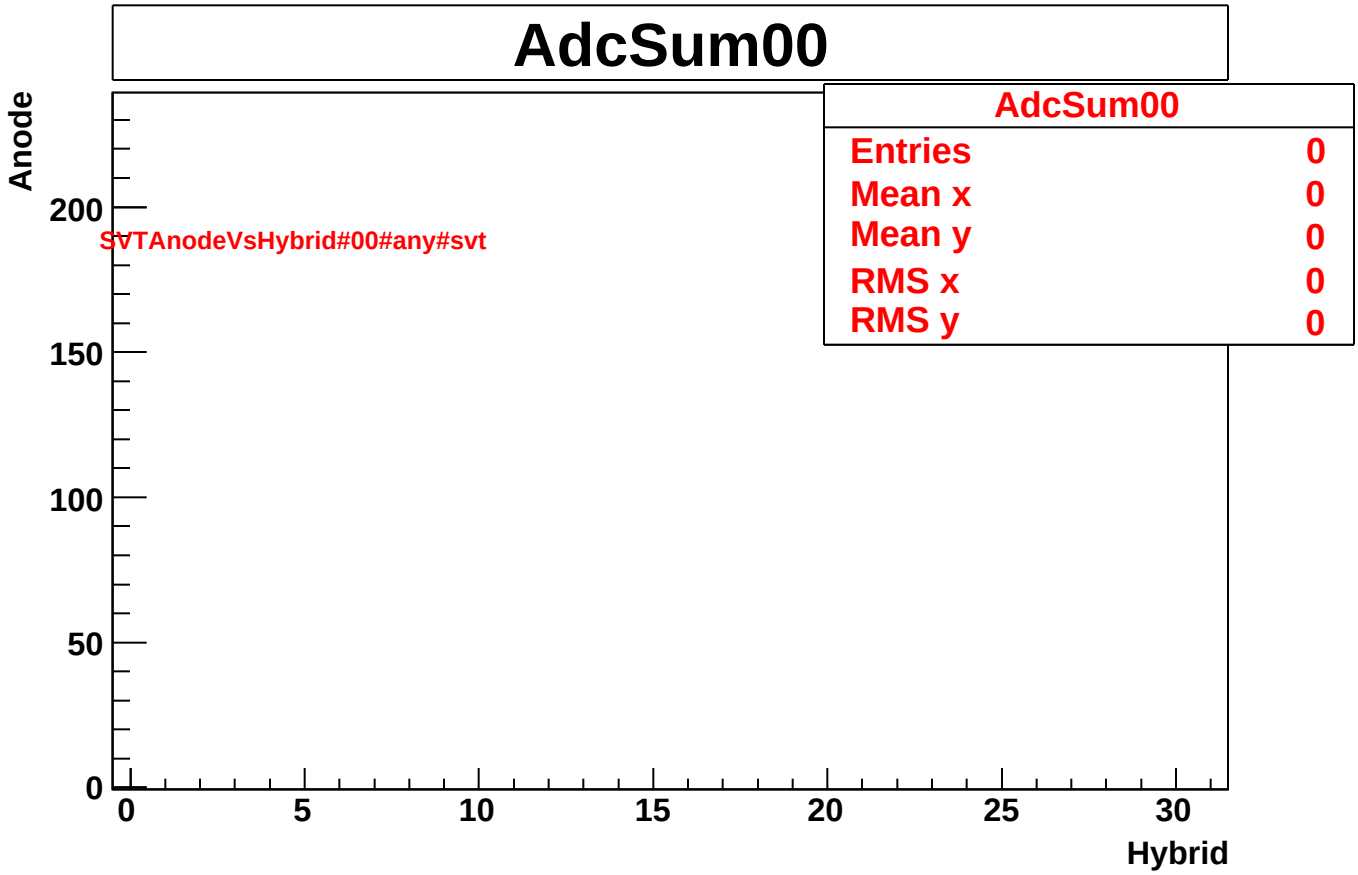
Run: #12130023

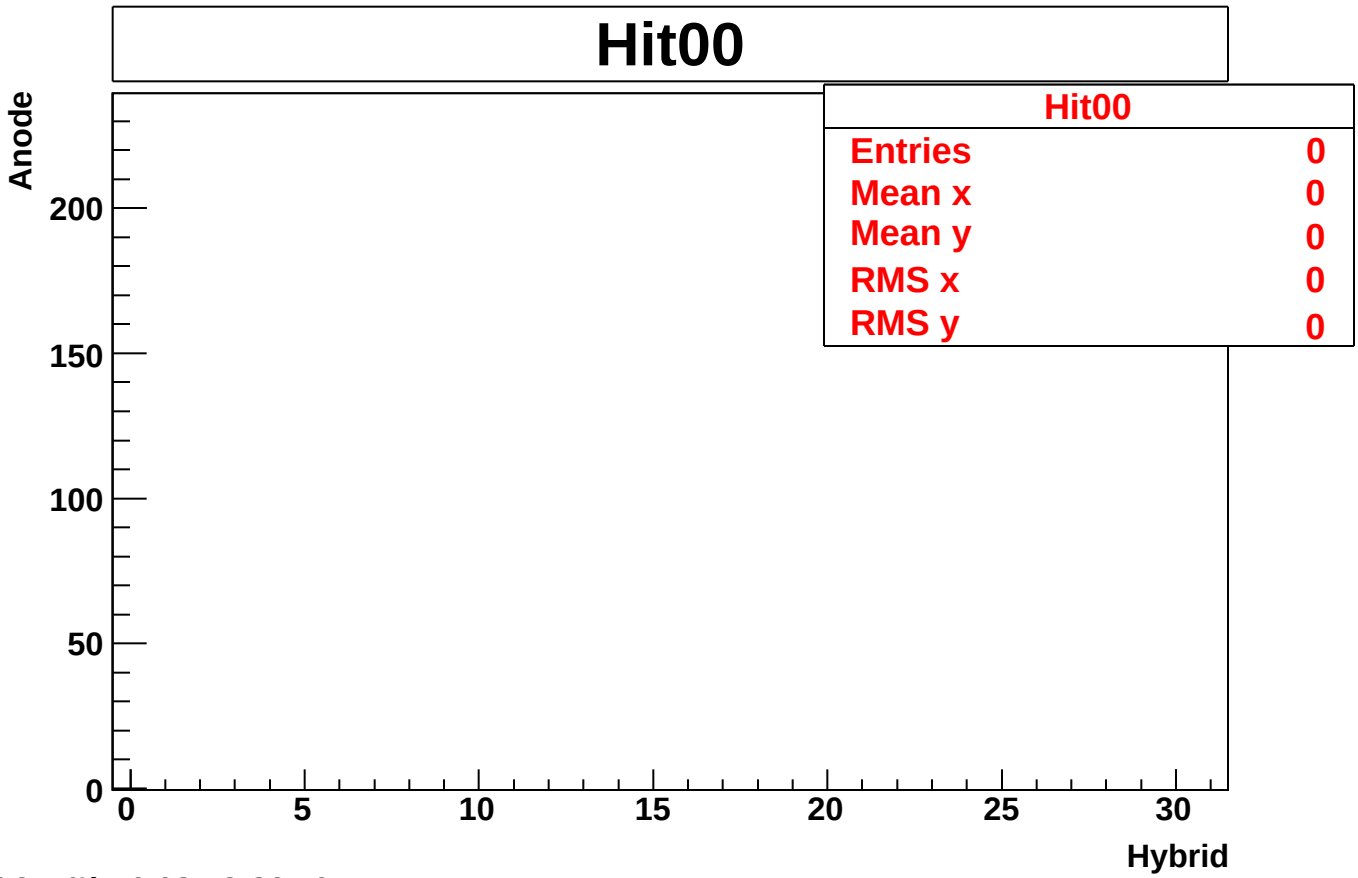
Tue May 10 08:45:36 2011

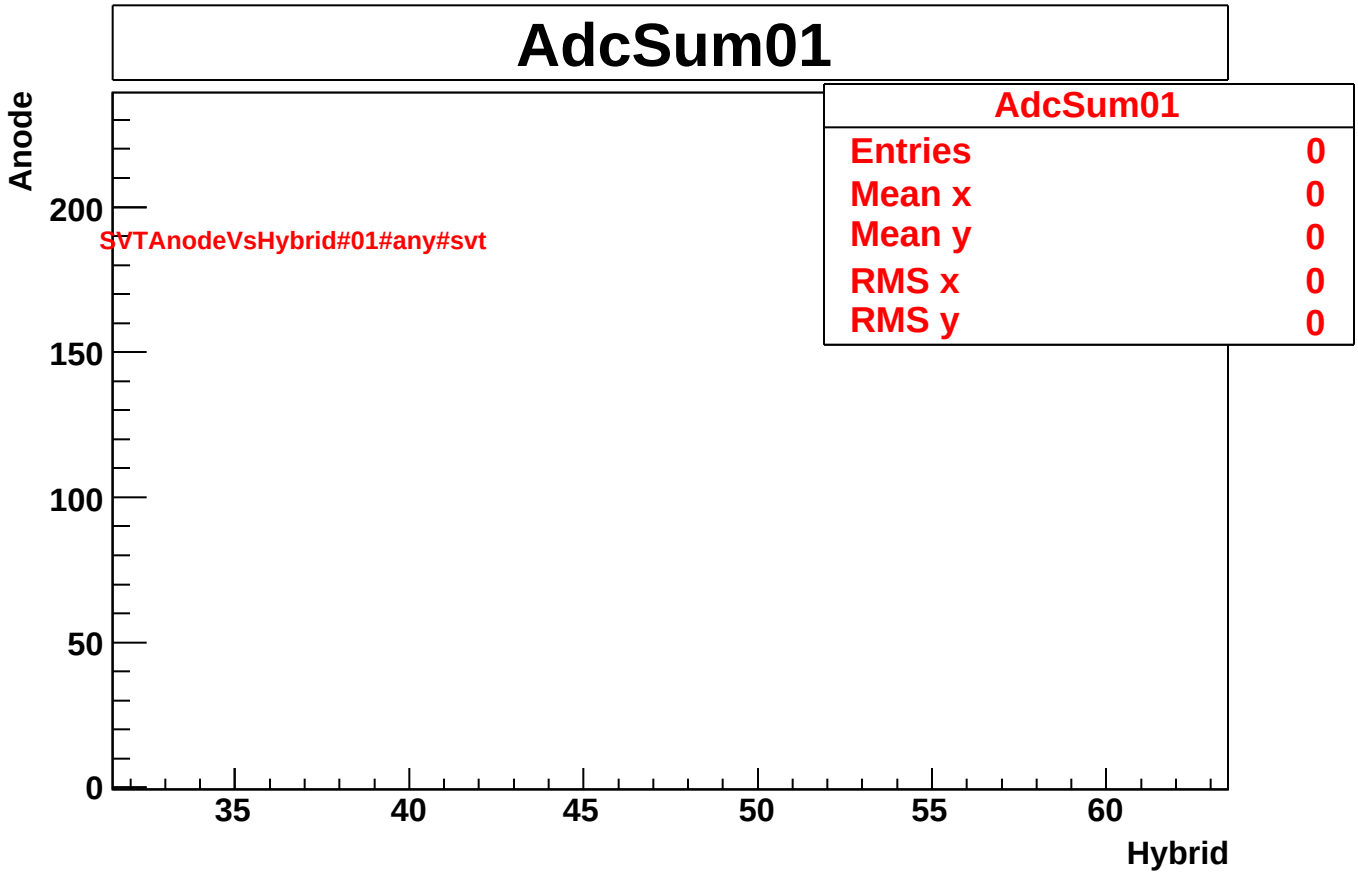
SVTAnodeOCC

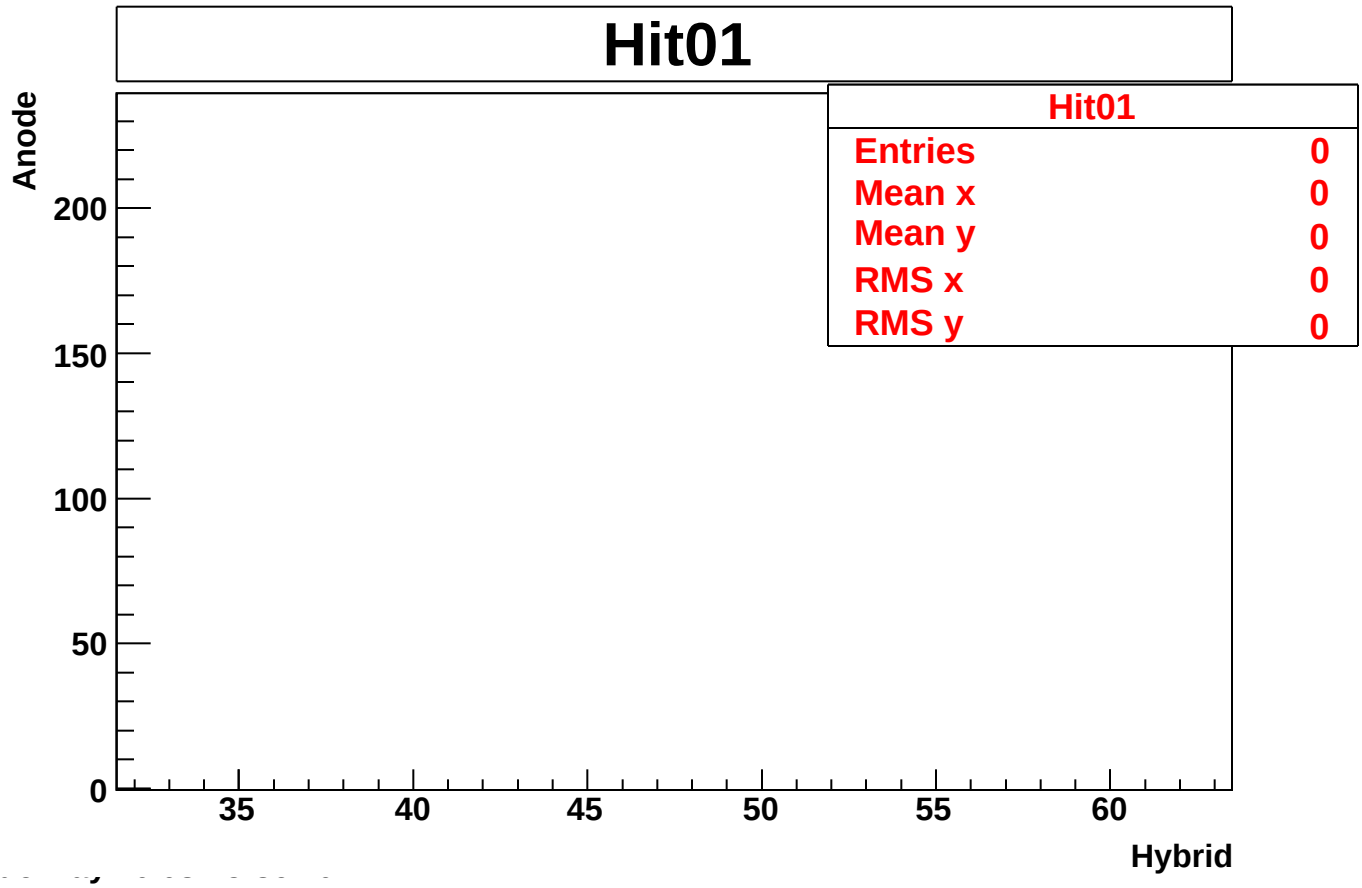


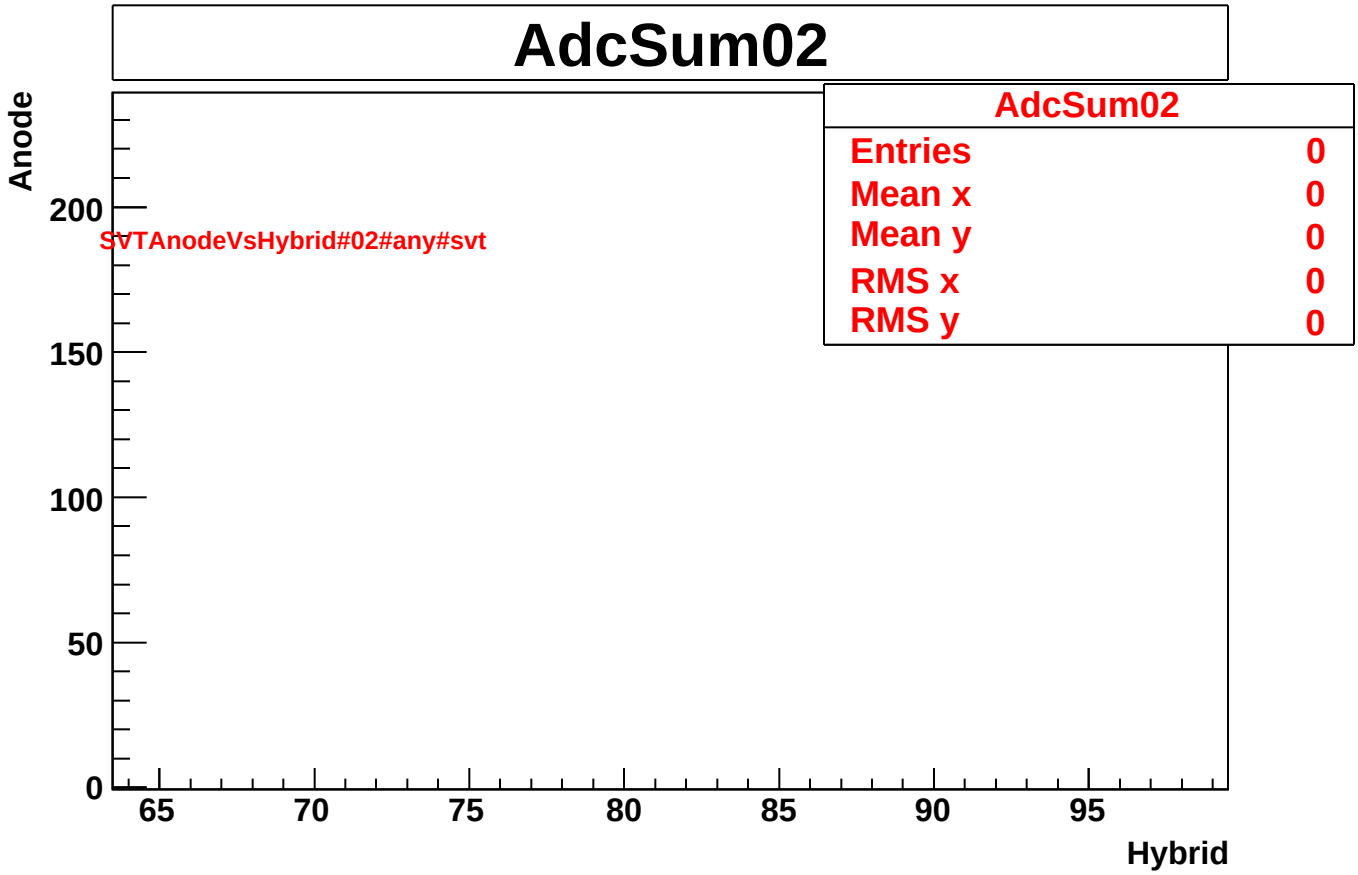
Tue May 10 08:45:36 2011

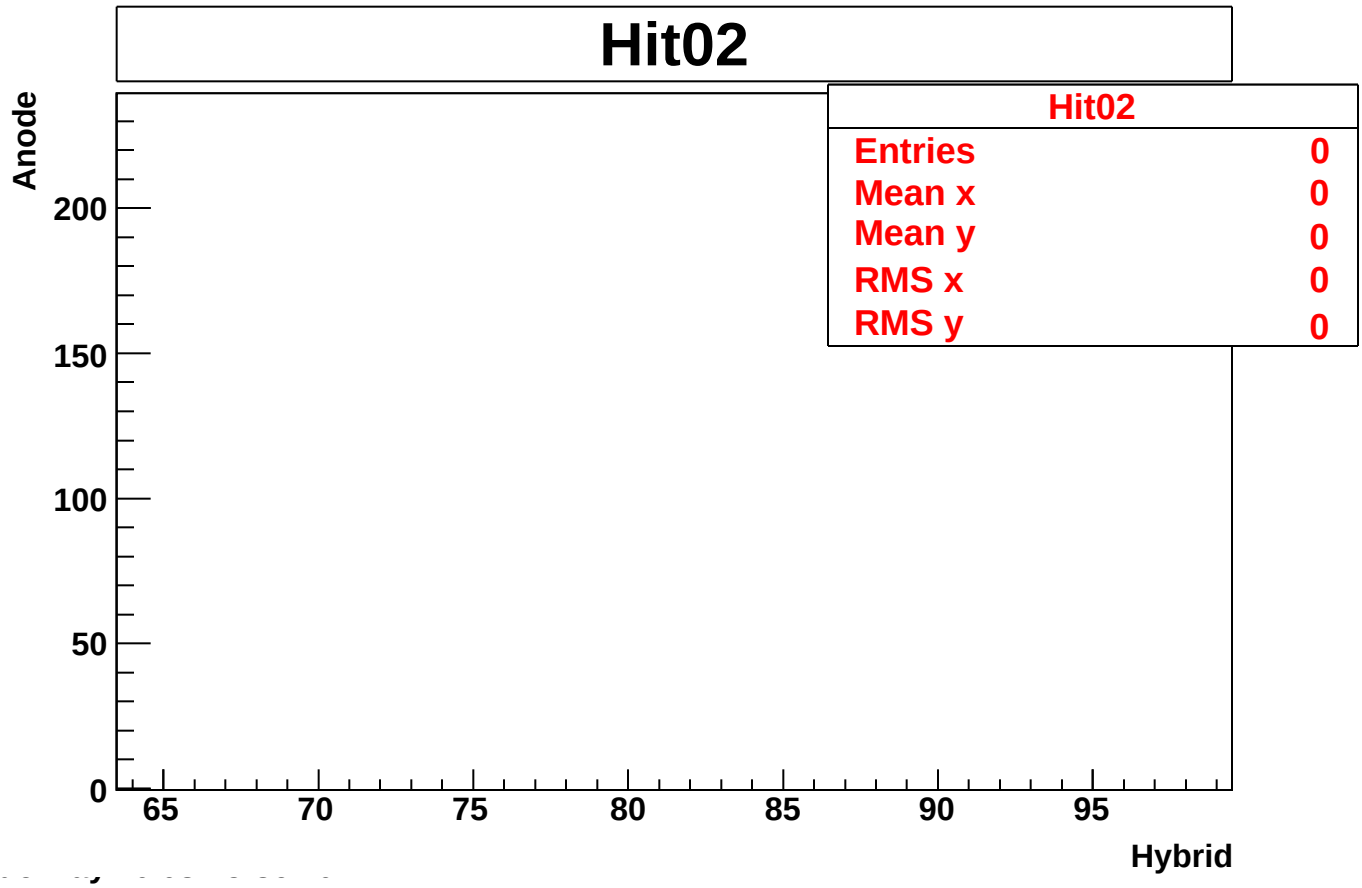

AdcSum00
AdcSum00
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#00#any#svt
Anode
0
50
100
150
200
0
5
10
15
20
25
30
Hybrid

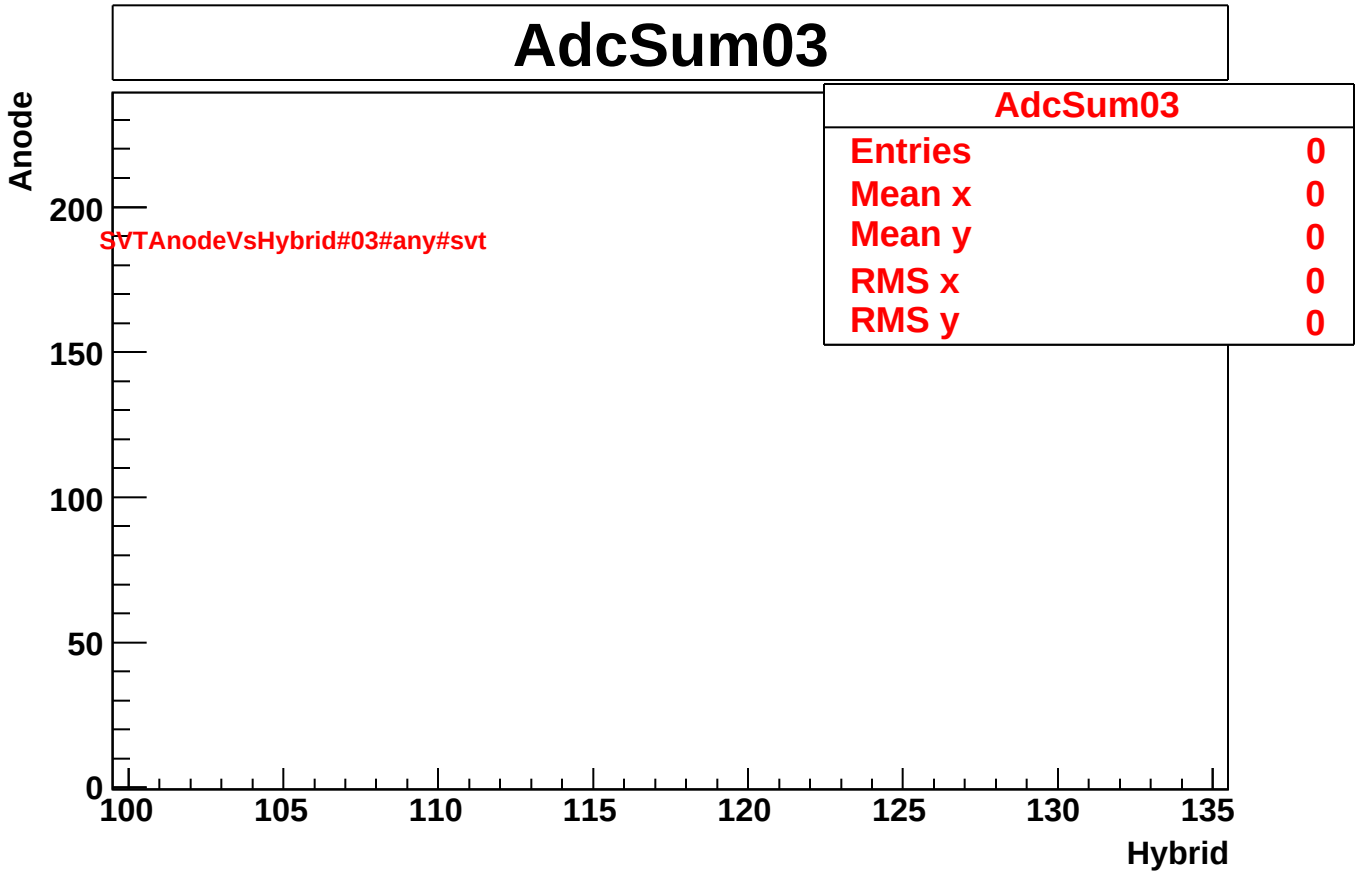

Hit00
Hit00
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
0
50
100
150
200
0
5
10
15
20
25
30
Hybrid

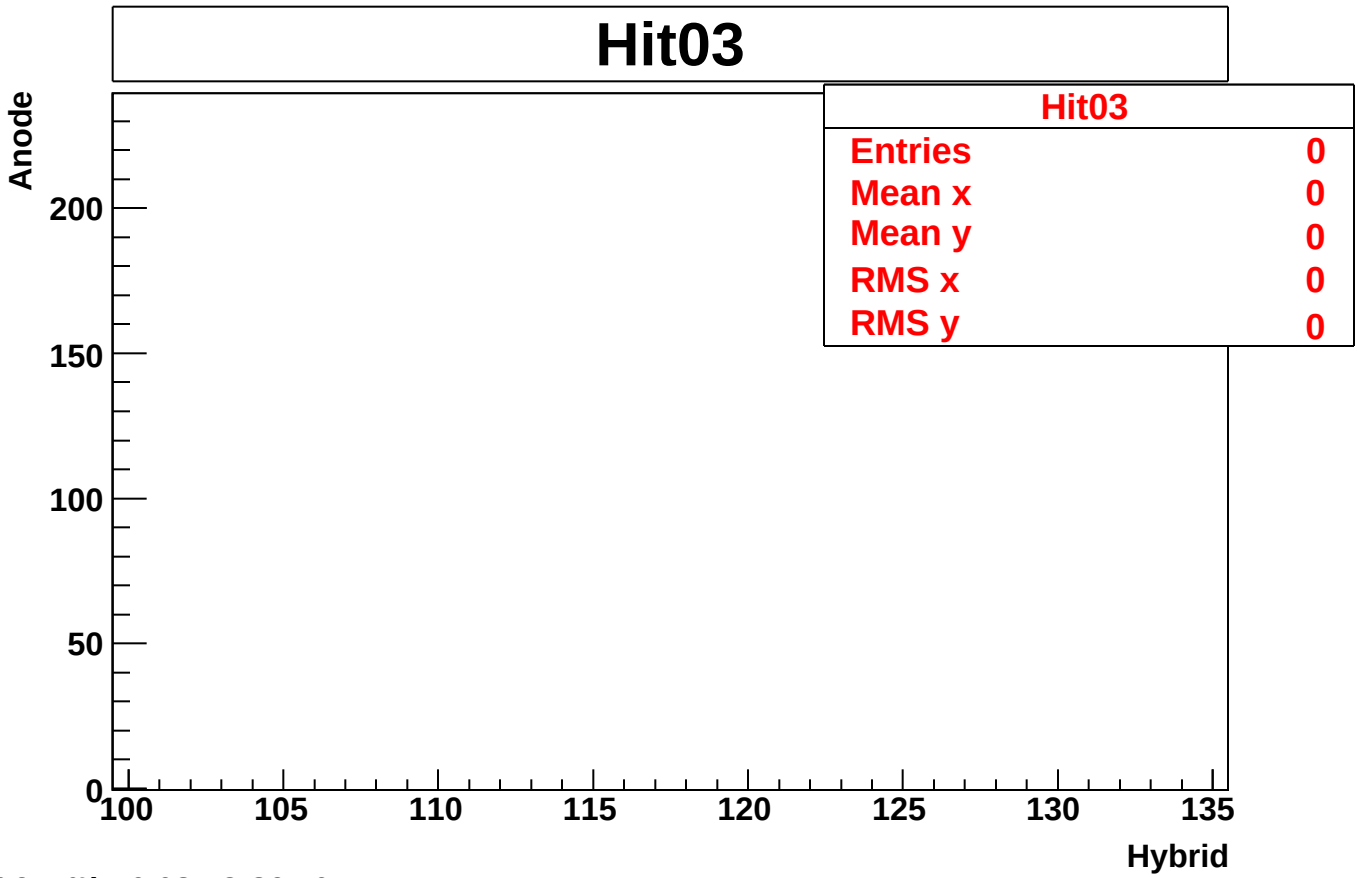

AdcSum01
AdcSum01
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#01#any#svt
Anode
200
150
100
50
0
35
40
45
50
55
60
Hybrid

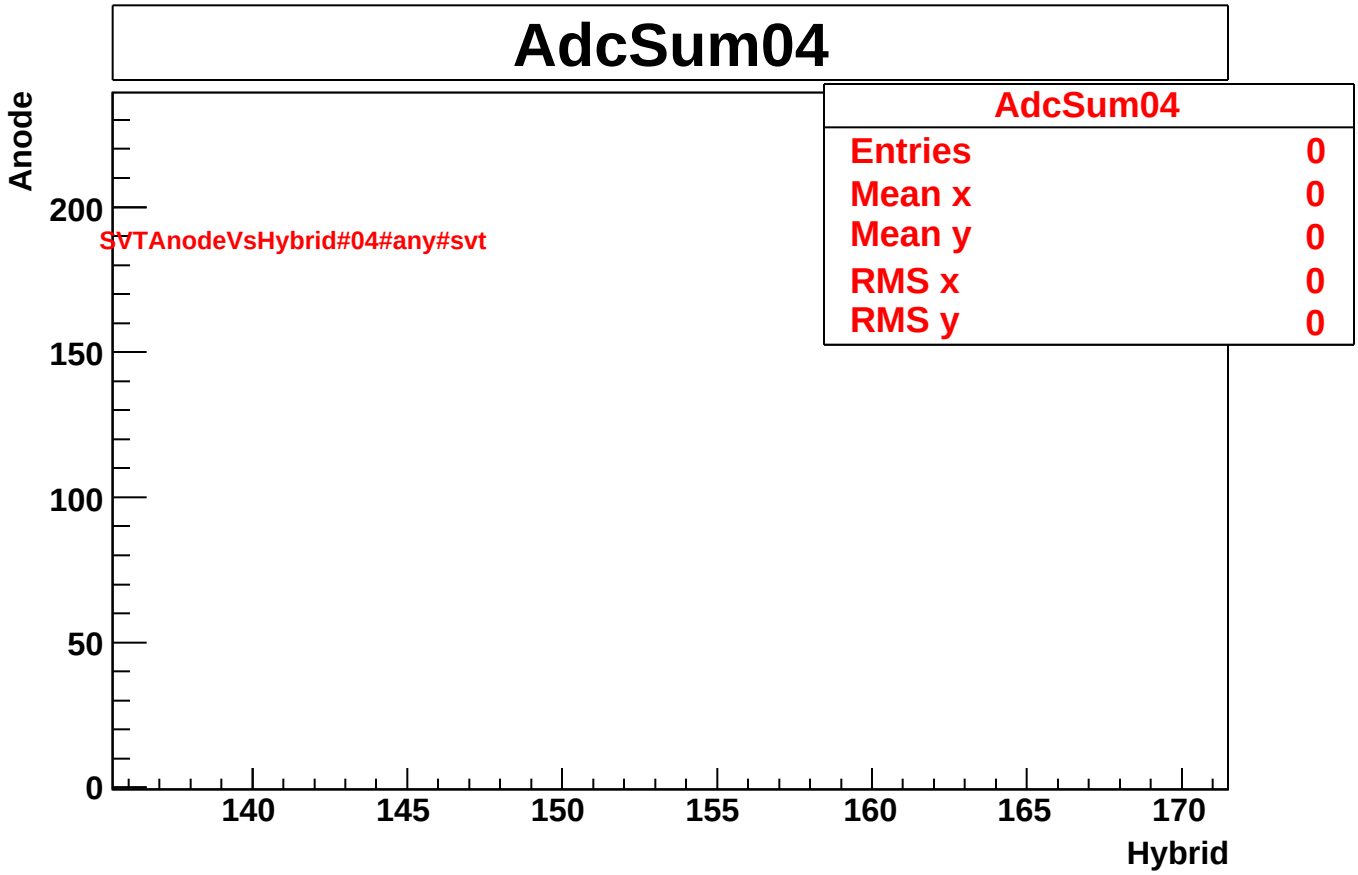

Hit01
Hit01
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
200
150
100
50
0
35
40
45
50
55
60
Hybrid

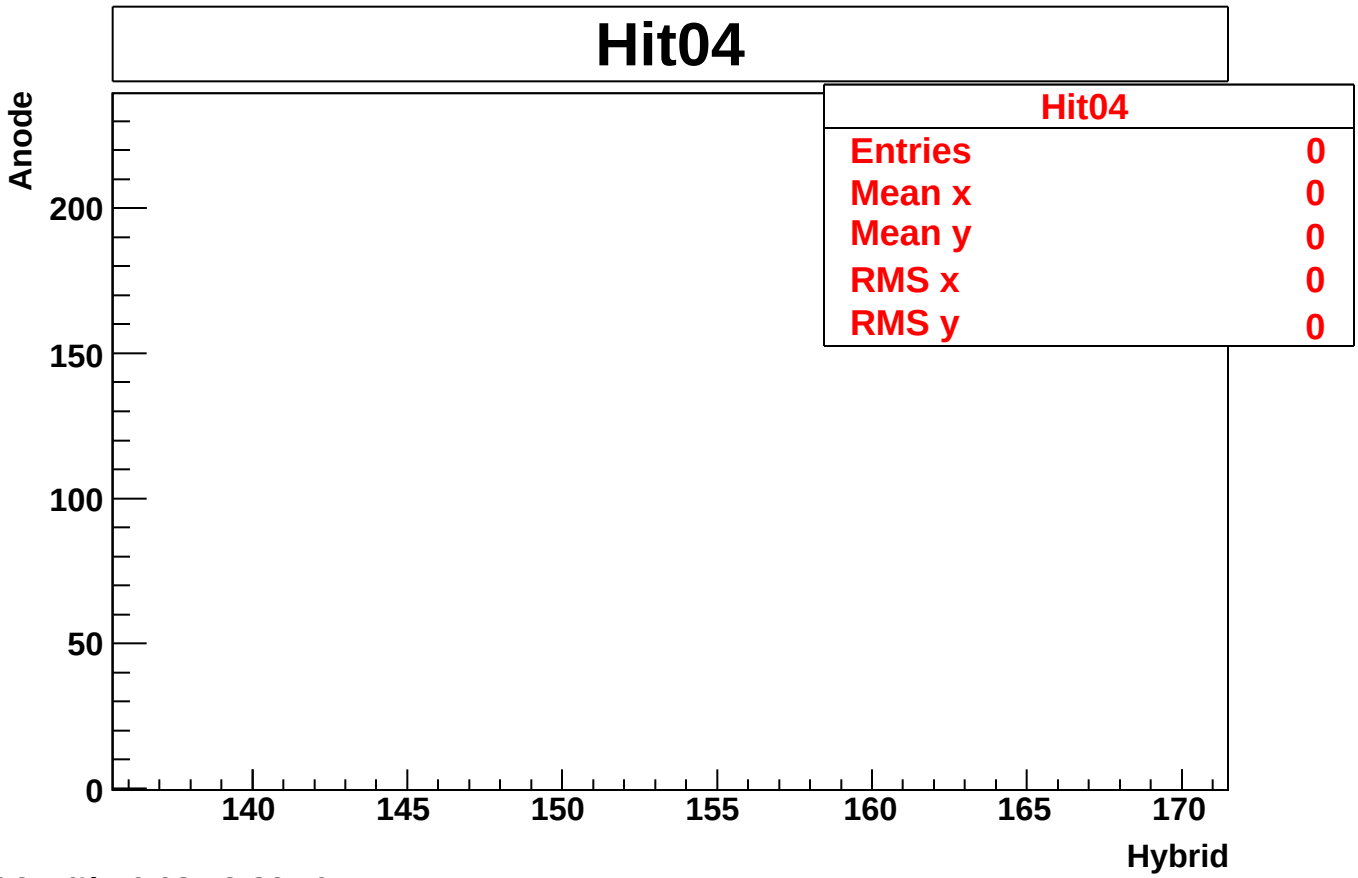

AdcSum02
AdcSum02
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#02#any#svt
Anode
200
150
100
50
0
65
70
75
80
85
90
95
Hybrid

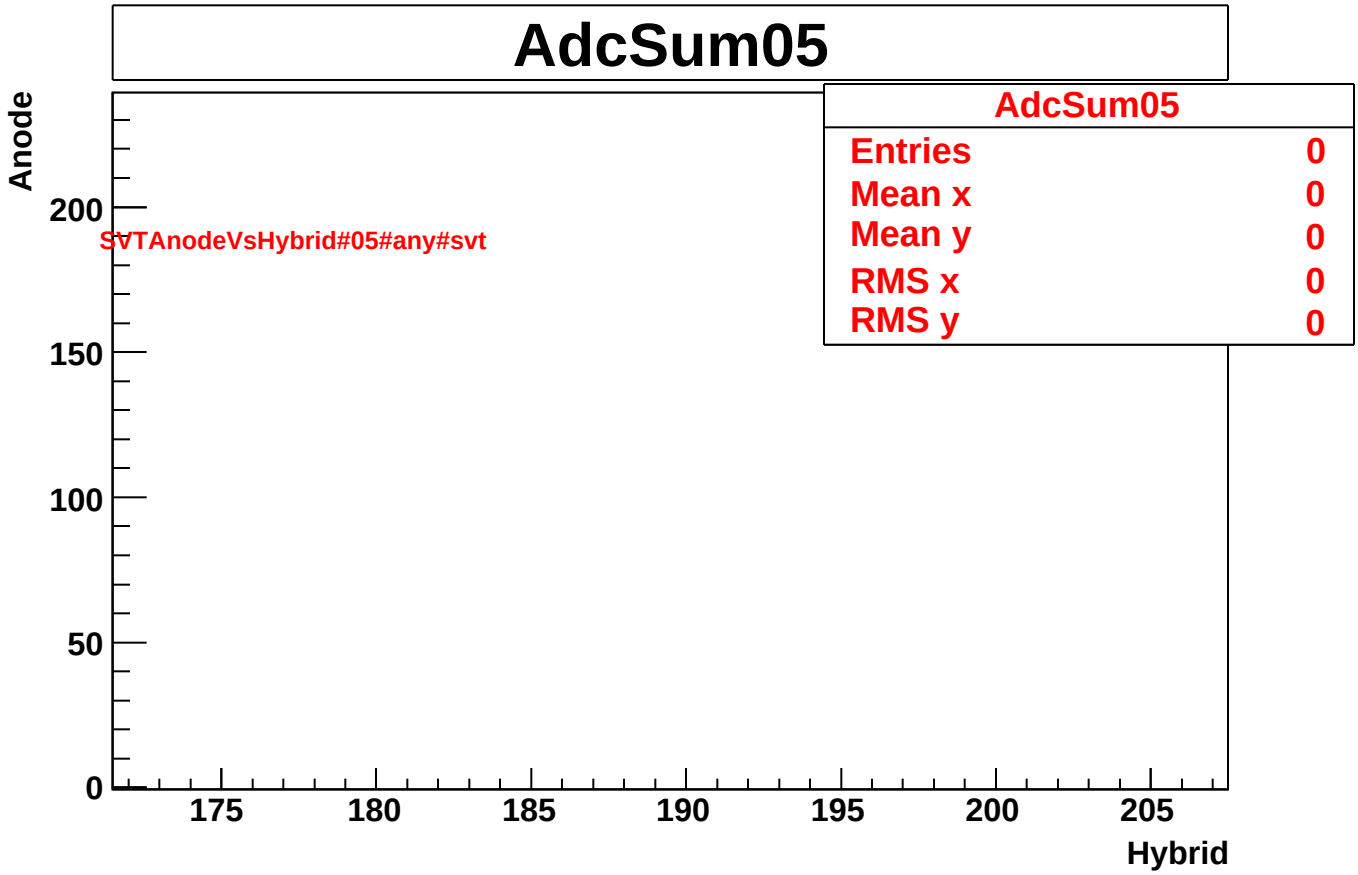

Hit02
Hit02
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
200
150
100
50
0
65
70
75
80
85
90
95
Hybrid

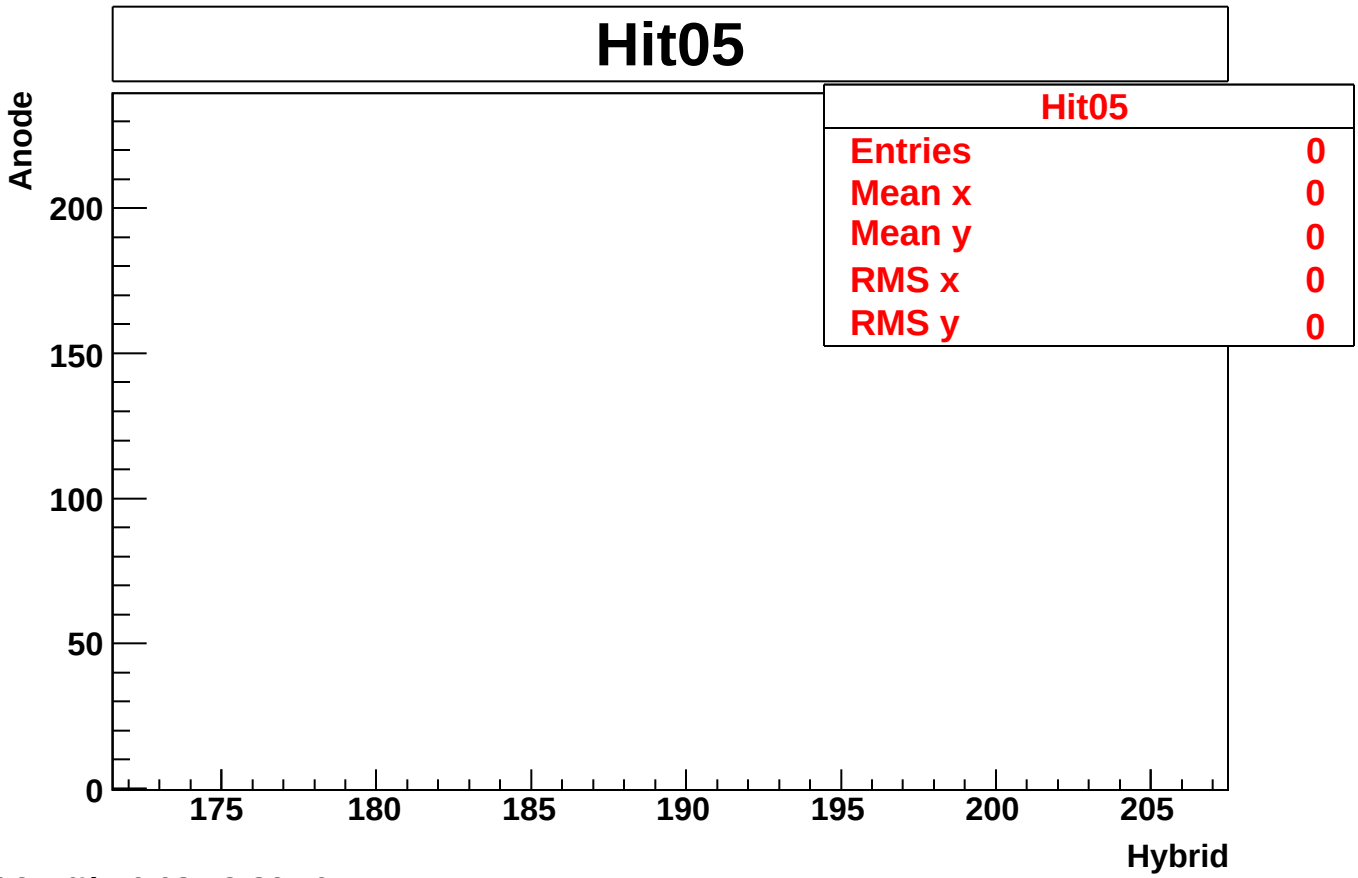

AdcSum03
AdcSum03
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#03#any#svt
Anode
0
50
100
150
200
100
105
110
115
120
125
130
135
Hybrid

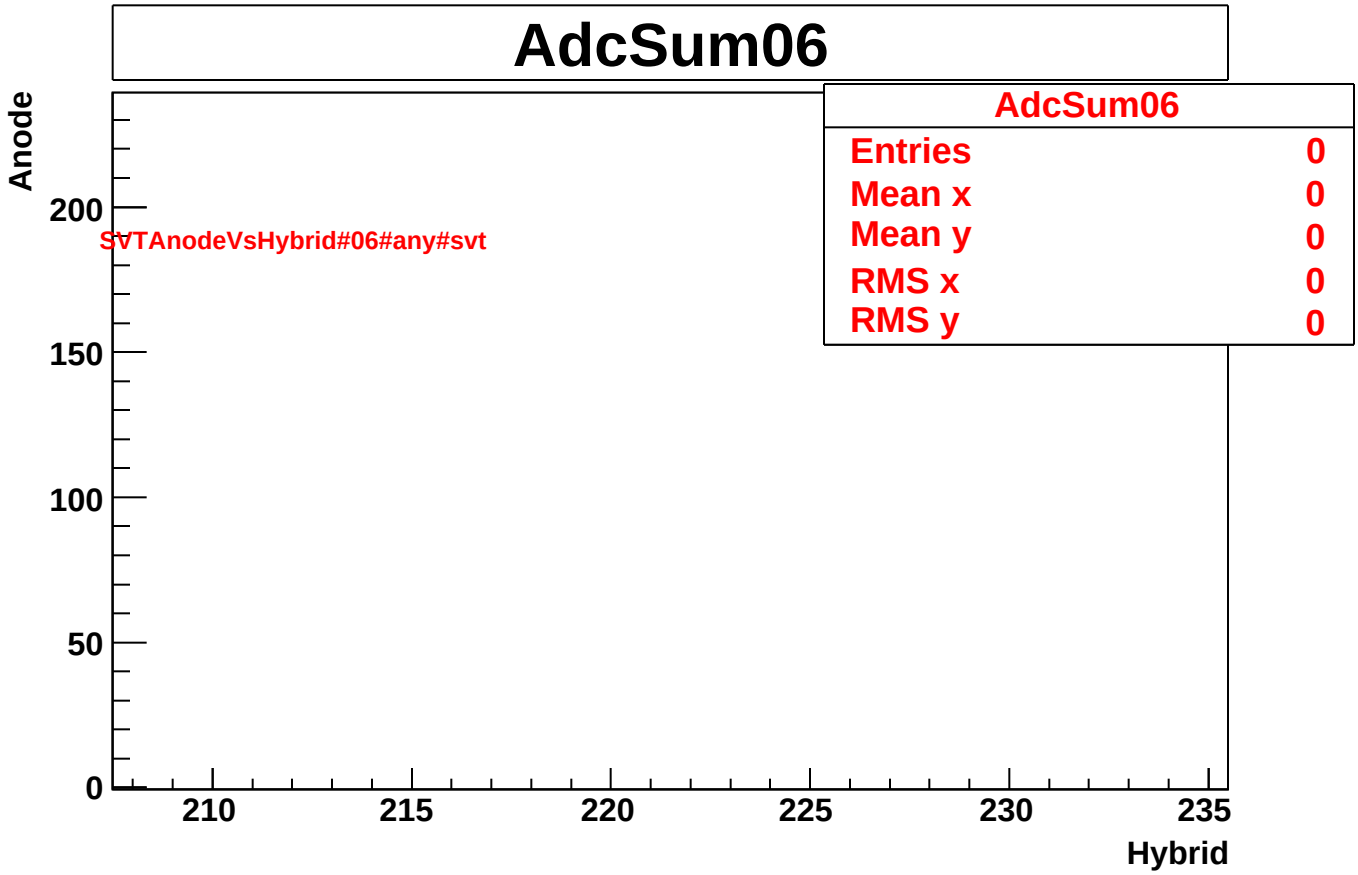

Hit03
Hit03
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
0
50
100
150
200
100
105
110
115
120
125
130
135
Hybrid

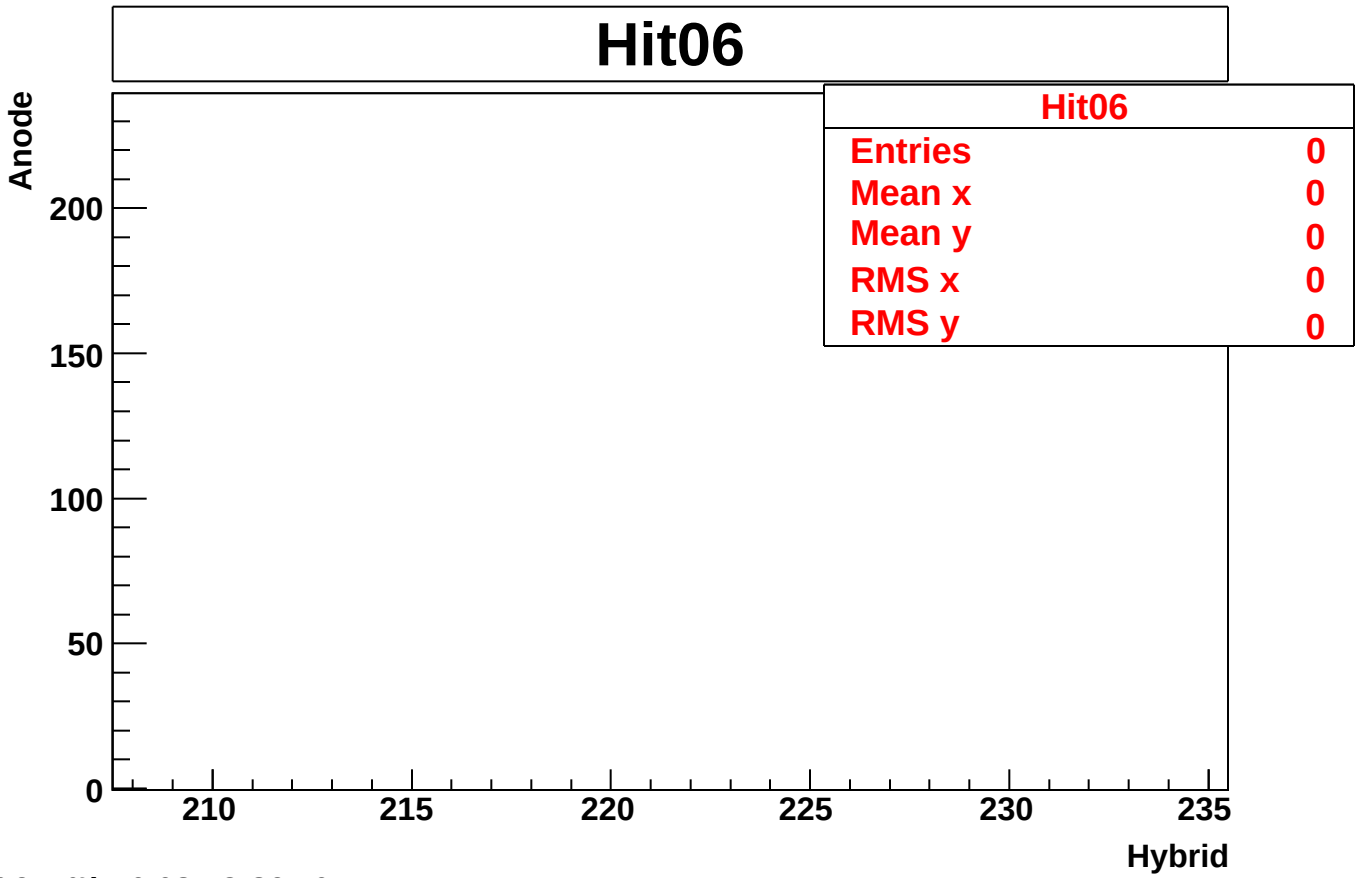

AdcSum04
AdcSum04
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#04#any#svt
Anode
200
150
100
50
0
140
145
150
155
160
165
170
Hybrid

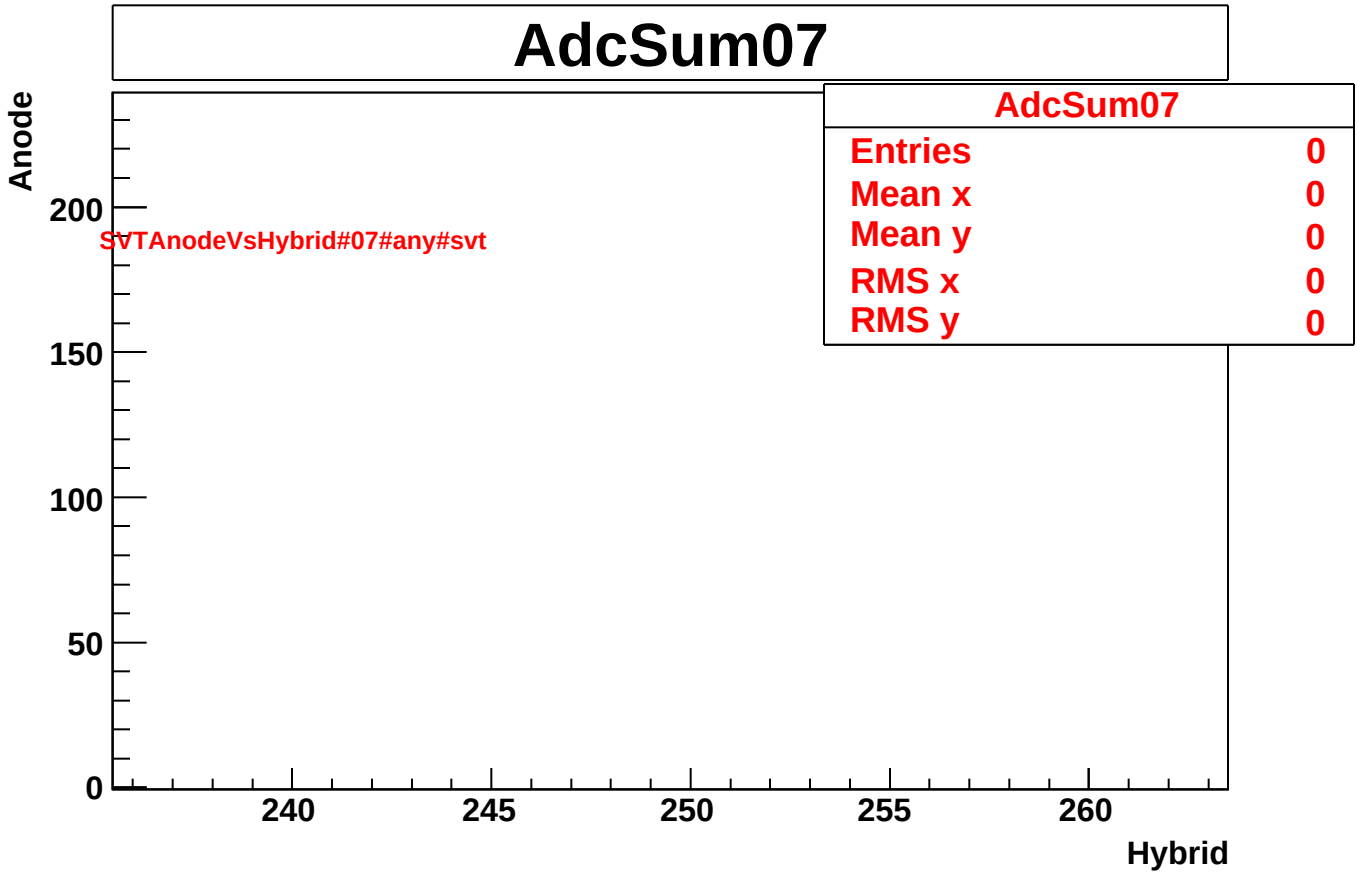

Hit04
Hit04
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
200
150
100
50
0
140
145
150
155
160
165
170
Hybrid

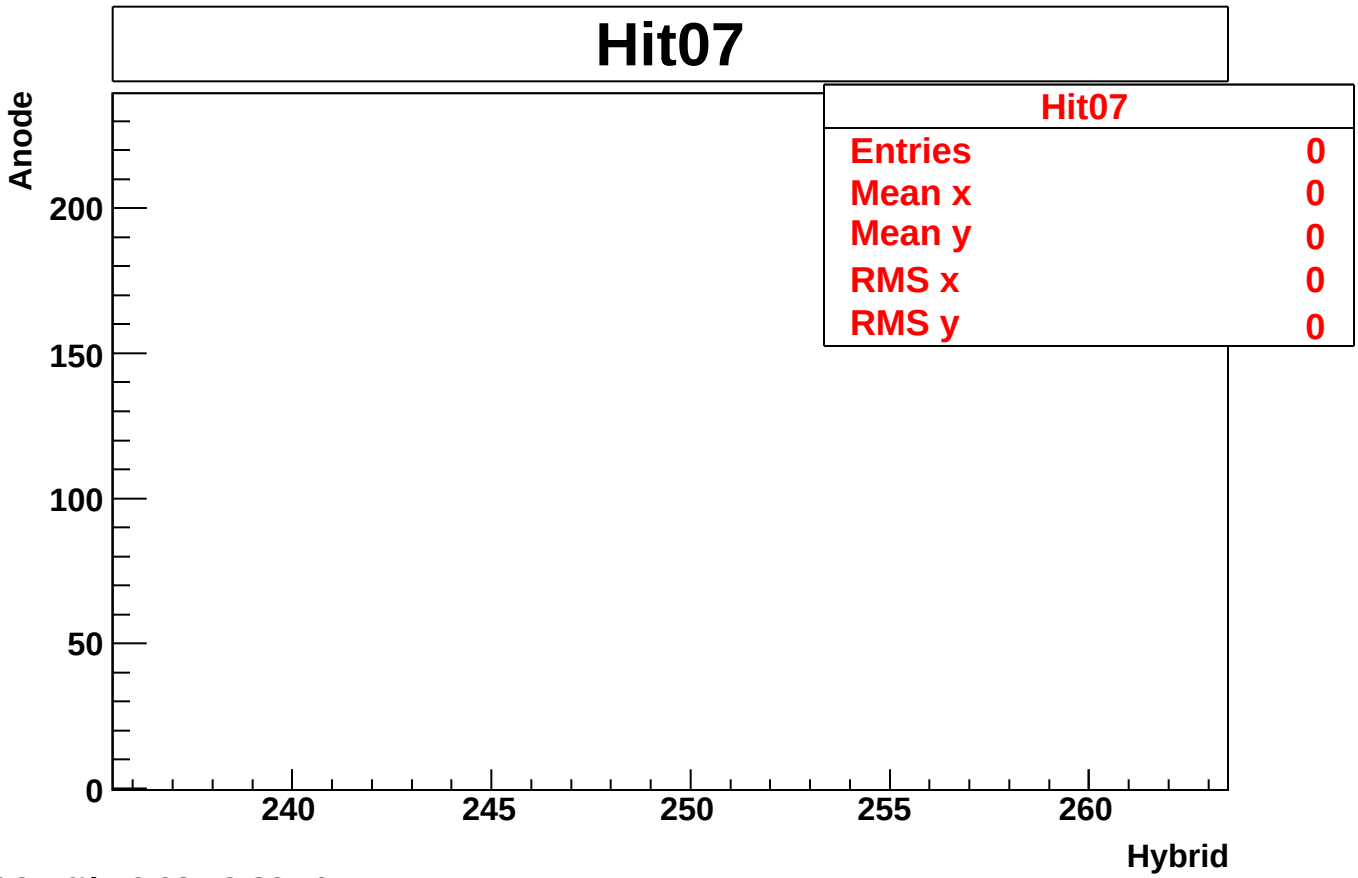

AdcSum05
AdcSum05
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#05#any#svt
Anode
0
50
100
150
200
175
180
185
190
195
200
205
Hybrid

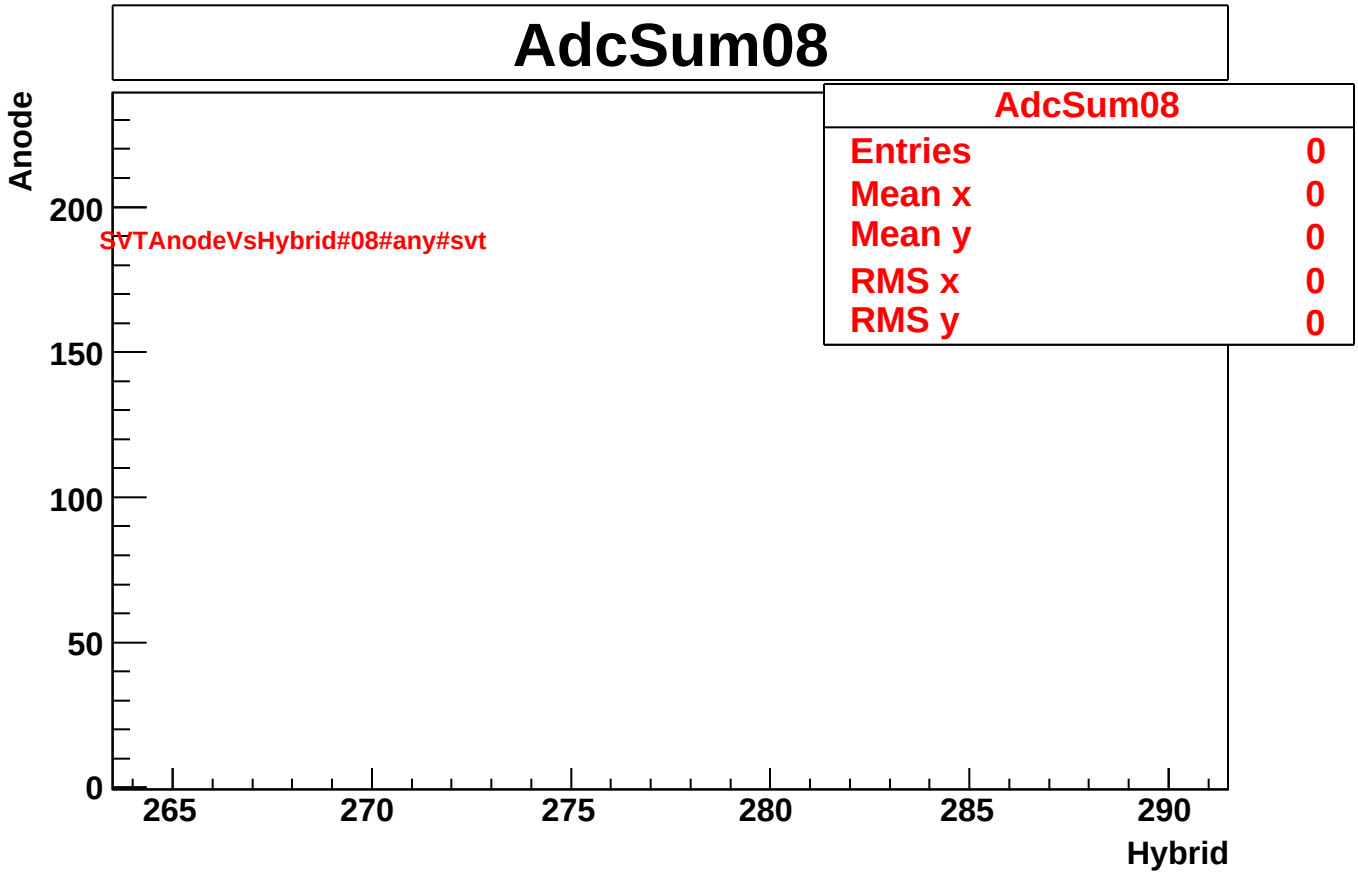

Hit05
Hit05
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
0
50
100
150
200
175
180
185
190
195
200
205
Hybrid

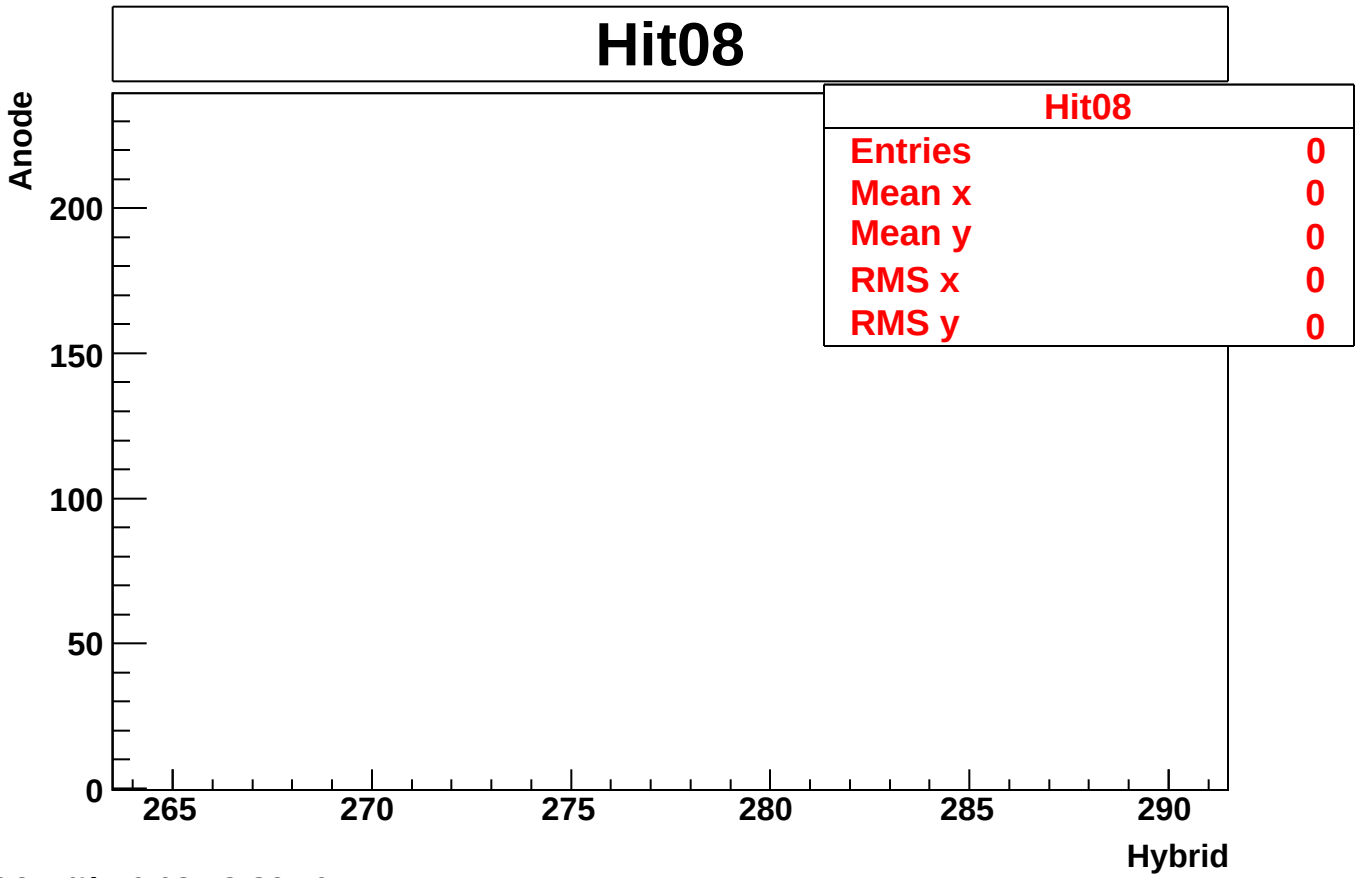

AdcSum06
AdcSum06
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#06#any#svt
Anode
0
50
100
150
200
210
215
220
225
230
235
Hybrid

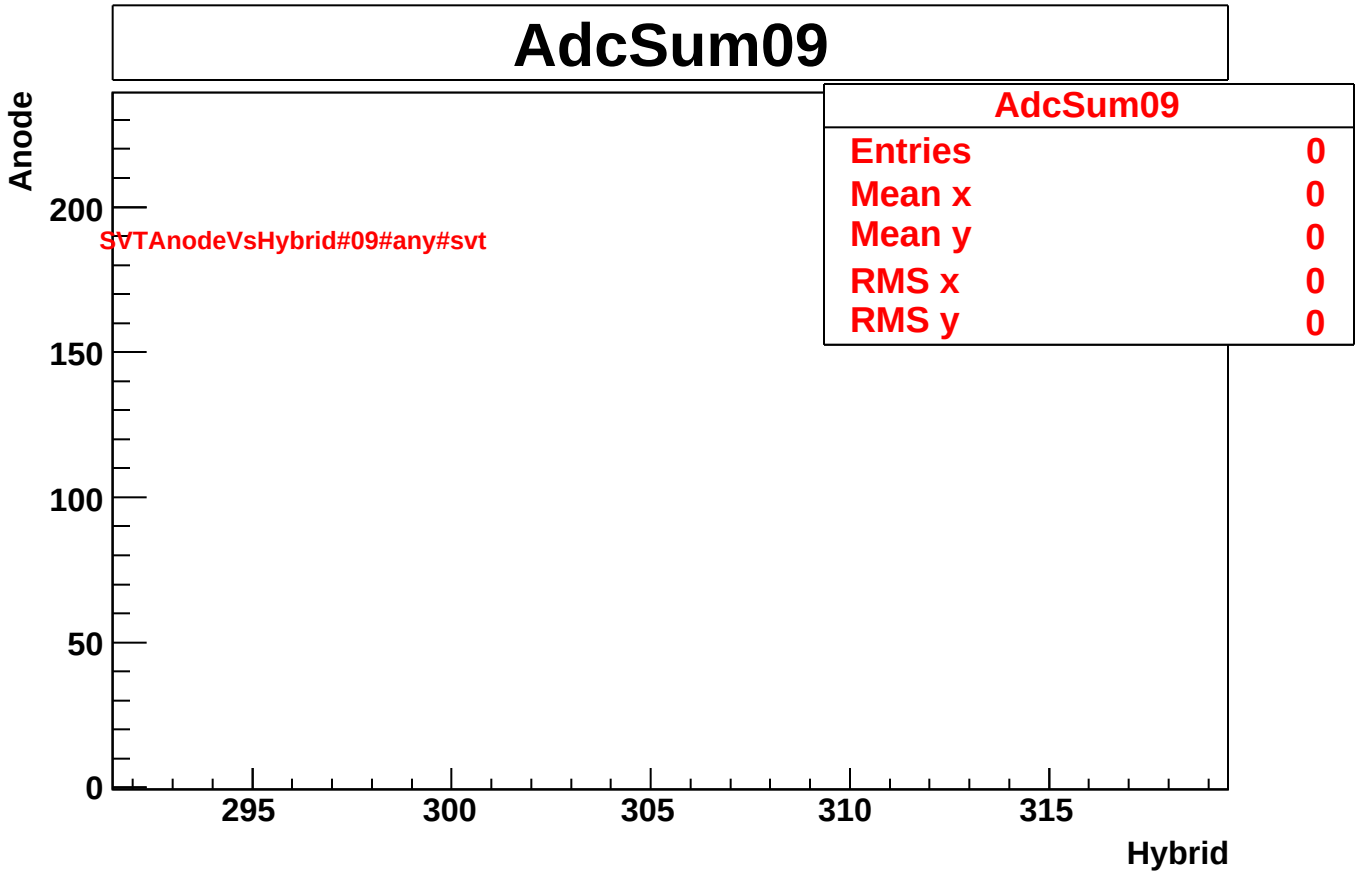

Hit06
Hit06
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
0
50
100
150
200
210
215
220
225
230
235
Hybrid

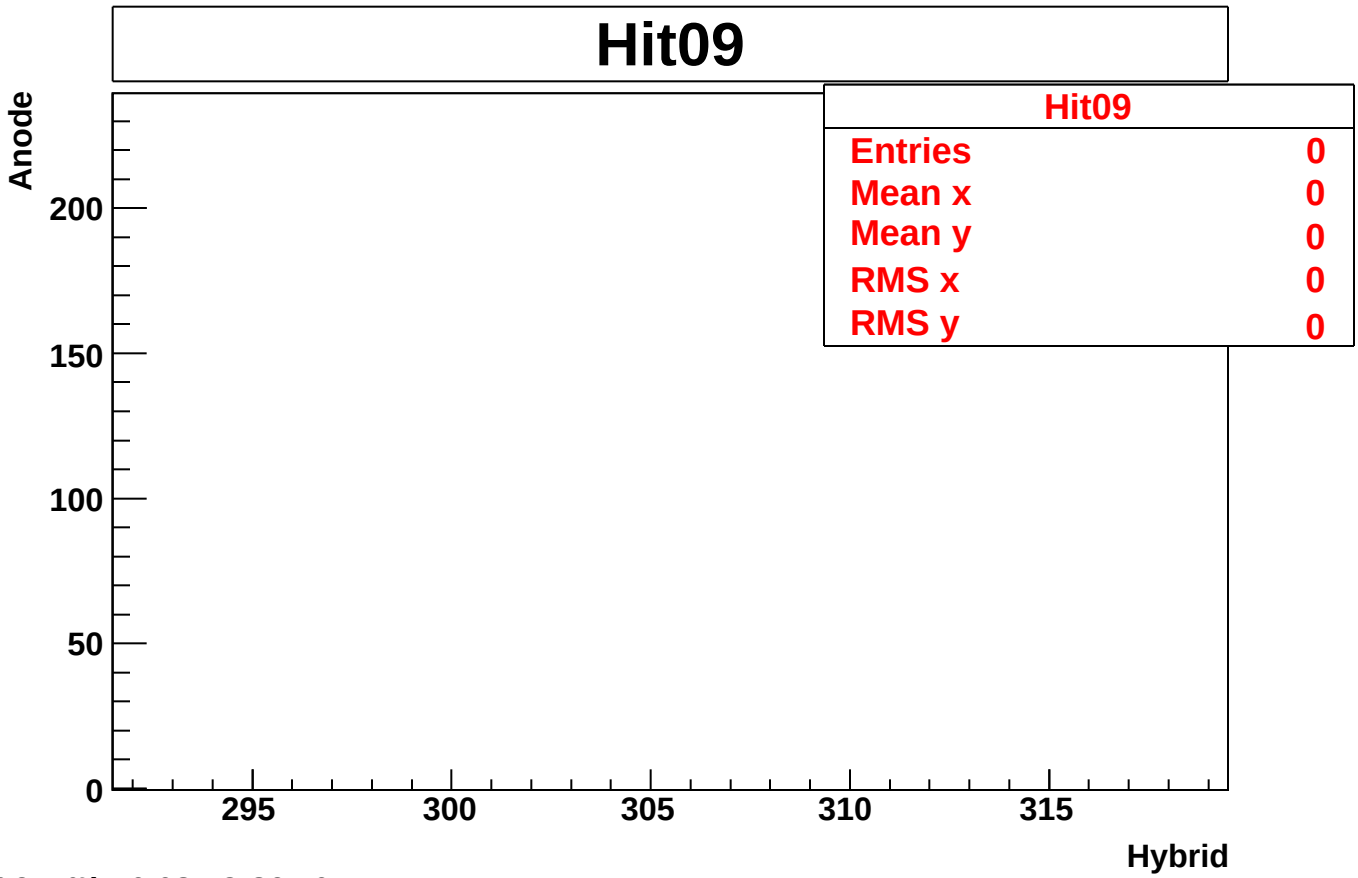

AdcSum07
AdcSum07
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#07#any#svt
Anode
200
150
100
50
0
240
245
250
255
260
Hybrid

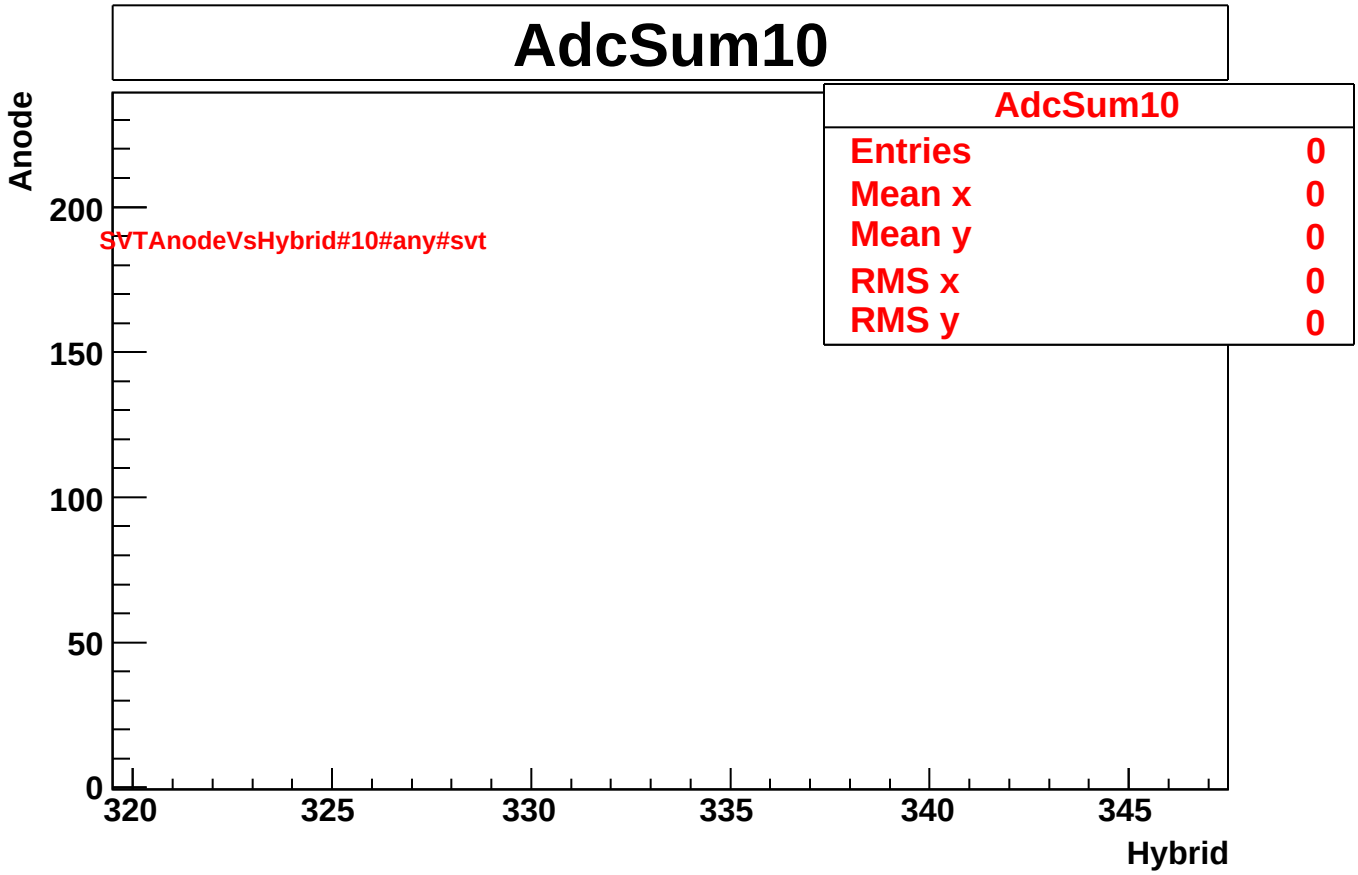

Hit07
Hit07
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
200
150
100
50
0
240
245
250
255
260
Hybrid

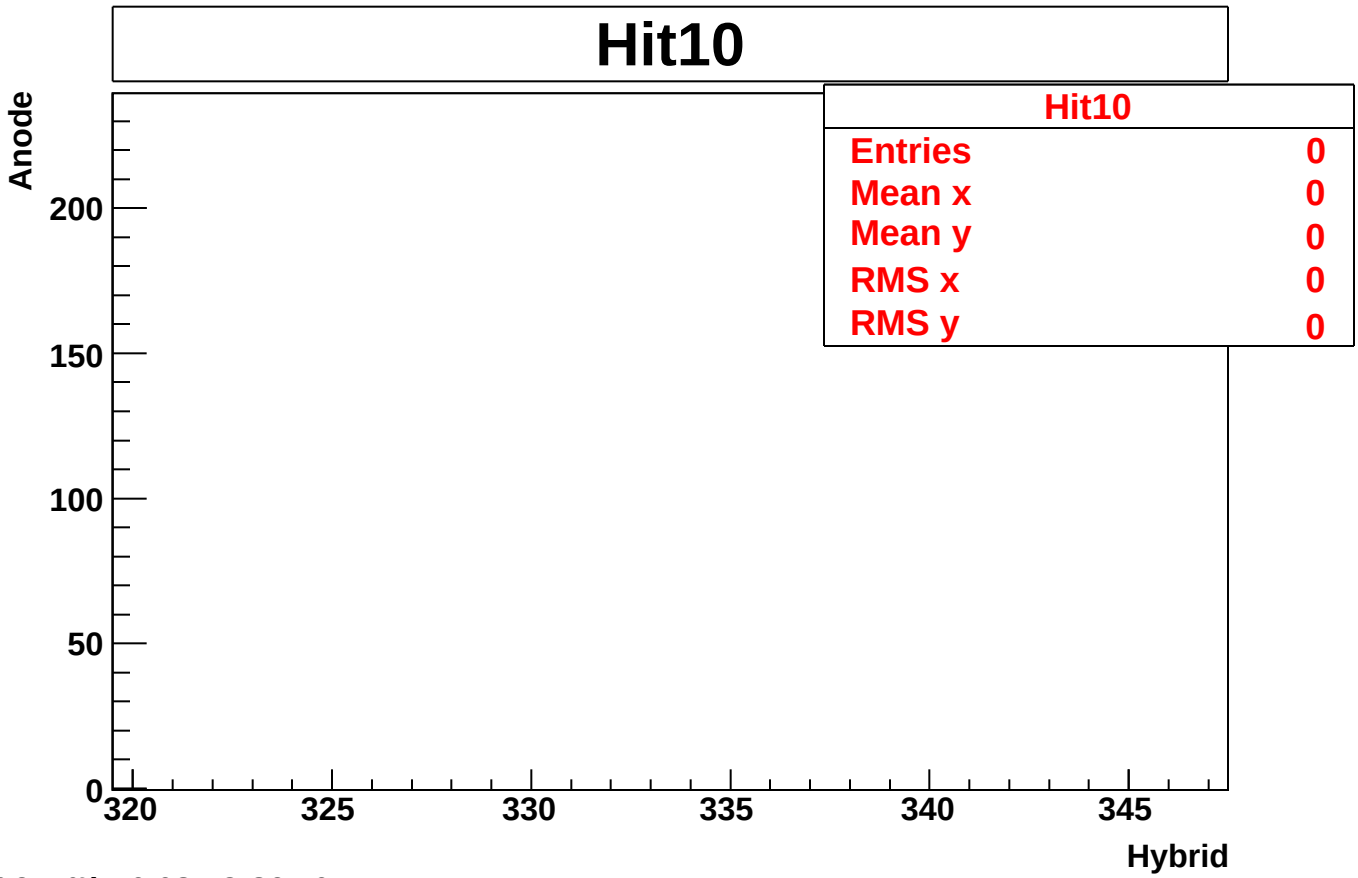

AdcSum08
AdcSum08
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#08#any#svt
Anode
0
50
100
150
200
265
270
275
280
285
290
Hybrid

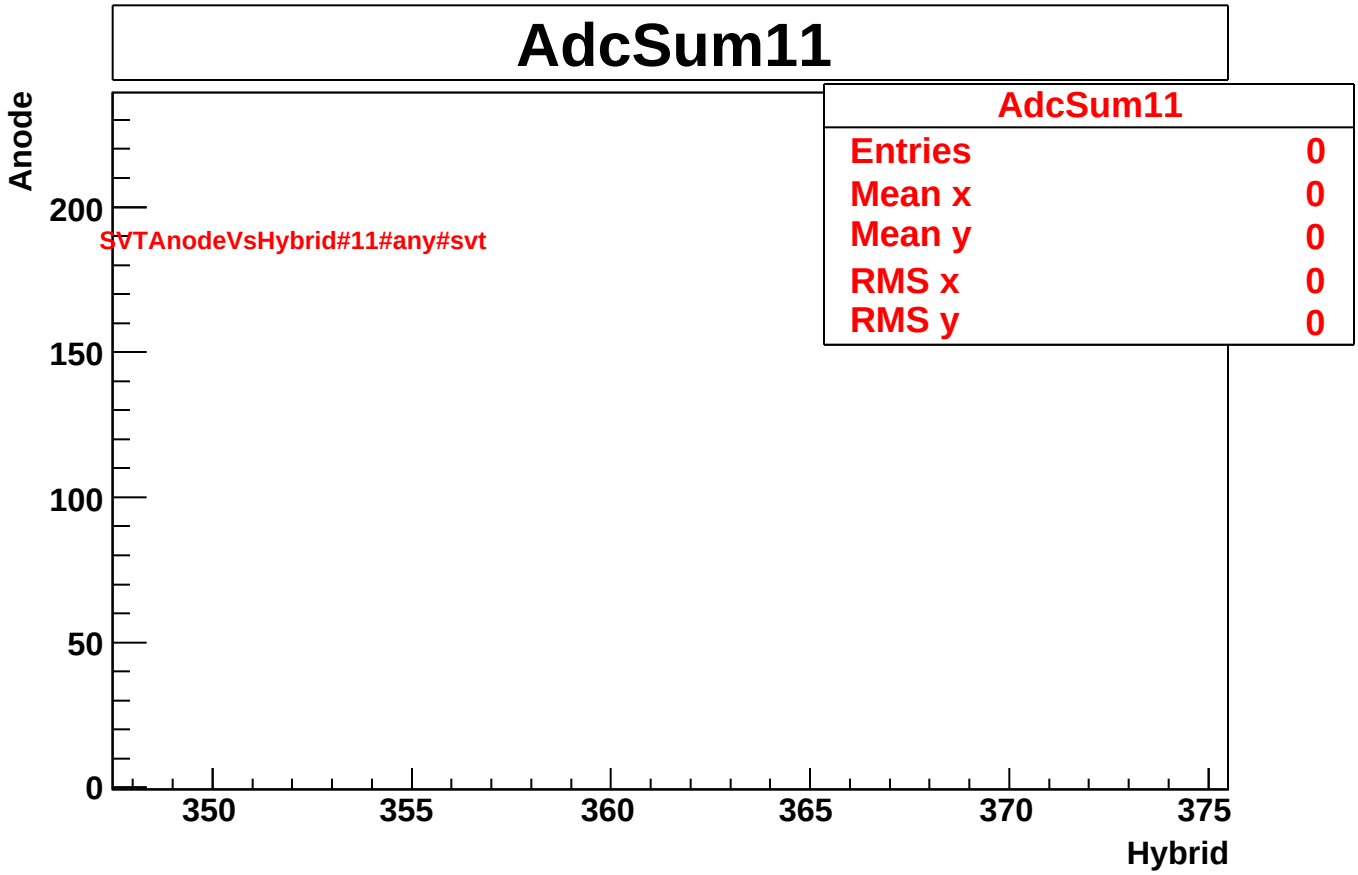

Hit08
Hit08
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
0
50
100
150
200
265
270
275
280
285
290
Hybrid

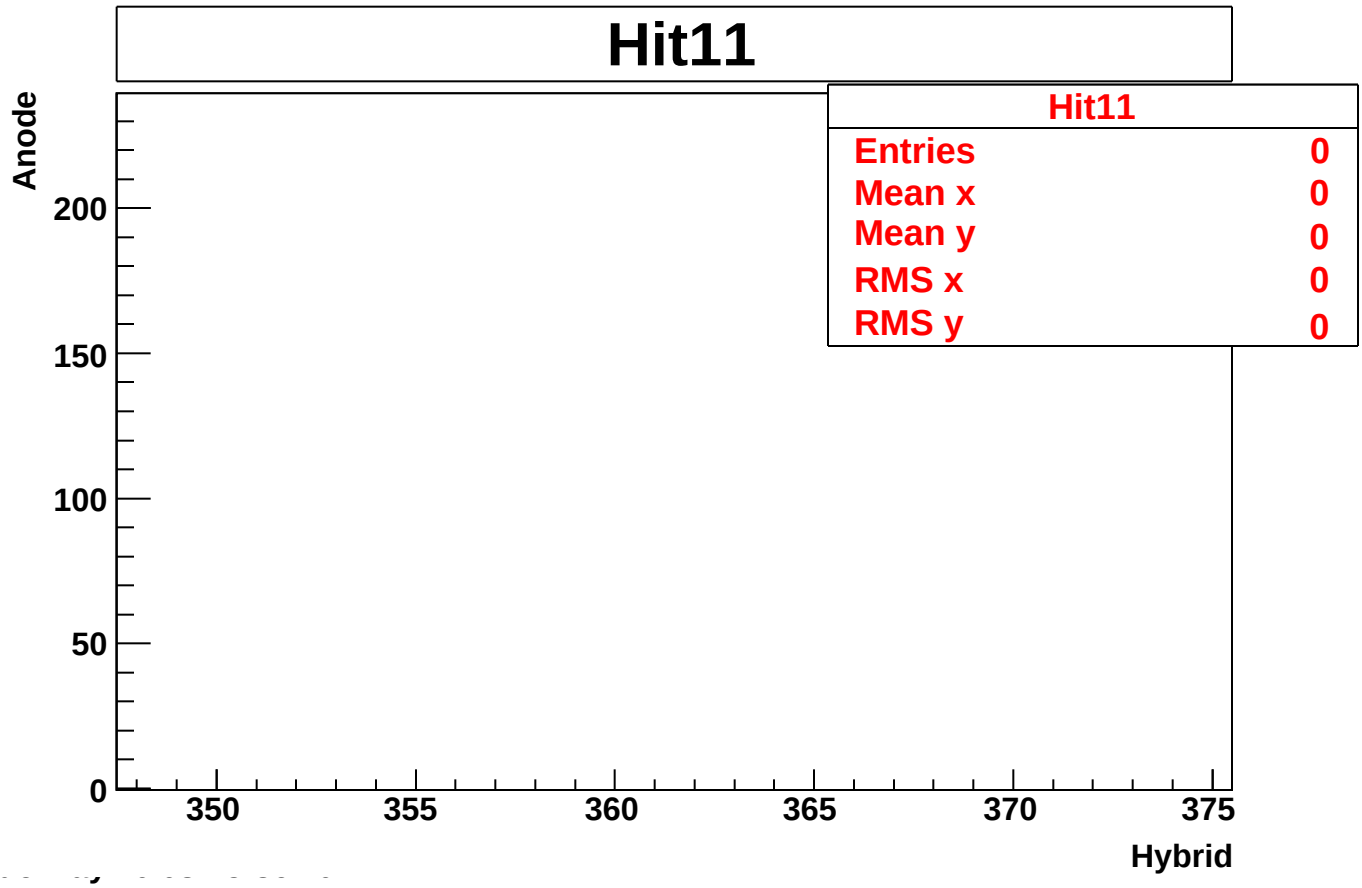

AdcSum09
AdcSum09
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#09#any#svt
Anode
200
150
100
50
0
295
300
305
310
315
Hybrid

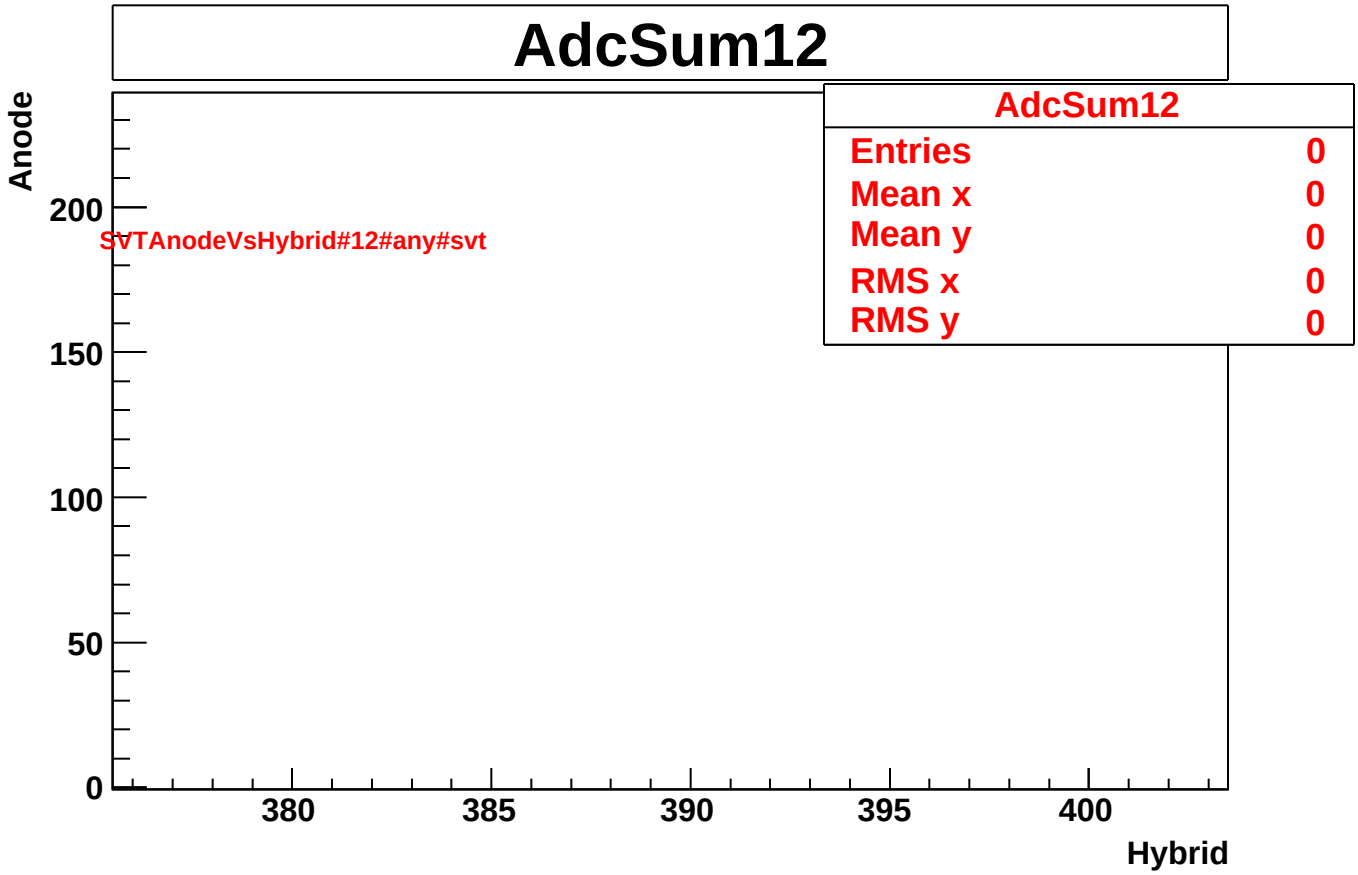

Hit09
Hit09
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
200
150
100
50
0
295
300
305
310
315
Hybrid

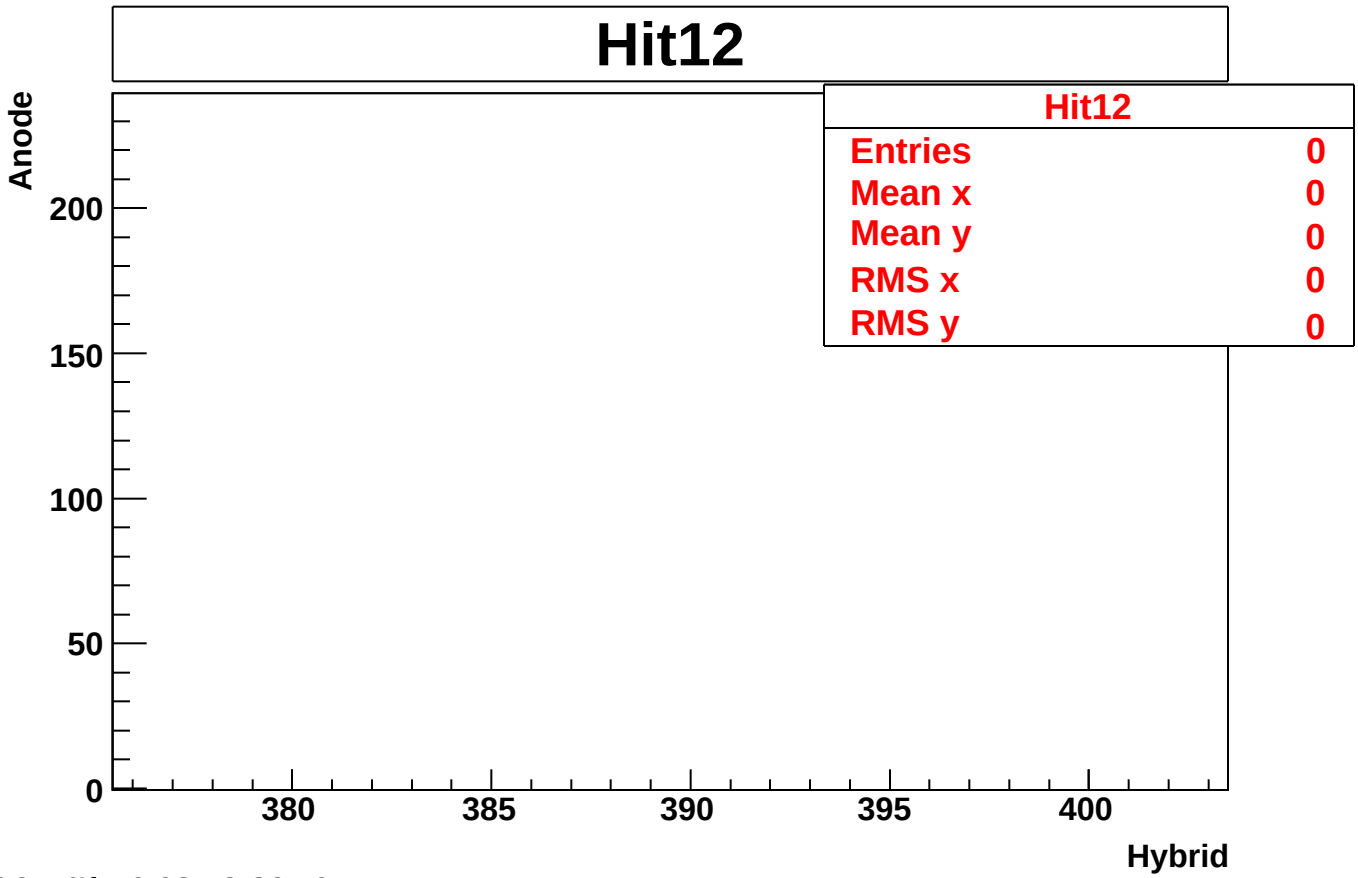

AdcSum10
AdcSum10
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#10#any#svt
Anode
0
50
100
150
200
320
325
330
335
340
345
Hybrid

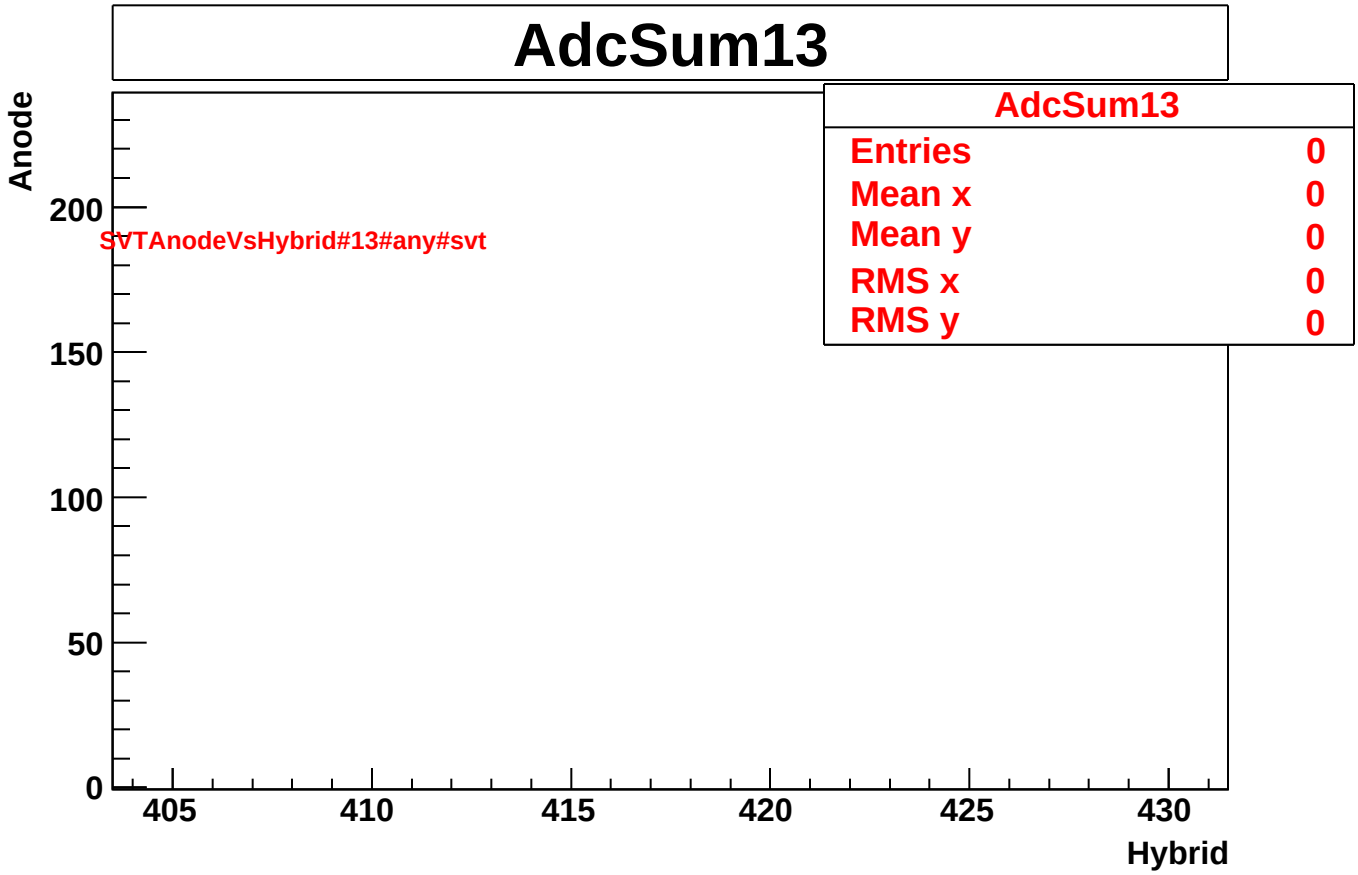

Hit10
Hit10
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
0
50
100
150
200
320
325
330
335
340
345
Hybrid

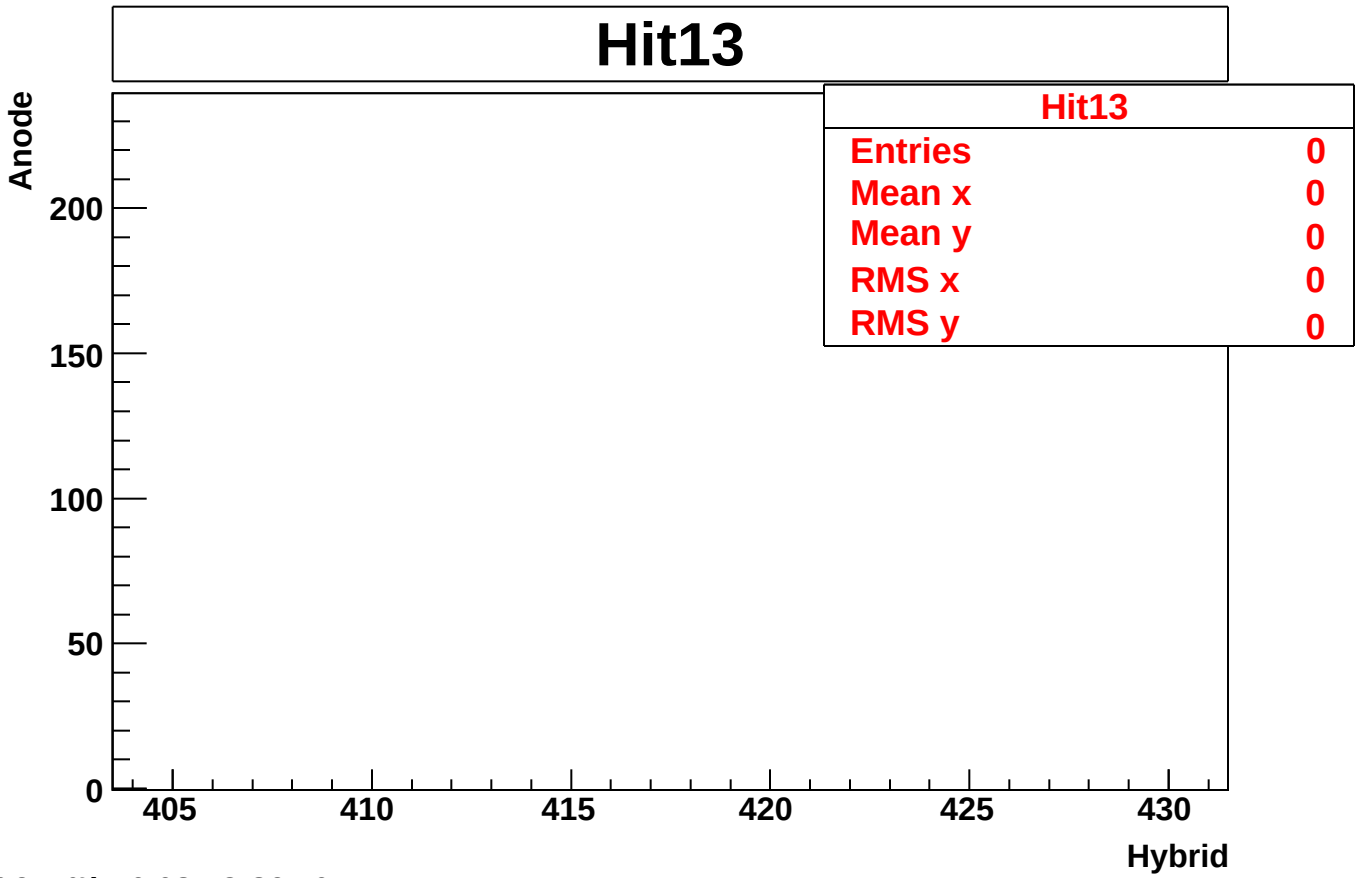

AdcSum11
AdcSum11
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#11#any#svt
Anode
200
150
100
50
0
350
355
360
365
370
375
Hybrid


Hit11
Hit11
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
200
150
100
50
0
350
355
360
365
370
375
Hybrid

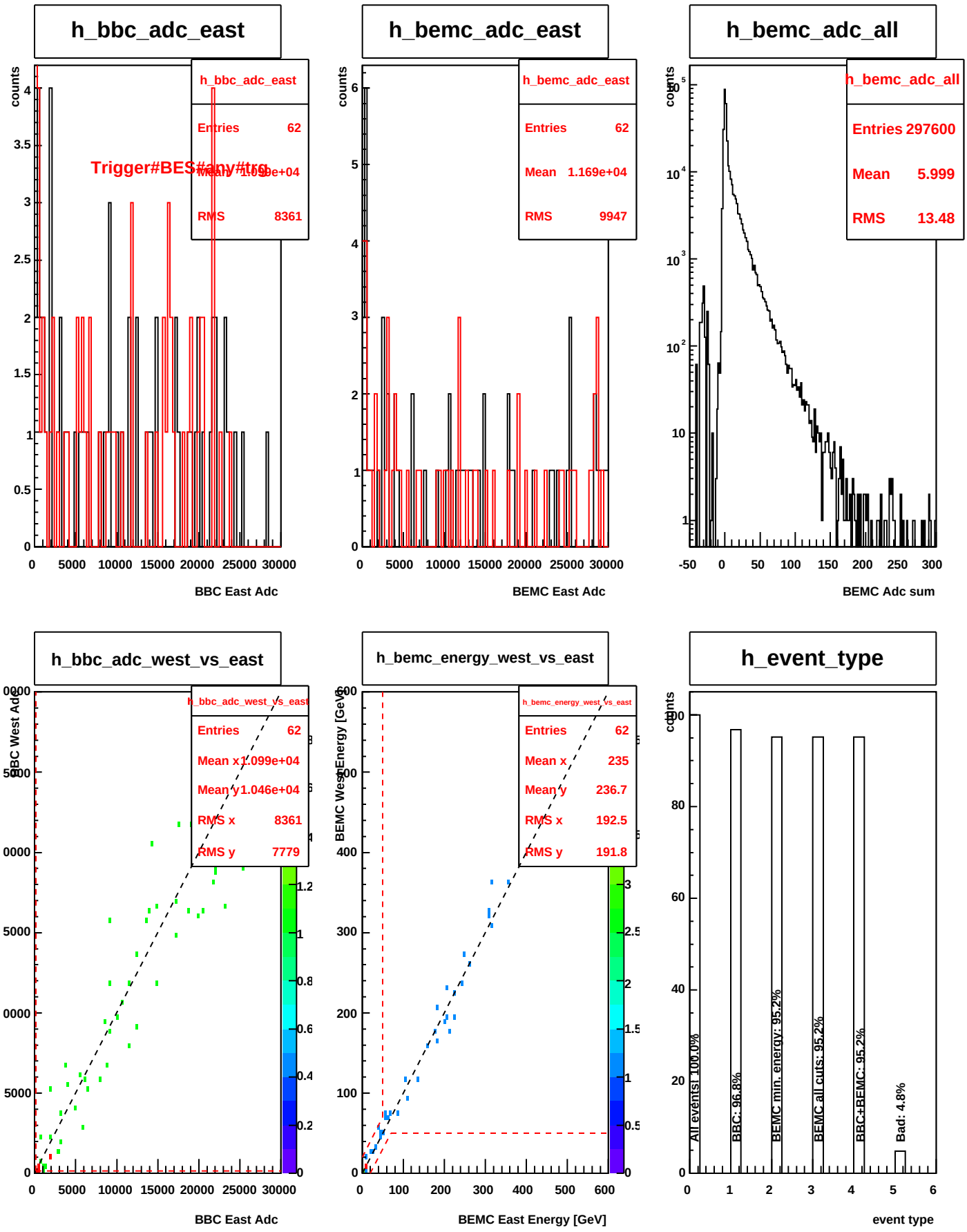

AdcSum12
AdcSum12
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#12#any#svt
Anode
200
150
100
50
0
380
385
390
395
400
Hybrid

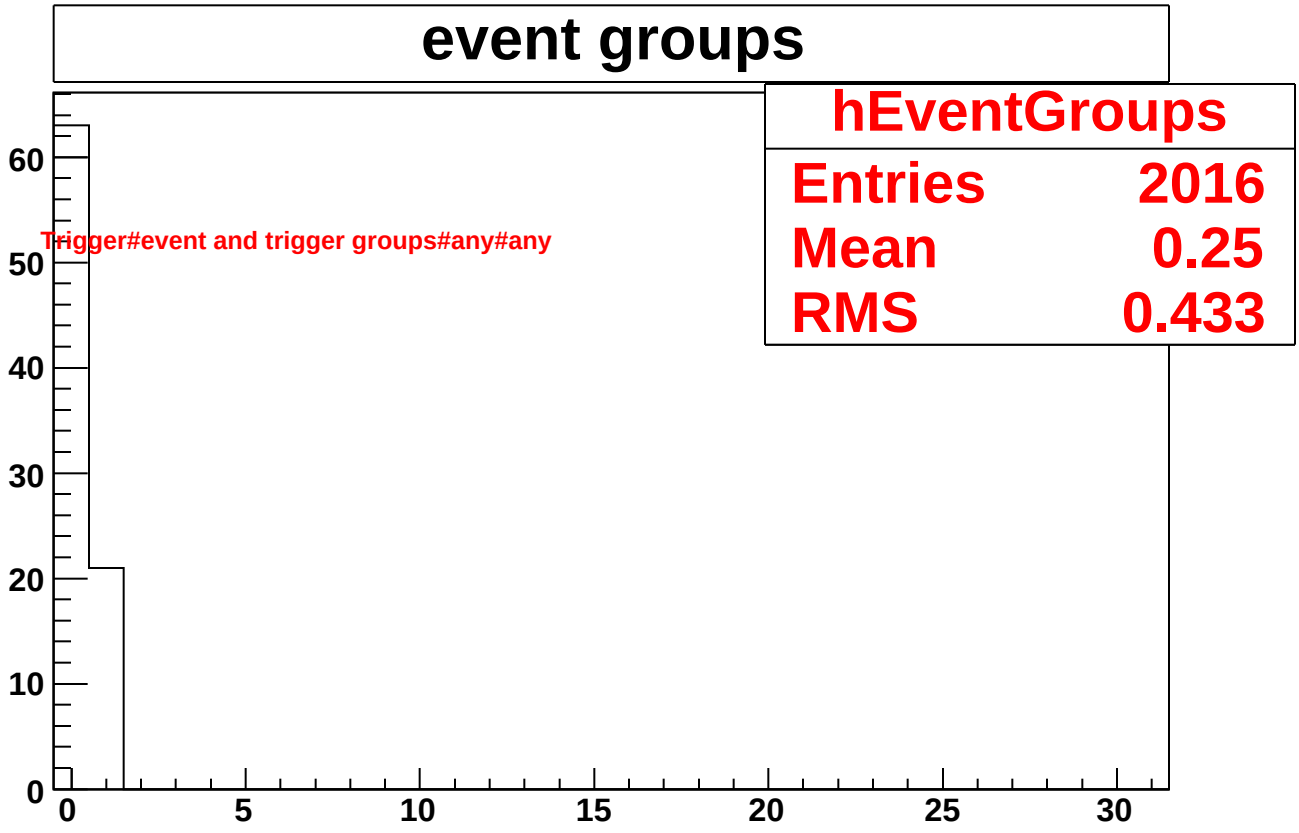

Hit12
Hit12
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
200
150
100
50
0
380
385
390
395
400
Hybrid

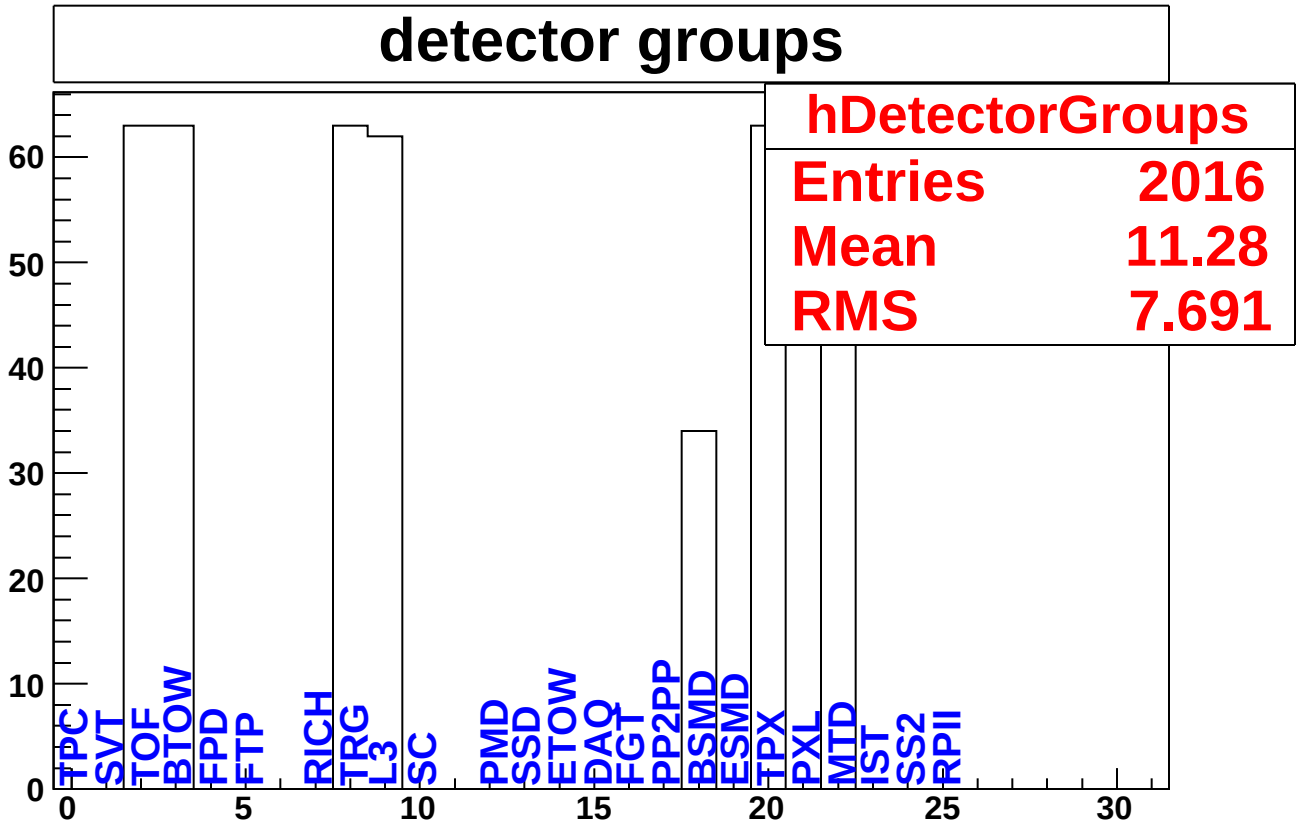

AdcSum13
AdcSum13
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
SVTAnodeVsHybrid#13#any#svt
Anode
0
50
100
150
200
405
410
415
420
425
430
Hybrid

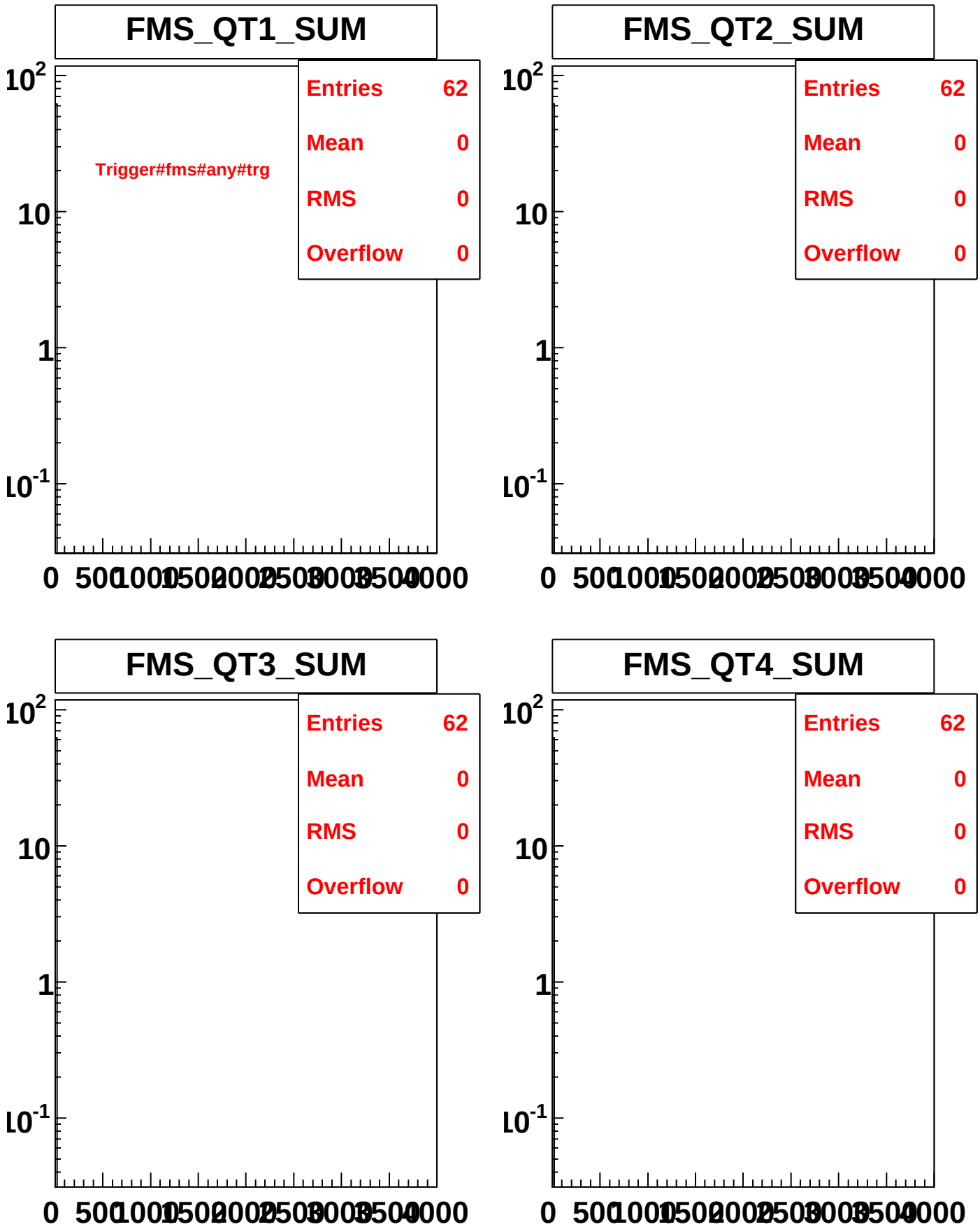

Hit13
Hit13
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
Anode
0
50
100
150
200
405
410
415
420
425
430
Hybrid

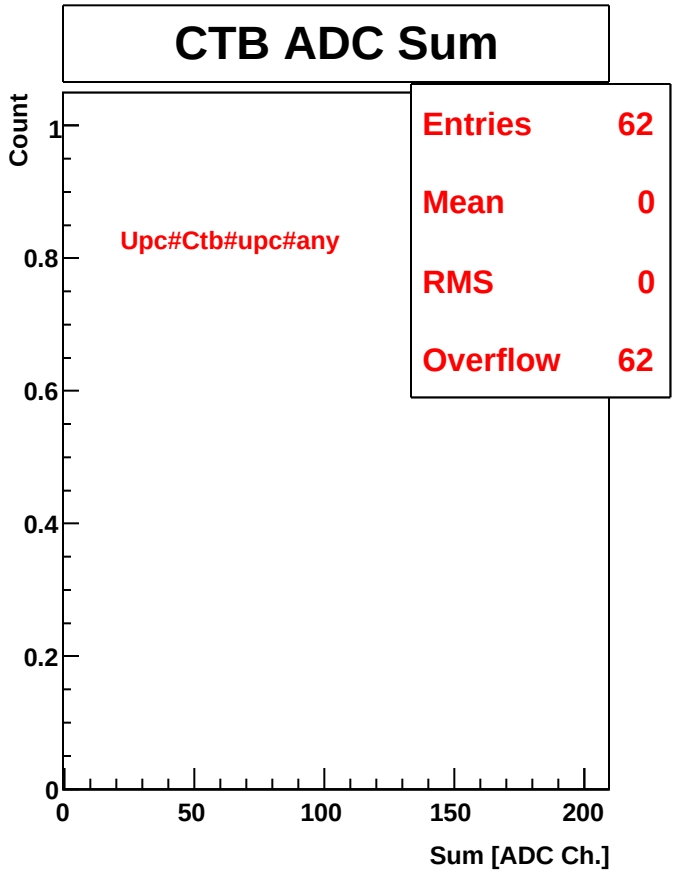
Ssd#Pedestal#pedestal#ssd

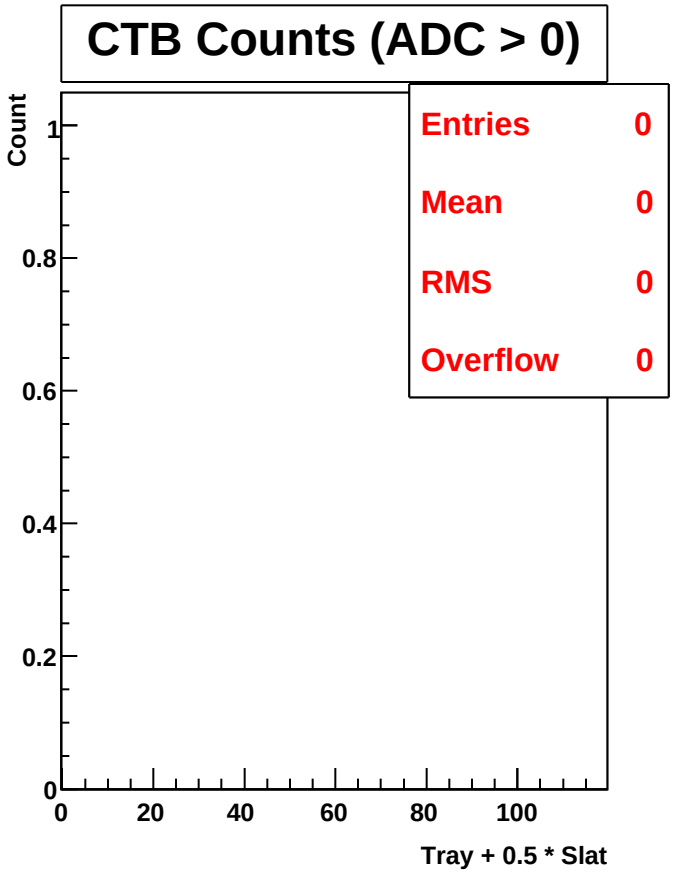

h_bbc_adc_east
h_bbc_adc_east
Entries 62
Mean 1.099e+04
RMS 8361
Trigger#BES#any#trg
counts
BBC East Adc
h_bemc_adc_east
h_bemc_adc_east
Entries 62
Mean 1.169e+04
RMS 9947
counts
BEMC East Adc
h_bemc_adc_all
h_bemc_adc_all
Entries 297600
Mean 5.999
RMS 13.48
counts
BEMC Adc sum
h_bbc_adc_west_vs_east
h_bbc_adc_west_vs_east
Entries 62
Mean x 1.099e+04
Mean y 1.046e+04
RMS x 8361
RMS y 7779
BBC West Adc
BBC East Adc
h_bemc_energy_west_vs_east
h_bemc_energy_west_vs_east
Entries 62
Mean x 235
Mean y 236.7
RMS x 192.5
RMS y 191.8
BEMC West Energy [GeV]
BEMC East Energy [GeV]
h_event_type
counts
All events: 100.0%
BBC: 96.8%
BEMC min. energy: 95.2%
BEMC all cuts: 95.2%
BBC+BEMC: 95.2%
Bad: 4.8%
event type

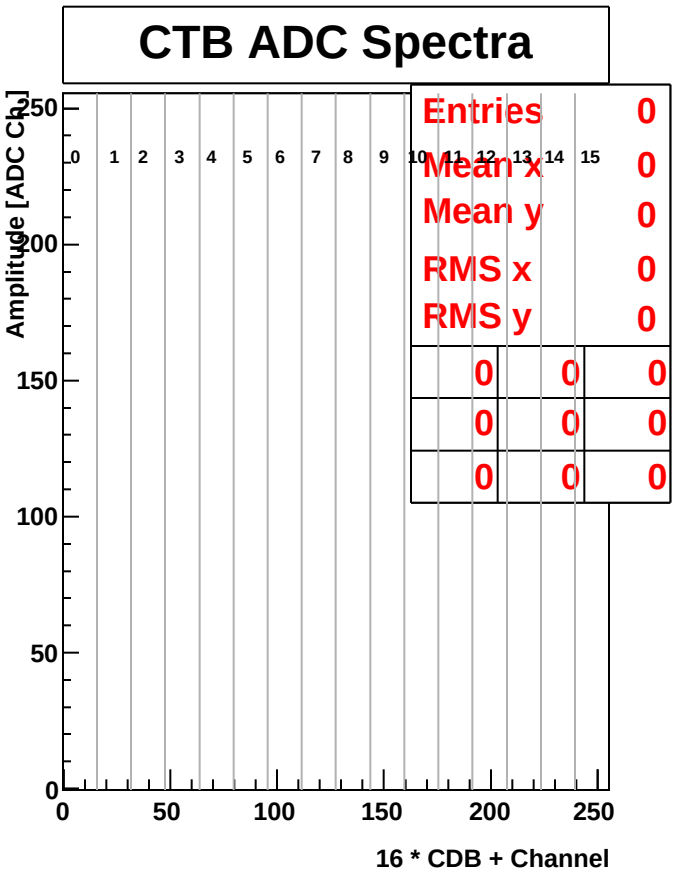

event groups
hEventGroups
Entries 2016
Mean 0.25
RMS 0.433
Trigger#event and trigger groups#any#any
0
10
20
30
40
50
60
0
5
10
15
20
25
30

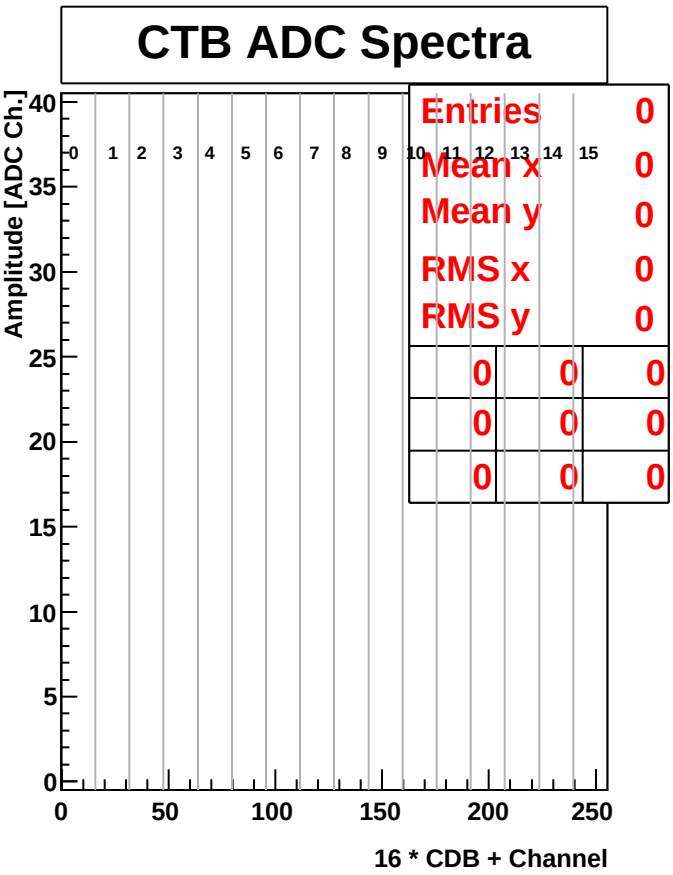

detector groups
hDetectorGroups
Entries 2016
Mean 11.28
RMS 7.691
TPC
SVT
TOF
BTOW
FPD
FTP
RICH
TRG
L3
SC
PMD
SSD
ETOW
DAQ
FGT
PP2PP
BSMD
ESMD
TPX
PXL
MTD
IST
SS2
RPII
0
10
20
30
40
50
60
0
5
10
15
20
25
30

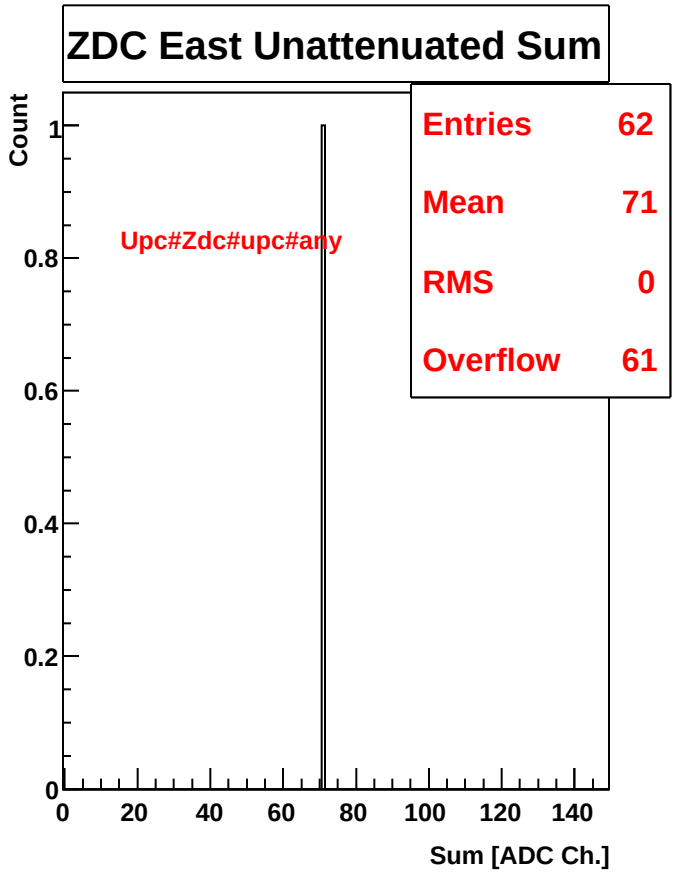

FMS_QT1_SUM
Entries 62
Mean 0
RMS 0
Overflow 0
Trigger#fms#any#trg
FMS_QT2_SUM
Entries 62
Mean 0
RMS 0
Overflow 0
FMS_QT3_SUM
Entries 62
Mean 0
RMS 0
Overflow 0
FMS_QT4_SUM
Entries 62
Mean 0
RMS 0
Overflow 0

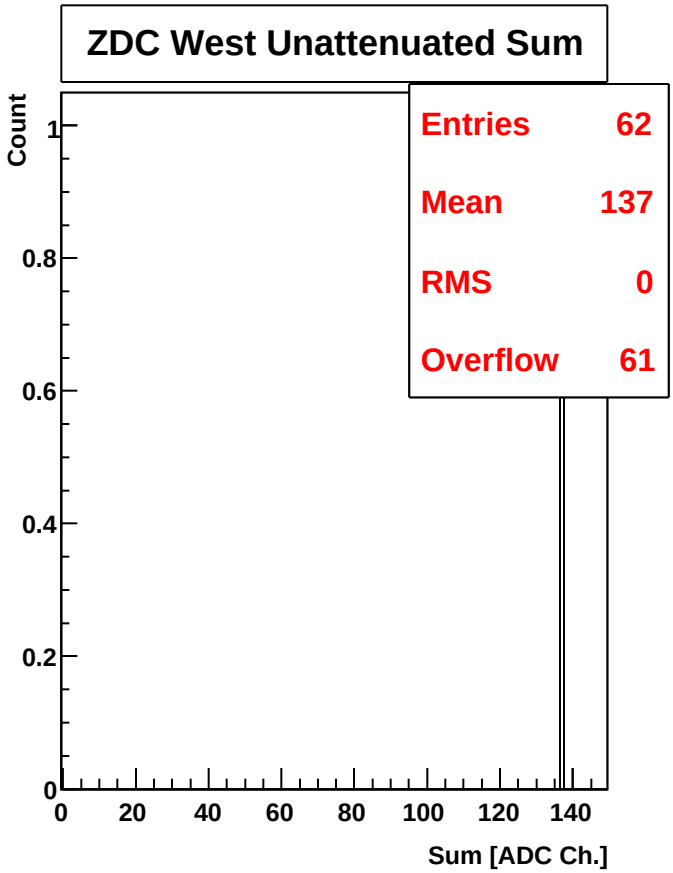

CTB ADC Sum
Count
Upc#Ctb#upc#any
Entries 62
Mean 0
RMS 0
Overflow 62
Sum [ADC Ch.]

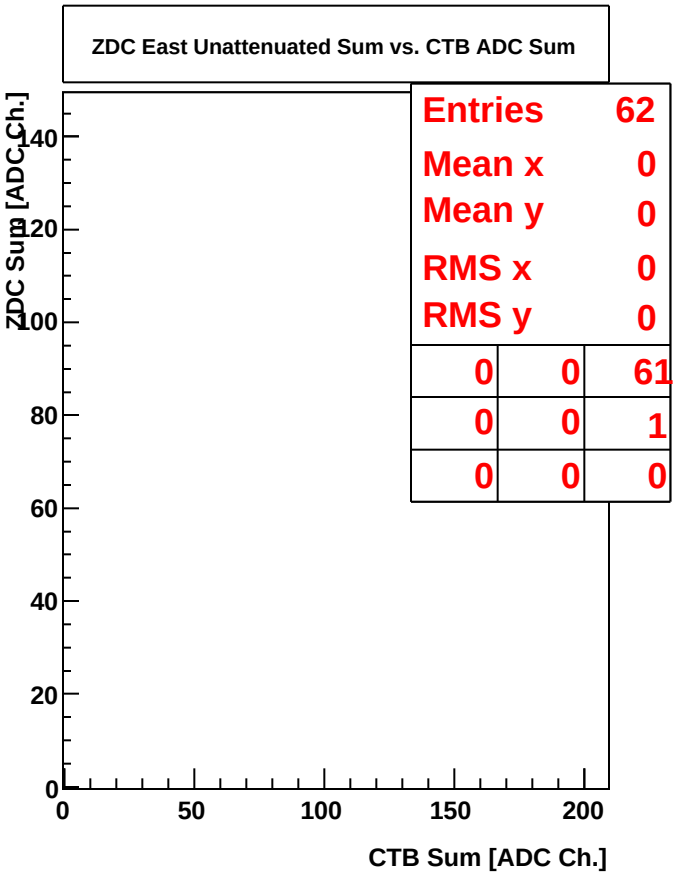

CTB Counts (ADC > 0)
Count
Entries 0
Mean 0
RMS 0
Overflow 0
Tray + 0.5 * Slat

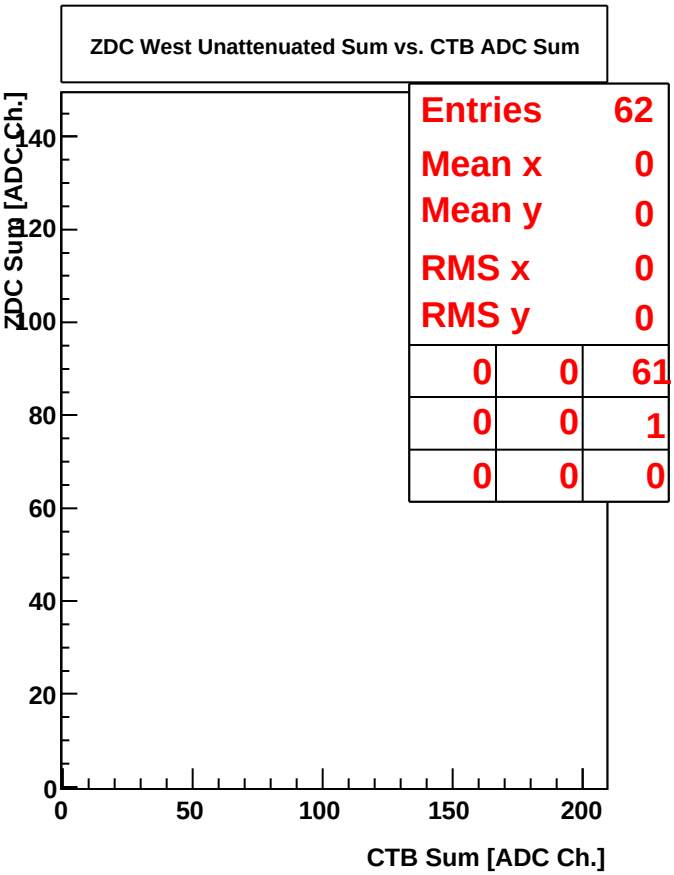

CTB ADC Spectra
Amplitude [ADC Ch.]
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
16 * CDB + Channel


CTB ADC Spectra
Amplitude [ADC Ch.]
Entries 0
Mean x 0
Mean y 0
RMS x 0
RMS y 0
16 * CDB + Channel


ZDC East Unattenuated Sum
Entries 62
Mean 71
RMS 0
Overflow 61
Upc#Zdc#upc#any
Count
Sum [ADC Ch.]


ZDC West Unattenuated Sum
Entries 62
Mean 137
RMS 0
Overflow 61
Count
Sum [ADC Ch.]


ZDC East Unattenuated Sum vs. CTB ADC Sum
Entries 62
Mean x 0
Mean y 0
RMS x 0
RMS y 0
0 0 61
0 0 1
0 0 0
ZDC Sum [ADC Ch.]
CTB Sum [ADC Ch.]


ZDC West Unattenuated Sum vs. CTB ADC Sum
Entries 62
Mean x 0
Mean y 0
RMS x 0
RMS y 0
0 0 61
0 0 1
0 0 0
ZDC Sum [ADC Ch.]
CTB Sum [ADC Ch.]