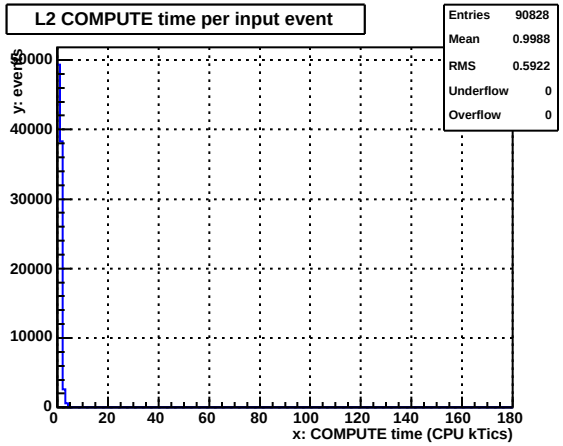
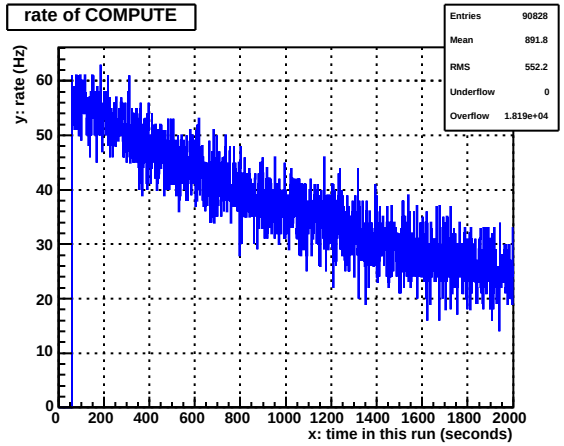
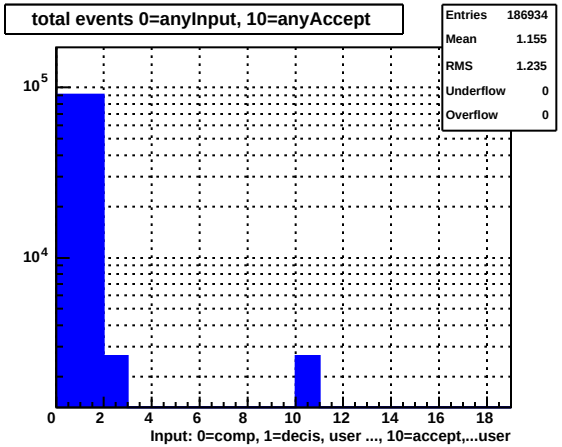
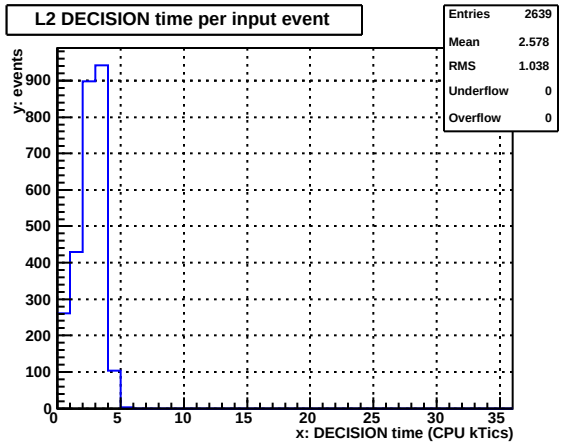
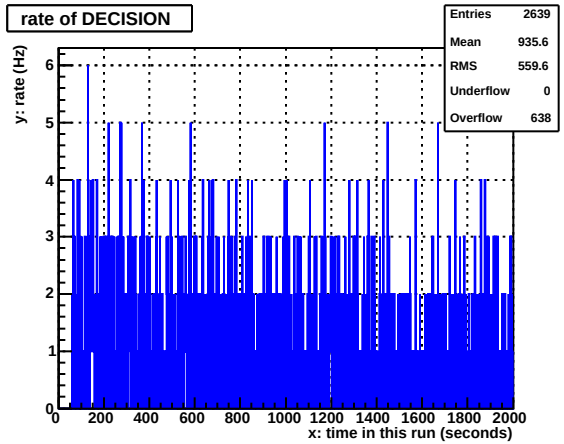
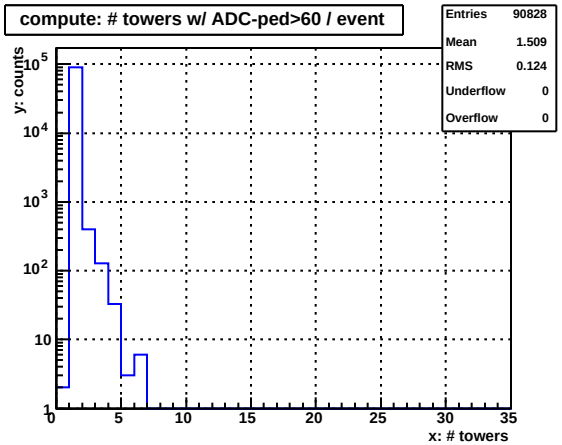

L2 COMPUTE time per input event
Entries 90828
Mean 0.9988
RMS 0.5922
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

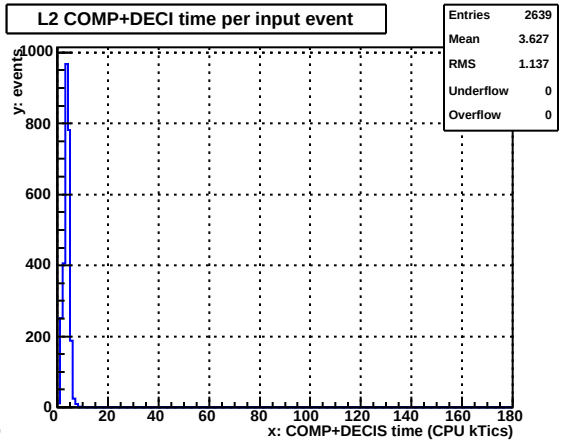

rate of COMPUTE
Entries 90828
Mean 891.8
RMS 552.2
Underflow 0
Overflow 1.819e+04
y: rate (Hz)
x: time in this run (seconds)

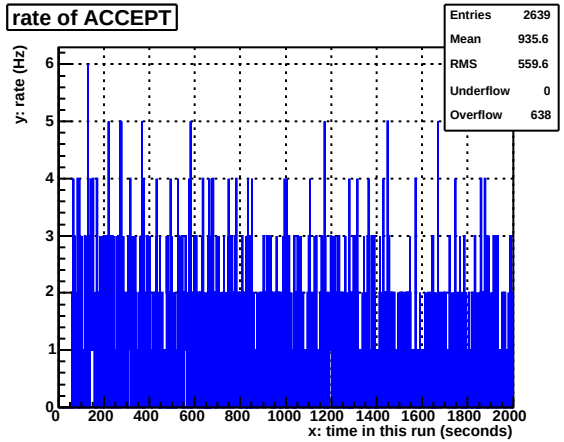

total events 0=anyInput, 10=anyAccept
Entries 186934
Mean 1.155
RMS 1.235
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

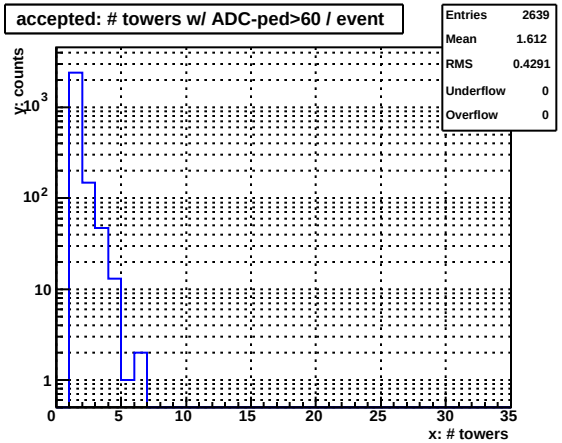

L2 DECISION time per input event
Entries 2639
Mean 2.578
RMS 1.038
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

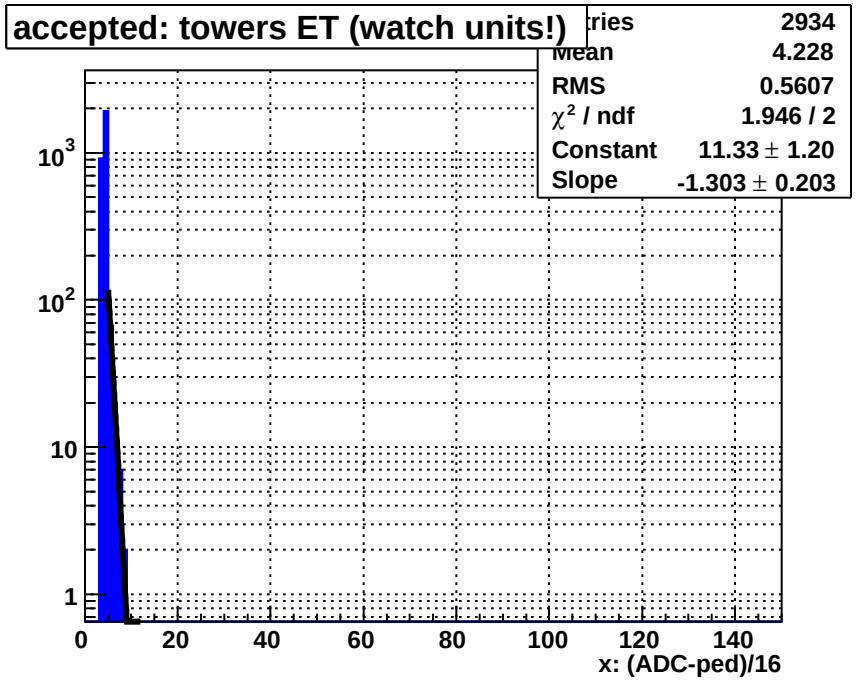

rate of DECISION
Entries 2639
Mean 935.6
RMS 559.6
Underflow 0
Overflow 638
y: rate (Hz)
x: time in this run (seconds)

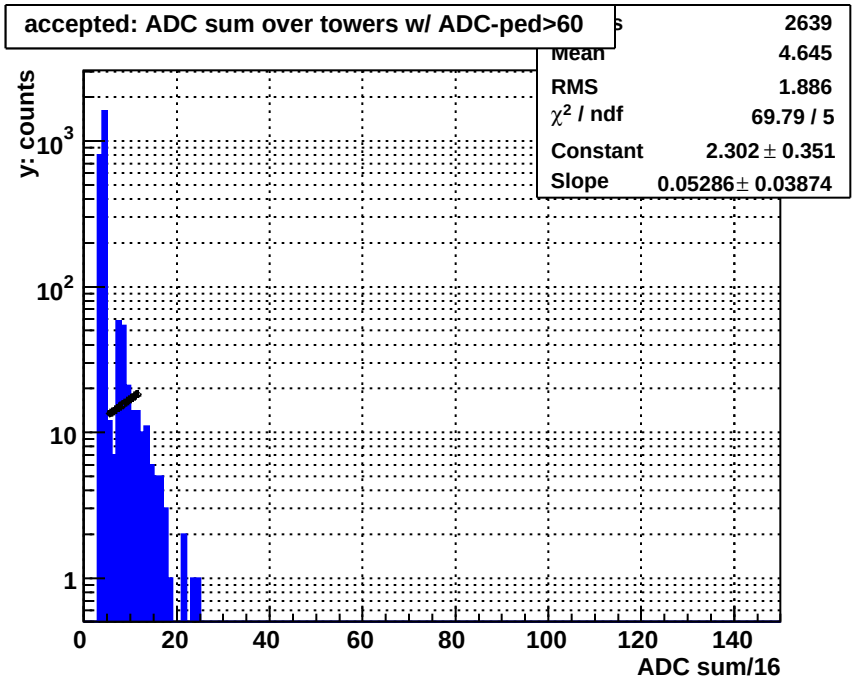

compute: # towers w/ ADC-ped>60 / event
Entries 90828
Mean 1.509
RMS 0.124
Underflow 0
Overflow 0
y: counts
x: # towers

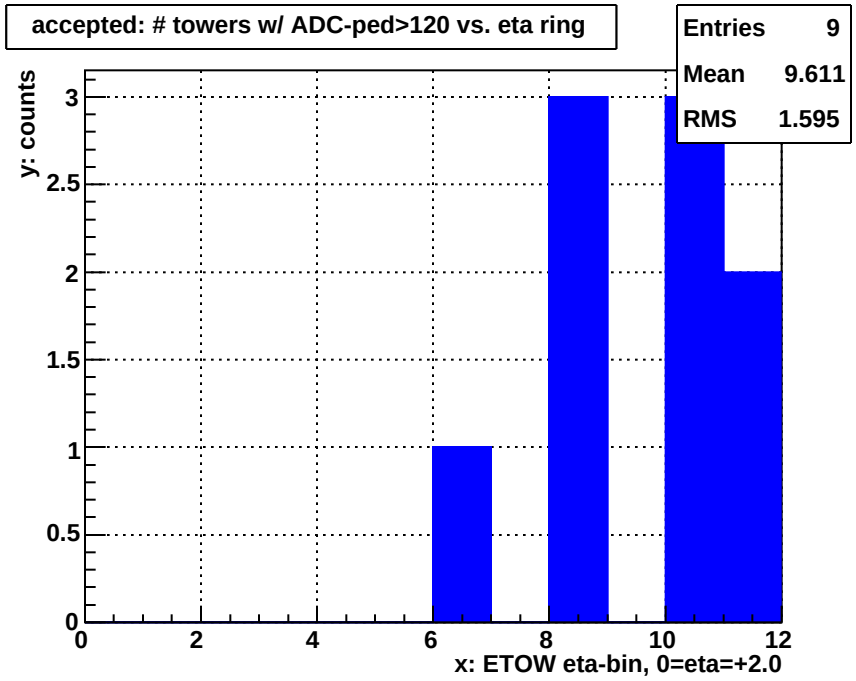

L2 COMP+DECI time per input event
Entries 2639
Mean 3.627
RMS 1.137
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

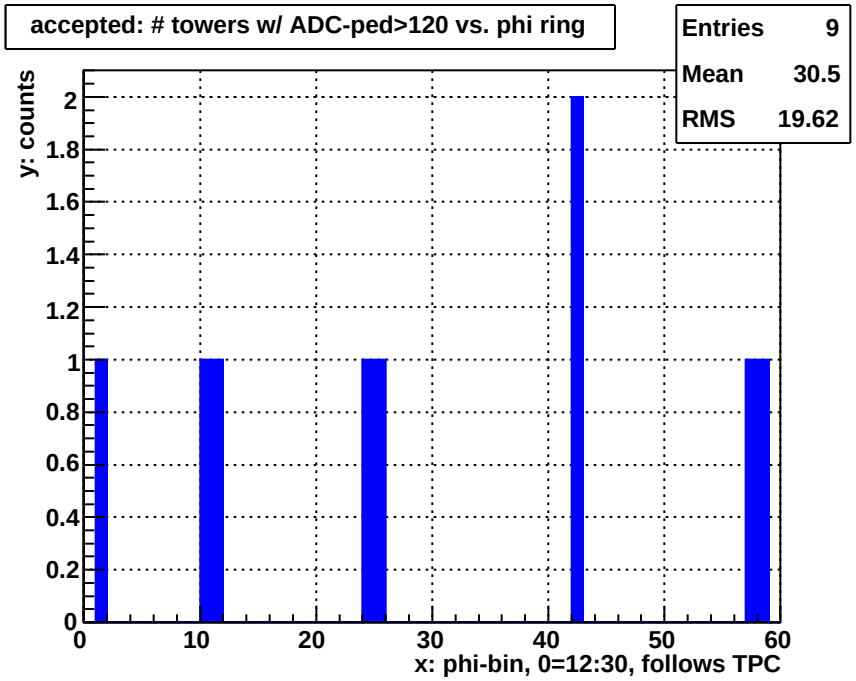

rate of ACCEPT
Entries 2639
Mean 935.6
RMS 559.6
Underflow 0
Overflow 638
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 2639
Mean 1.612
RMS 0.4291
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
Entries 2934
Mean 4.228
RMS 0.5607
χ² / ndf 1.946 / 2
Constant 11.33 ± 1.20
Slope -1.303 ± 0.203
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
Entries 2639
Mean 4.645
RMS 1.886
χ² / ndf 69.79 / 5
Constant 2.302 ± 0.351
Slope 0.05286 ± 0.03874
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 9
Mean 9.611
RMS 1.595
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 9
Mean 30.5
RMS 19.62
y: counts
x: phi-bin, 0=12:30, follows TPC