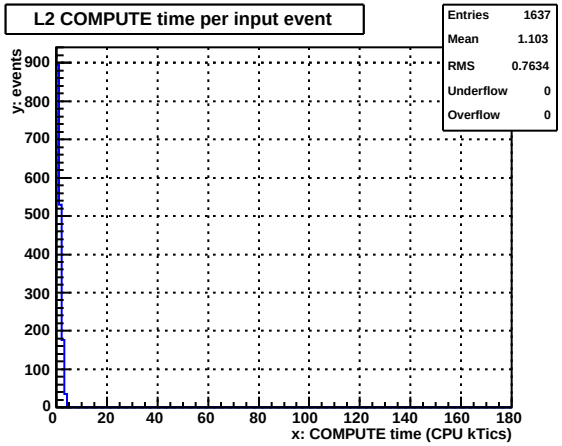
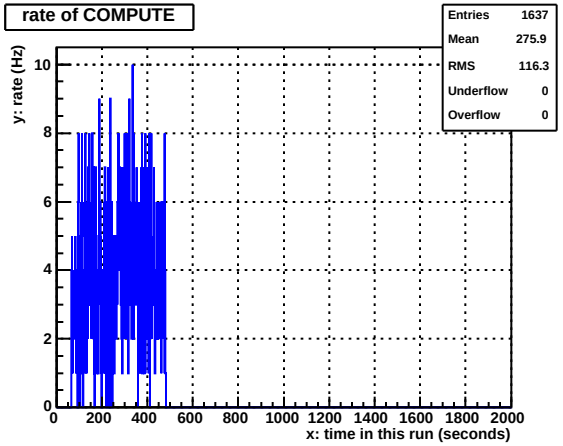
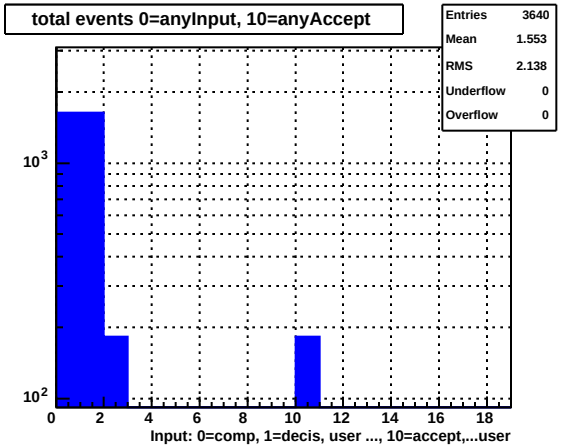
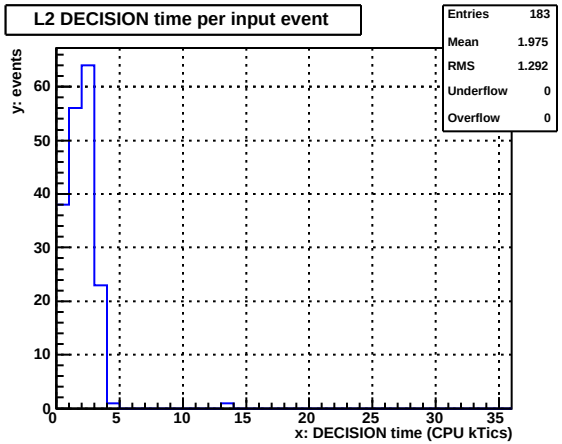
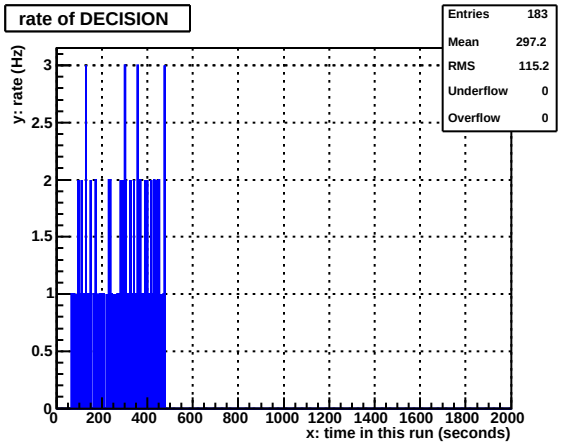
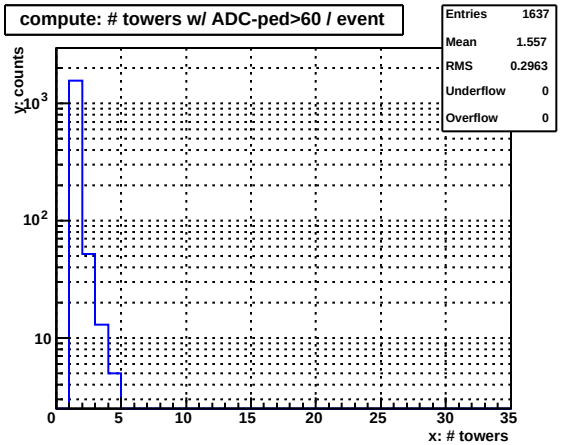

L2 COMPUTE time per input event
Entries 1637
Mean 1.103
RMS 0.7634
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

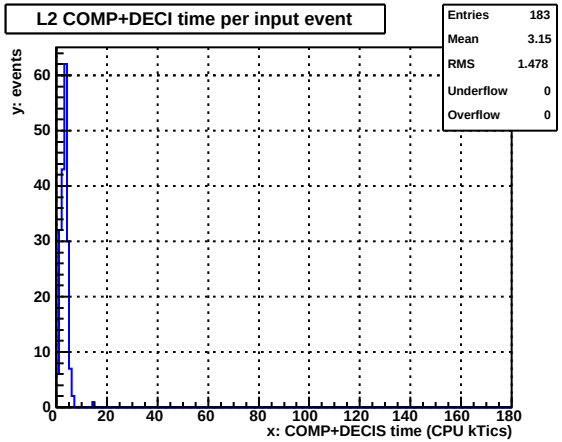

rate of COMPUTE
Entries 1637
Mean 275.9
RMS 116.3
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

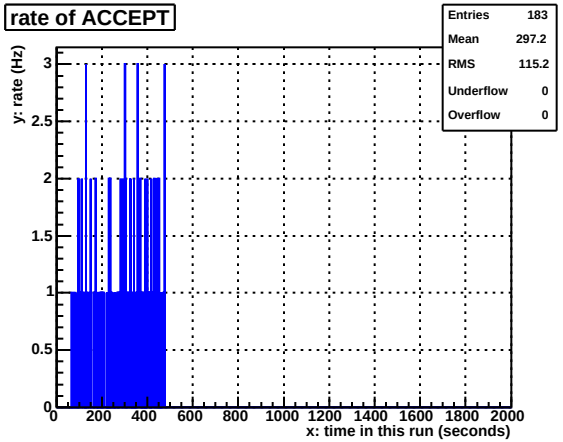

total events 0=anyInput, 10=anyAccept
Entries 3640
Mean 1.553
RMS 2.138
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

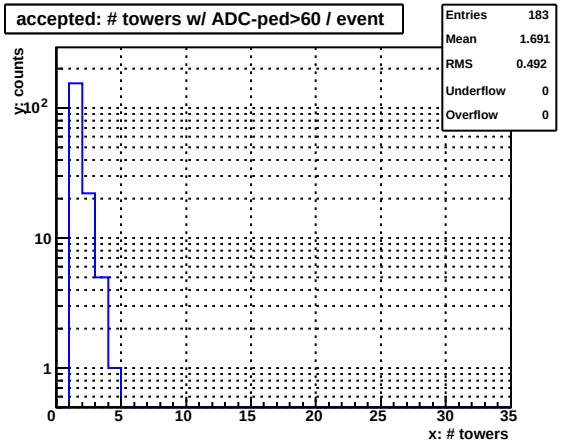

L2 DECISION time per input event
Entries 183
Mean 1.975
RMS 1.292
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

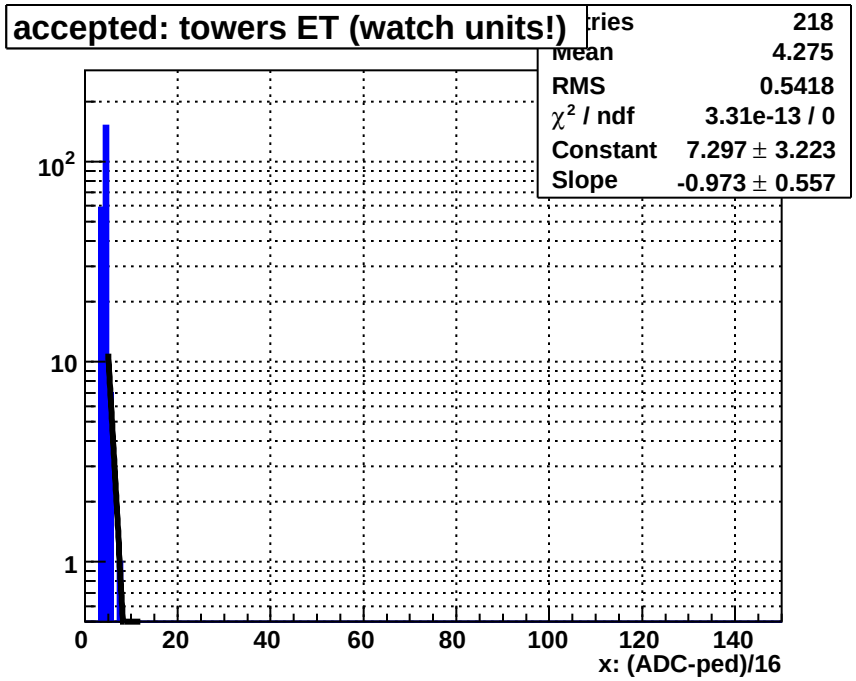

rate of DECISION
Entries 183
Mean 297.2
RMS 115.2
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

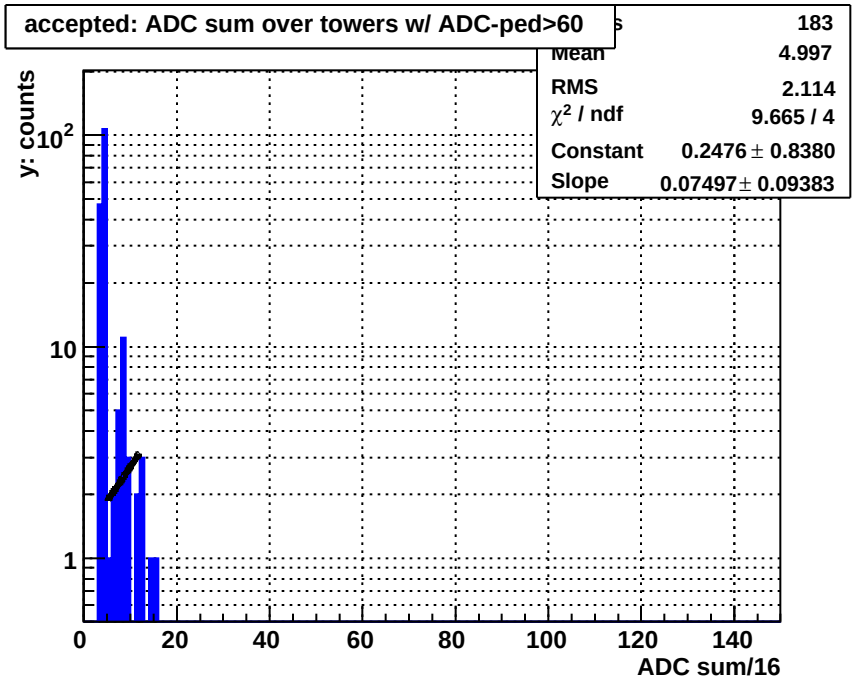

compute: # towers w/ ADC-ped>60 / event
Entries 1637
Mean 1.557
RMS 0.2963
Underflow 0
Overflow 0
y: counts
x: # towers

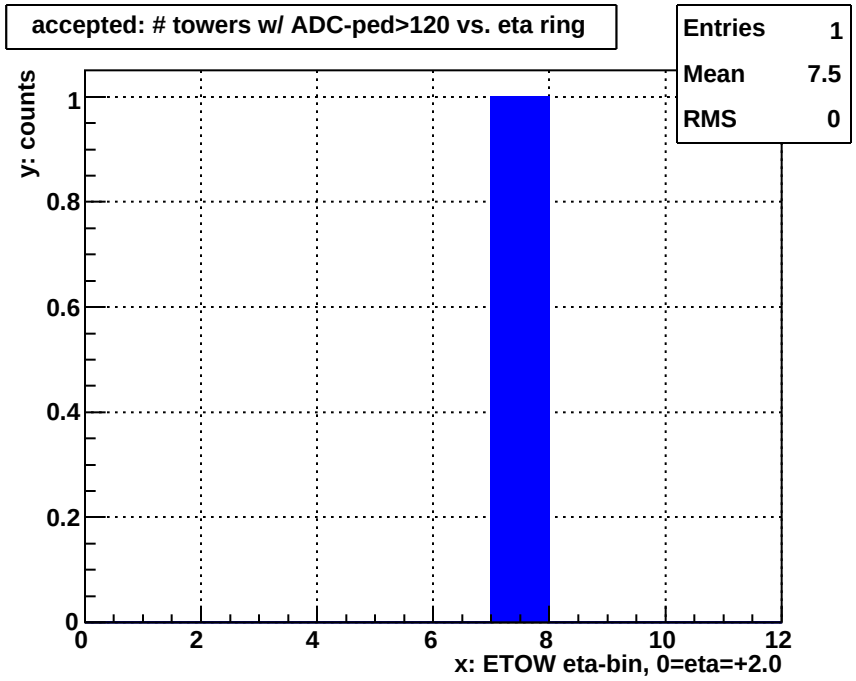

L2 COMP+DECI time per input event
Entries 183
Mean 3.15
RMS 1.478
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

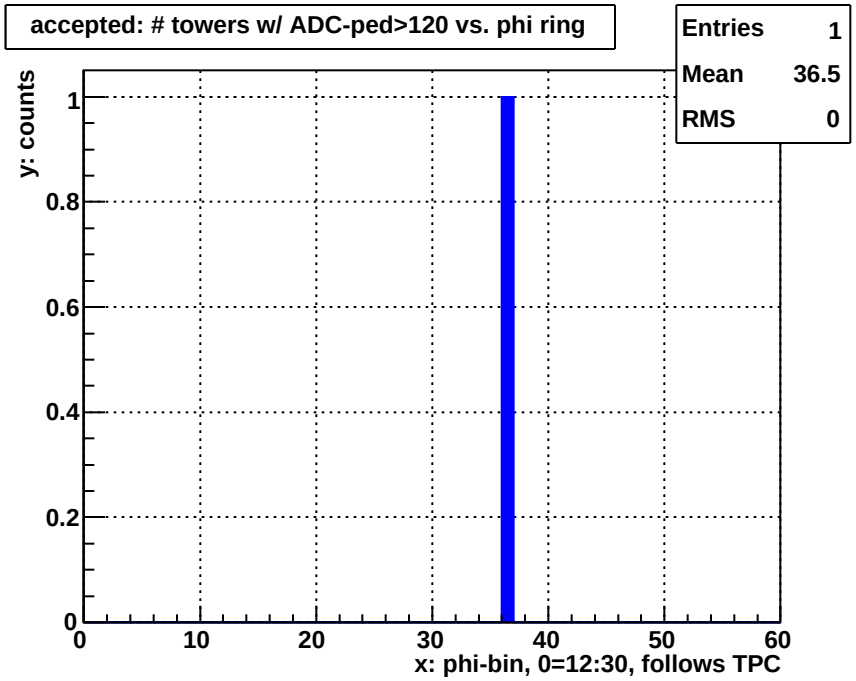

rate of ACCEPT
Entries 183
Mean 297.2
RMS 115.2
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 183
Mean 1.691
RMS 0.492
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
Entries 218
Mean 4.275
RMS 0.5418
χ² / ndf 3.31e-13 / 0
Constant 7.297 ± 3.223
Slope -0.973 ± 0.557
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
183
Mean 4.997
RMS 2.114
χ² / ndf 9.665 / 4
Constant 0.2476 ± 0.8380
Slope 0.07497 ± 0.09383
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 1
Mean 7.5
RMS 0
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 1
Mean 36.5
RMS 0
y: counts
x: phi-bin, 0=12:30, follows TPC