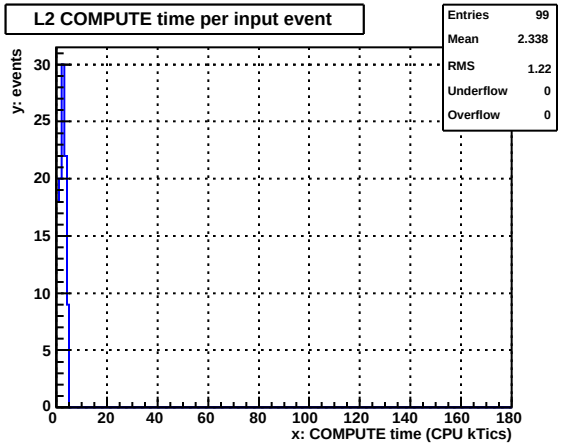
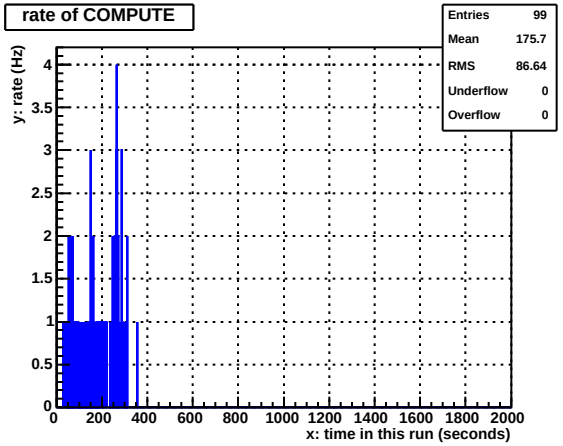
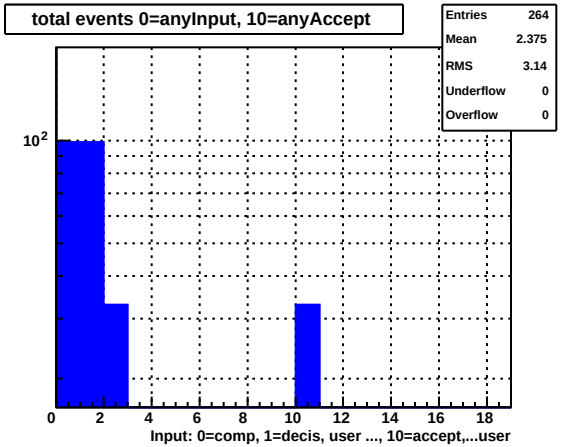
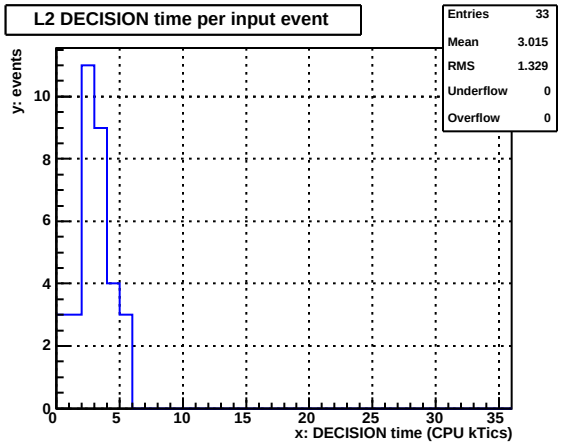
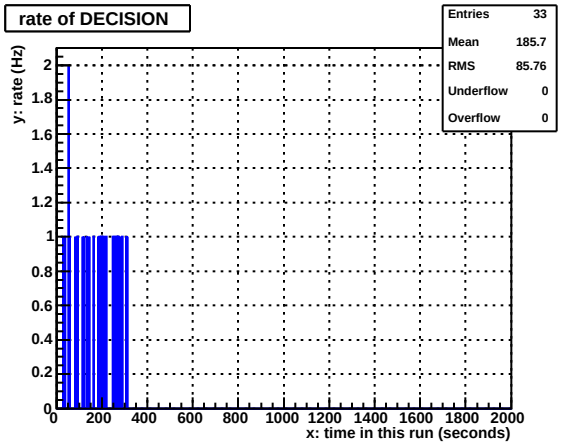
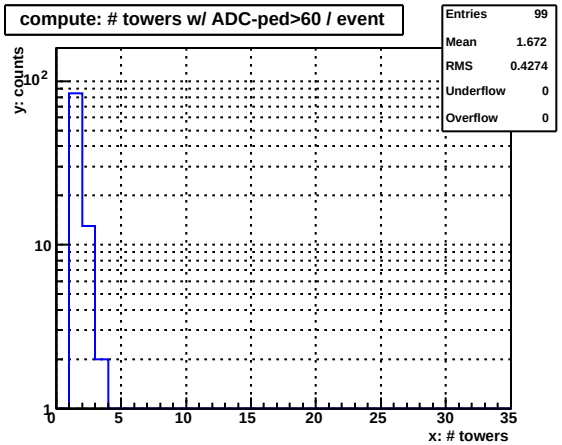

L2 COMPUTE time per input event
Entries 99
Mean 2.338
RMS 1.22
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

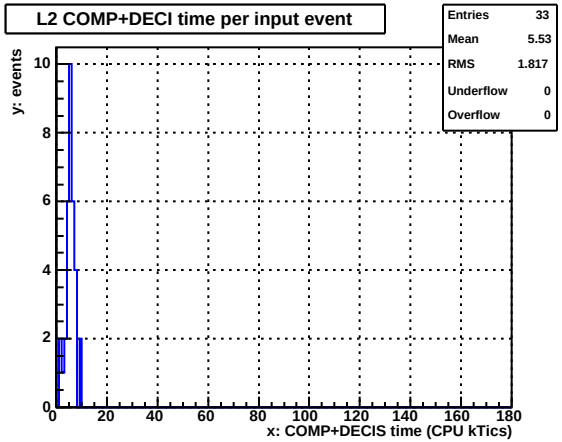

rate of COMPUTE
Entries 99
Mean 175.7
RMS 86.64
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

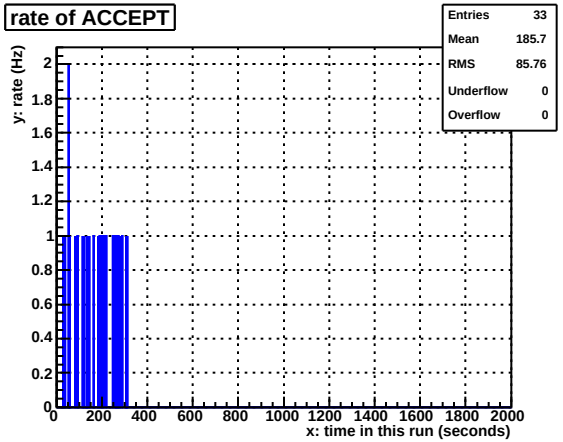

total events 0=anyInput, 10=anyAccept
Entries 264
Mean 2.375
RMS 3.14
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

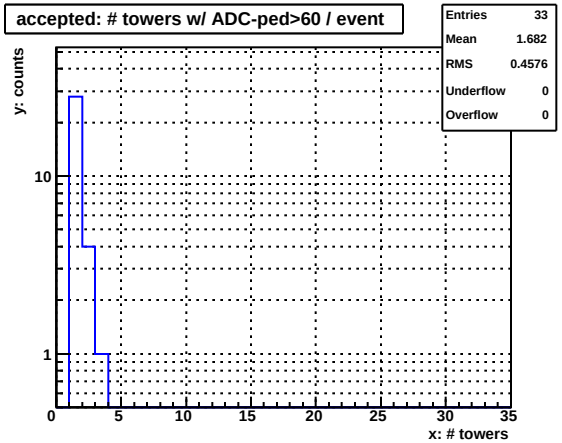

L2 DECISION time per input event
Entries 33
Mean 3.015
RMS 1.329
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

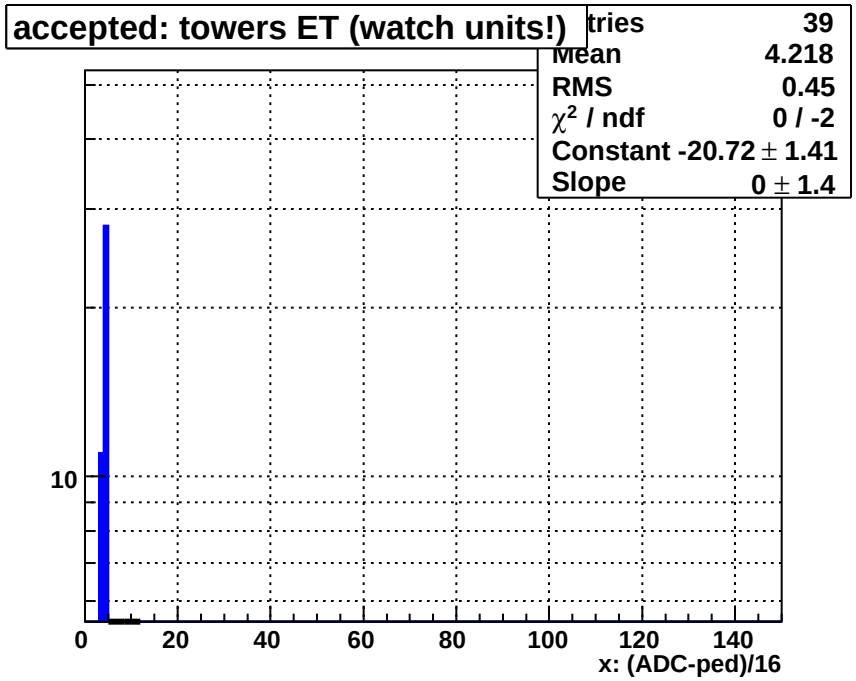

rate of DECISION
Entries 33
Mean 185.7
RMS 85.76
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

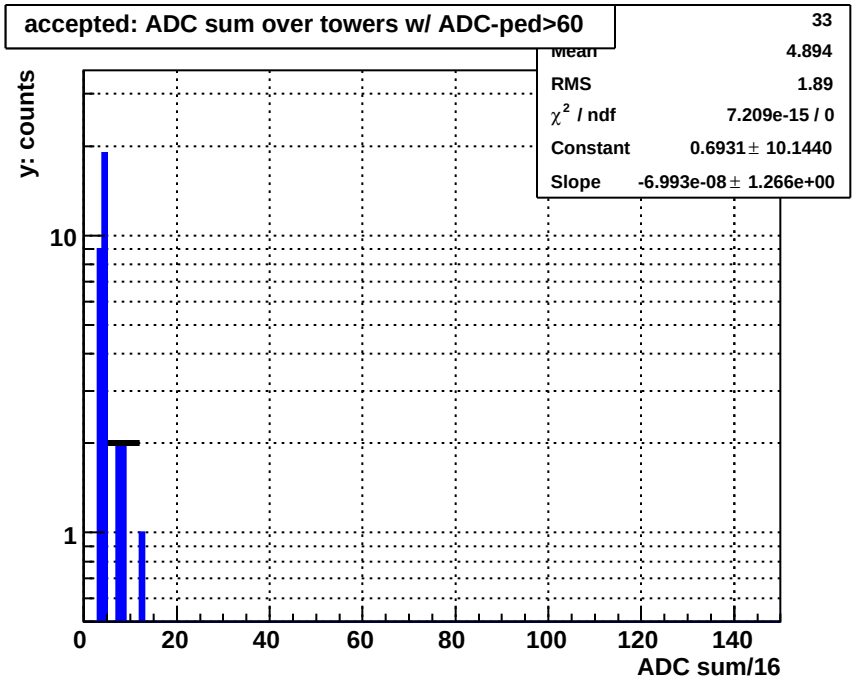

compute: # towers w/ ADC-ped>60 / event
Entries 99
Mean 1.672
RMS 0.4274
Underflow 0
Overflow 0
y: counts
x: # towers

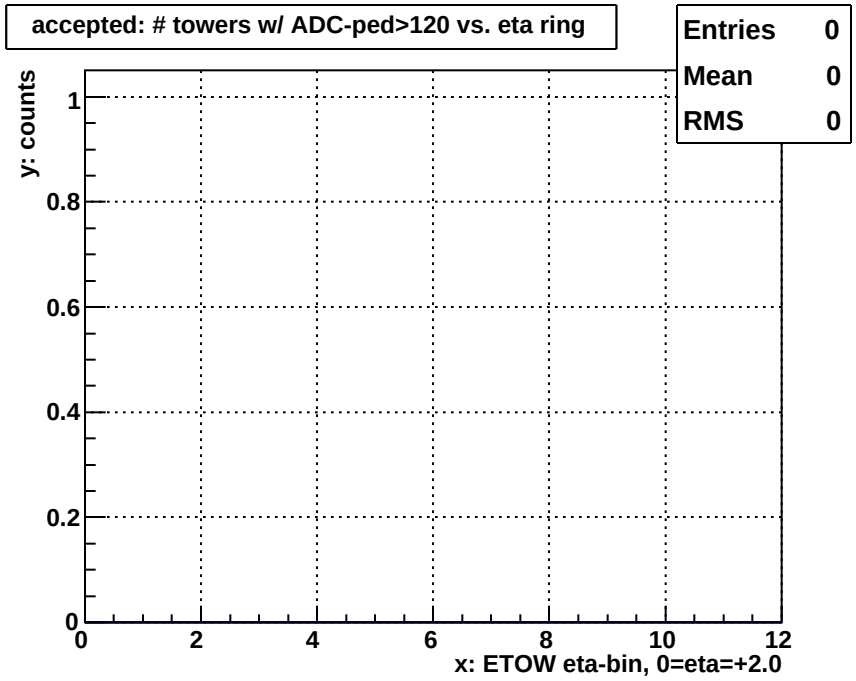

L2 COMP+DECI time per input event
Entries 33
Mean 5.53
RMS 1.817
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

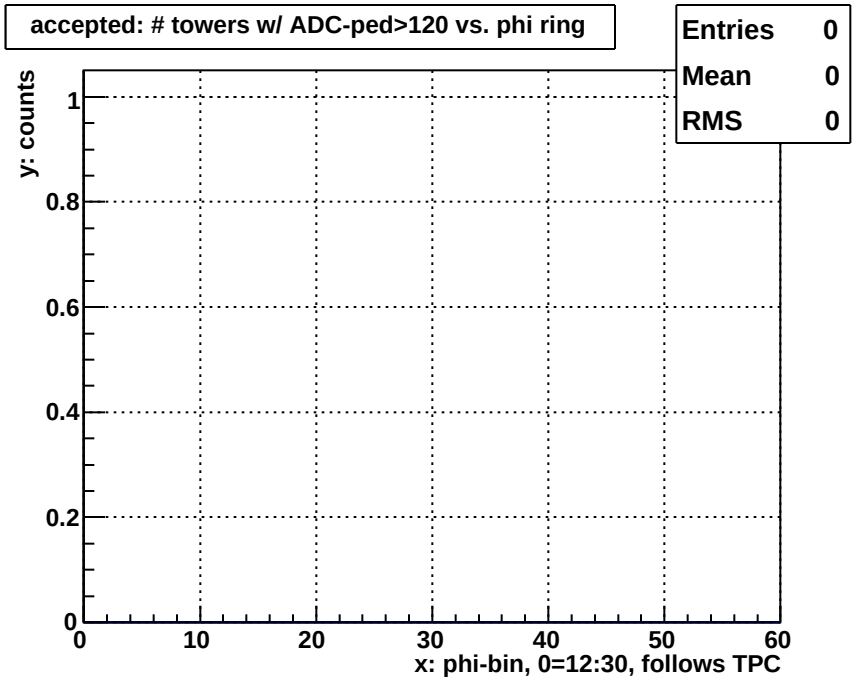

rate of ACCEPT
Entries 33
Mean 185.7
RMS 85.76
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 33
Mean 1.682
RMS 0.4576
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
tries 39
Mean 4.218
RMS 0.45
χ² / ndf 0 / -2
Constant -20.72 ± 1.41
Slope 0 ± 1.4
10
0 20 40 60 80 100 120 140
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
33
Mean 4.894
RMS 1.89
χ² / ndf 7.209e-15 / 0
Constant 0.6931 ± 10.1440
Slope -6.993e-08 ± 1.266e+00
y: counts
10
1
0 20 40 60 80 100 120 140
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 0
Mean 0
RMS 0
y: counts
1
0.8
0.6
0.4
0.2
0
0 2 4 6 8 10 12
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 0
Mean 0
RMS 0
y: counts
1
0.8
0.6
0.4
0.2
0
0 10 20 30 40 50 60
x: phi-bin, 0=12:30, follows TPC