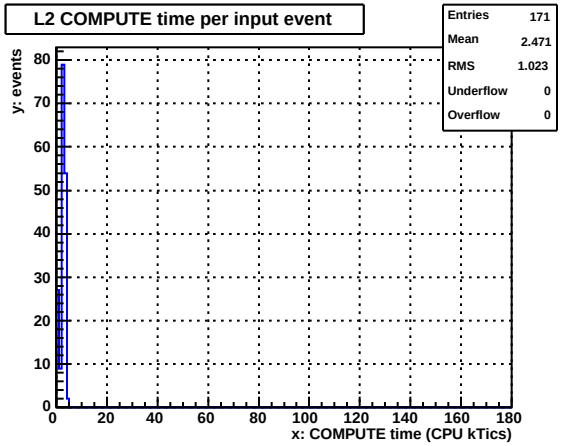
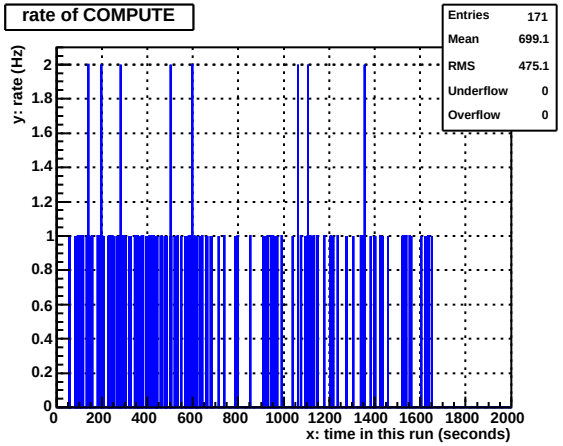
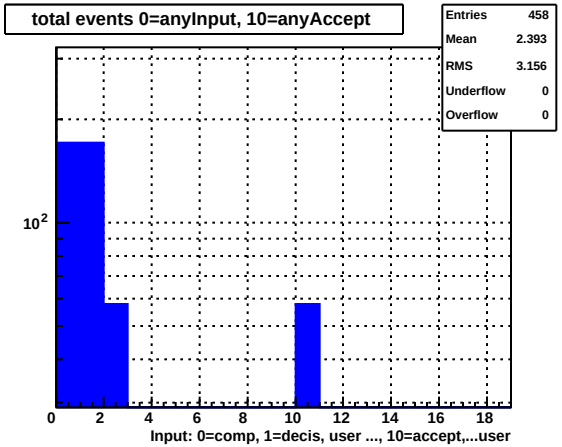
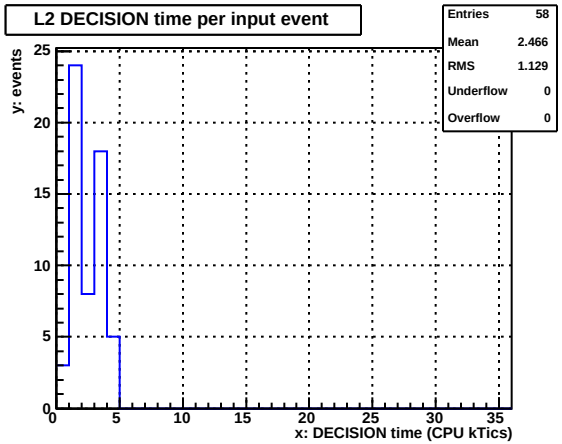
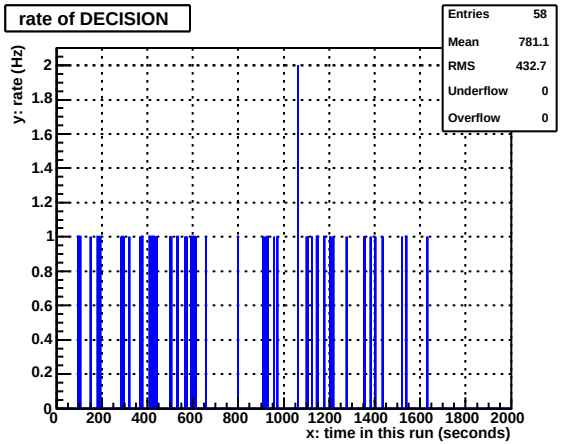
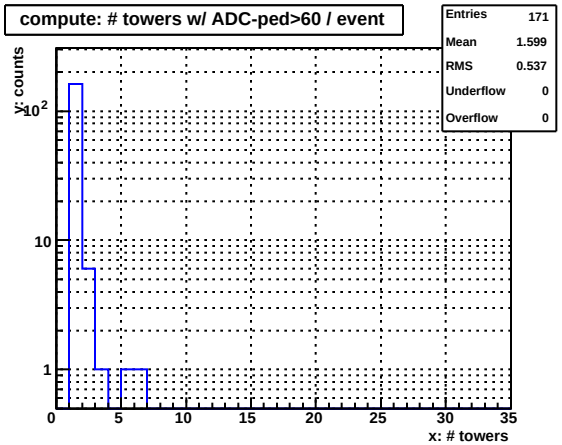

L2 COMPUTE time per input event
Entries 171
Mean 2.471
RMS 1.023
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

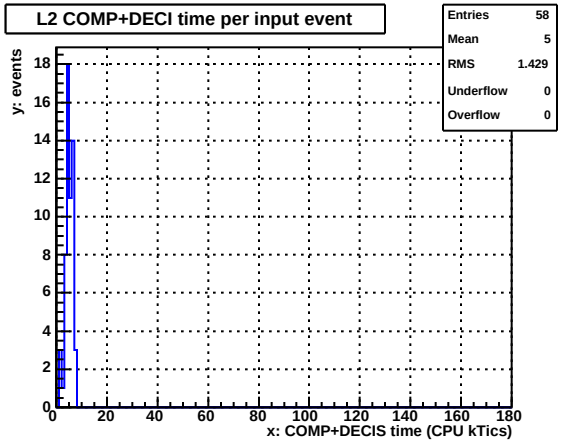

rate of COMPUTE
Entries 171
Mean 699.1
RMS 475.1
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

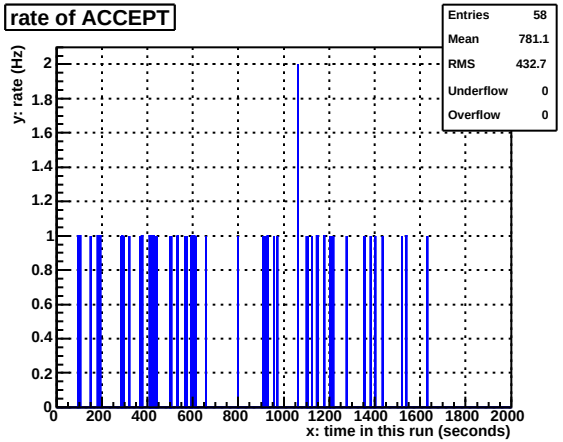

total events 0=anyInput, 10=anyAccept
Entries 458
Mean 2.393
RMS 3.156
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

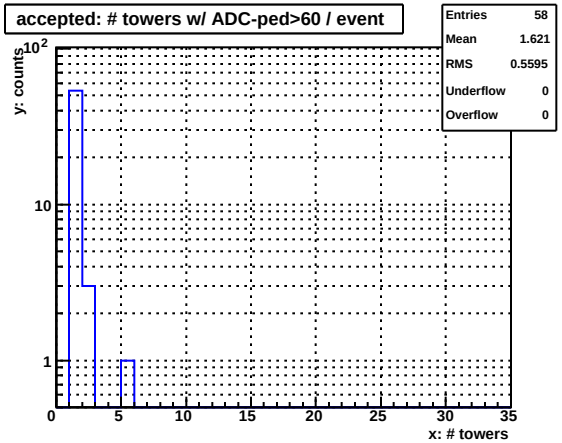

L2 DECISION time per input event
Entries 58
Mean 2.466
RMS 1.129
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

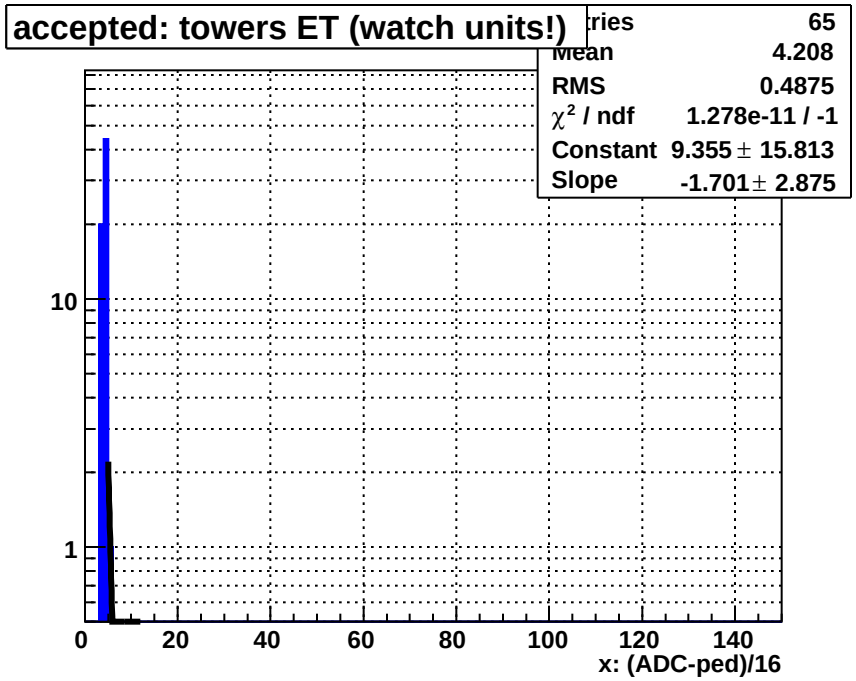

rate of DECISION
Entries 58
Mean 781.1
RMS 432.7
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

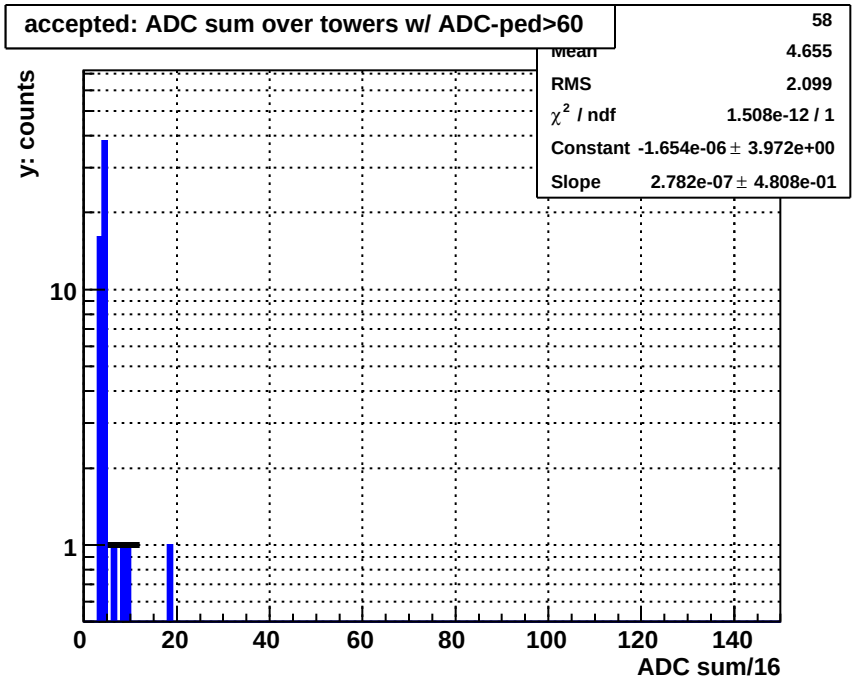

compute: # towers w/ ADC-ped>60 / event
Entries 171
Mean 1.599
RMS 0.537
Underflow 0
Overflow 0
y: counts
x: # towers

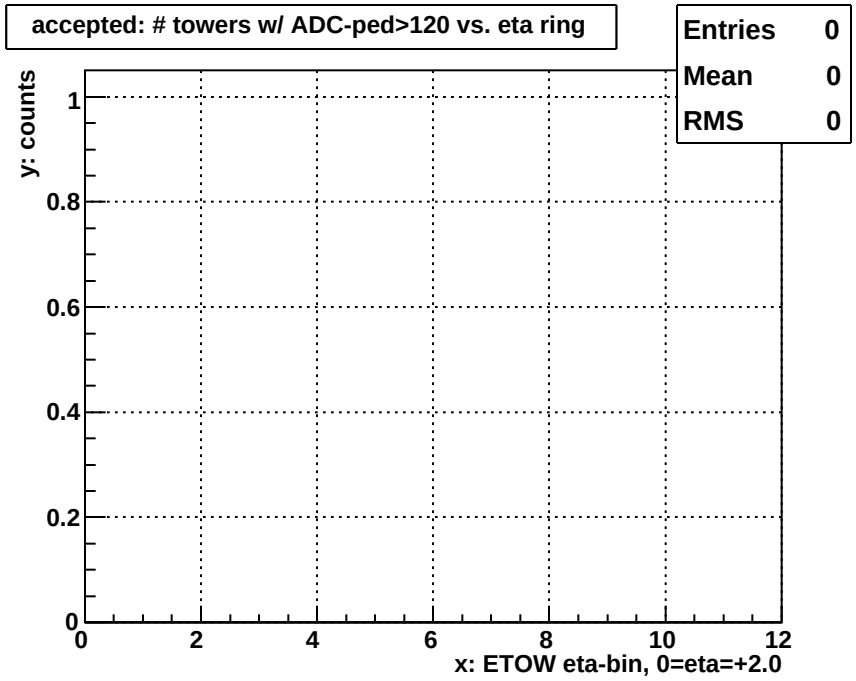

L2 COMP+DECI time per input event
Entries 58
Mean 5
RMS 1.429
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

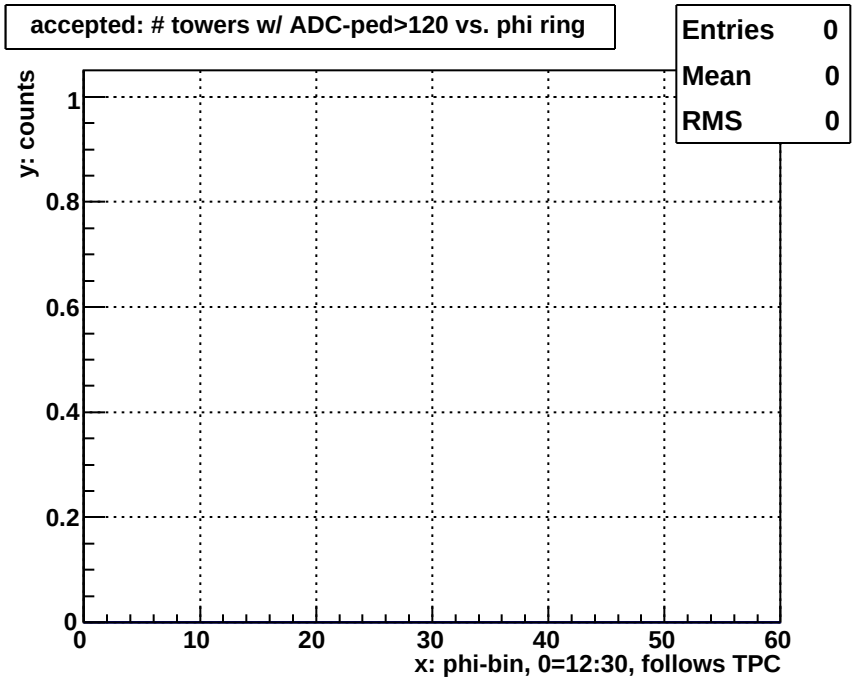

rate of ACCEPT
Entries 58
Mean 781.1
RMS 432.7
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 58
Mean 1.621
RMS 0.5595
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
Entries 65
Mean 4.208
RMS 0.4875
χ² / ndf 1.278e-11 / -1
Constant 9.355 ± 15.813
Slope -1.701 ± 2.875
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
58
Mean 4.655
RMS 2.099
χ² / ndf 1.508e-12 / 1
Constant -1.654e-06 ± 3.972e+00
Slope 2.782e-07 ± 4.808e-01
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 0
Mean 0
RMS 0
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 0
Mean 0
RMS 0
y: counts
x: phi-bin, 0=12:30, follows TPC