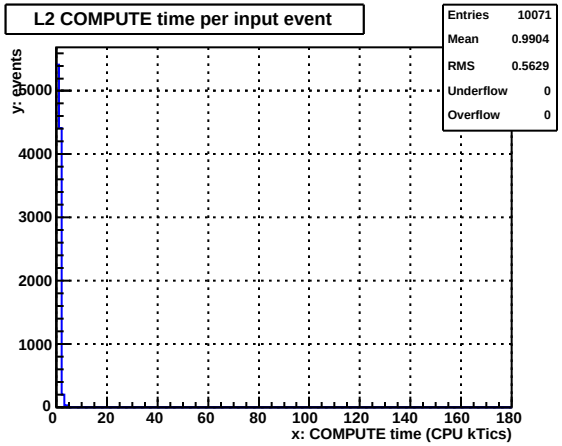
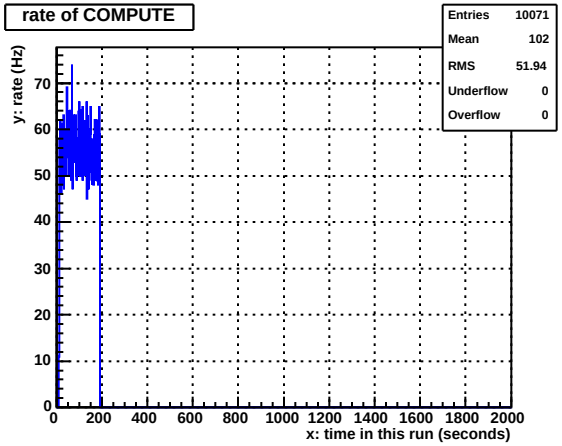
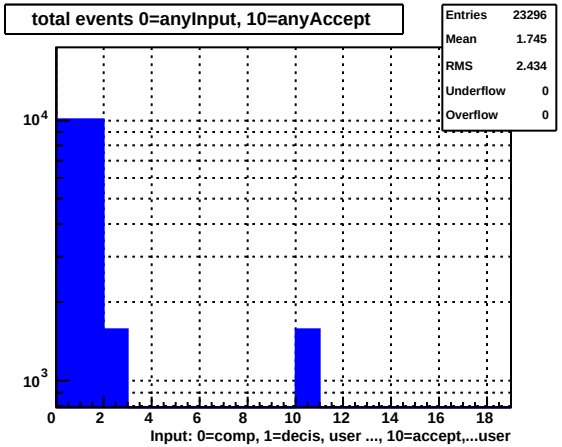
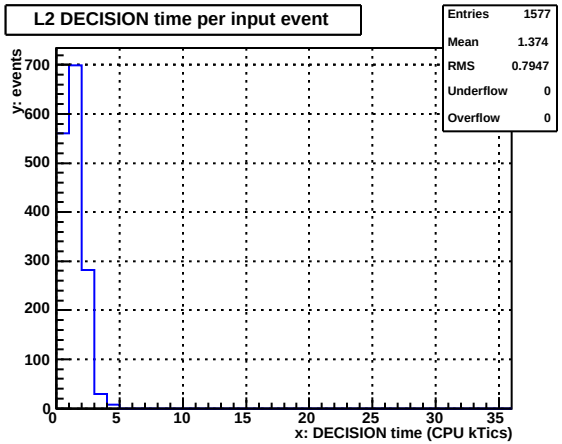
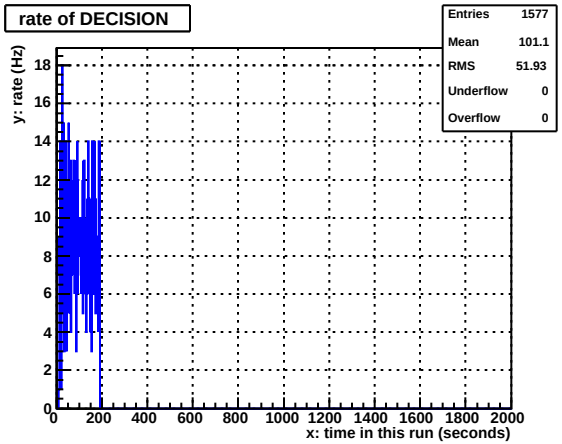
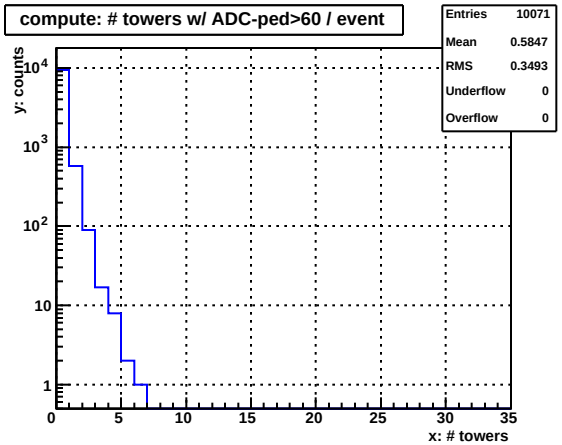

L2 COMPUTE time per input event
Entries 10071
Mean 0.9904
RMS 0.5629
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

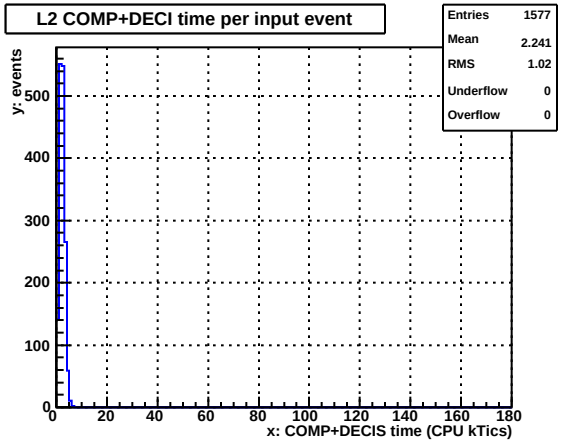

rate of COMPUTE
Entries 10071
Mean 102
RMS 51.94
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

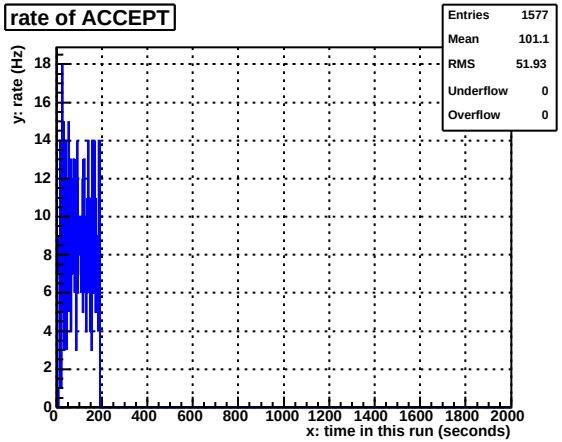

total events 0=anyInput, 10=anyAccept
Entries 23296
Mean 1.745
RMS 2.434
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

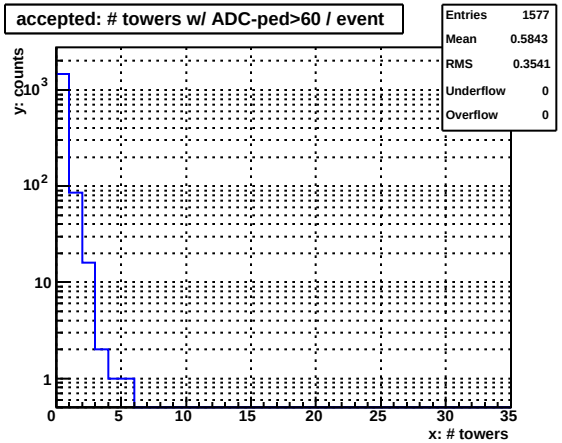

L2 DECISION time per input event
Entries 1577
Mean 1.374
RMS 0.7947
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

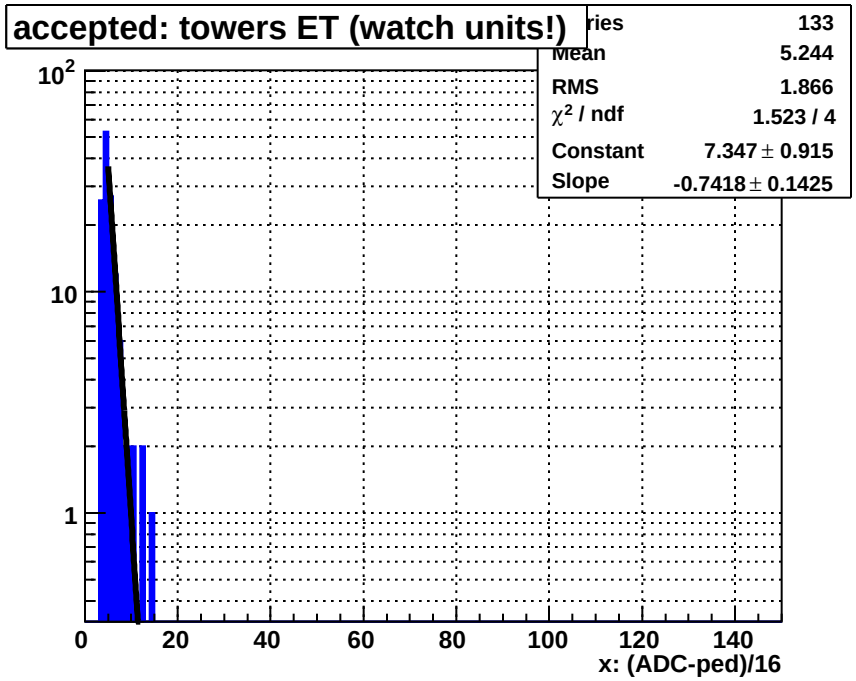

rate of DECISION
Entries 1577
Mean 101.1
RMS 51.93
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

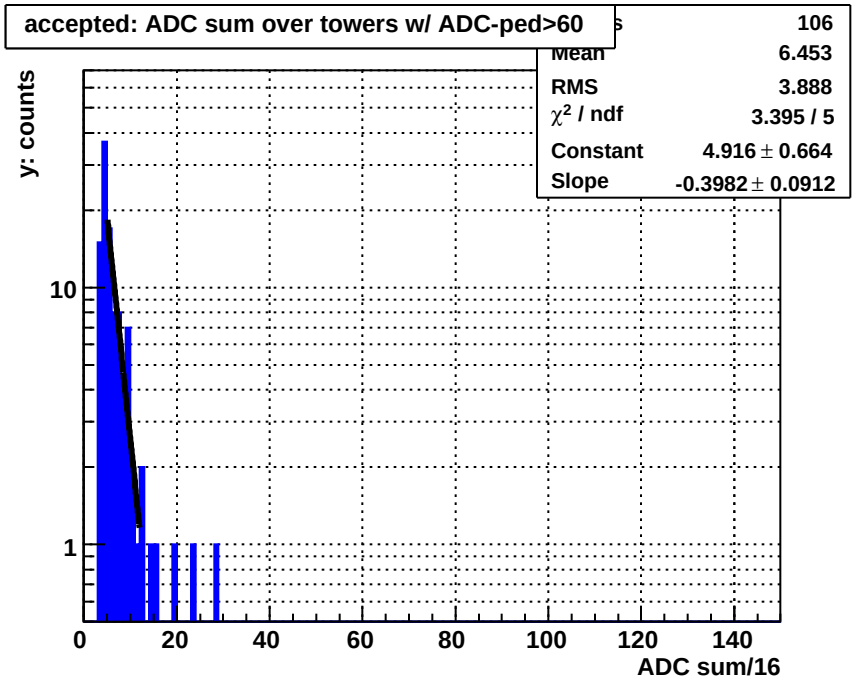

compute: # towers w/ ADC-ped>60 / event
Entries 10071
Mean 0.5847
RMS 0.3493
Underflow 0
Overflow 0
y: counts
x: # towers

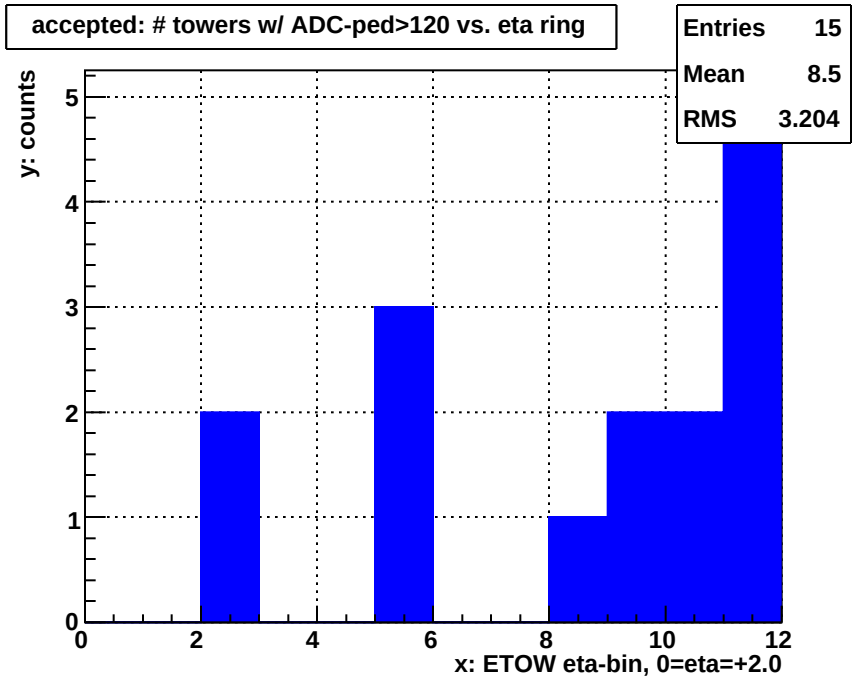

L2 COMP+DECI time per input event
Entries 1577
Mean 2.241
RMS 1.02
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

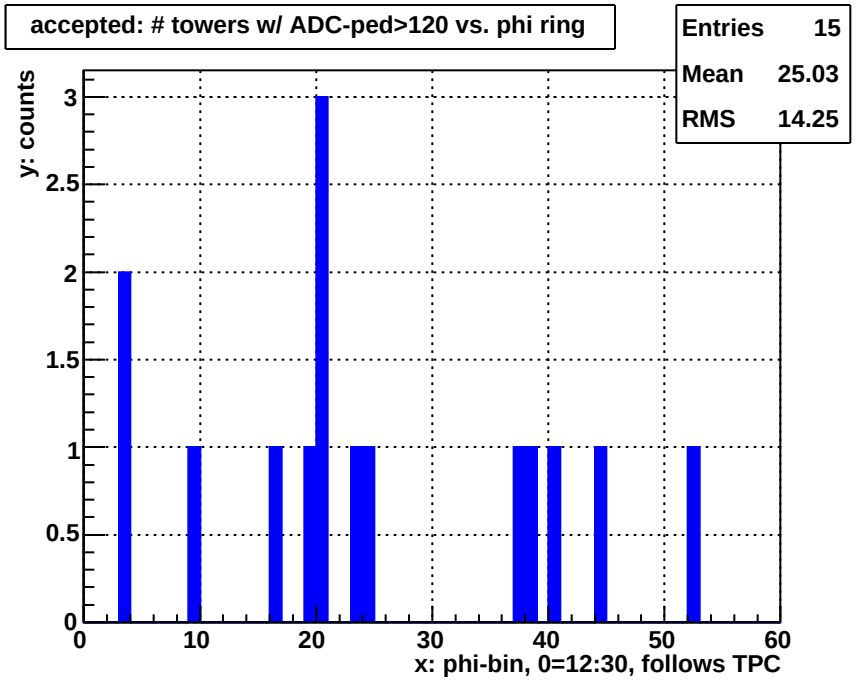

rate of ACCEPT
Entries 1577
Mean 101.1
RMS 51.93
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 1577
Mean 0.5843
RMS 0.3541
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 133
Mean 5.244
RMS 1.866
χ² / ndf 1.523 / 4
Constant 7.347 ± 0.915
Slope -0.7418 ± 0.1425
10²
10
1
0 20 40 60 80 100 120 140
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 106
Mean 6.453
RMS 3.888
χ² / ndf 3.395 / 5
Constant 4.916 ± 0.664
Slope -0.3982 ± 0.0912
y: counts
10
1
0 20 40 60 80 100 120 140
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 15
Mean 8.5
RMS 3.204
y: counts
0 1 2 3 4 5
0 2 4 6 8 10 12
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 15
Mean 25.03
RMS 14.25
y: counts
0 0.5 1 1.5 2 2.5 3
0 10 20 30 40 50 60
x: phi-bin, 0=12:30, follows TPC