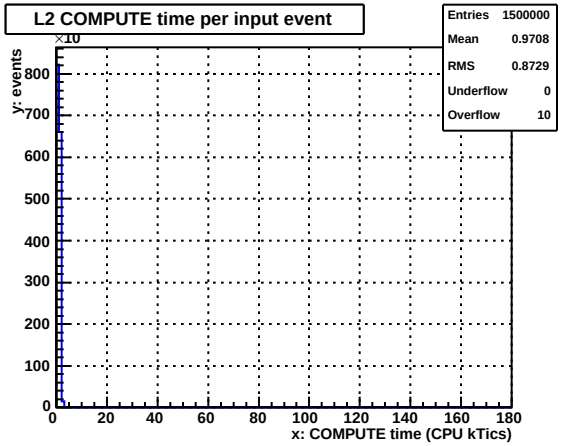
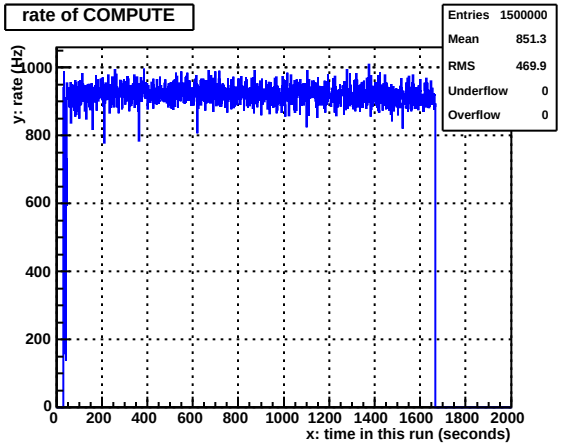
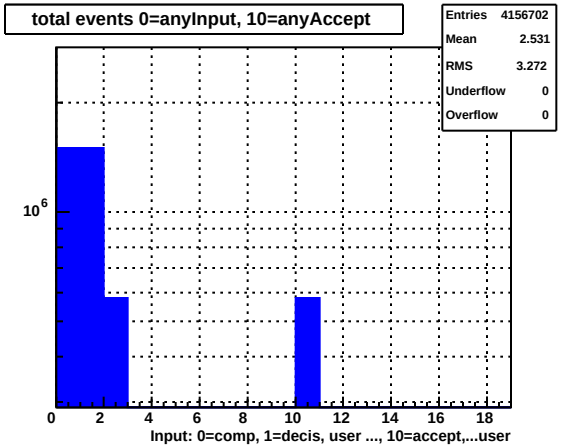
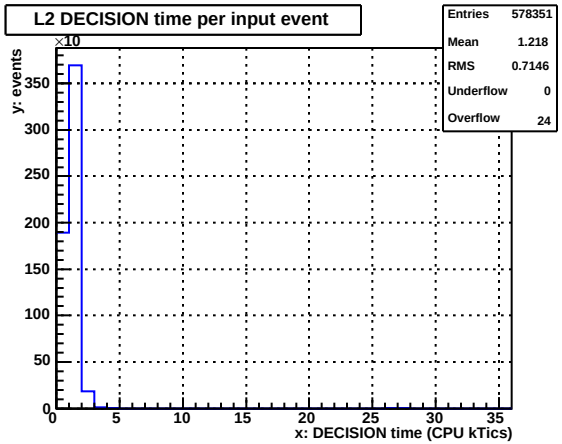
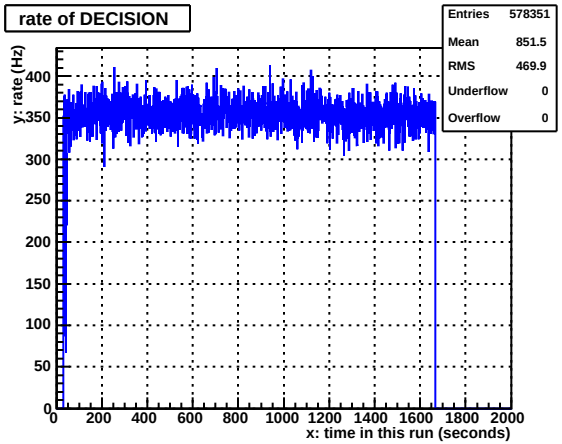
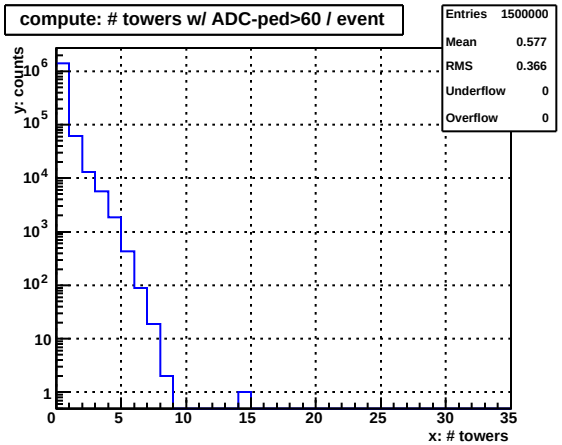

L2 COMPUTE time per input event
Entries 1500000
Mean 0.9708
RMS 0.8729
Underflow 0
Overflow 10
y: events
x: COMPUTE time (CPU kTics)

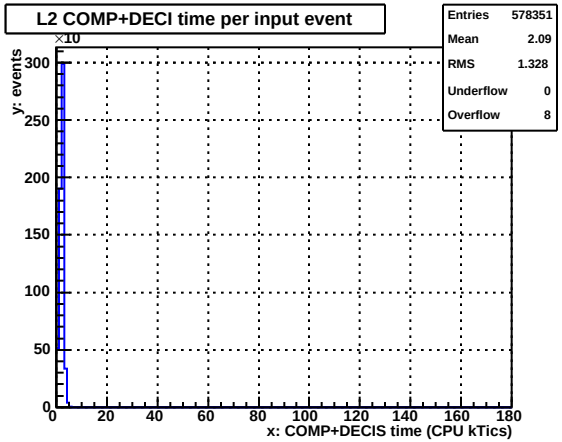

rate of COMPUTE
Entries 1500000
Mean 851.3
RMS 469.9
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

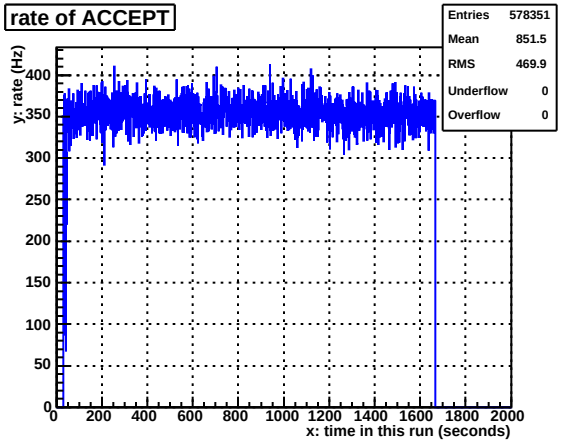

total events 0=anyInput, 10=anyAccept
Entries 4156702
Mean 2.531
RMS 3.272
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

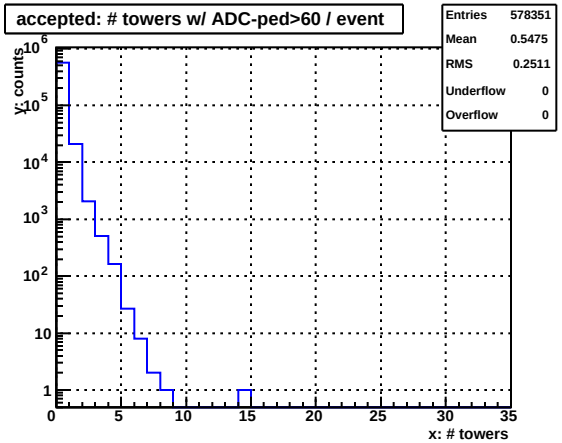

L2 DECISION time per input event
Entries 578351
Mean 1.218
RMS 0.7146
Underflow 0
Overflow 24
y: events
x: DECISION time (CPU kTics)

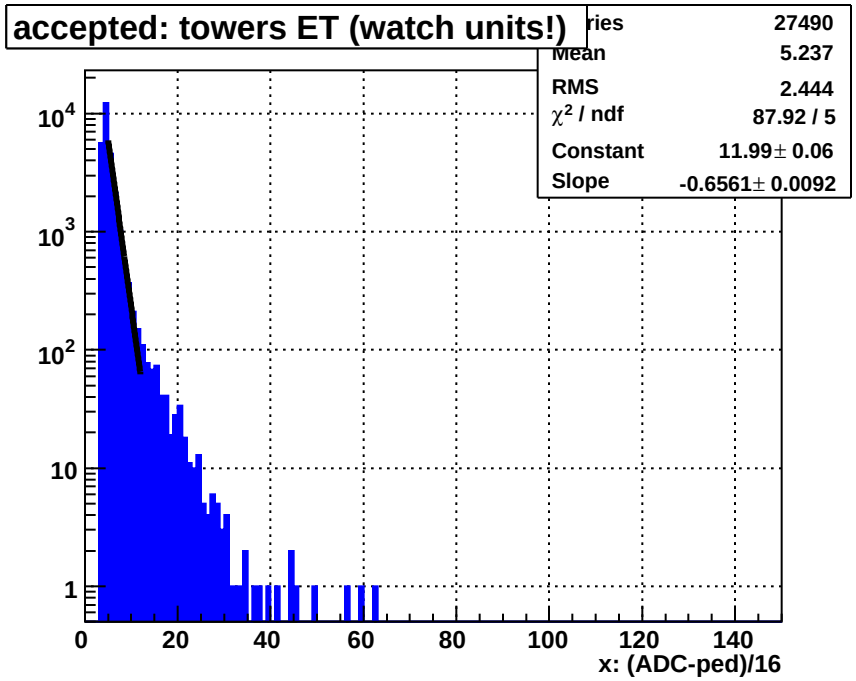

rate of DECISION
Entries 578351
Mean 851.5
RMS 469.9
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

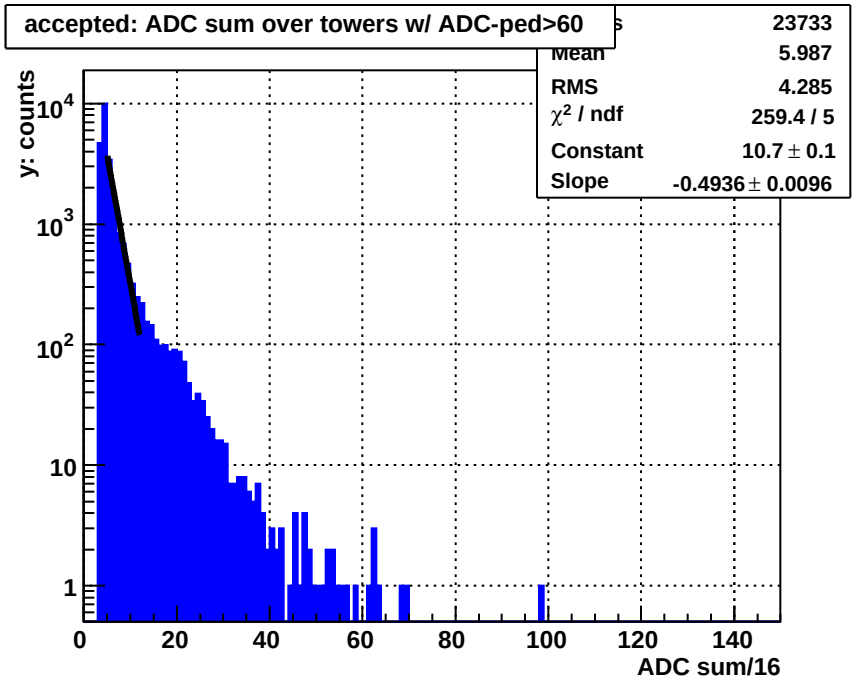

compute: # towers w/ ADC-ped>60 / event
Entries 1500000
Mean 0.577
RMS 0.366
Underflow 0
Overflow 0
y: counts
x: # towers

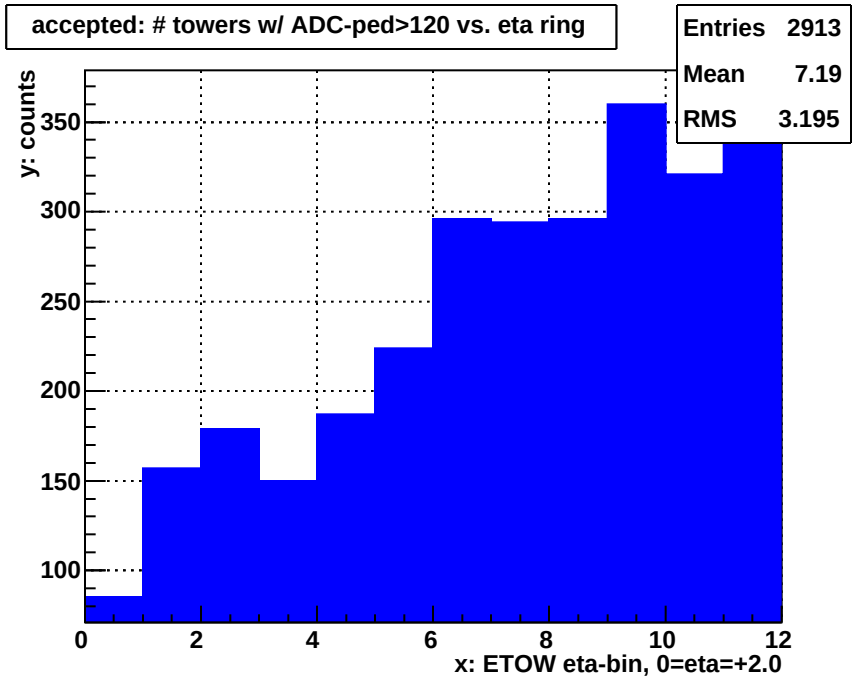

L2 COMP+DECI time per input event
Entries 578351
Mean 2.09
RMS 1.328
Underflow 0
Overflow 8
y: events
x: COMP+DECIS time (CPU kTics)

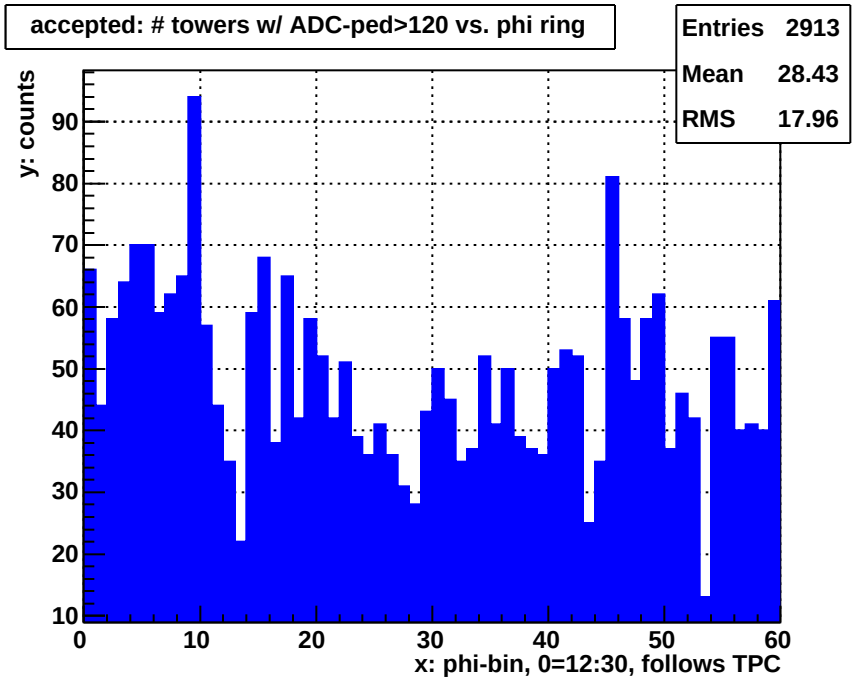

rate of ACCEPT
Entries 578351
Mean 851.5
RMS 469.9
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 578351
Mean 0.5475
RMS 0.2511
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 27490
Mean 5.237
RMS 2.444
χ² / ndf 87.92 / 5
Constant 11.99 ± 0.06
Slope -0.6561 ± 0.0092
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 23733
Mean 5.987
RMS 4.285
χ² / ndf 259.4 / 5
Constant 10.7 ± 0.1
Slope -0.4936 ± 0.0096
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 2913
Mean 7.19
RMS 3.195
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 2913
Mean 28.43
RMS 17.96
y: counts
x: phi-bin, 0=12:30, follows TPC