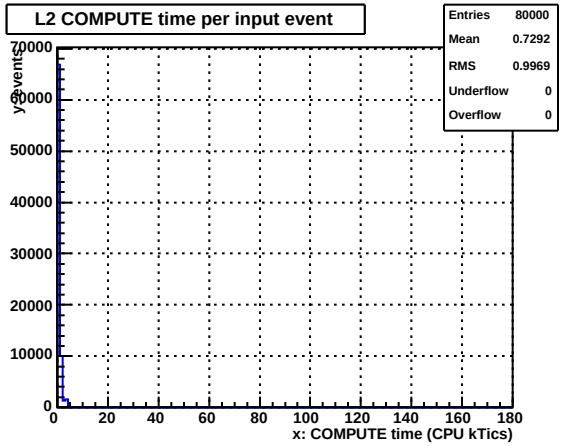
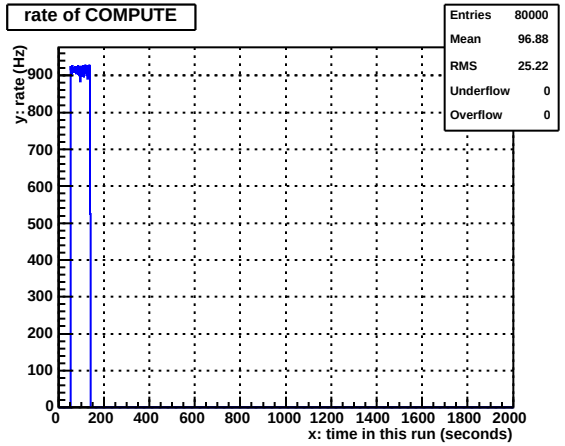
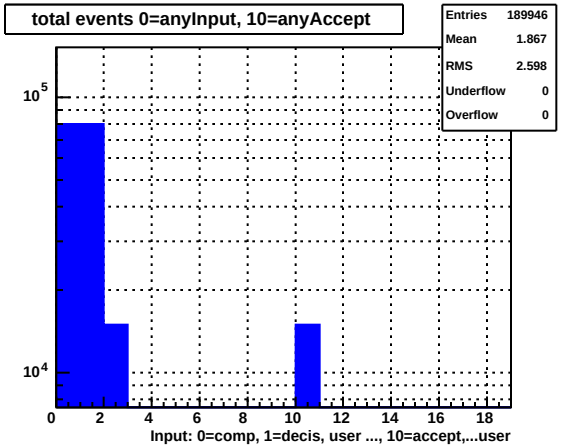
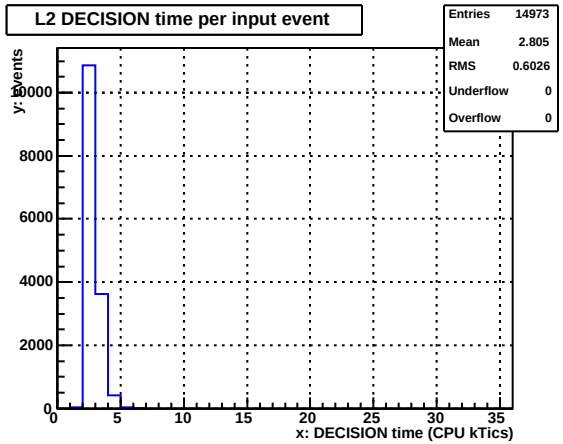
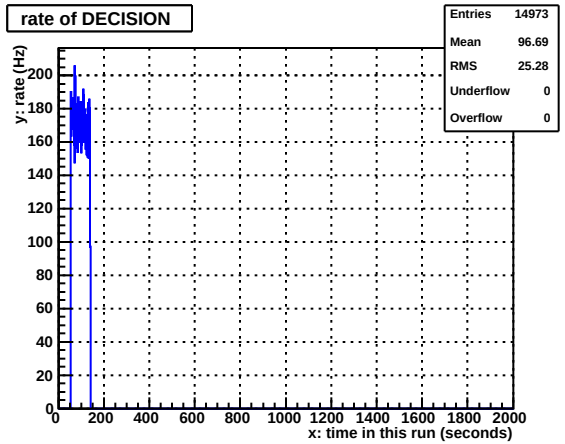
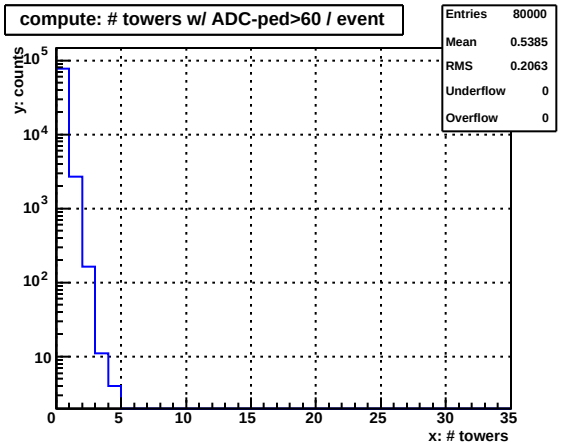

L2 COMPUTE time per input event
Entries 80000
Mean 0.7292
RMS 0.9969
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

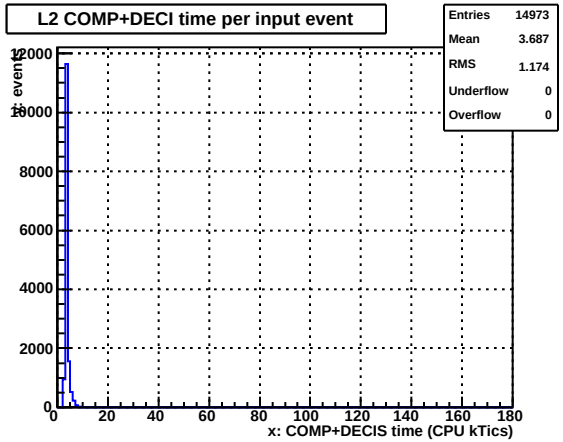

rate of COMPUTE
Entries 80000
Mean 96.88
RMS 25.22
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

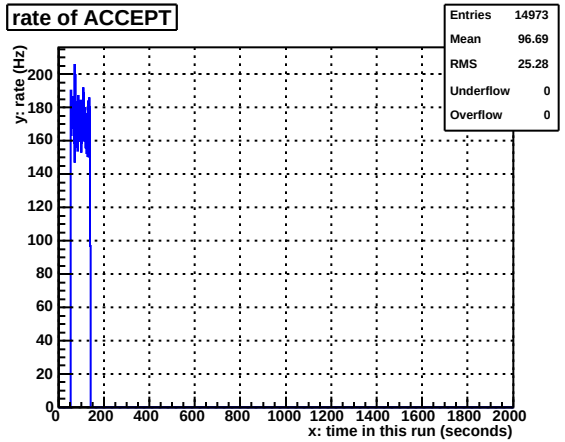

total events 0=anyInput, 10=anyAccept
Entries 189946
Mean 1.867
RMS 2.598
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

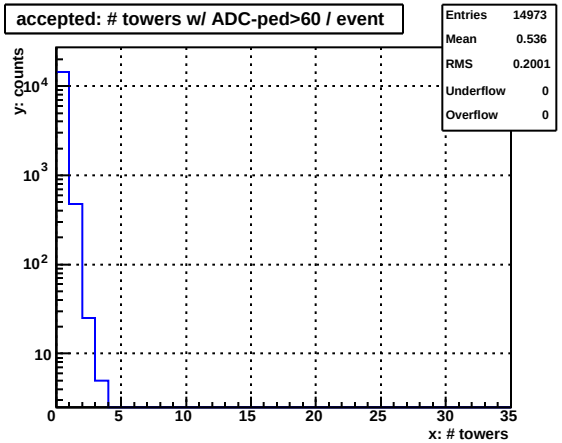

L2 DECISION time per input event
Entries 14973
Mean 2.805
RMS 0.6026
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

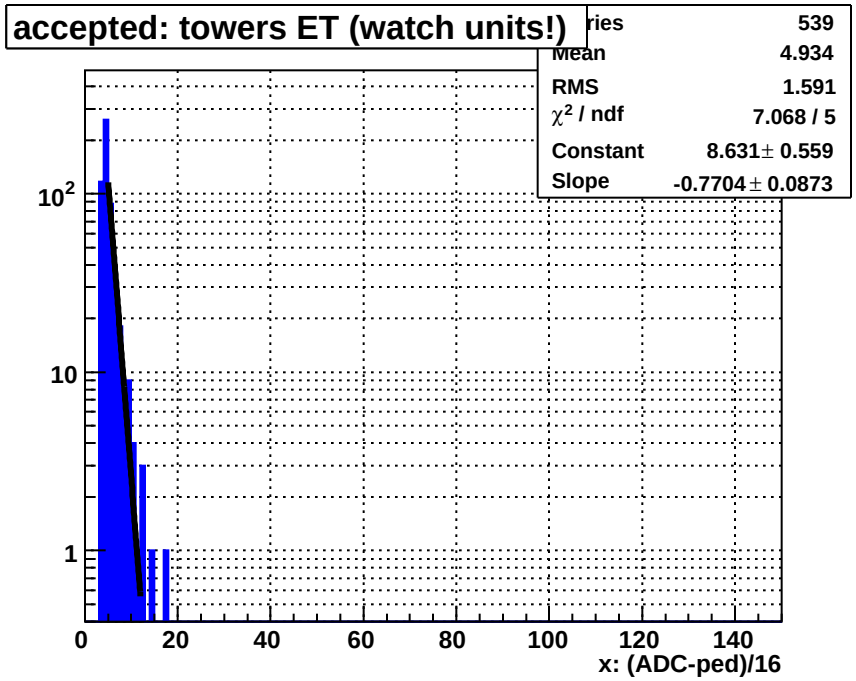

rate of DECISION
Entries 14973
Mean 96.69
RMS 25.28
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

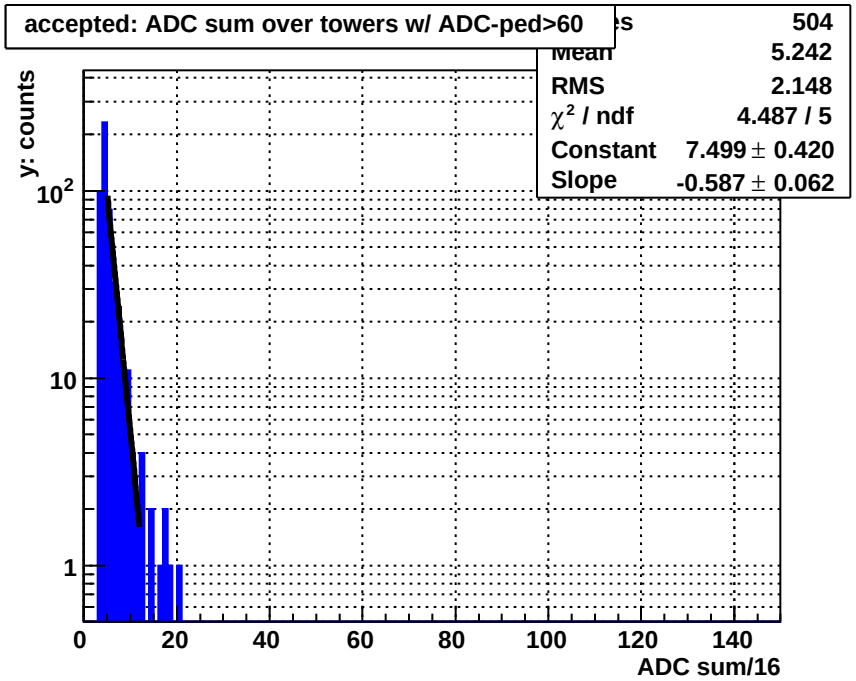

compute: # towers w/ ADC-ped>60 / event
Entries 80000
Mean 0.5385
RMS 0.2063
Underflow 0
Overflow 0
y: counts
x: # towers

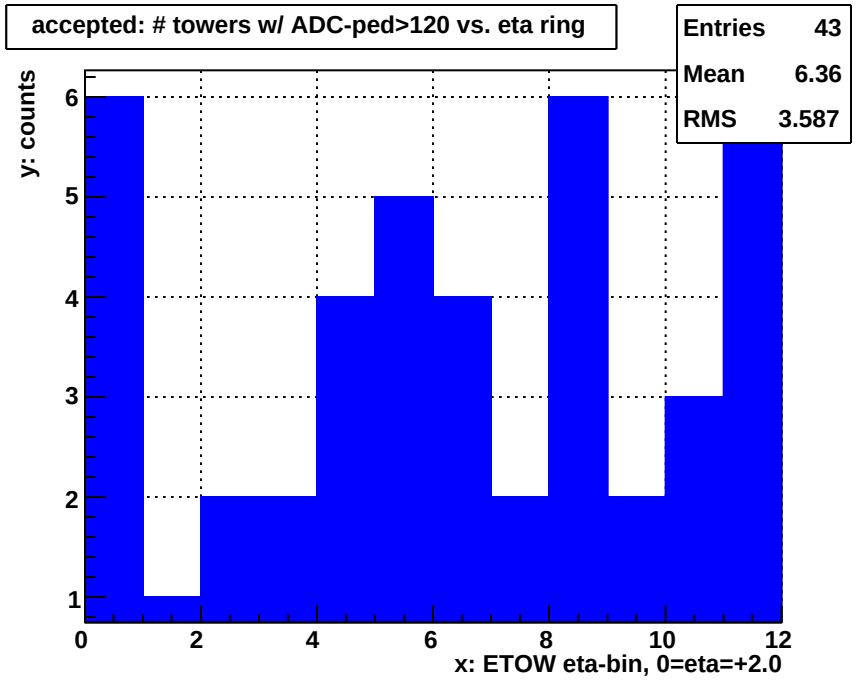

L2 COMP+DECI time per input event
Entries 14973
Mean 3.687
RMS 1.174
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

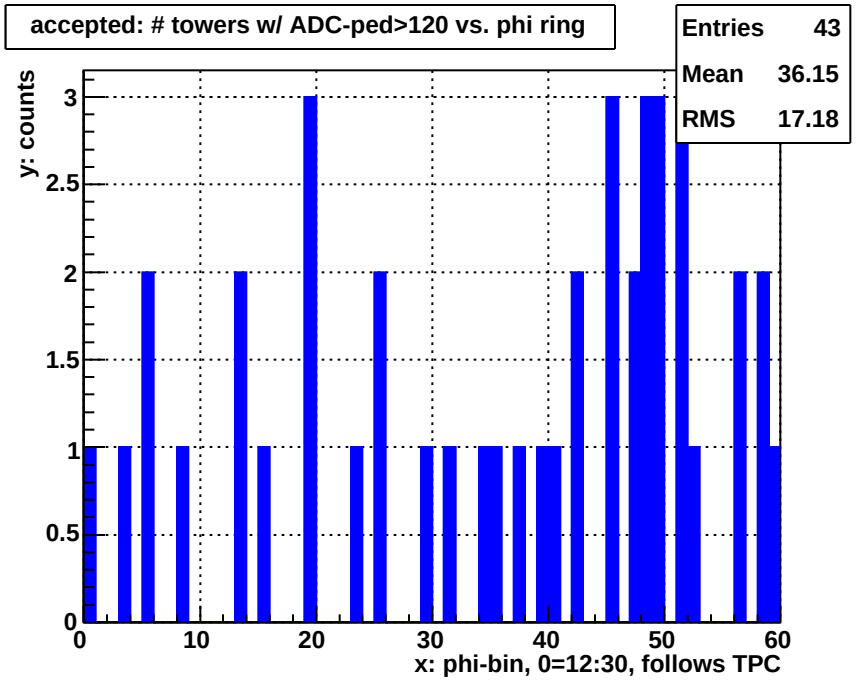

rate of ACCEPT
Entries 14973
Mean 96.69
RMS 25.28
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 14973
Mean 0.536
RMS 0.2001
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 539
Mean 4.934
RMS 1.591
χ² / ndf 7.068 / 5
Constant 8.631 ± 0.559
Slope -0.7704 ± 0.0873
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
es 504
Mean 5.242
RMS 2.148
χ² / ndf 4.487 / 5
Constant 7.499 ± 0.420
Slope -0.587 ± 0.062
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 43
Mean 6.36
RMS 3.587
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 43
Mean 36.15
RMS 17.18
y: counts
x: phi-bin, 0=12:30, follows TPC