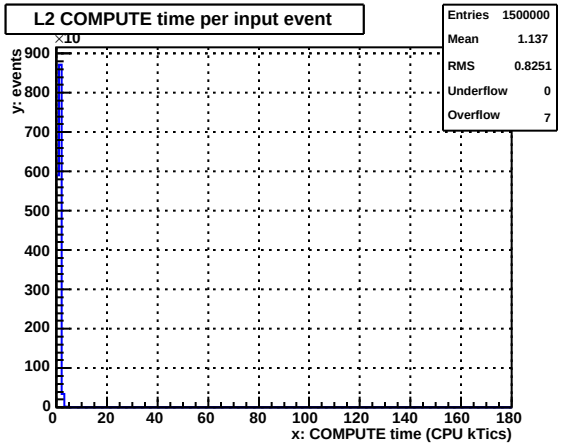
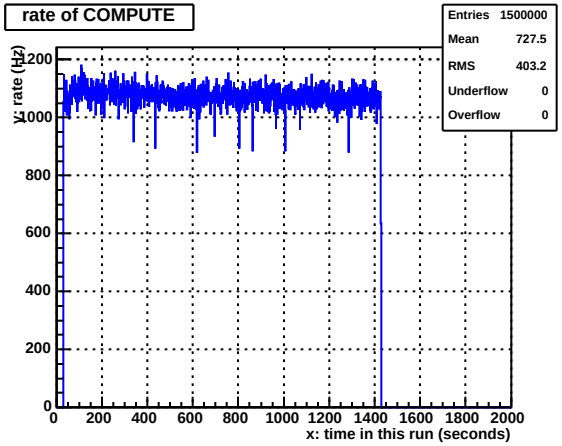
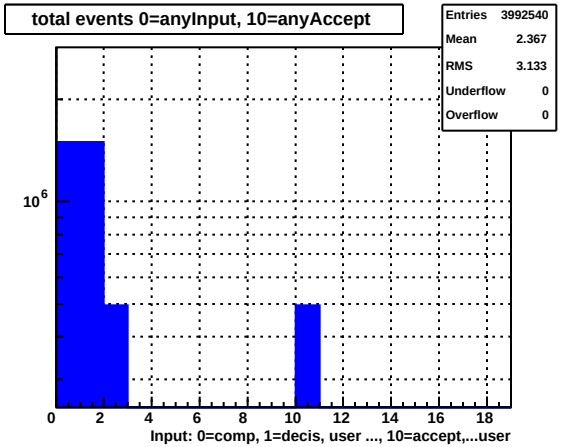
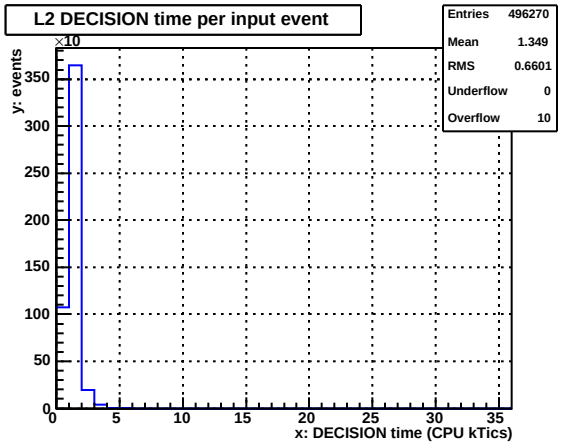
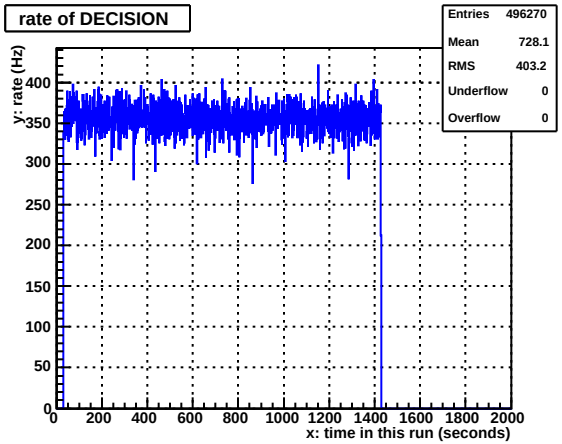
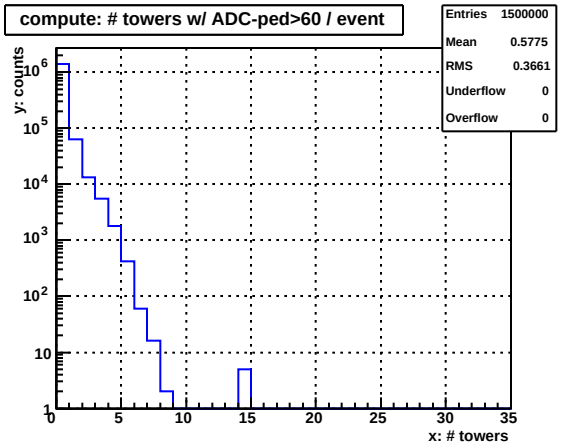

L2 COMPUTE time per input event
Entries 1500000
Mean 1.137
RMS 0.8251
Underflow 0
Overflow 7
y: events
x: COMPUTE time (CPU kTics)

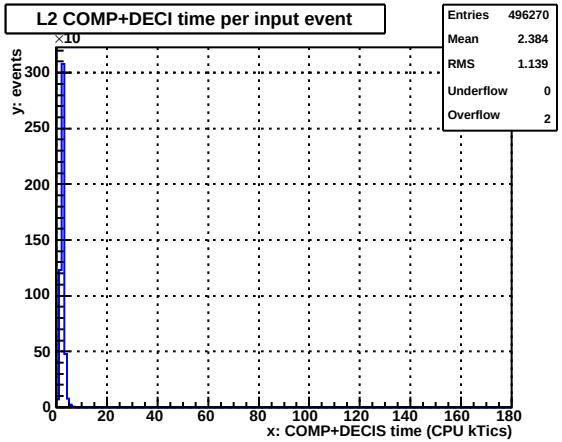

rate of COMPUTE
Entries 1500000
Mean 727.5
RMS 403.2
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

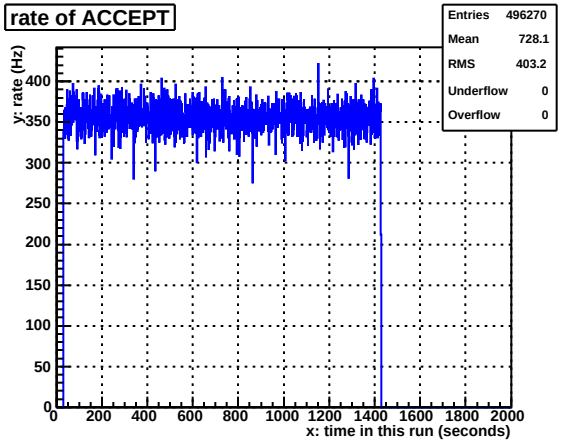

total events 0=anyInput, 10=anyAccept
Entries 3992540
Mean 2.367
RMS 3.133
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

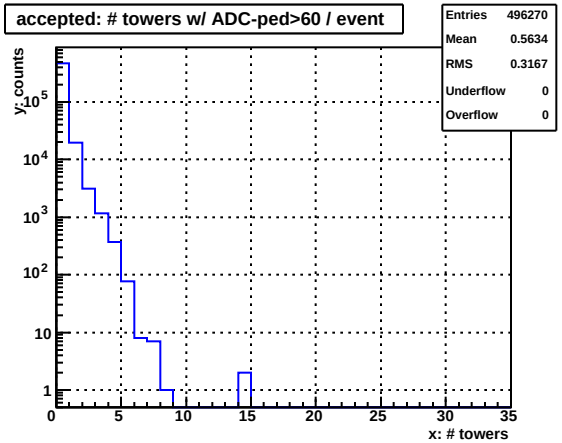

L2 DECISION time per input event
Entries 496270
Mean 1.349
RMS 0.6601
Underflow 0
Overflow 10
y: events
x: DECISION time (CPU kTics)

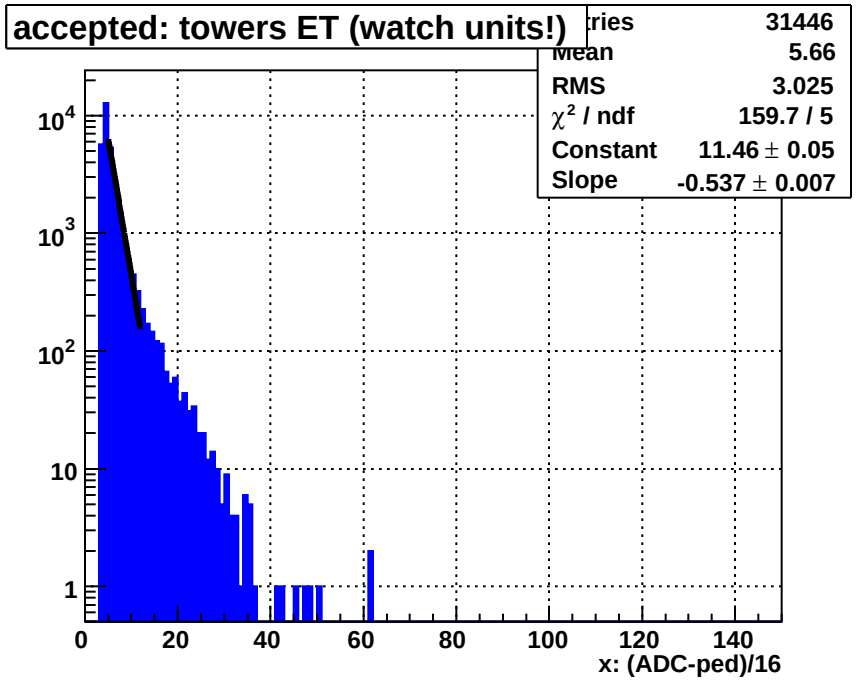

rate of DECISION
Entries 496270
Mean 728.1
RMS 403.2
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

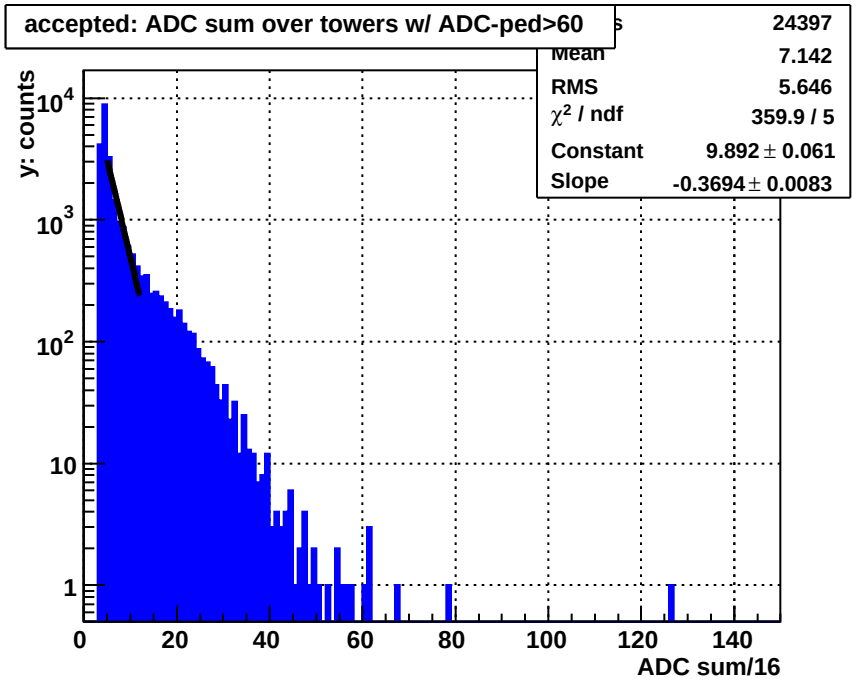

compute: # towers w/ ADC-ped>60 / event
Entries 1500000
Mean 0.5775
RMS 0.3661
Underflow 0
Overflow 0
y: counts
x: # towers

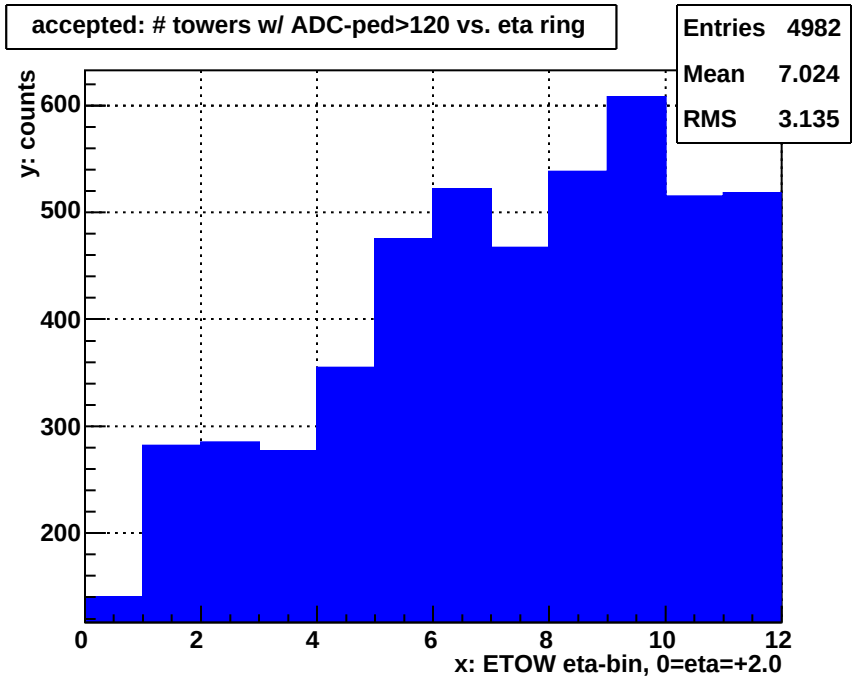

L2 COMP+DECI time per input event
Entries 496270
Mean 2.384
RMS 1.139
Underflow 0
Overflow 2
y: events
x: COMP+DECIS time (CPU kTics)

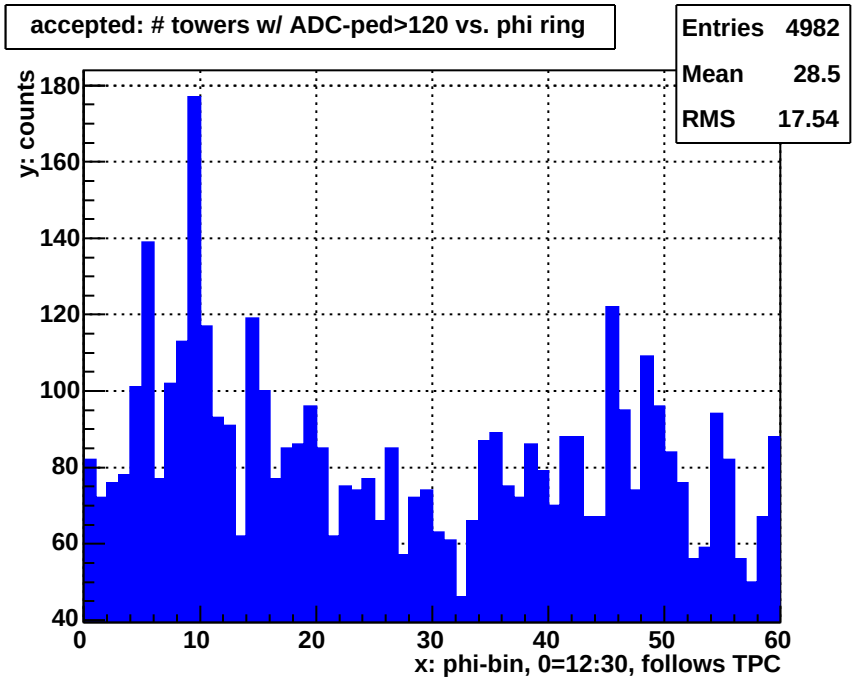

rate of ACCEPT
Entries 496270
Mean 728.1
RMS 403.2
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 496270
Mean 0.5634
RMS 0.3167
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
Entries 31446
Mean 5.66
RMS 3.025
χ² / ndf 159.7 / 5
Constant 11.46 ± 0.05
Slope -0.537 ± 0.007
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
Entries 24397
Mean 7.142
RMS 5.646
χ² / ndf 359.9 / 5
Constant 9.892 ± 0.061
Slope -0.3694 ± 0.0083
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 4982
Mean 7.024
RMS 3.135
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 4982
Mean 28.5
RMS 17.54
y: counts
x: phi-bin, 0=12:30, follows TPC