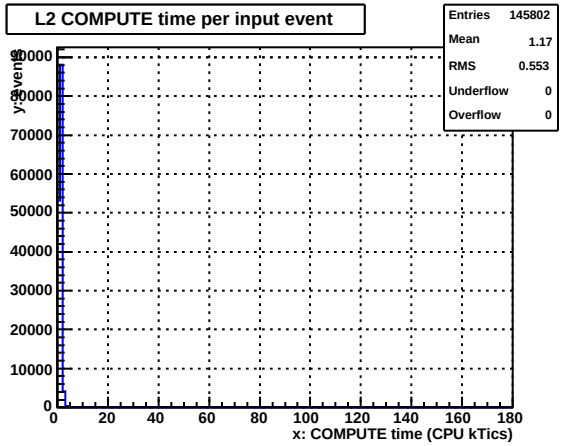
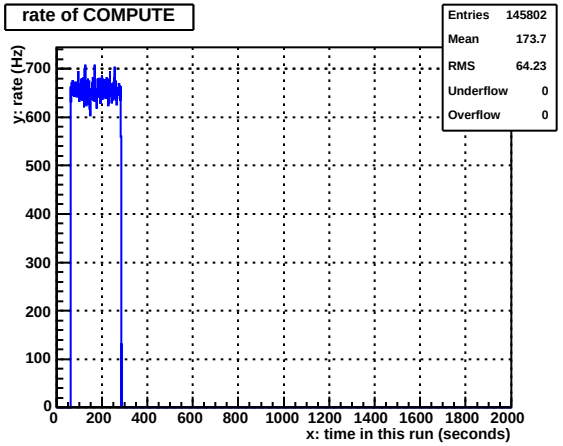
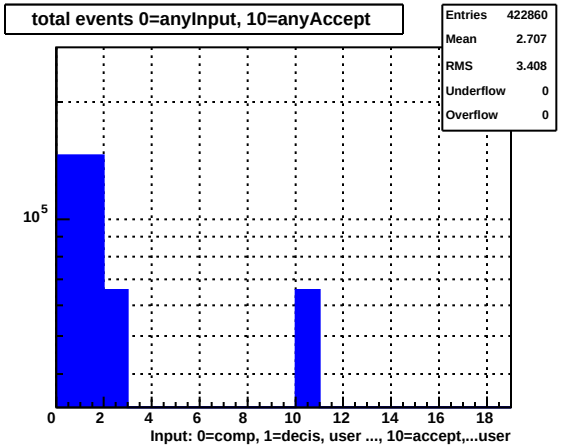
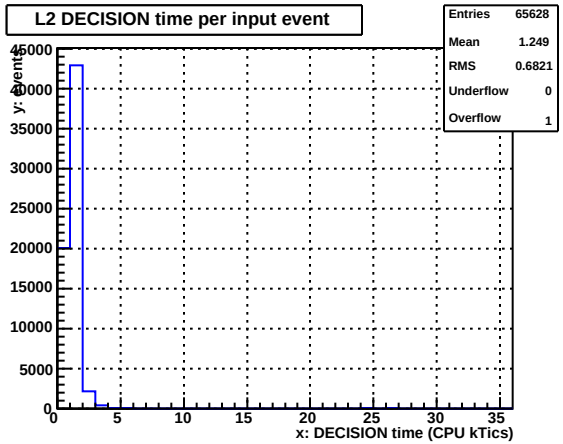
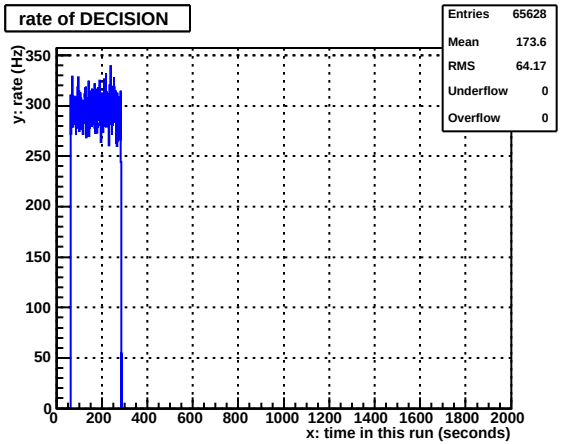
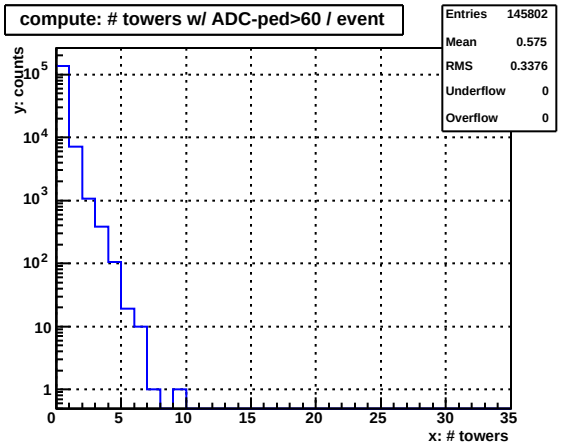

L2 COMPUTE time per input event
Entries 145802
Mean 1.17
RMS 0.553
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

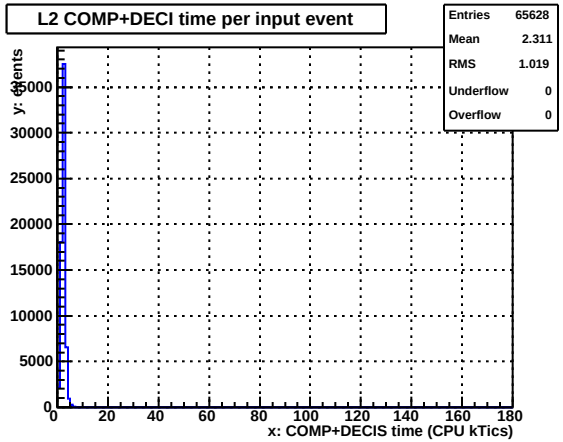

rate of COMPUTE
Entries 145802
Mean 173.7
RMS 64.23
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

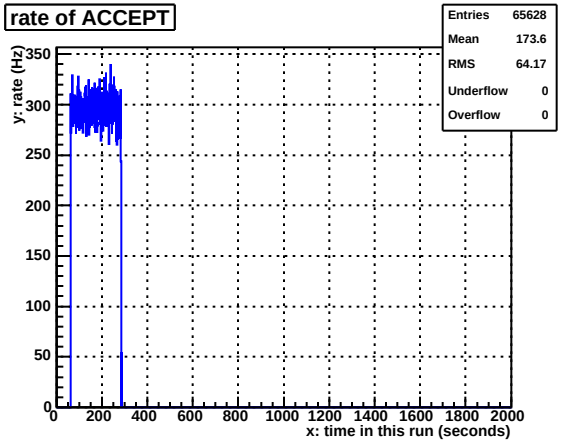

total events 0=anyInput, 10=anyAccept
Entries 422860
Mean 2.707
RMS 3.408
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

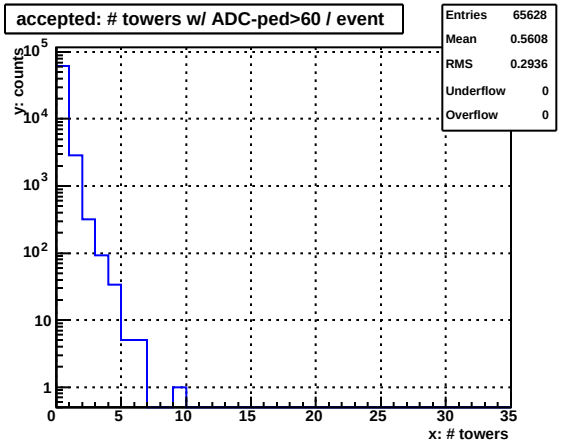

L2 DECISION time per input event
Entries 65628
Mean 1.249
RMS 0.6821
Underflow 0
Overflow 1
y: events
x: DECISION time (CPU kTics)

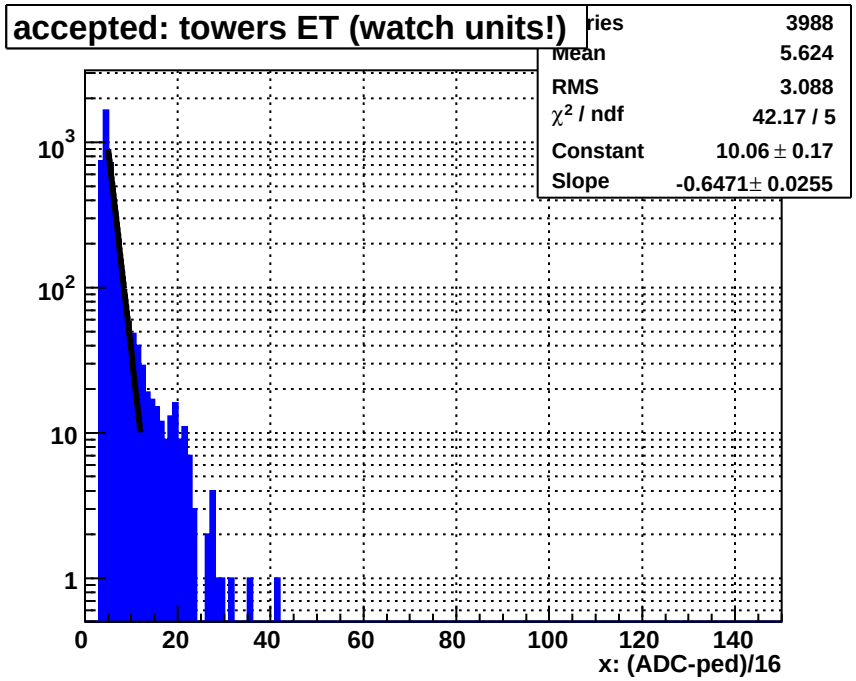

rate of DECISION
Entries 65628
Mean 173.6
RMS 64.17
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

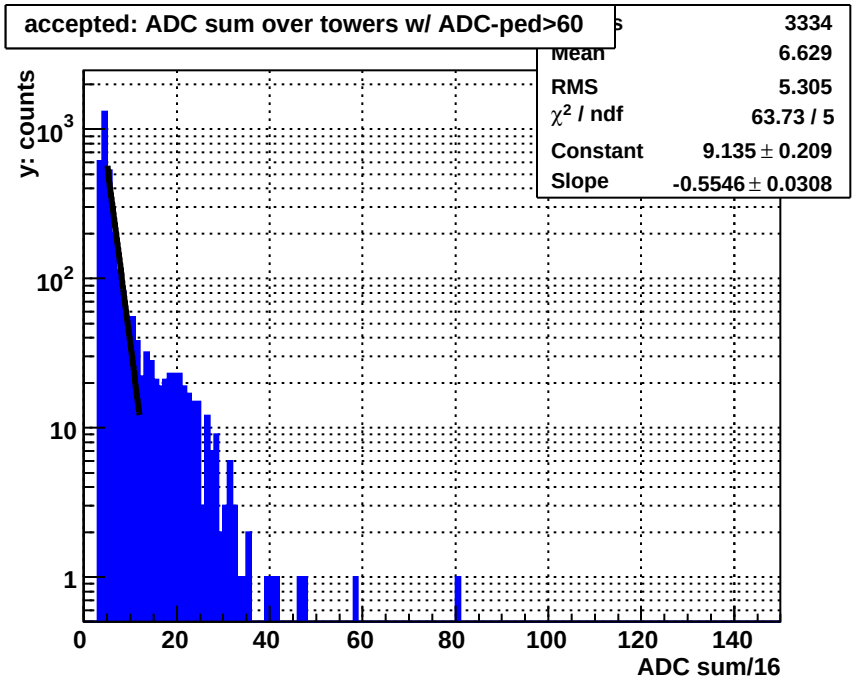

compute: # towers w/ ADC-ped>60 / event
Entries 145802
Mean 0.575
RMS 0.3376
Underflow 0
Overflow 0
y: counts
x: # towers

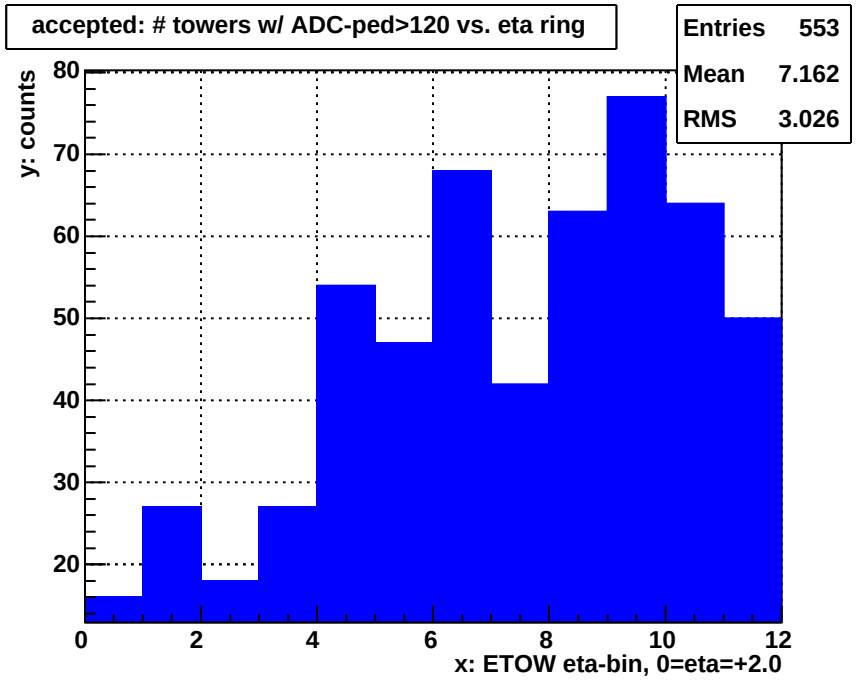

L2 COMP+DECI time per input event
Entries 65628
Mean 2.311
RMS 1.019
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

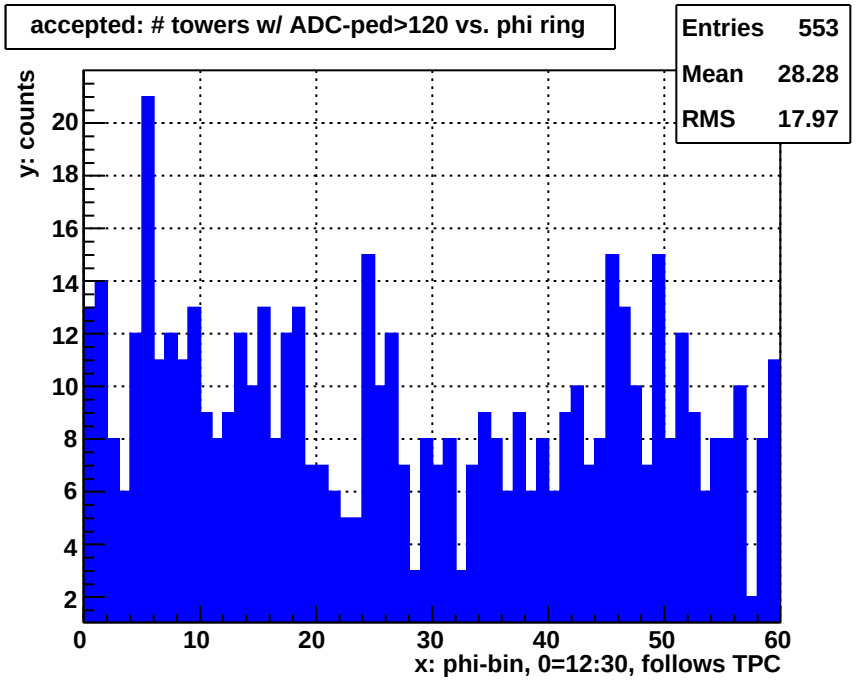

rate of ACCEPT
Entries 65628
Mean 173.6
RMS 64.17
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 65628
Mean 0.5608
RMS 0.2936
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 3988
Mean 5.624
RMS 3.088
χ² / ndf 42.17 / 5
Constant 10.06 ± 0.17
Slope -0.6471 ± 0.0255
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 3334
Mean 6.629
RMS 5.305
χ² / ndf 63.73 / 5
Constant 9.135 ± 0.209
Slope -0.5546 ± 0.0308
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 553
Mean 7.162
RMS 3.026
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 553
Mean 28.28
RMS 17.97
y: counts
x: phi-bin, 0=12:30, follows TPC