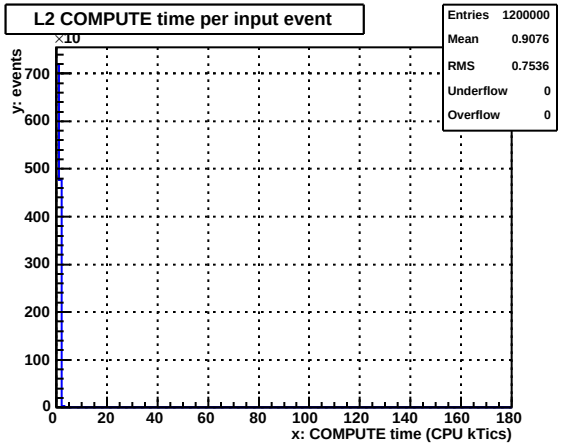
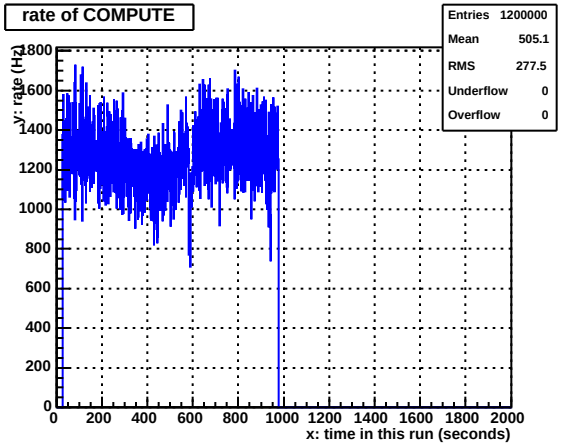
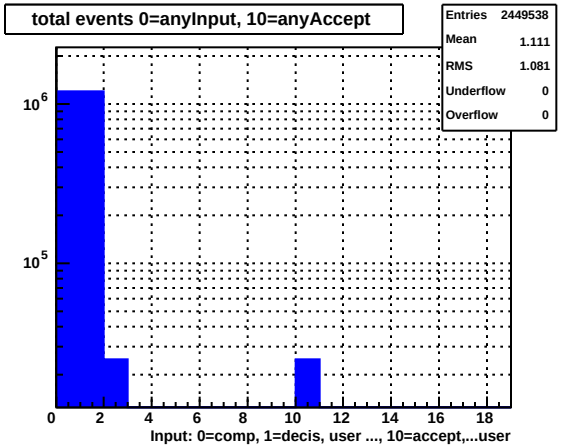
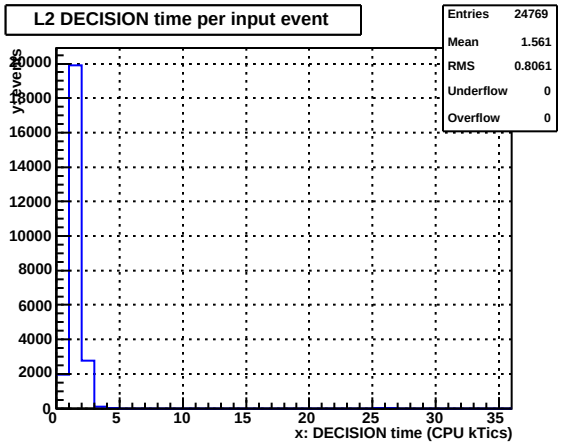
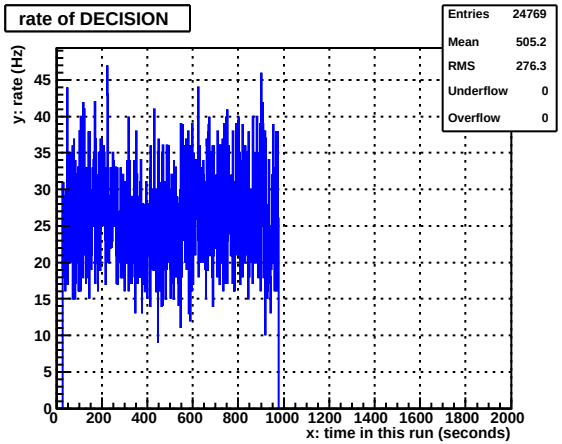
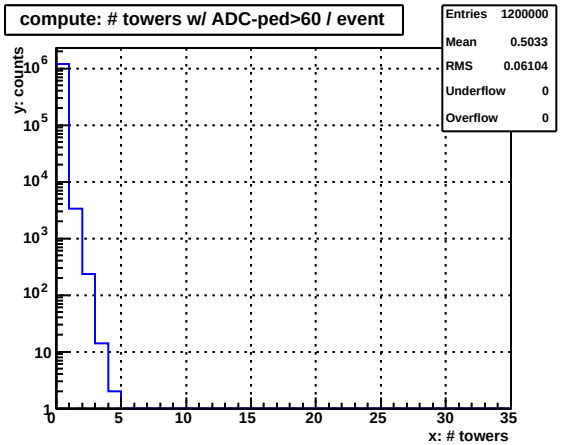

L2 COMPUTE time per input event
Entries 1200000
Mean 0.9076
RMS 0.7536
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

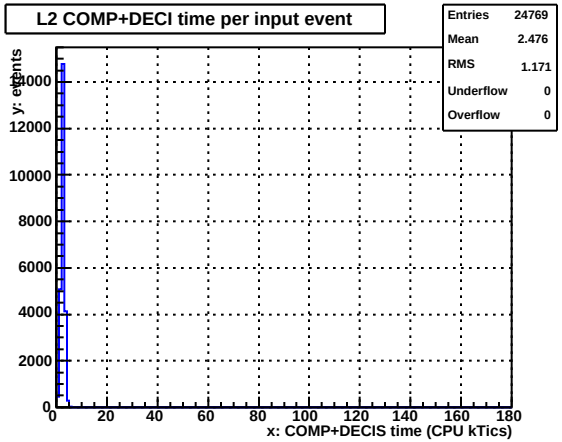

rate of COMPUTE
Entries 1200000
Mean 505.1
RMS 277.5
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

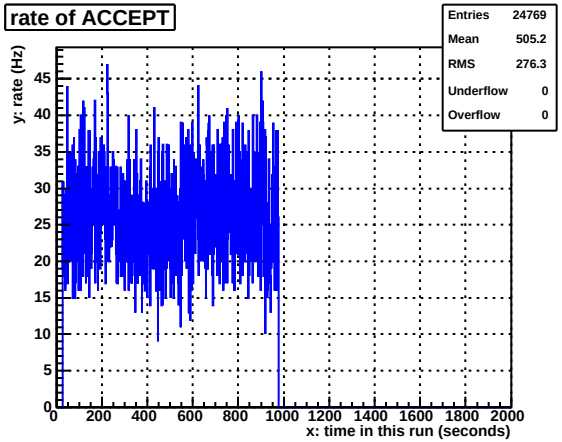

total events 0=anyInput, 10=anyAccept
Entries 2449538
Mean 1.111
RMS 1.081
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

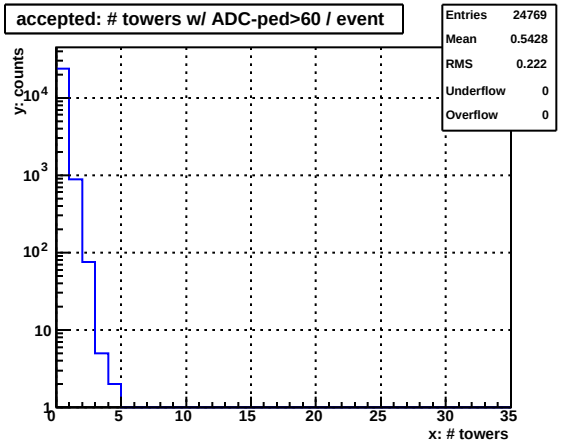

L2 DECISION time per input event
Entries 24769
Mean 1.561
RMS 0.8061
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

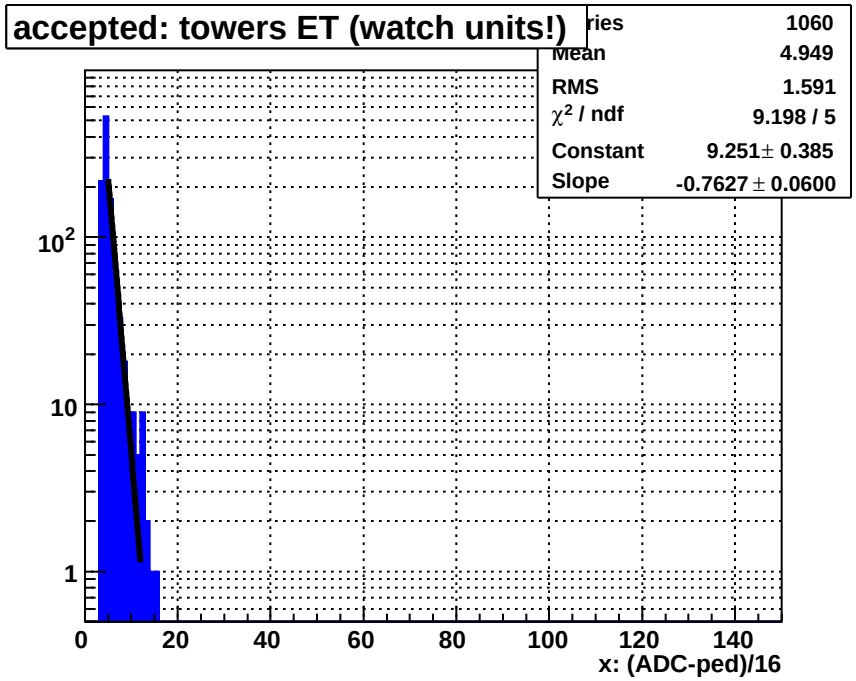

rate of DECISION
Entries 24769
Mean 505.2
RMS 276.3
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

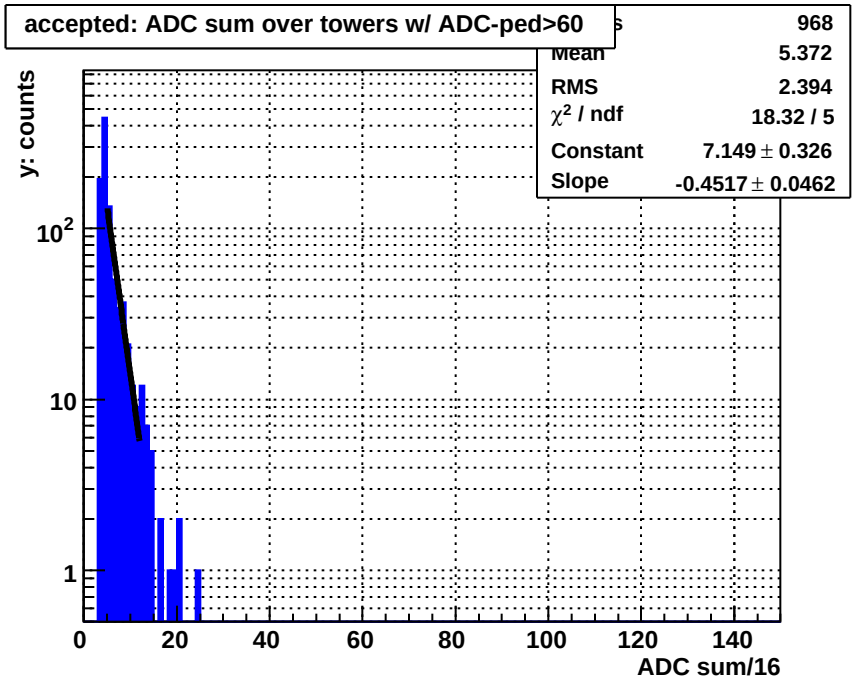

compute: # towers w/ ADC-ped>60 / event
Entries 1200000
Mean 0.5033
RMS 0.06104
Underflow 0
Overflow 0
y: counts
x: # towers

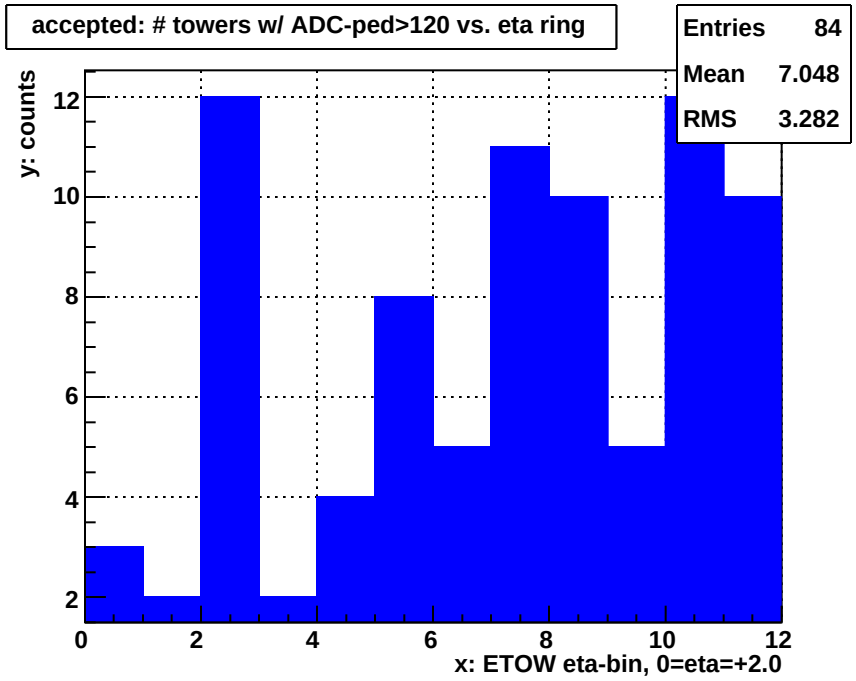

L2 COMP+DECI time per input event
Entries 24769
Mean 2.476
RMS 1.171
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

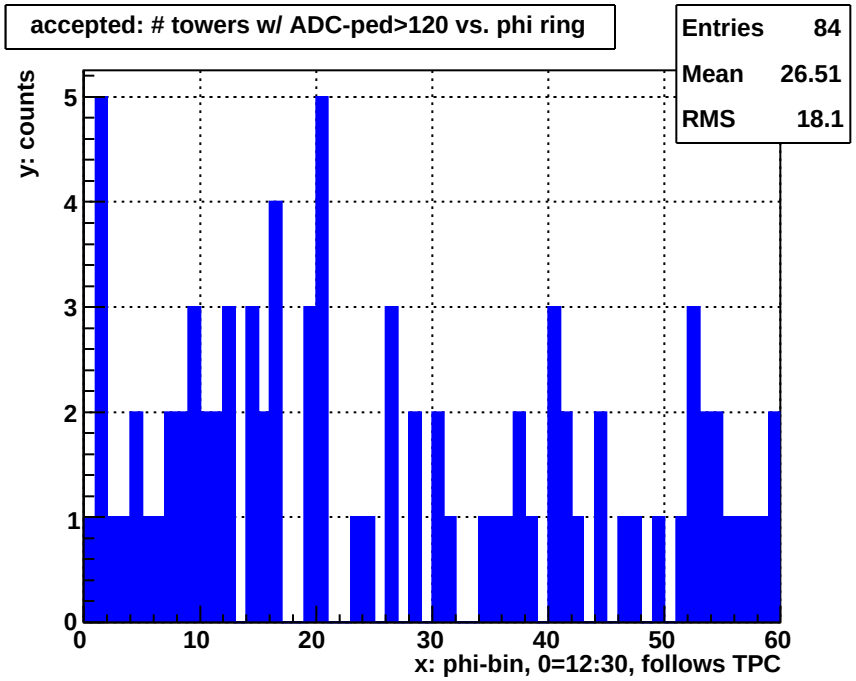

rate of ACCEPT
Entries 24769
Mean 505.2
RMS 276.3
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 24769
Mean 0.5428
RMS 0.222
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 1060
Mean 4.949
RMS 1.591
χ² / ndf 9.198 / 5
Constant 9.251 ± 0.385
Slope -0.7627 ± 0.0600
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 968
Mean 5.372
RMS 2.394
χ² / ndf 18.32 / 5
Constant 7.149 ± 0.326
Slope -0.4517 ± 0.0462
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 84
Mean 7.048
RMS 3.282
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 84
Mean 26.51
RMS 18.1
y: counts
x: phi-bin, 0=12:30, follows TPC