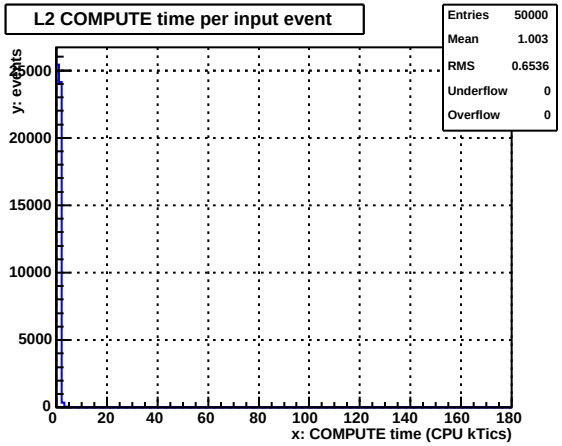
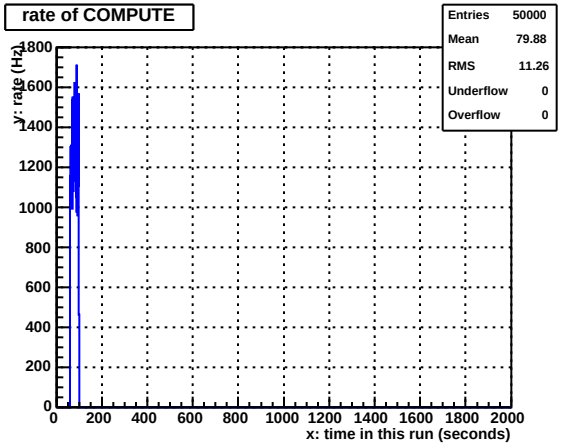
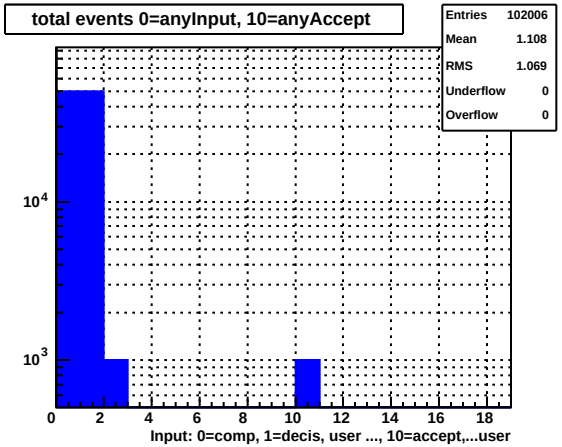
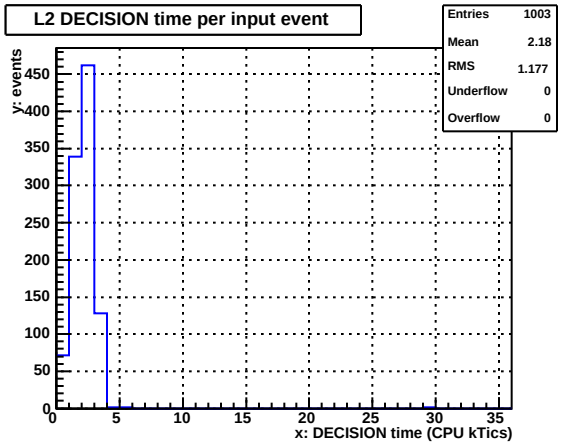
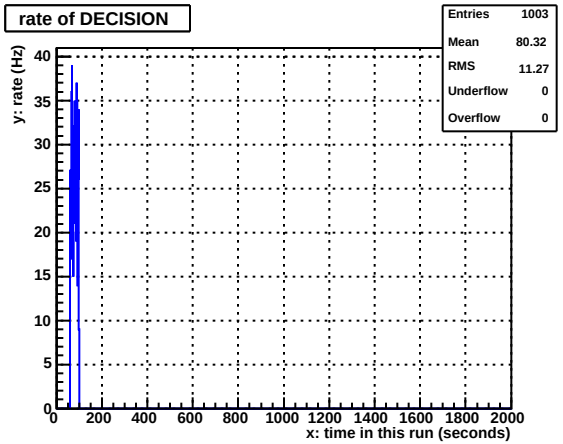
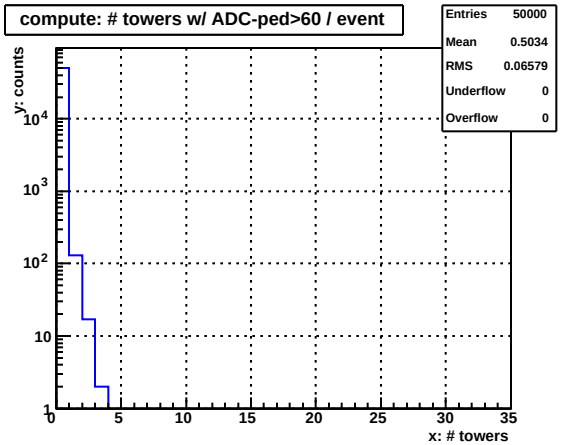

L2 COMPUTE time per input event
Entries 50000
Mean 1.003
RMS 0.6536
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

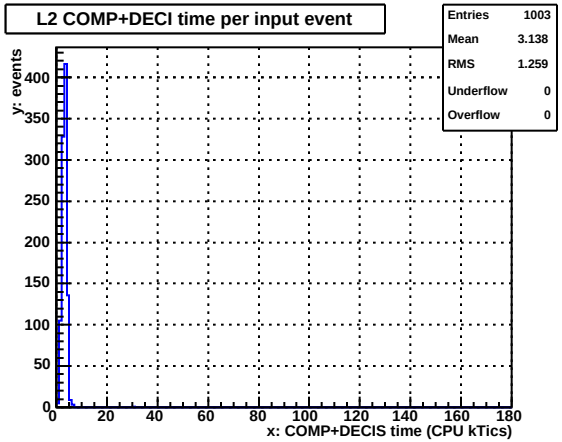

rate of COMPUTE
Entries 50000
Mean 79.88
RMS 11.26
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

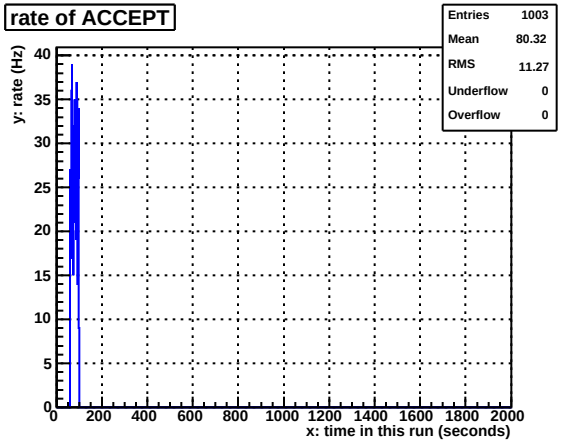

total events 0=anyInput, 10=anyAccept
Entries 102006
Mean 1.108
RMS 1.069
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

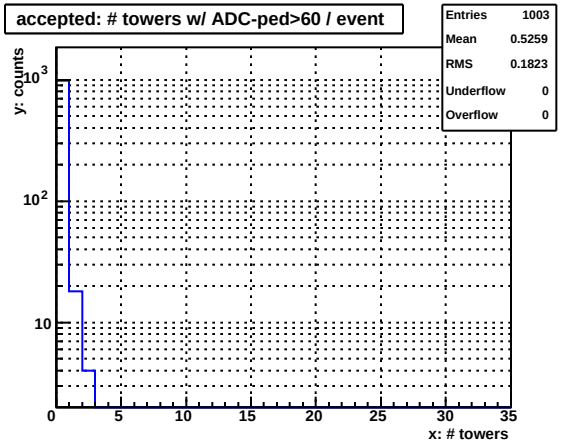

L2 DECISION time per input event
Entries 1003
Mean 2.18
RMS 1.177
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

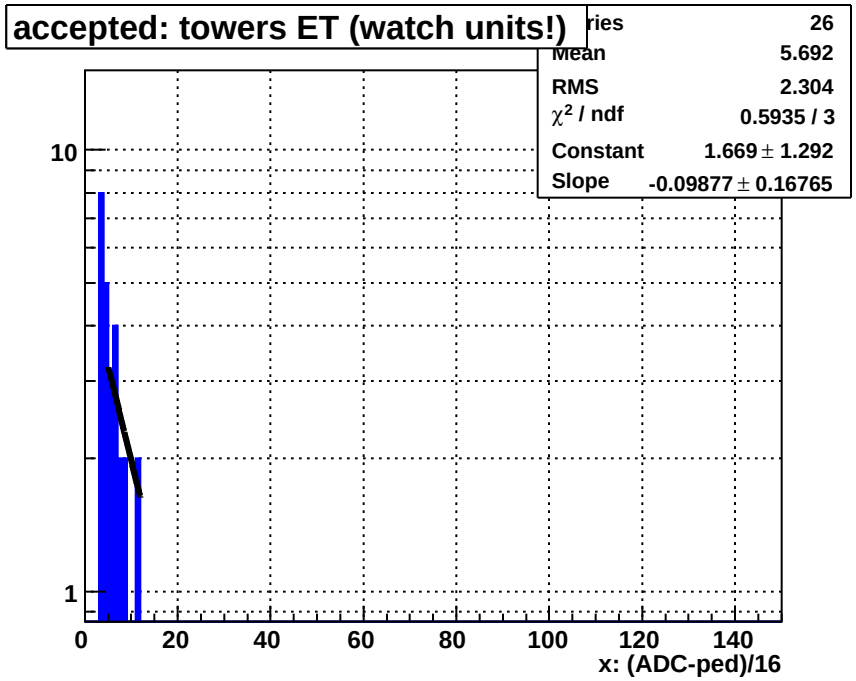

rate of DECISION
Entries 1003
Mean 80.32
RMS 11.27
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

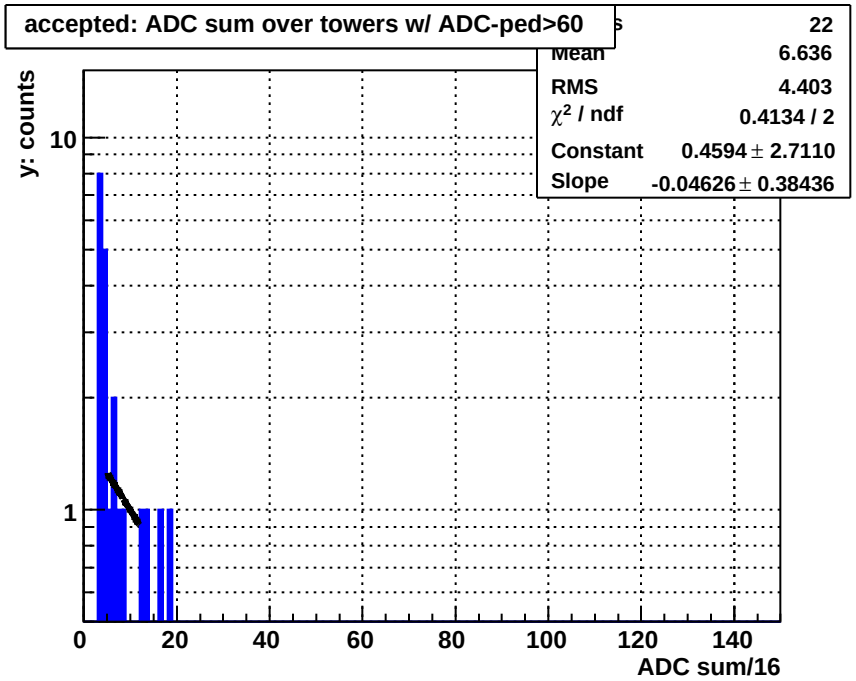

compute: # towers w/ ADC-ped>60 / event
Entries 50000
Mean 0.5034
RMS 0.06579
Underflow 0
Overflow 0
y: counts
x: # towers

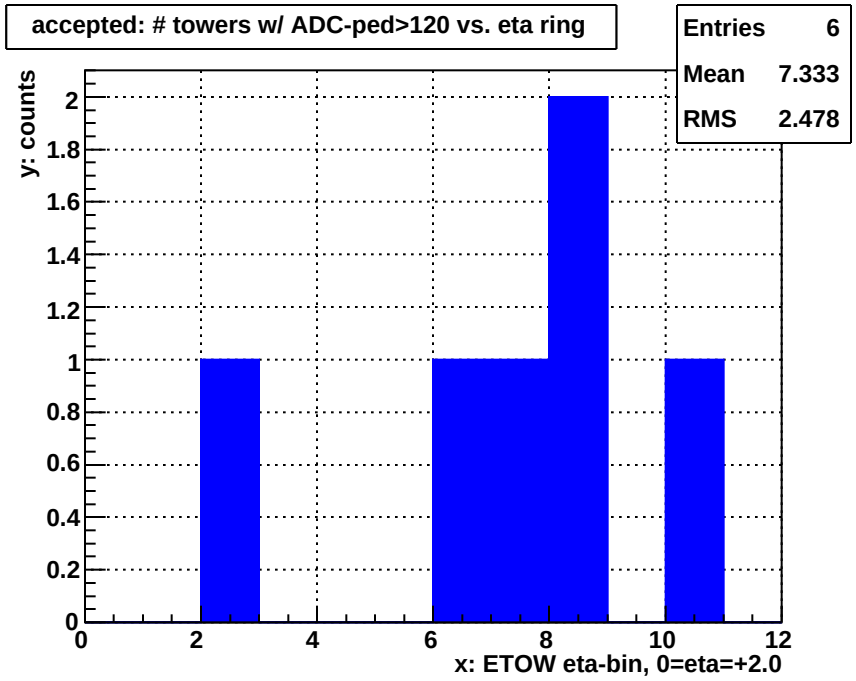

L2 COMP+DECI time per input event
Entries 1003
Mean 3.138
RMS 1.259
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

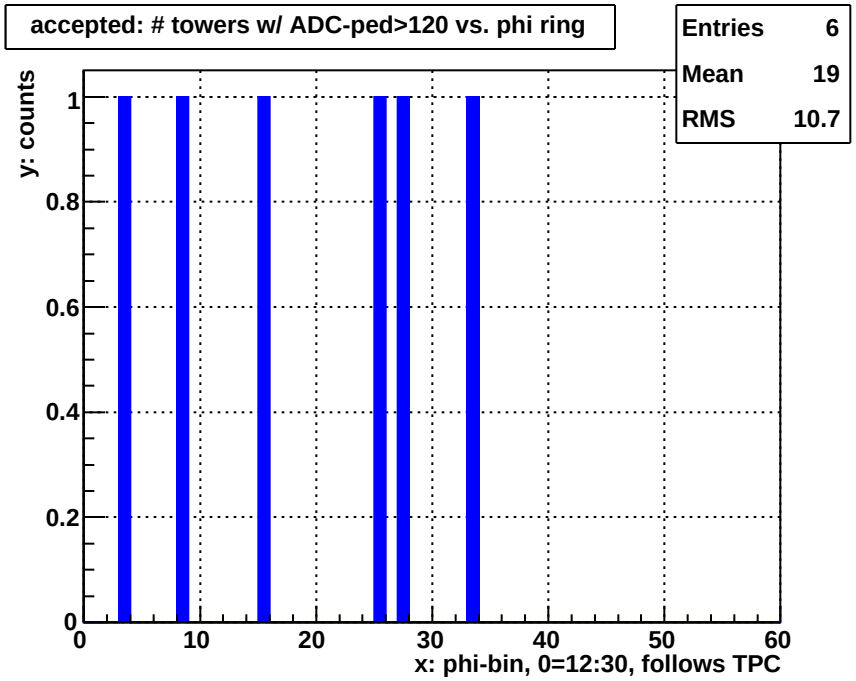

rate of ACCEPT
Entries 1003
Mean 80.32
RMS 11.27
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 1003
Mean 0.5259
RMS 0.1823
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 26
Mean 5.692
RMS 2.304
χ² / ndf 0.5935 / 3
Constant 1.669 ± 1.292
Slope -0.09877 ± 0.16765
10
1
0 20 40 60 80 100 120 140
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 22
Mean 6.636
RMS 4.403
χ² / ndf 0.4134 / 2
Constant 0.4594 ± 2.7110
Slope -0.04626 ± 0.38436
y: counts
10
1
0 20 40 60 80 100 120 140
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 6
Mean 7.333
RMS 2.478
y: counts
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0 2 4 6 8 10 12
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 6
Mean 19
RMS 10.7
y: counts
1
0.8
0.6
0.4
0.2
0
0 10 20 30 40 50 60
x: phi-bin, 0=12:30, follows TPC