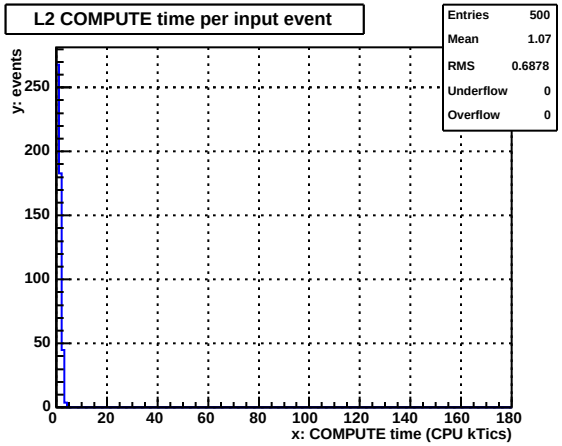
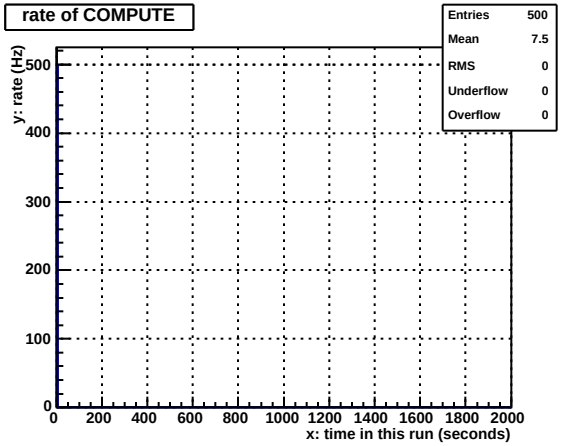
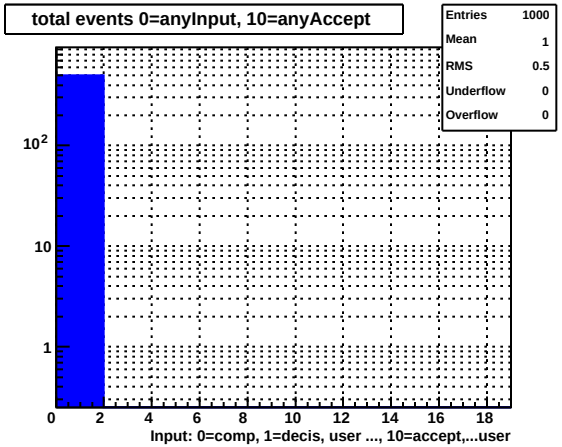
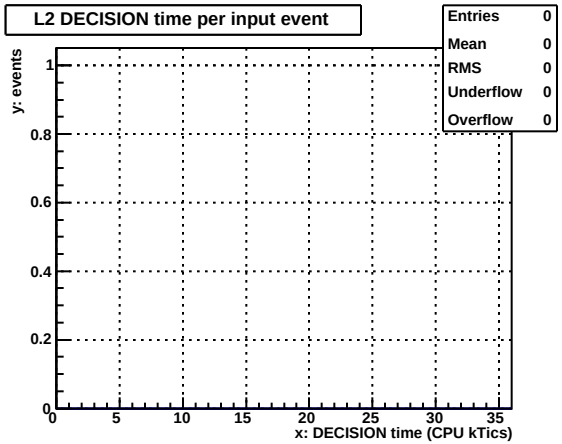
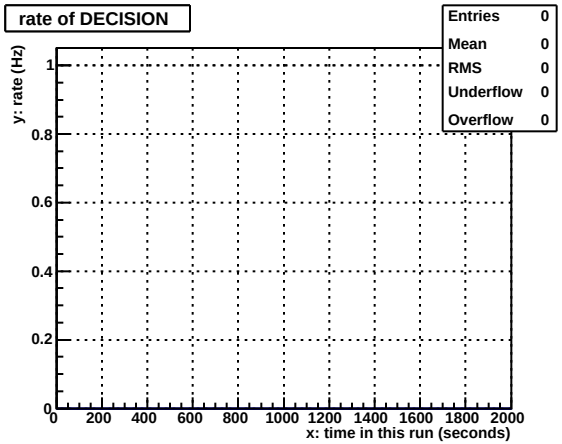
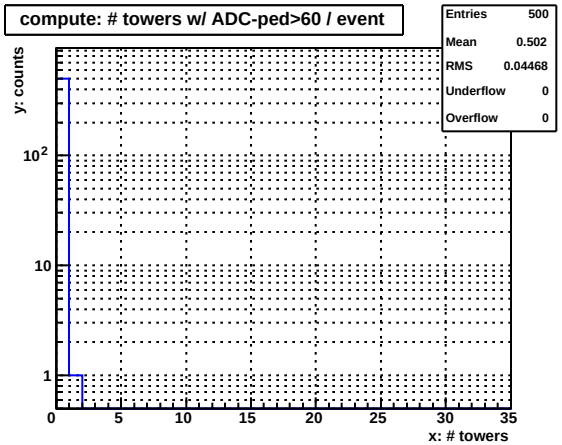

L2 COMPUTE time per input event
Entries 500
Mean 1.07
RMS 0.6878
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

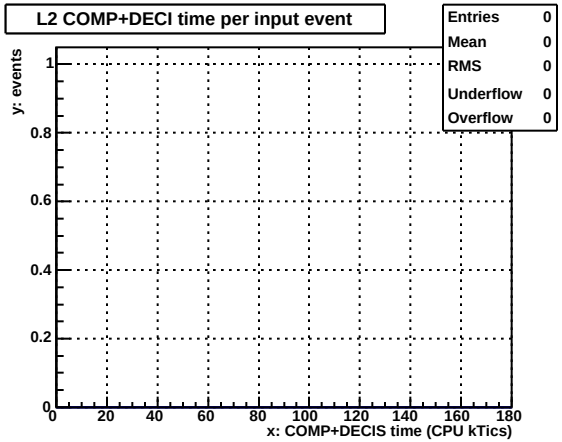

rate of COMPUTE
Entries 500
Mean 7.5
RMS 0
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

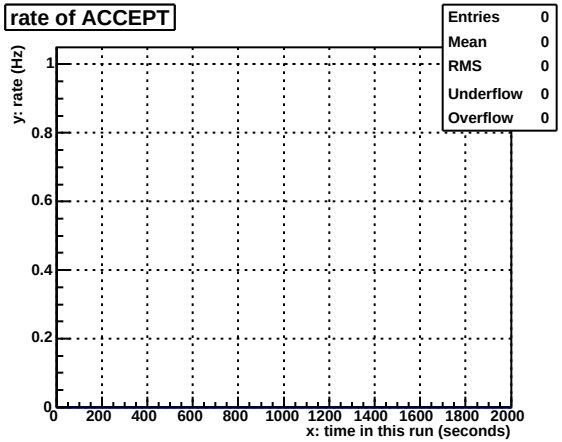

total events 0=anyInput, 10=anyAccept
Entries 1000
Mean 1
RMS 0.5
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

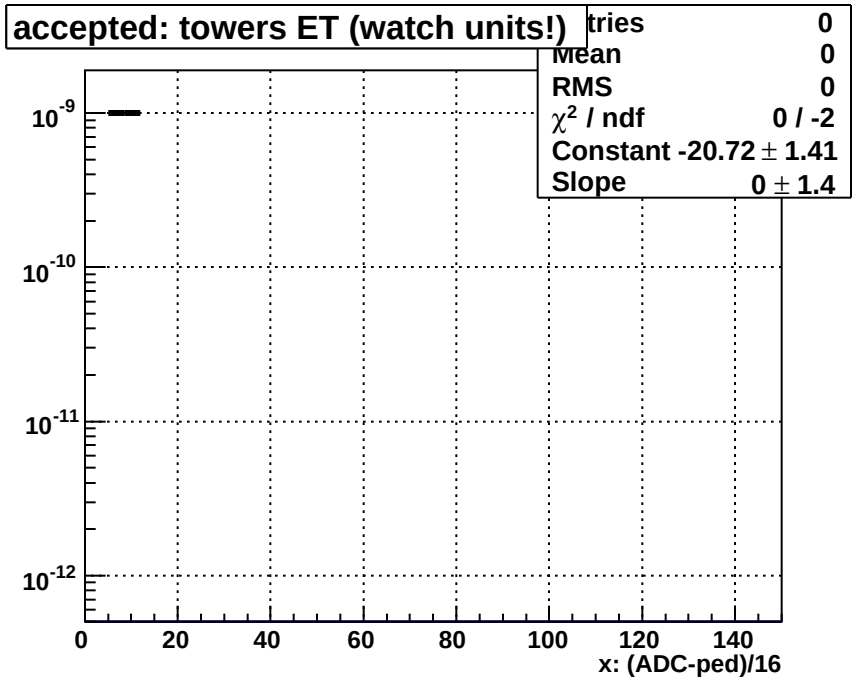

L2 DECISION time per input event
Entries 0
Mean 0
RMS 0
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

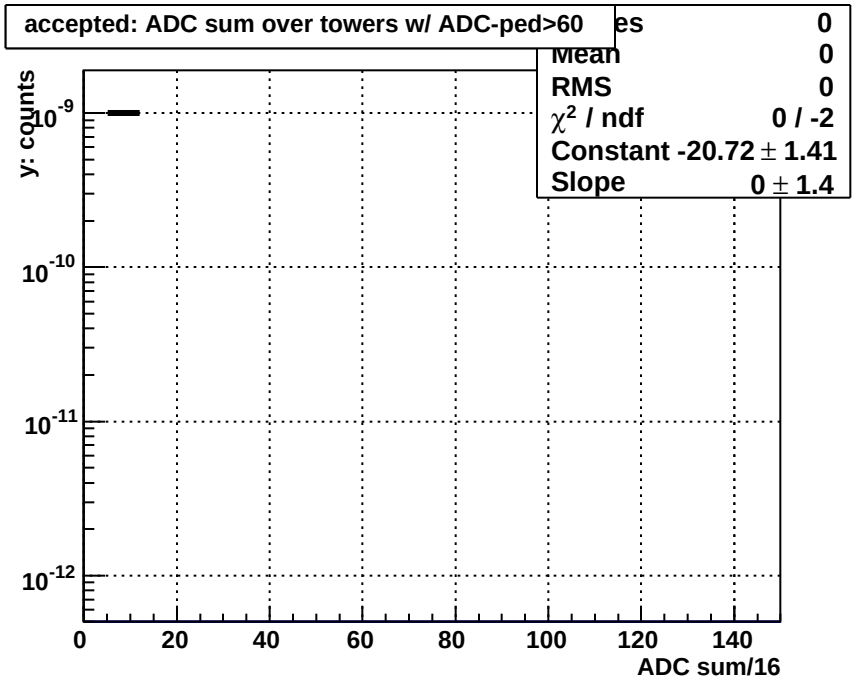

rate of DECISION
Entries 0
Mean 0
RMS 0
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

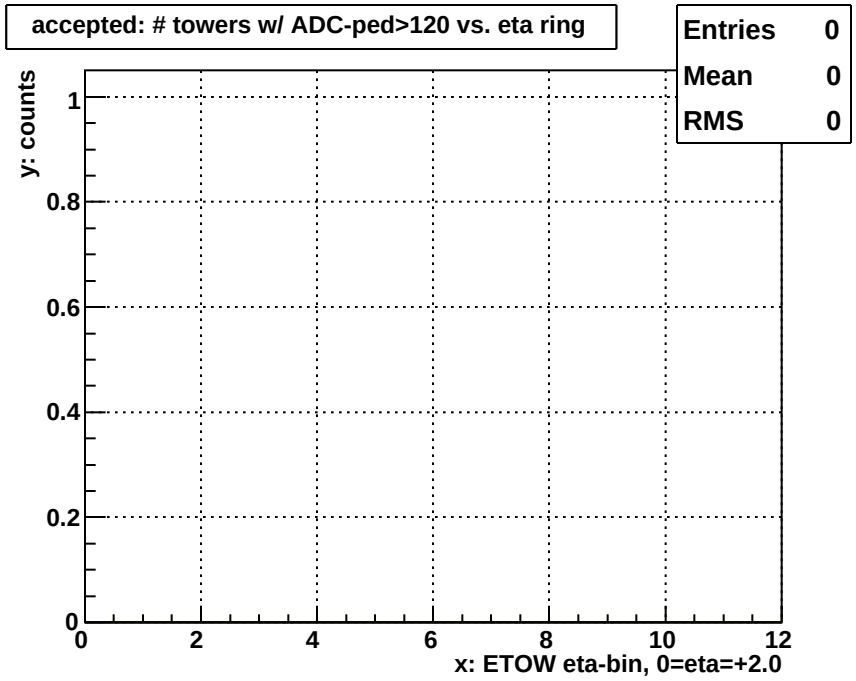

compute: # towers w/ ADC-ped>60 / event
Entries 500
Mean 0.502
RMS 0.04468
Underflow 0
Overflow 0
y: counts
x: # towers

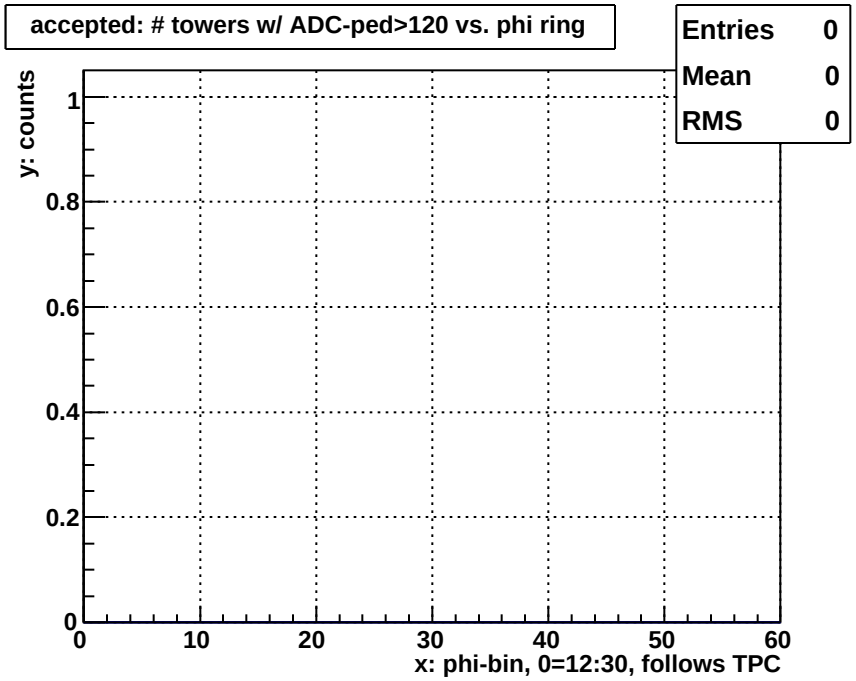

L2 COMP+DECI time per input event
Entries 0
Mean 0
RMS 0
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)


rate of ACCEPT
Entries 0
Mean 0
RMS 0
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: towers ET (watch units!)
tries 0
Mean 0
RMS 0
χ^2 / ndf 0 / -2
Constant -20.72 ± 1.41
Slope 0 ± 1.4
10^{-9}
10^{-10}
10^{-11}
10^{-12}
0 20 40 60 80 100 120 140
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
es 0
Mean 0
RMS 0
χ^2 / ndf 0 / -2
Constant -20.72 ± 1.41
Slope 0 ± 1.4
y: counts
10^{-9}
10^{-10}
10^{-11}
10^{-12}
0 20 40 60 80 100 120 140
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 0
Mean 0
RMS 0
y: counts
1
0.8
0.6
0.4
0.2
0
0 2 4 6 8 10 12
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 0
Mean 0
RMS 0
y: counts
1
0.8
0.6
0.4
0.2
0
0 10 20 30 40 50 60
x: phi-bin, 0=12:30, follows TPC