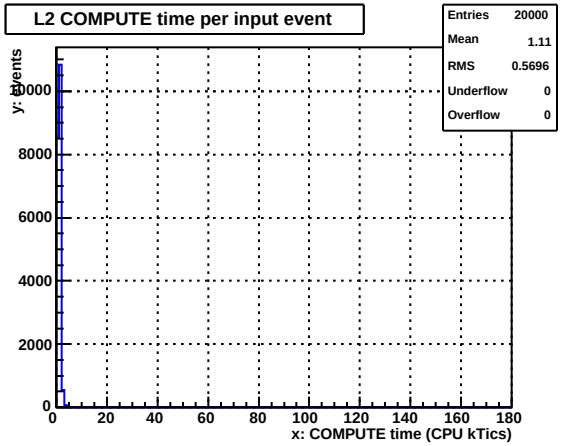
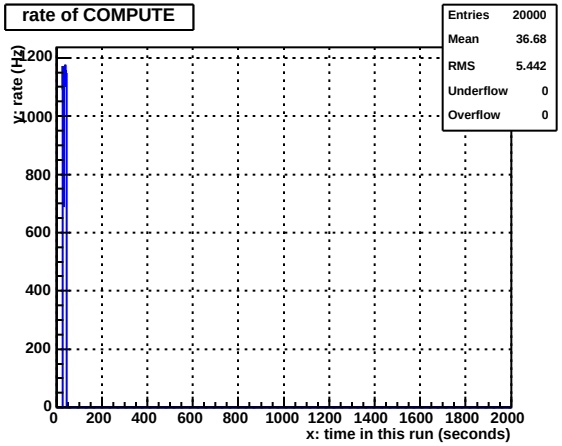
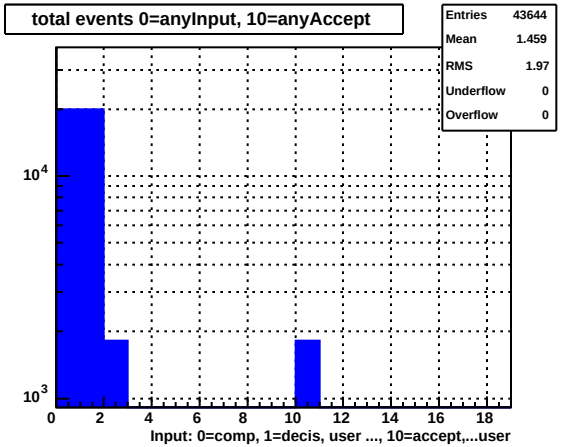
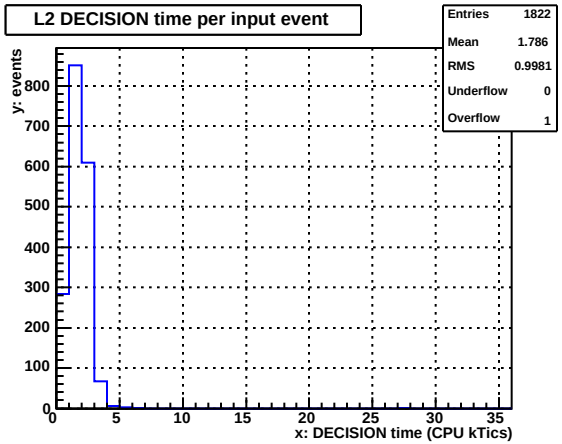
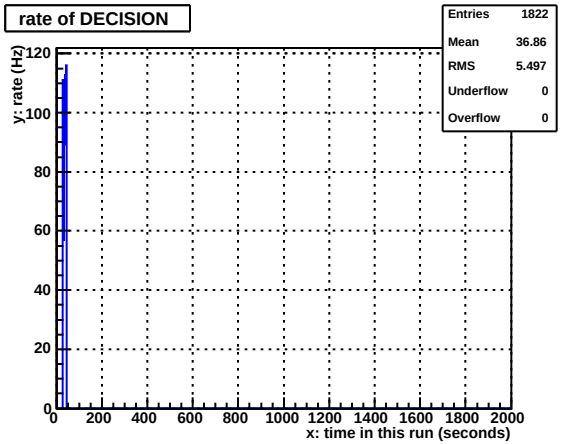
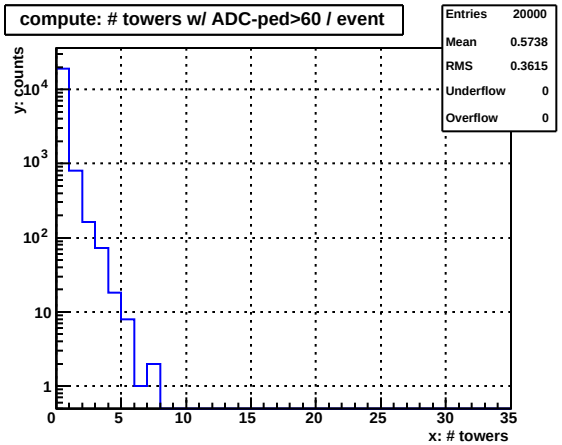

L2 COMPUTE time per input event
Entries 20000
Mean 1.11
RMS 0.5696
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

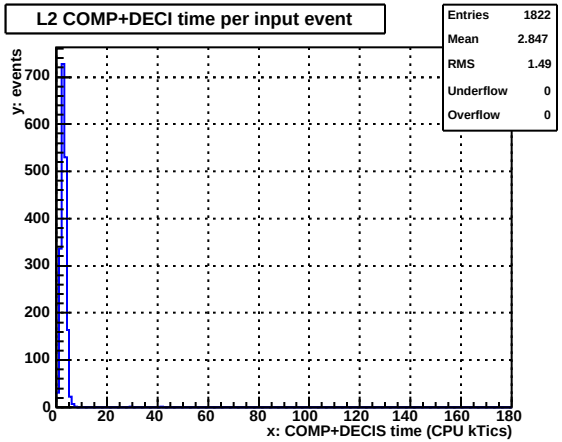

rate of COMPUTE
Entries 20000
Mean 36.68
RMS 5.442
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

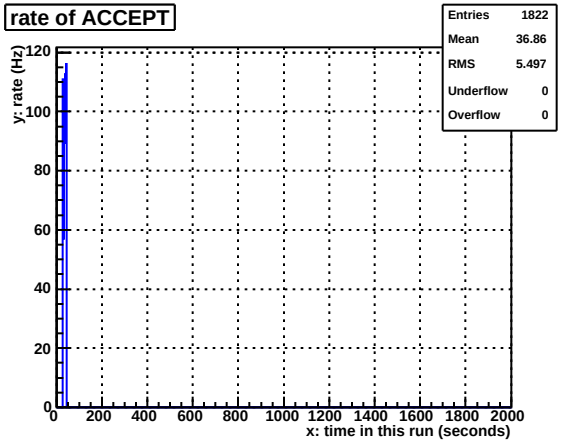

total events 0=anyInput, 10=anyAccept
Entries 43644
Mean 1.459
RMS 1.97
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

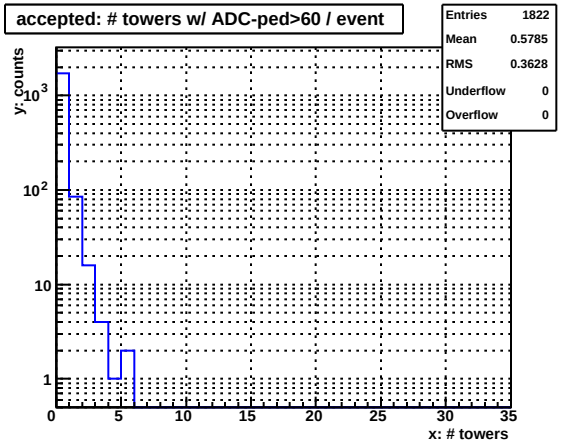

L2 DECISION time per input event
Entries 1822
Mean 1.786
RMS 0.9981
Underflow 0
Overflow 1
y: events
x: DECISION time (CPU kTics)

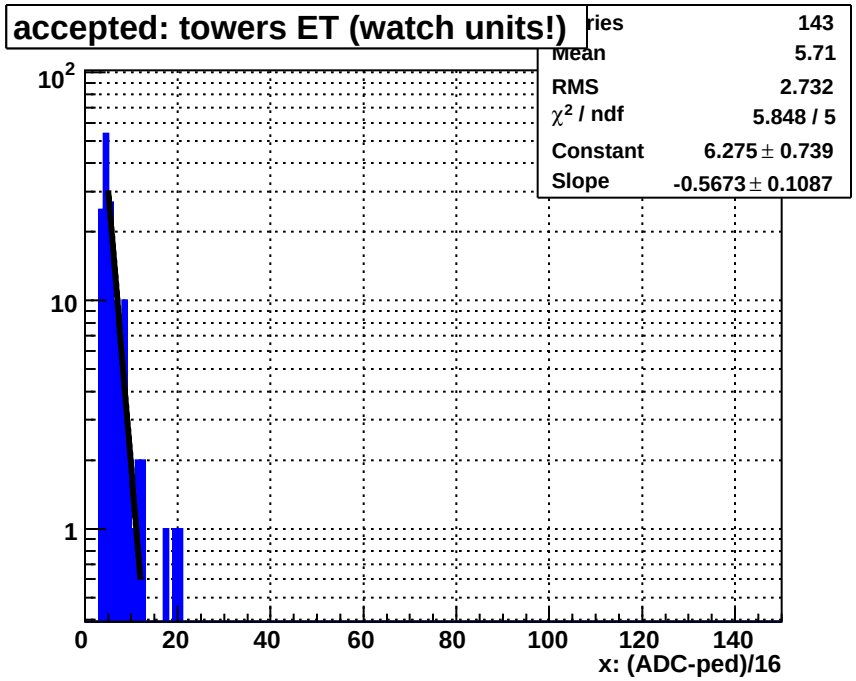

rate of DECISION
Entries 1822
Mean 36.86
RMS 5.497
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

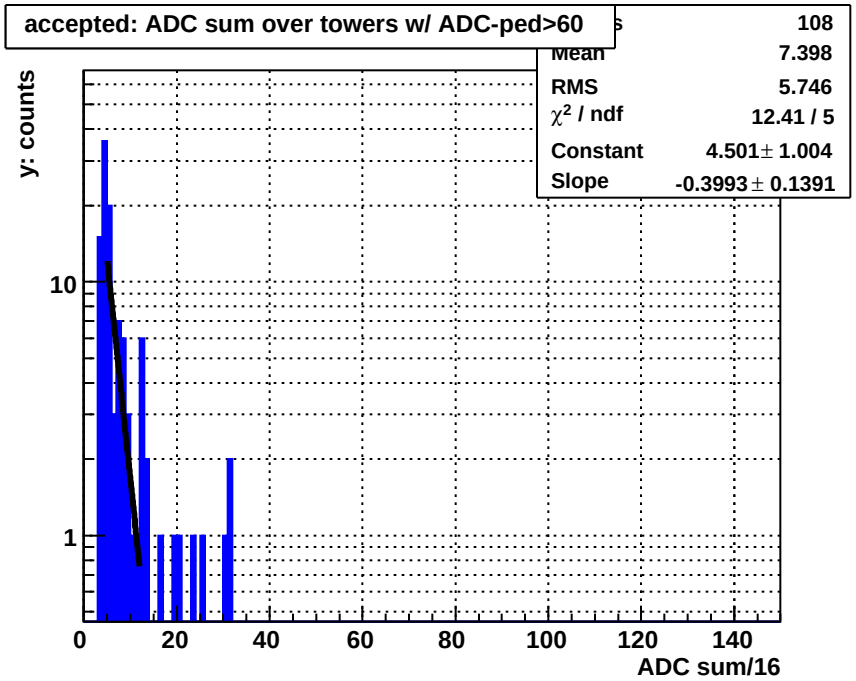

compute: # towers w/ ADC-ped>60 / event
Entries 20000
Mean 0.5738
RMS 0.3615
Underflow 0
Overflow 0
y: counts
x: # towers

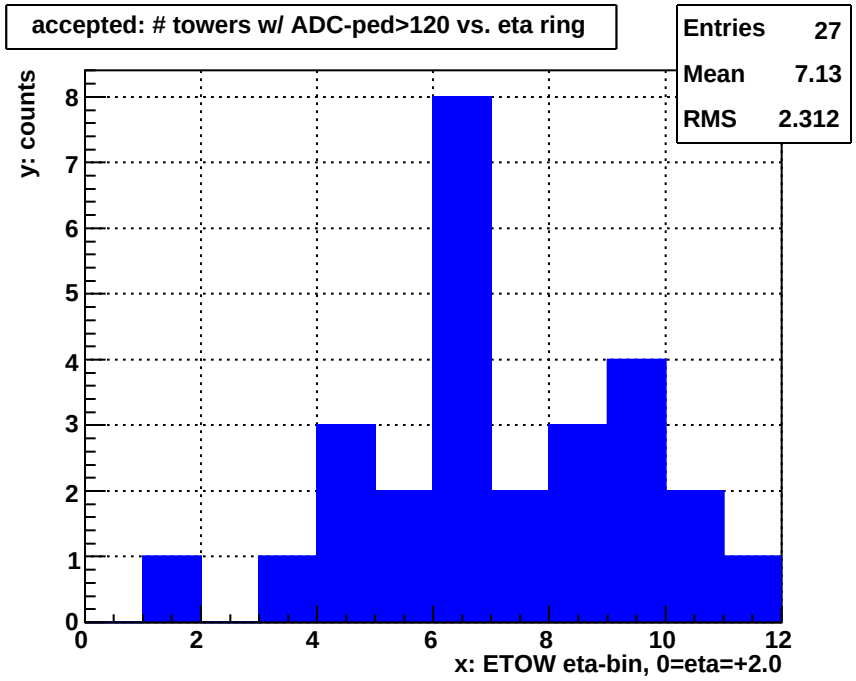

L2 COMP+DECI time per input event
Entries 1822
Mean 2.847
RMS 1.49
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

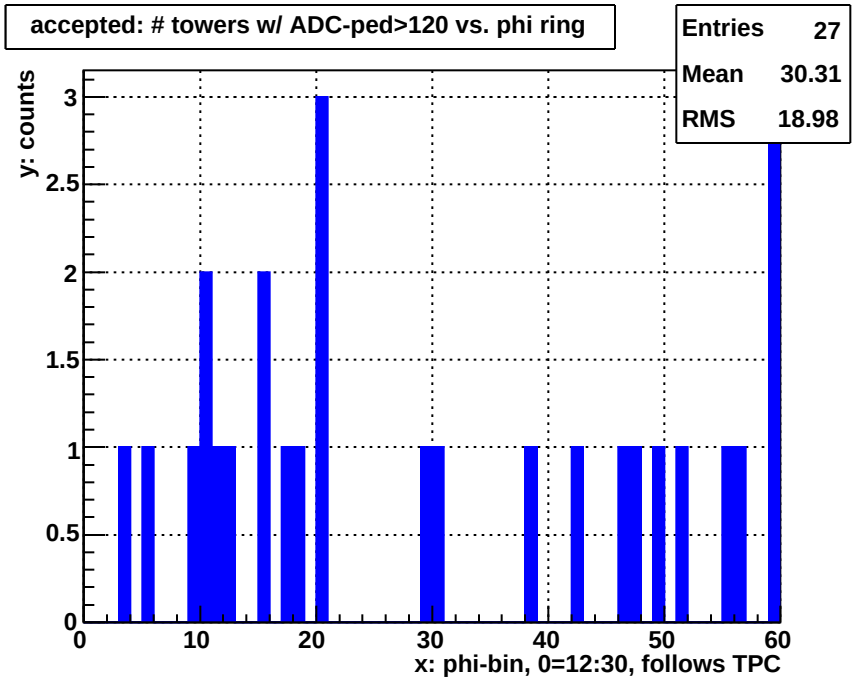

rate of ACCEPT
Entries 1822
Mean 36.86
RMS 5.497
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 1822
Mean 0.5785
RMS 0.3628
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 143
Mean 5.71
RMS 2.732
χ² / ndf 5.848 / 5
Constant 6.275 ± 0.739
Slope -0.5673 ± 0.1087
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 108
Mean 7.398
RMS 5.746
χ² / ndf 12.41 / 5
Constant 4.501 ± 1.004
Slope -0.3993 ± 0.1391
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 27
Mean 7.13
RMS 2.312
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 27
Mean 30.31
RMS 18.98
y: counts
x: phi-bin, 0=12:30, follows TPC