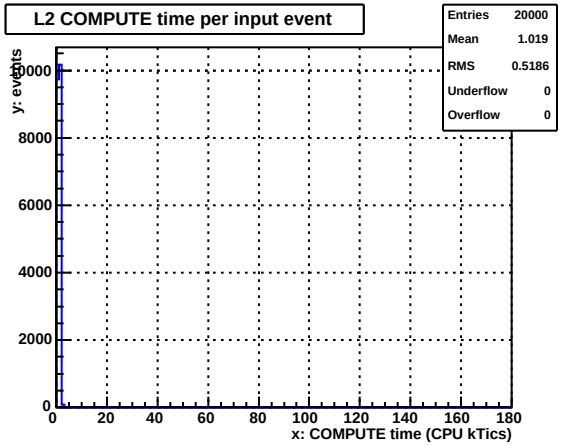
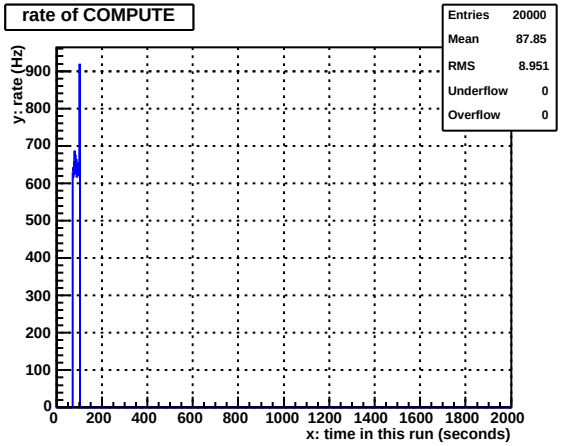
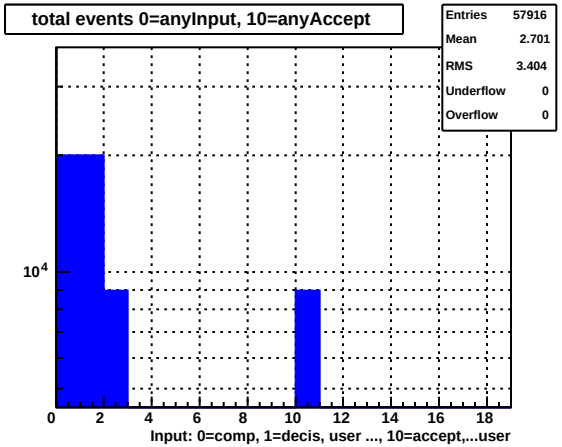
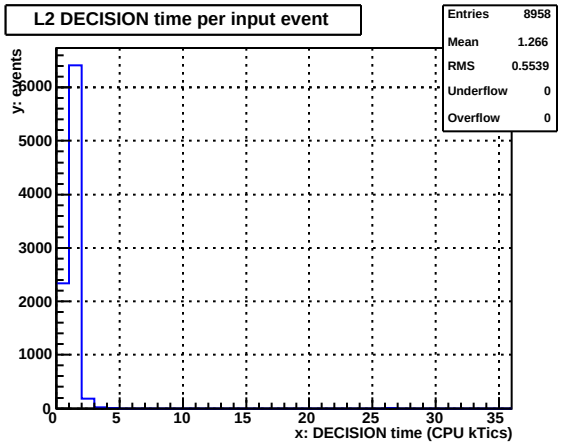
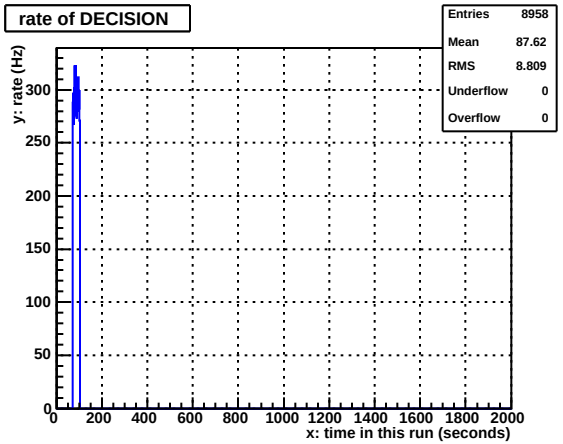
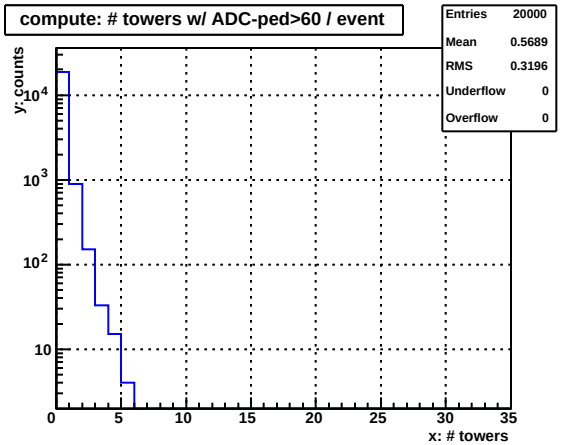

L2 COMPUTE time per input event
Entries 20000
Mean 1.019
RMS 0.5186
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

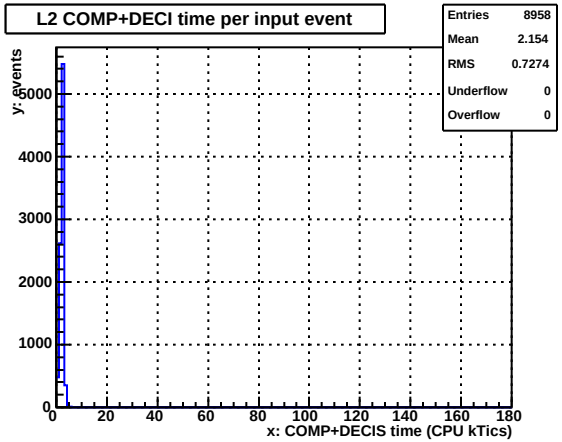

rate of COMPUTE
Entries 20000
Mean 87.85
RMS 8.951
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

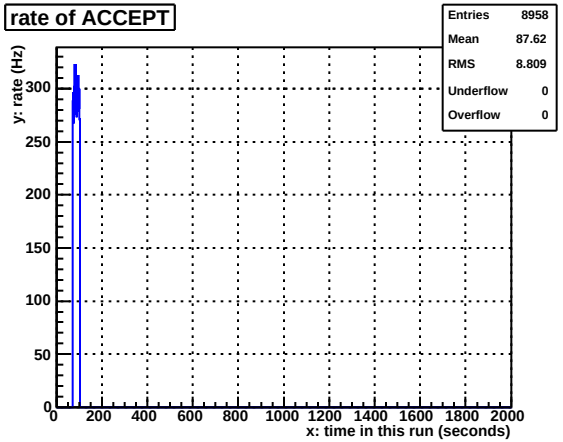

total events 0=anyInput, 10=anyAccept
Entries 57916
Mean 2.701
RMS 3.404
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

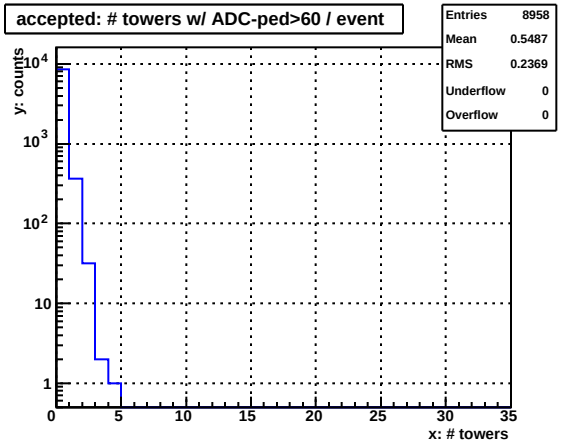

L2 DECISION time per input event
Entries 8958
Mean 1.266
RMS 0.5539
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

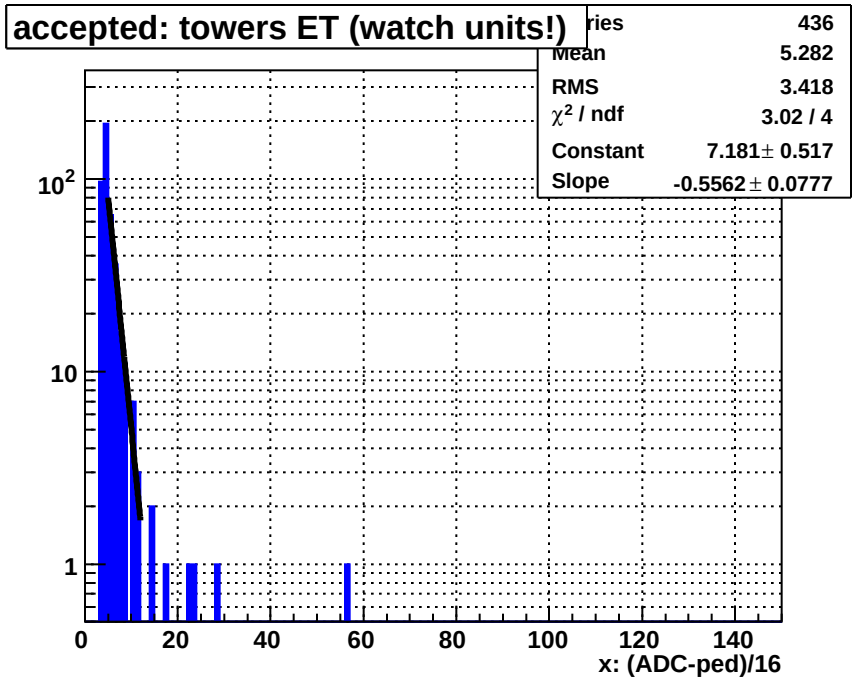

rate of DECISION
Entries 8958
Mean 87.62
RMS 8.809
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

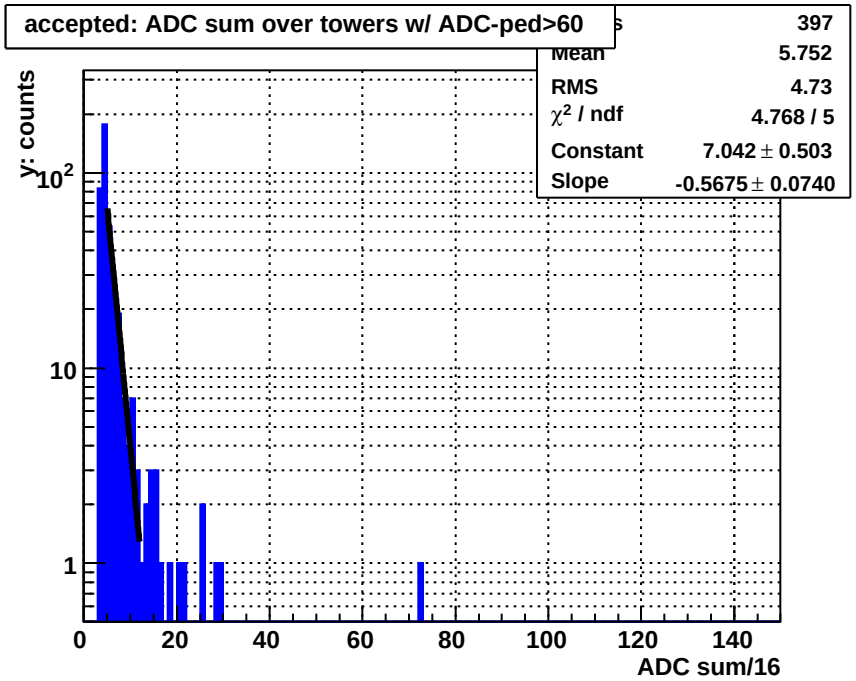

compute: # towers w/ ADC-ped>60 / event
Entries 20000
Mean 0.5689
RMS 0.3196
Underflow 0
Overflow 0
y: counts
x: # towers

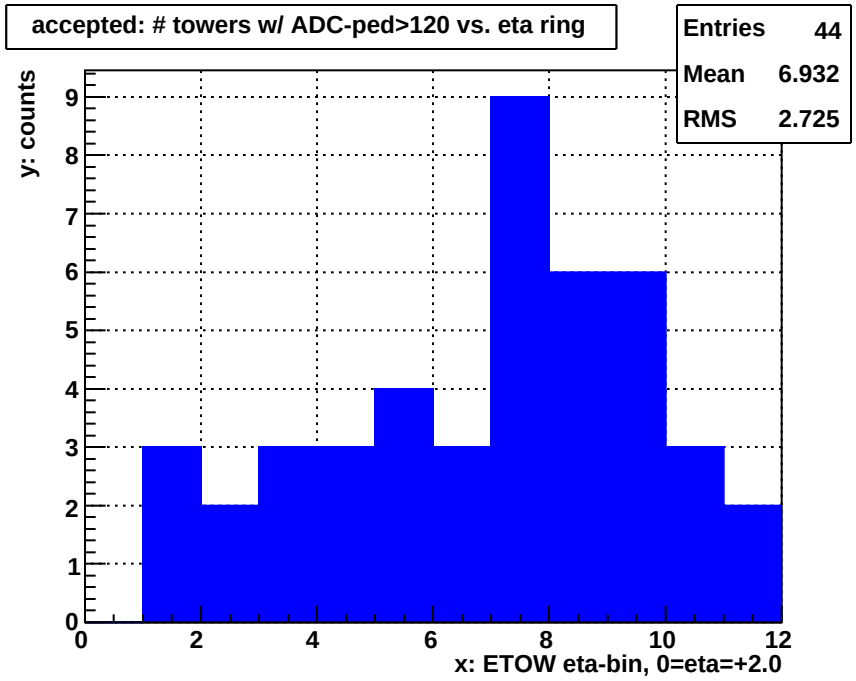

L2 COMP+DECI time per input event
Entries 8958
Mean 2.154
RMS 0.7274
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

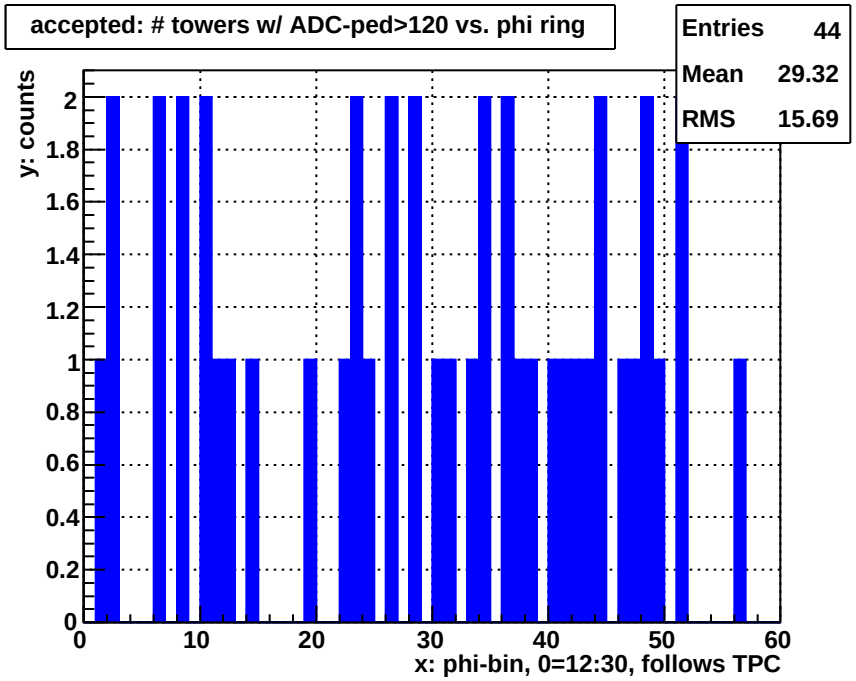

rate of ACCEPT
Entries 8958
Mean 87.62
RMS 8.809
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 8958
Mean 0.5487
RMS 0.2369
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
Entries 436
Mean 5.282
RMS 3.418
χ² / ndf 3.02 / 4
Constant 7.181 ± 0.517
Slope -0.5562 ± 0.0777
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
Entries 397
Mean 5.752
RMS 4.73
χ² / ndf 4.768 / 5
Constant 7.042 ± 0.503
Slope -0.5675 ± 0.0740
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 44
Mean 6.932
RMS 2.725
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 44
Mean 29.32
RMS 15.69
y: counts
x: phi-bin, 0=12:30, follows TPC