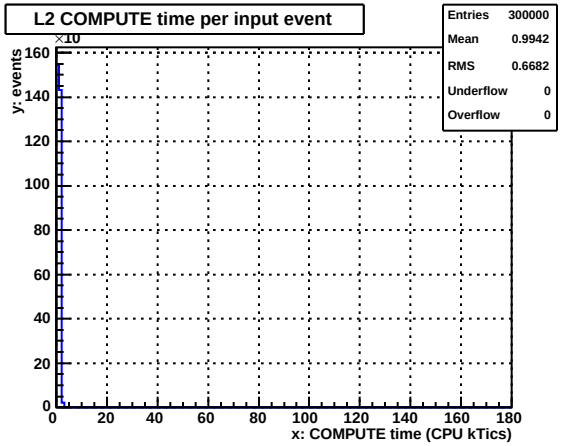
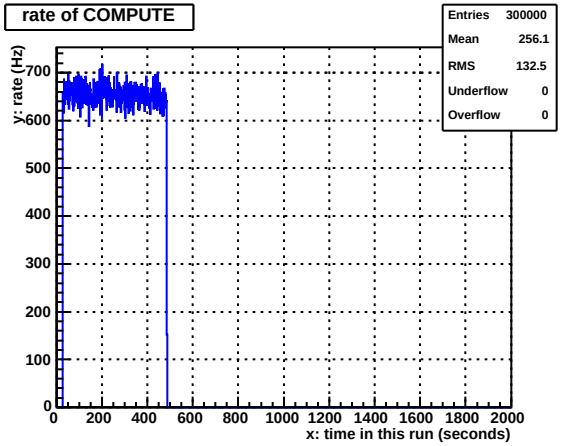
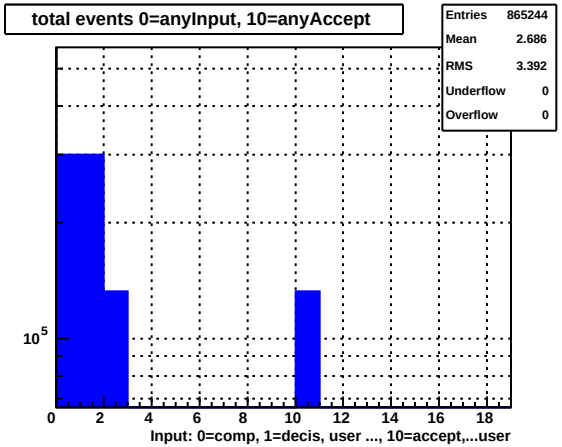
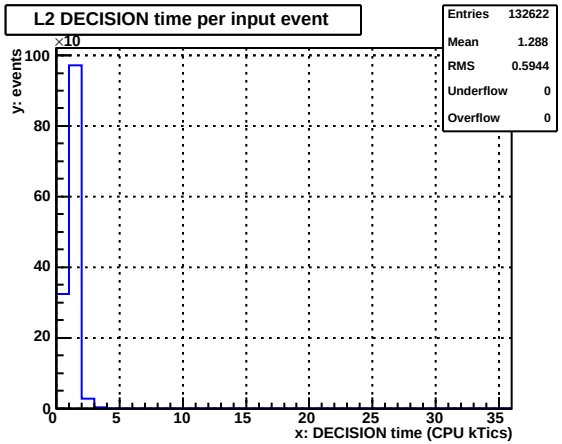
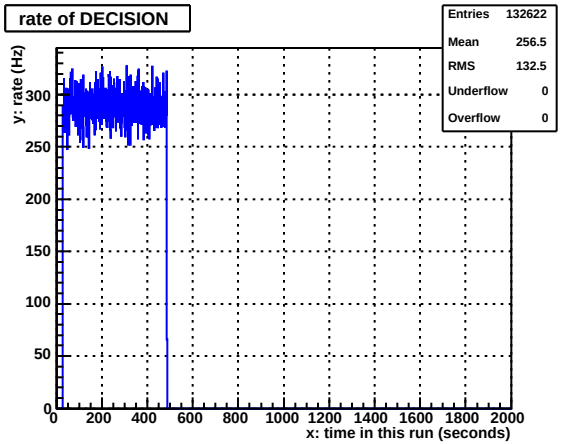
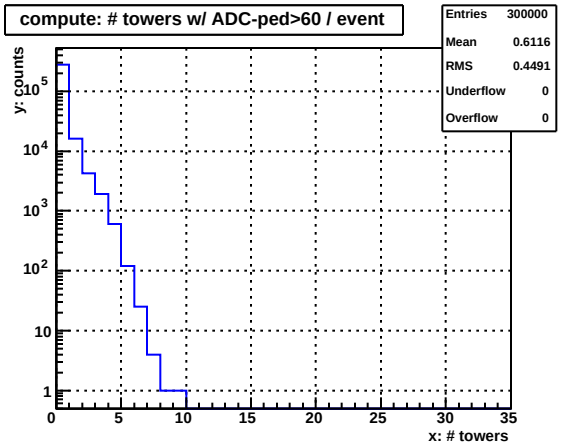

L2 COMPUTE time per input event
Entries 300000
Mean 0.9942
RMS 0.6682
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

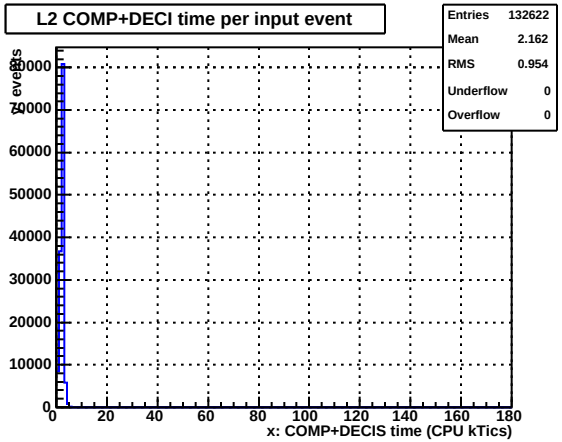

rate of COMPUTE
Entries 300000
Mean 256.1
RMS 132.5
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

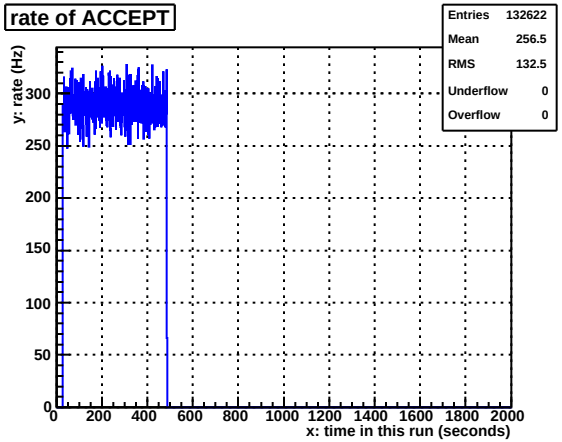

total events 0=anyInput, 10=anyAccept
Entries 865244
Mean 2.686
RMS 3.392
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

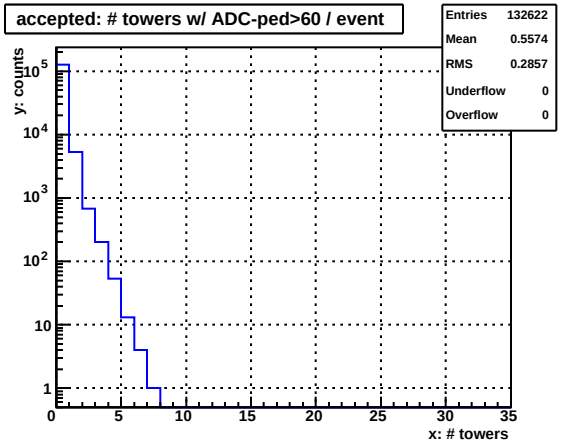

L2 DECISION time per input event
Entries 132622
Mean 1.288
RMS 0.5944
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

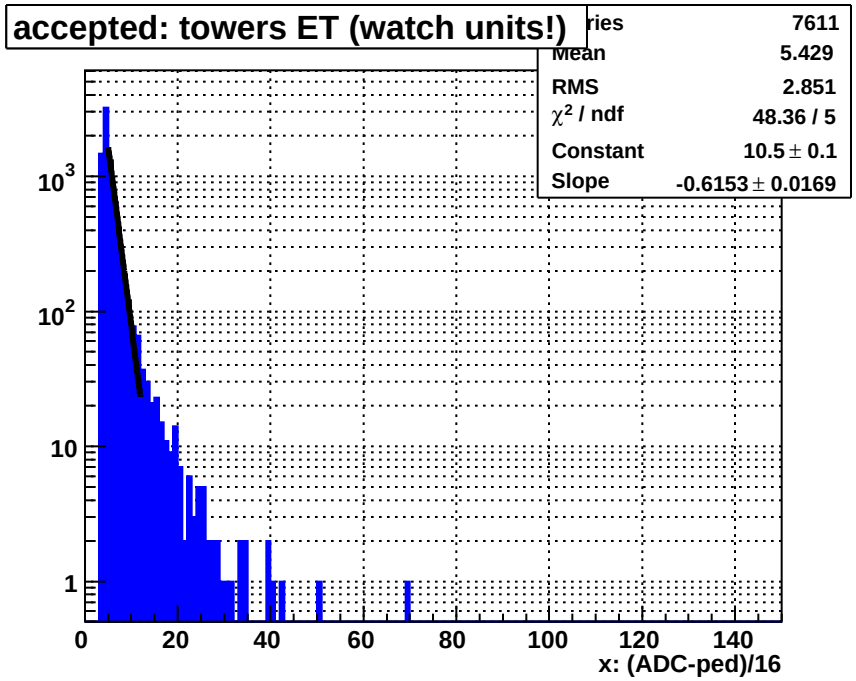

rate of DECISION
Entries 132622
Mean 256.5
RMS 132.5
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

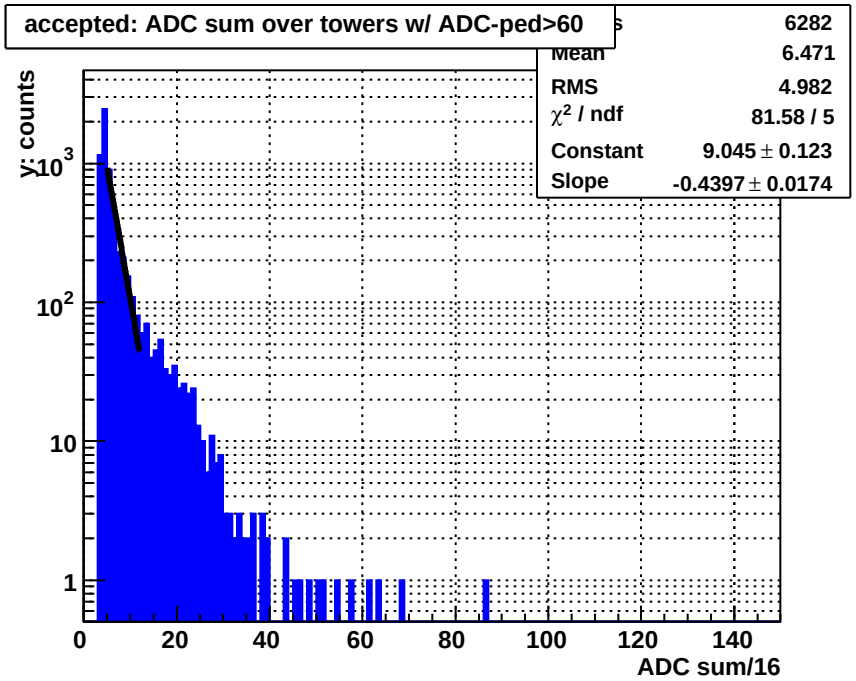

compute: # towers w/ ADC-ped>60 / event
Entries 300000
Mean 0.6116
RMS 0.4491
Underflow 0
Overflow 0
y: counts
x: # towers

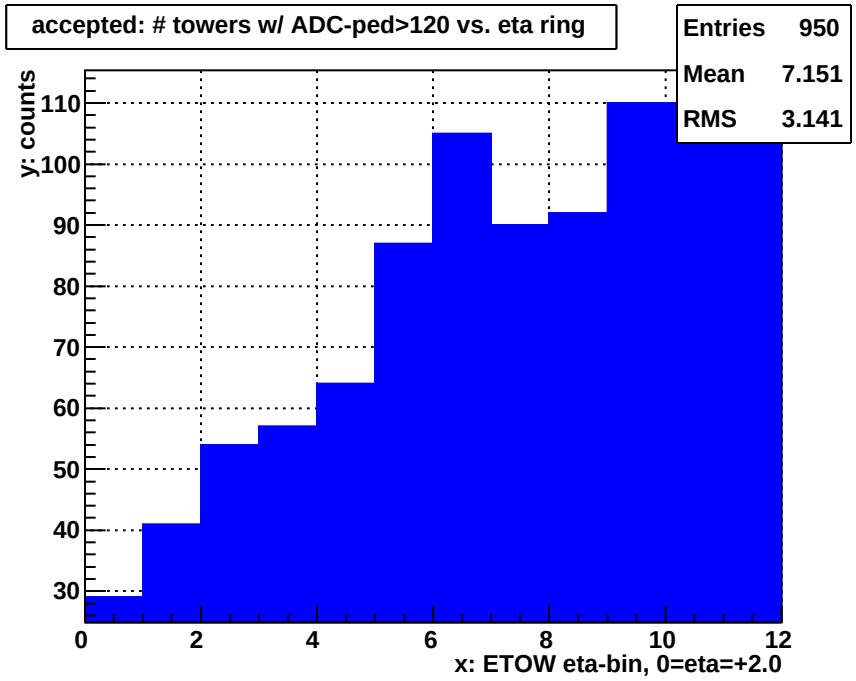

L2 COMP+DECI time per input event
Entries 132622
Mean 2.162
RMS 0.954
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

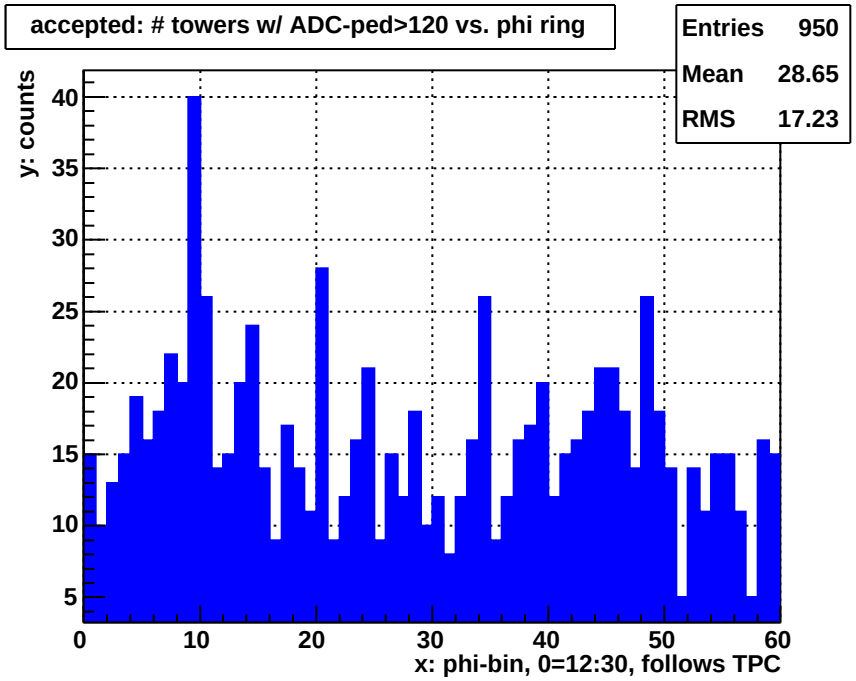

rate of ACCEPT
Entries 132622
Mean 256.5
RMS 132.5
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 132622
Mean 0.5574
RMS 0.2857
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 7611
Mean 5.429
RMS 2.851
χ² / ndf 48.36 / 5
Constant 10.5 ± 0.1
Slope -0.6153 ± 0.0169
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 6282
Mean 6.471
RMS 4.982
χ² / ndf 81.58 / 5
Constant 9.045 ± 0.123
Slope -0.4397 ± 0.0174
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 950
Mean 7.151
RMS 3.141
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 950
Mean 28.65
RMS 17.23
y: counts
x: phi-bin, 0=12:30, follows TPC