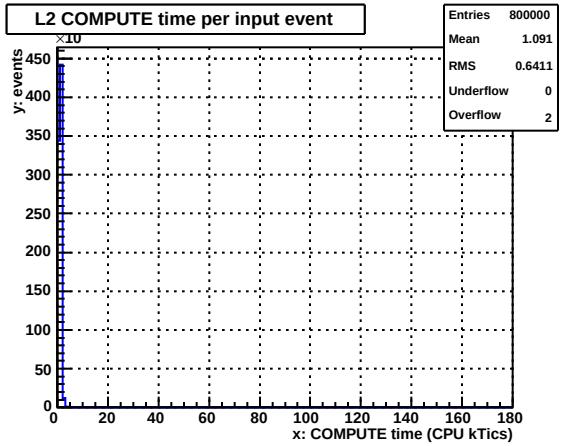
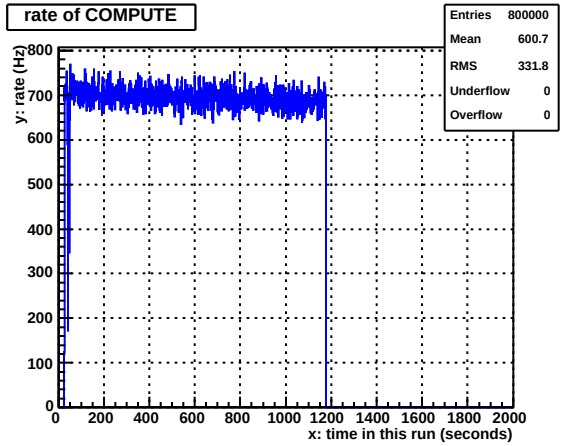
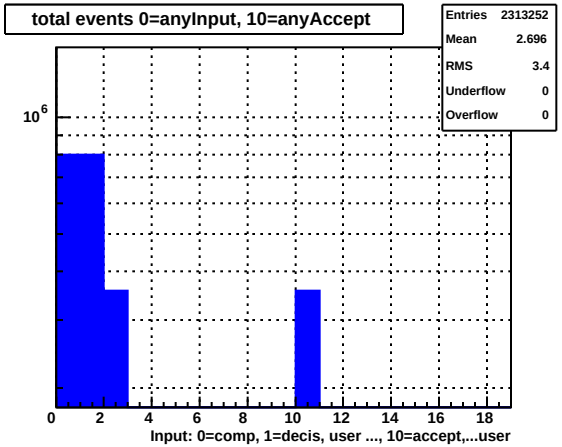
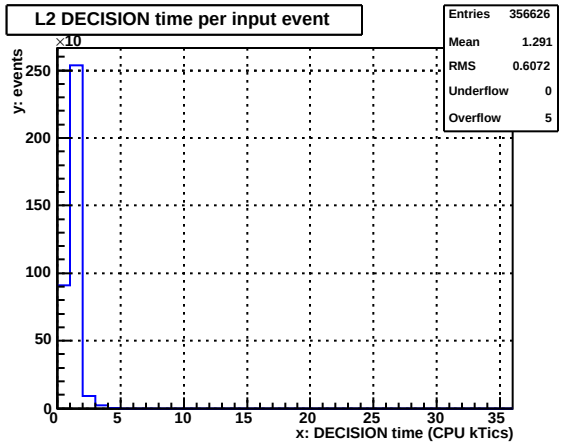
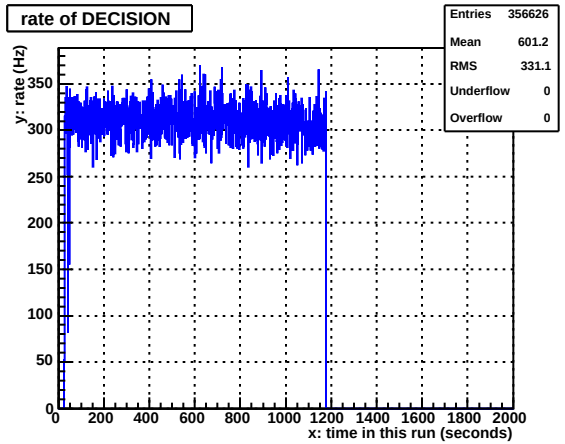
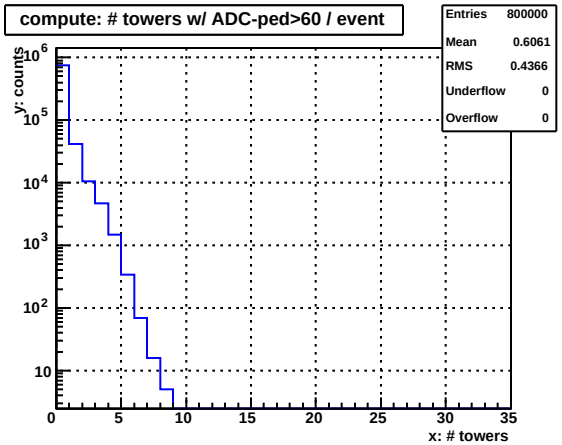

L2 COMPUTE time per input event
Entries 800000
Mean 1.091
RMS 0.6411
Underflow 0
Overflow 2
y: events
x: COMPUTE time (CPU kTics)

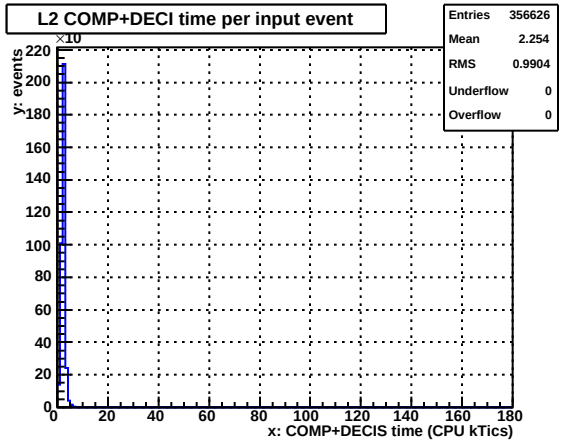

rate of COMPUTE
Entries 800000
Mean 600.7
RMS 331.8
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

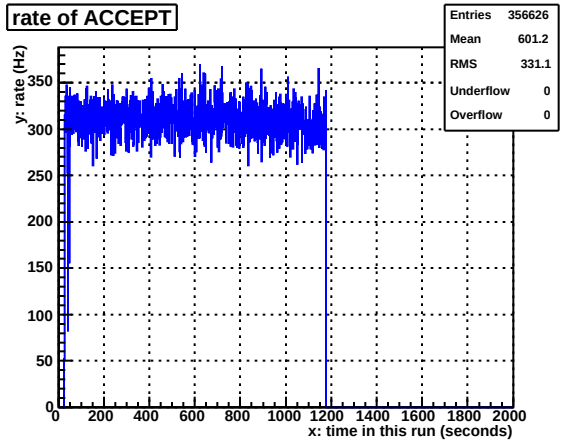

total events 0=anyInput, 10=anyAccept
Entries 2313252
Mean 2.696
RMS 3.4
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

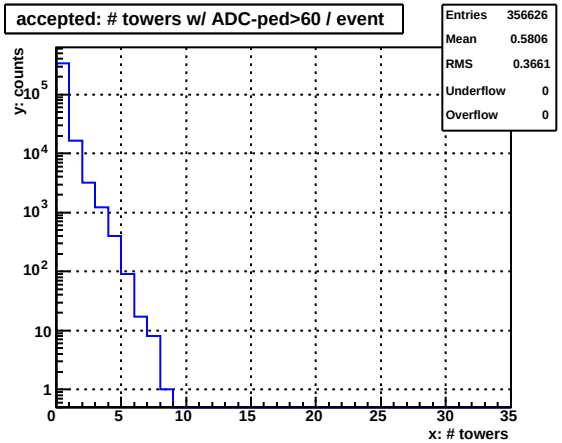

L2 DECISION time per input event
Entries 356626
Mean 1.291
RMS 0.6072
Underflow 0
Overflow 5
y: events
x: DECISION time (CPU kTics)

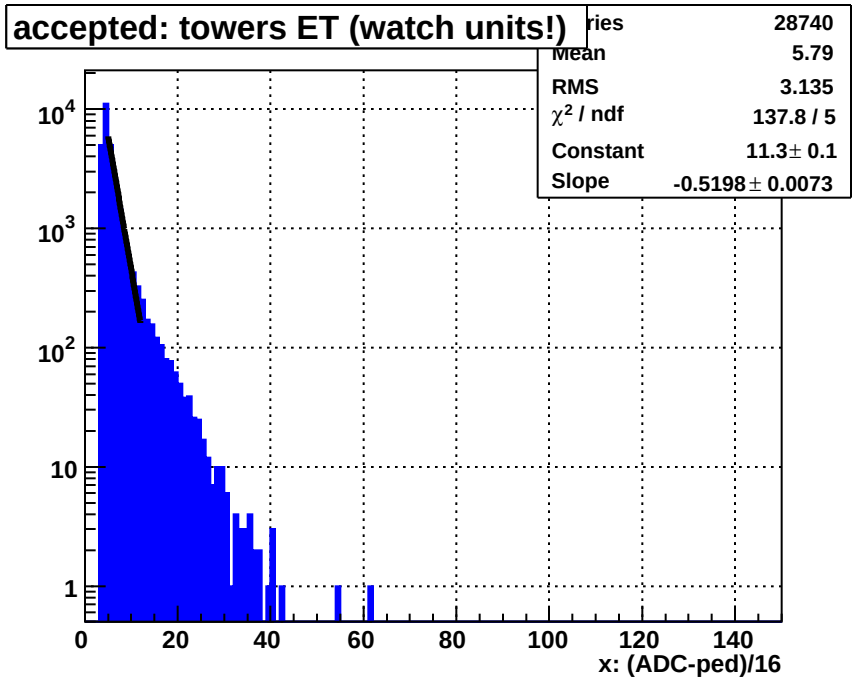

rate of DECISION
Entries 356626
Mean 601.2
RMS 331.1
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

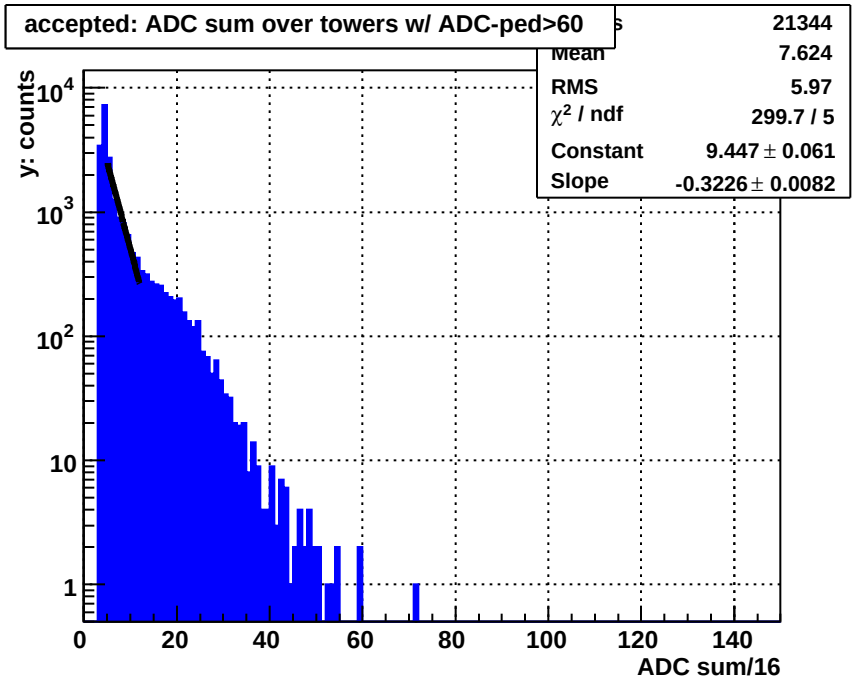

compute: # towers w/ ADC-ped>60 / event
Entries 800000
Mean 0.6061
RMS 0.4366
Underflow 0
Overflow 0
y: counts
x: # towers

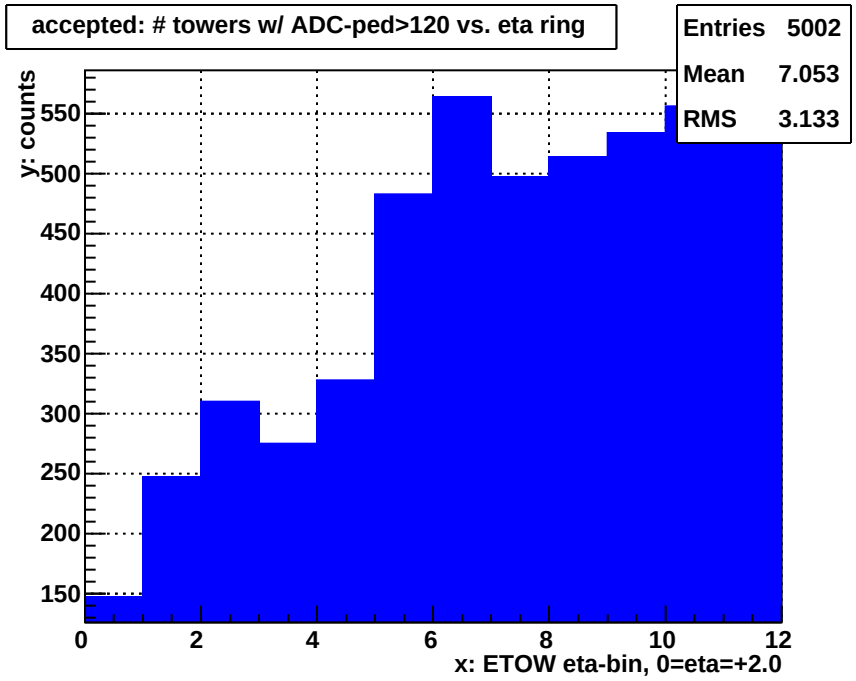

L2 COMP+DECI time per input event
Entries 356626
Mean 2.254
RMS 0.9904
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

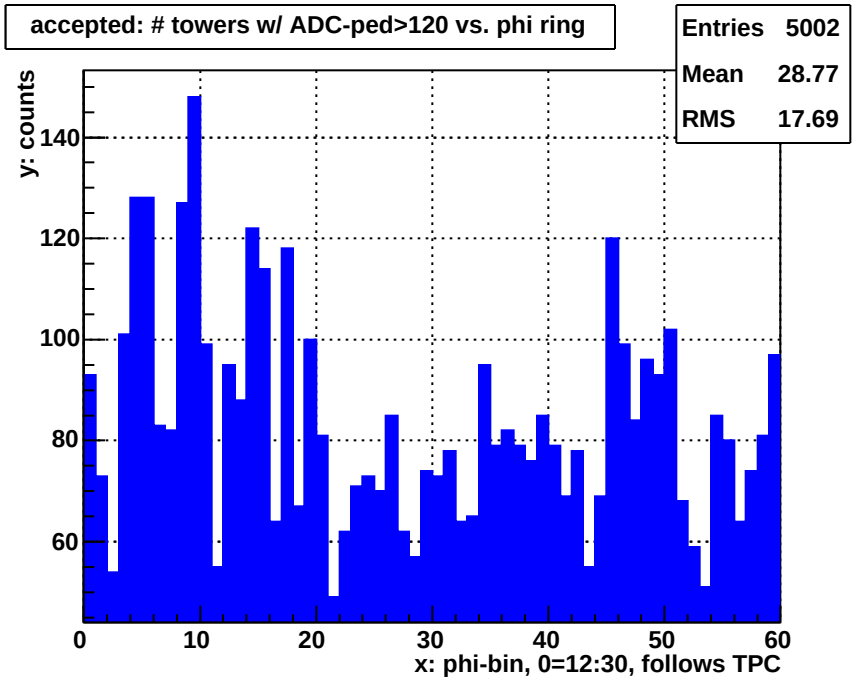

rate of ACCEPT
Entries 356626
Mean 601.2
RMS 331.1
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 356626
Mean 0.5806
RMS 0.3661
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 28740
Mean 5.79
RMS 3.135
χ² / ndf 137.8 / 5
Constant 11.3 ± 0.1
Slope -0.5198 ± 0.0073
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 21344
Mean 7.624
RMS 5.97
χ² / ndf 299.7 / 5
Constant 9.447 ± 0.061
Slope -0.3226 ± 0.0082
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 5002
Mean 7.053
RMS 3.133
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 5002
Mean 28.77
RMS 17.69
y: counts
x: phi-bin, 0=12:30, follows TPC