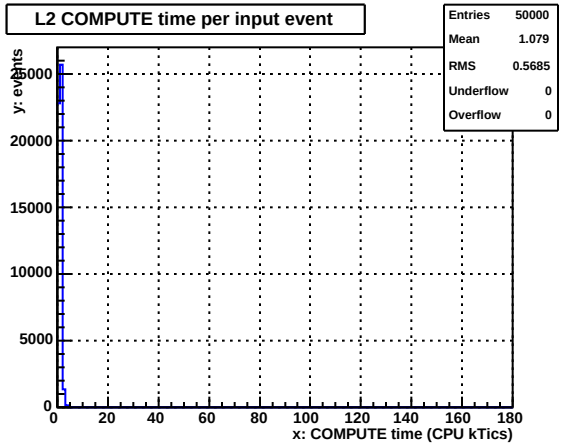
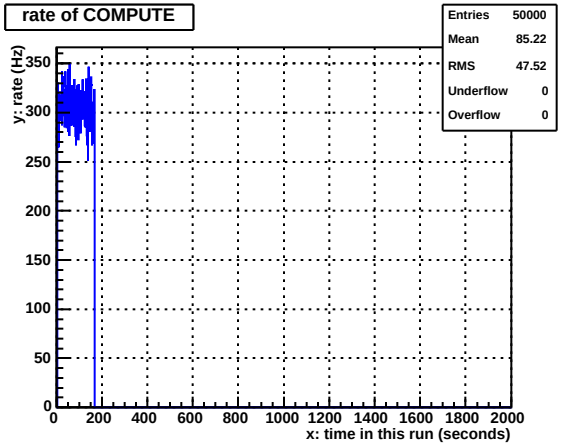
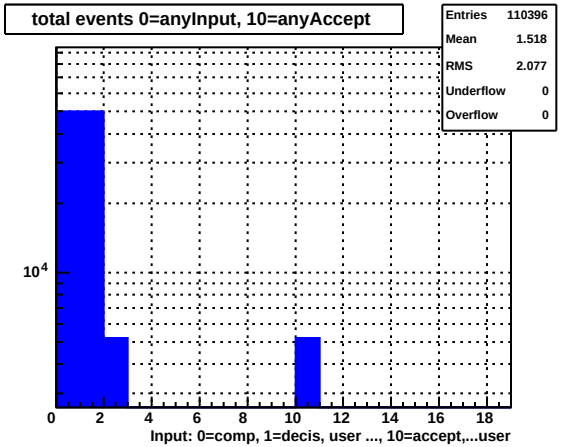
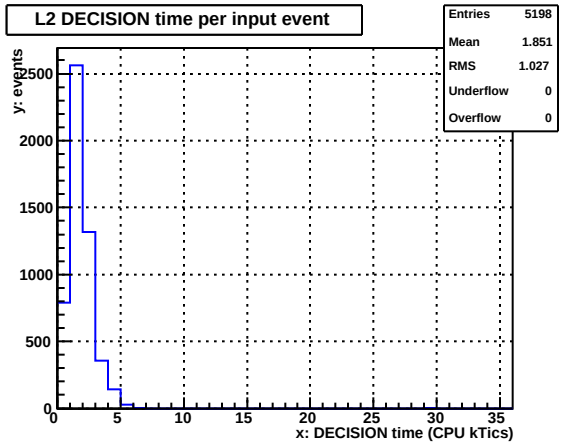
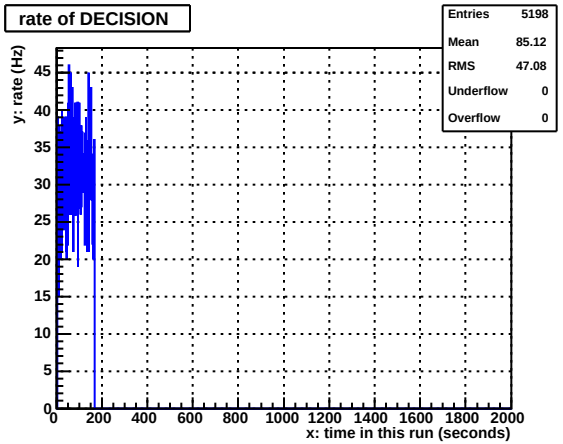
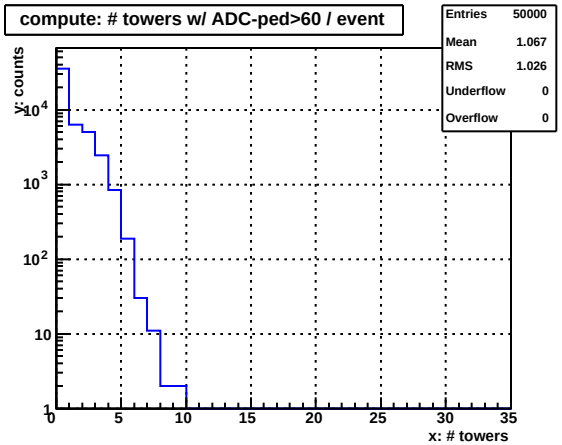

L2 COMPUTE time per input event
Entries 50000
Mean 1.079
RMS 0.5685
Underflow 0
Overflow 0
y: events
x: COMPUTE time (CPU kTics)

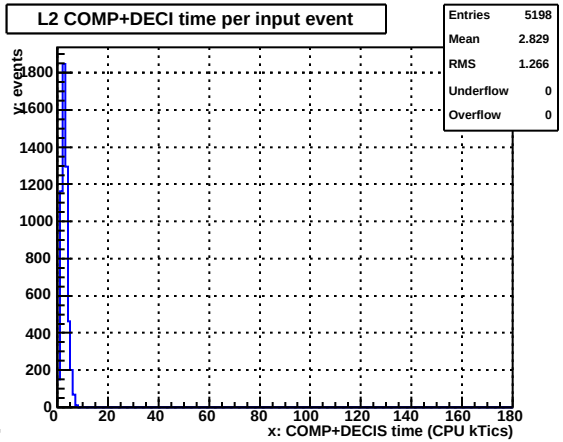

rate of COMPUTE
Entries 50000
Mean 85.22
RMS 47.52
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

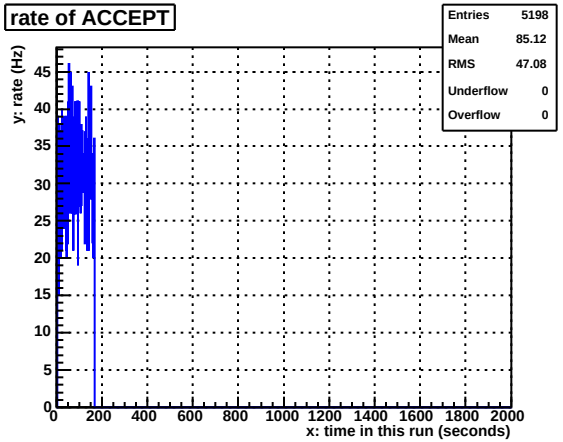

total events 0=anyInput, 10=anyAccept
Entries 110396
Mean 1.518
RMS 2.077
Underflow 0
Overflow 0
Input: 0=comp, 1=decis, user ..., 10=accept,...user

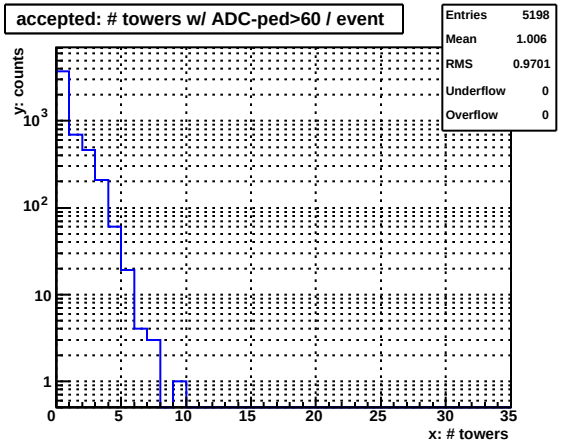

L2 DECISION time per input event
Entries 5198
Mean 1.851
RMS 1.027
Underflow 0
Overflow 0
y: events
x: DECISION time (CPU kTics)

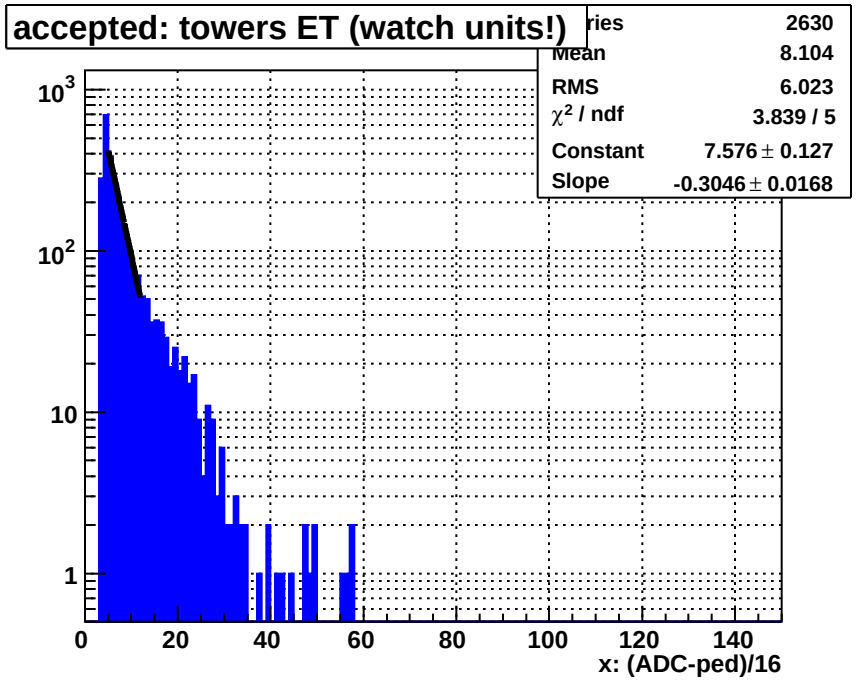

rate of DECISION
Entries 5198
Mean 85.12
RMS 47.08
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)

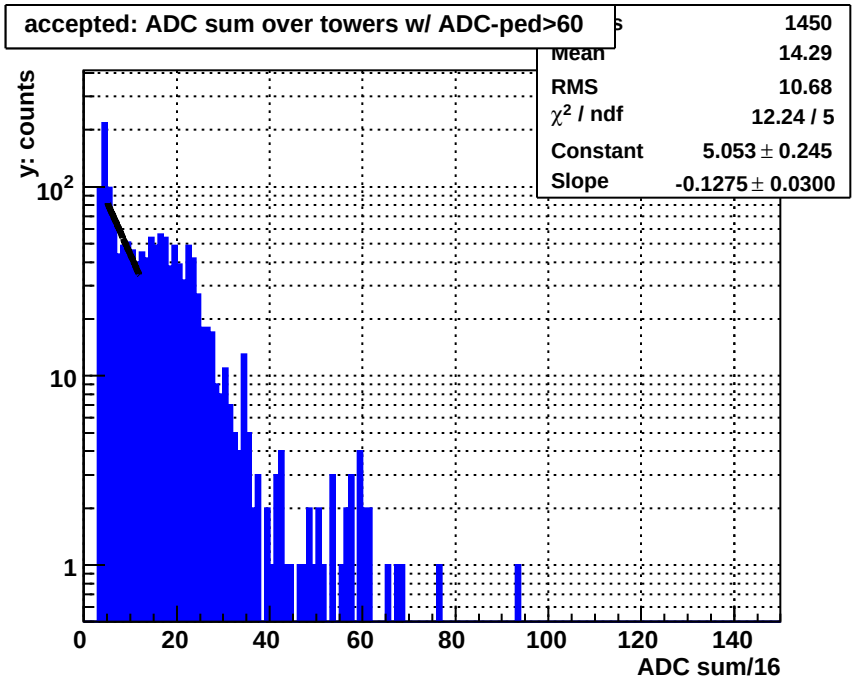

compute: # towers w/ ADC-ped>60 / event
Entries 50000
Mean 1.067
RMS 1.026
Underflow 0
Overflow 0
y: counts
x: # towers

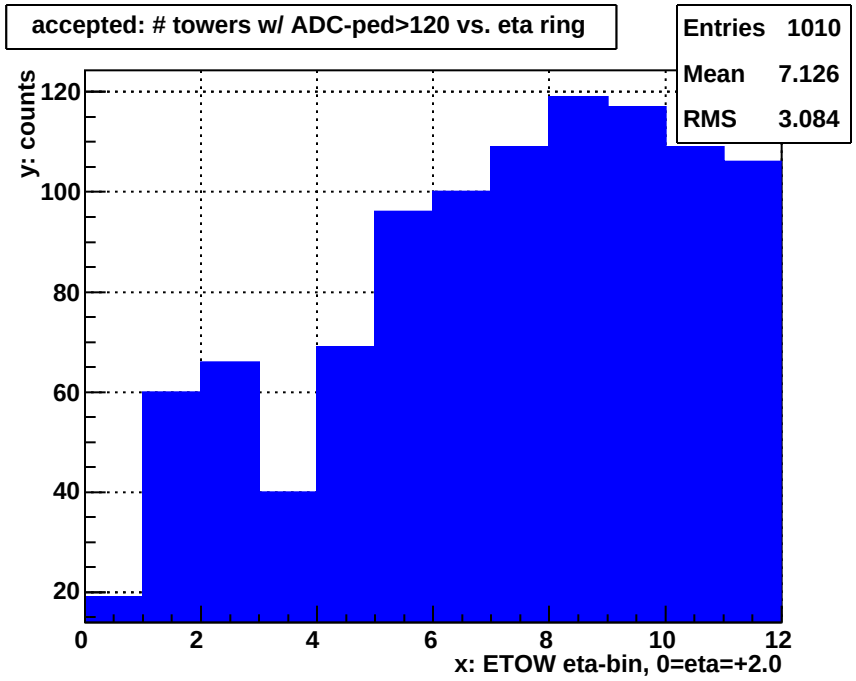

L2 COMP+DECI time per input event
Entries 5198
Mean 2.829
RMS 1.266
Underflow 0
Overflow 0
y: events
x: COMP+DECIS time (CPU kTics)

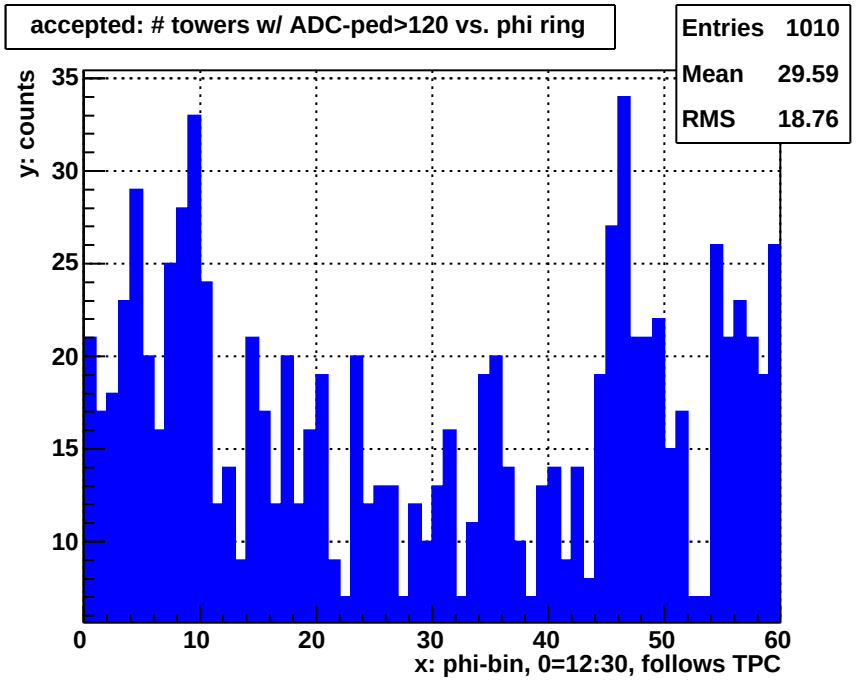

rate of ACCEPT
Entries 5198
Mean 85.12
RMS 47.08
Underflow 0
Overflow 0
y: rate (Hz)
x: time in this run (seconds)


accepted: # towers w/ ADC-ped>60 / event
Entries 5198
Mean 1.006
RMS 0.9701
Underflow 0
Overflow 0
y: counts
x: # towers


accepted: towers ET (watch units!)
ries 2630
Mean 8.104
RMS 6.023
χ² / ndf 3.839 / 5
Constant 7.576 ± 0.127
Slope -0.3046 ± 0.0168
x: (ADC-ped)/16


accepted: ADC sum over towers w/ ADC-ped>60
s 1450
Mean 14.29
RMS 10.68
χ² / ndf 12.24 / 5
Constant 5.053 ± 0.245
Slope -0.1275 ± 0.0300
y: counts
ADC sum/16


accepted: # towers w/ ADC-ped>120 vs. eta ring
Entries 1010
Mean 7.126
RMS 3.084
y: counts
x: ETOW eta-bin, 0=eta=+2.0


accepted: # towers w/ ADC-ped>120 vs. phi ring
Entries 1010
Mean 29.59
RMS 18.76
y: counts
x: phi-bin, 0=12:30, follows TPC